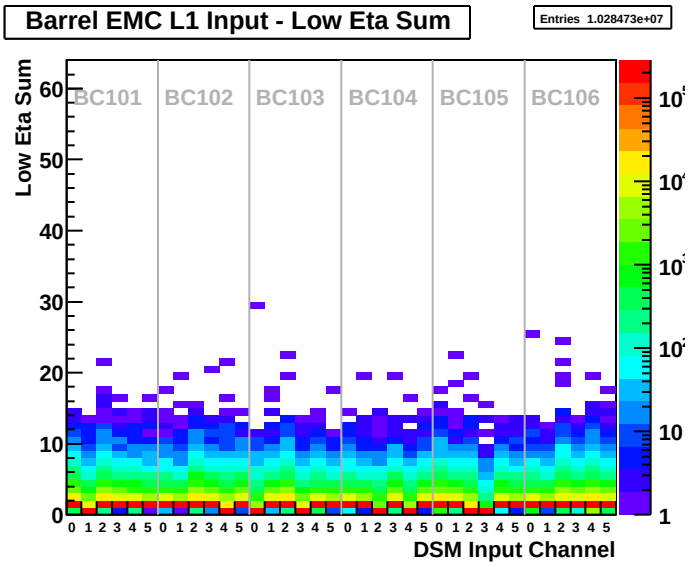
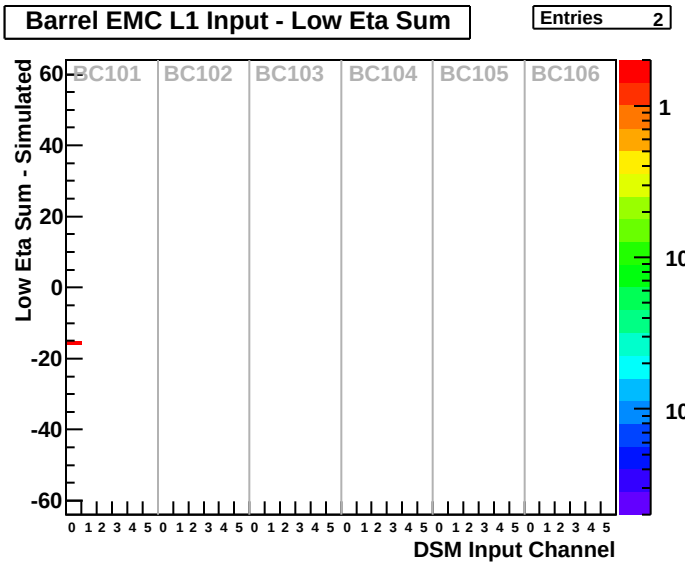
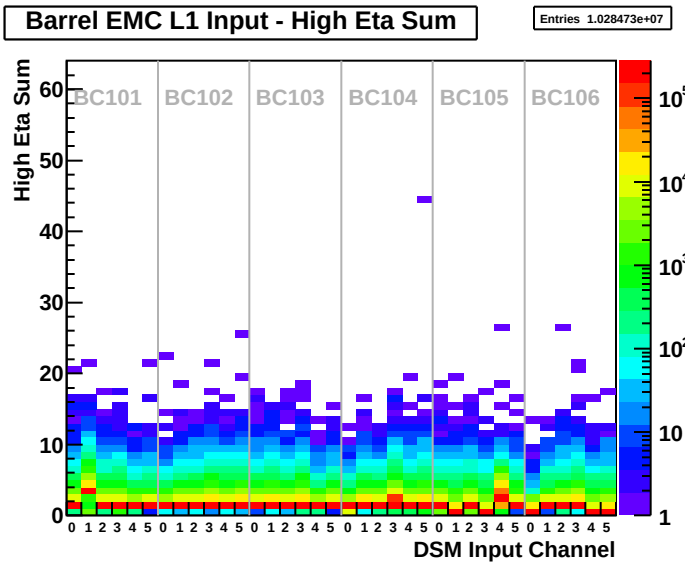
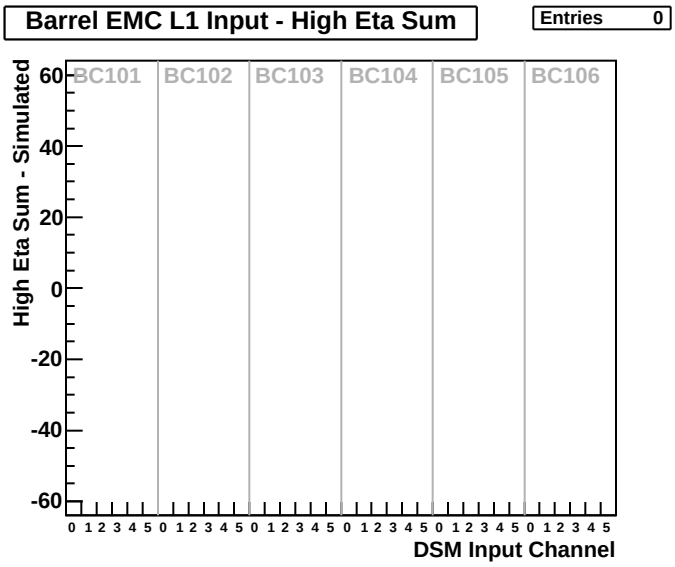
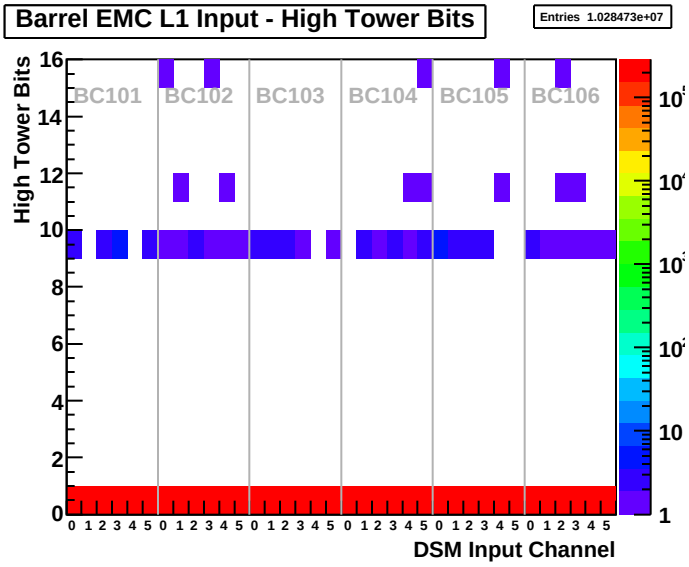
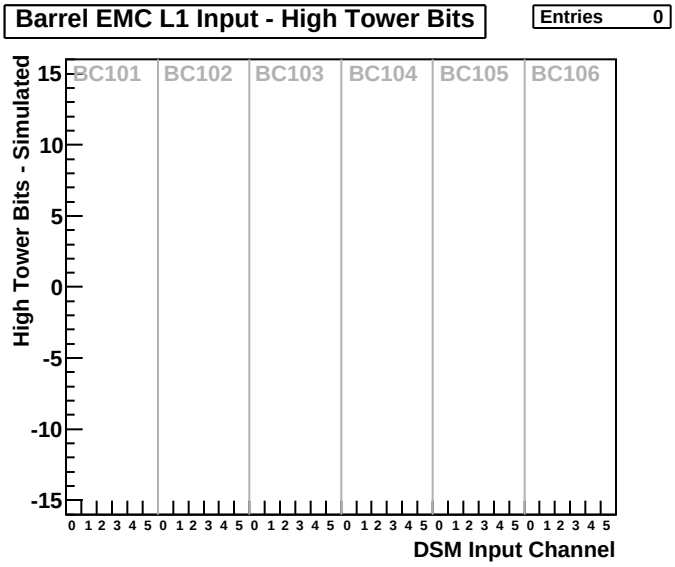

Barrel EMC L1 Input - Low Eta Sum
Entries 1.028473e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

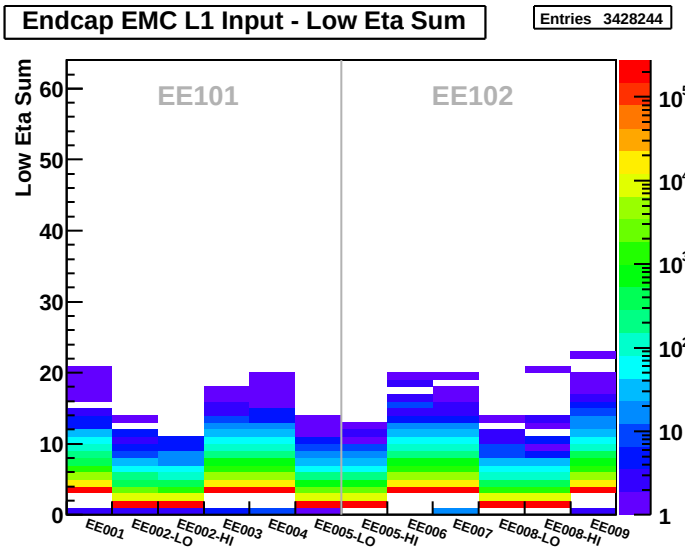

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

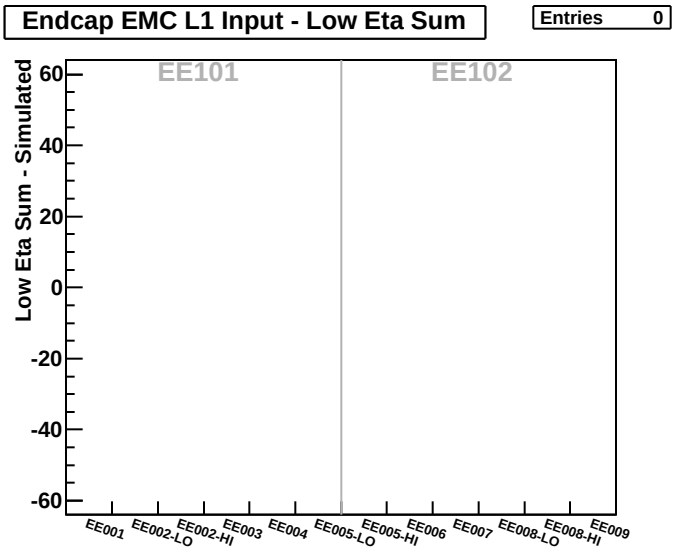

Barrel EMC L1 Input - High Eta Sum
Entries 1.028473e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

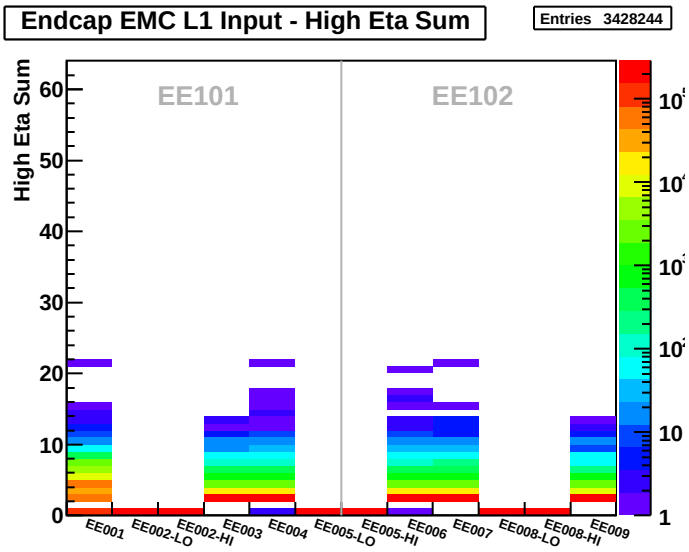

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

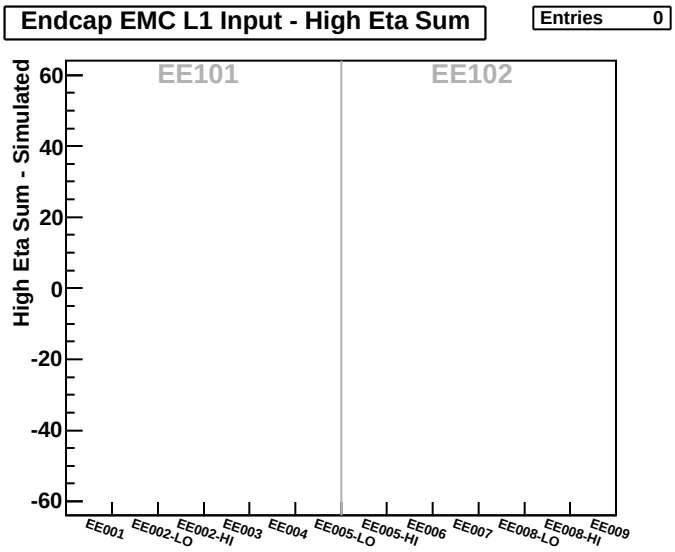

Barrel EMC L1 Input - High Tower Bits
Entries 1.028473e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

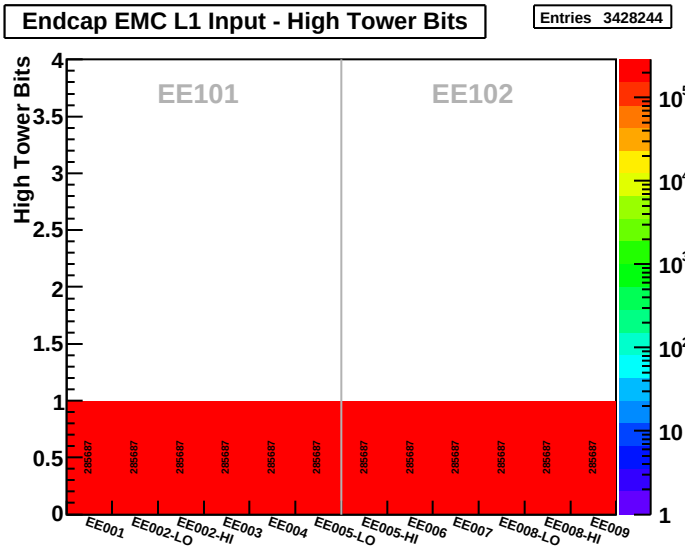

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

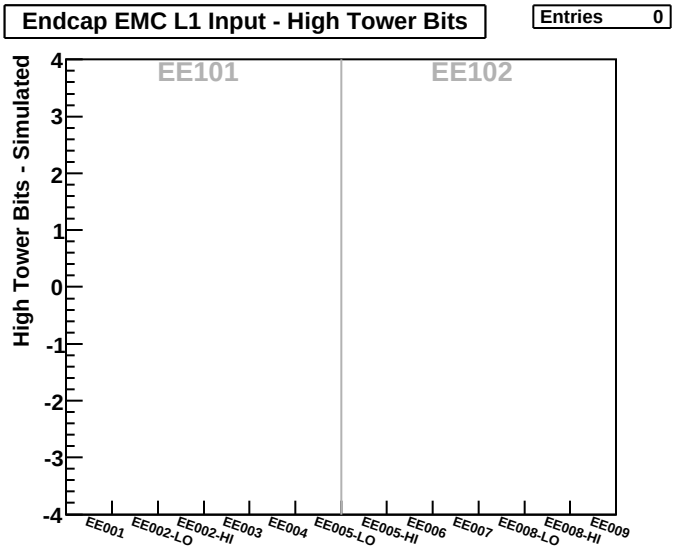

Endcap EMC L1 Input - Low Eta Sum
Entries 3428244
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

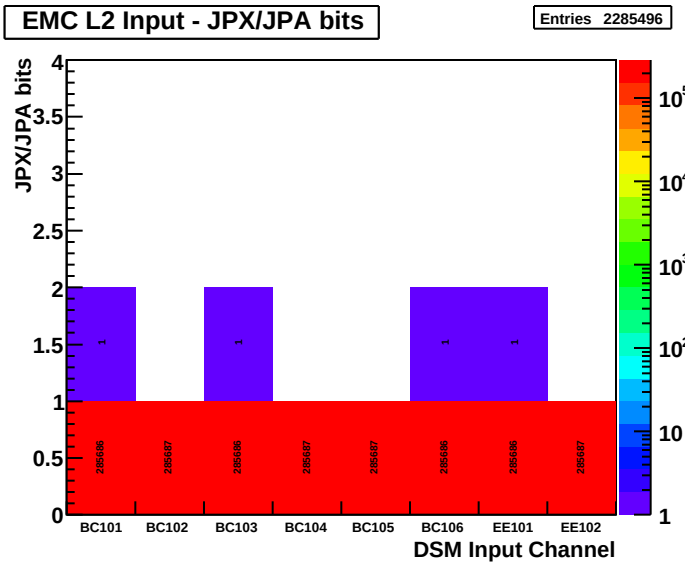

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

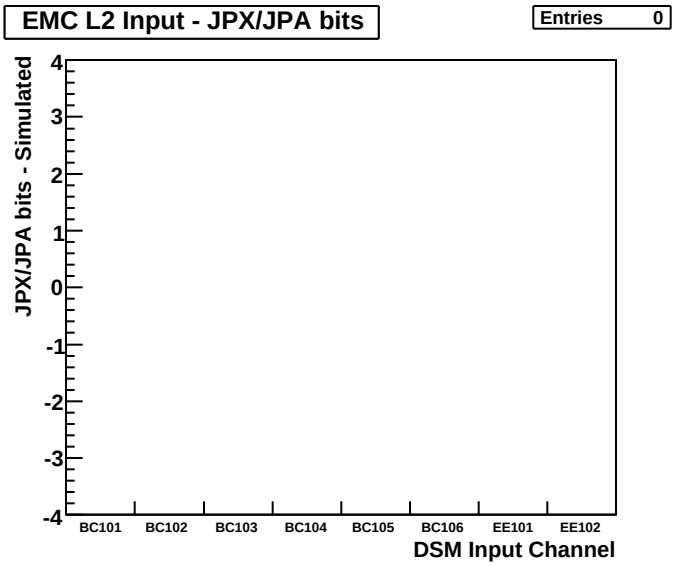

Endcap EMC L1 Input - High Eta Sum
Entries 3428244
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

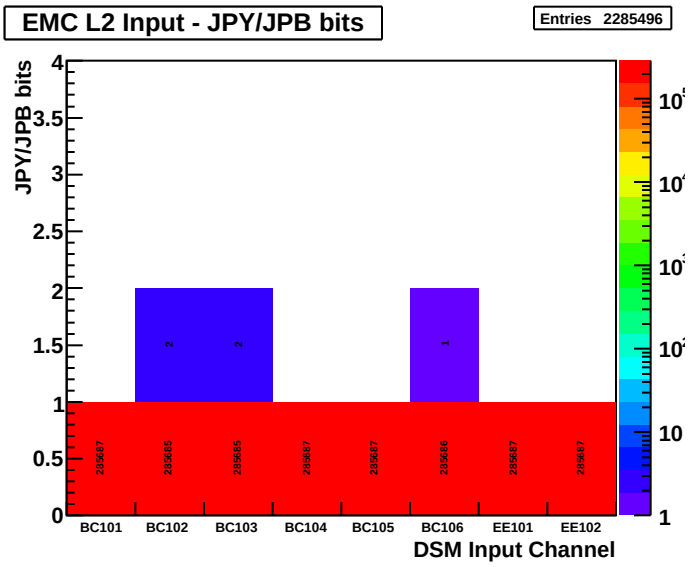

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

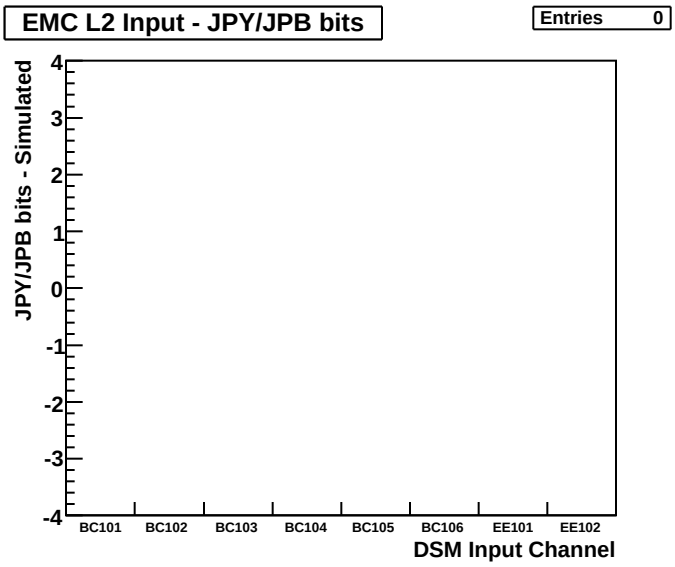

Endcap EMC L1 Input - High Tower Bits
Entries 3428244
High Tower Bits
EE101
EE102
285687
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

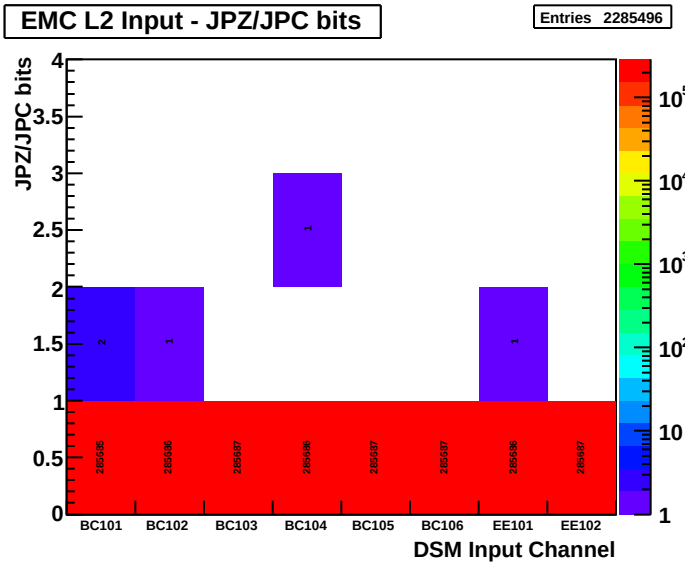

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

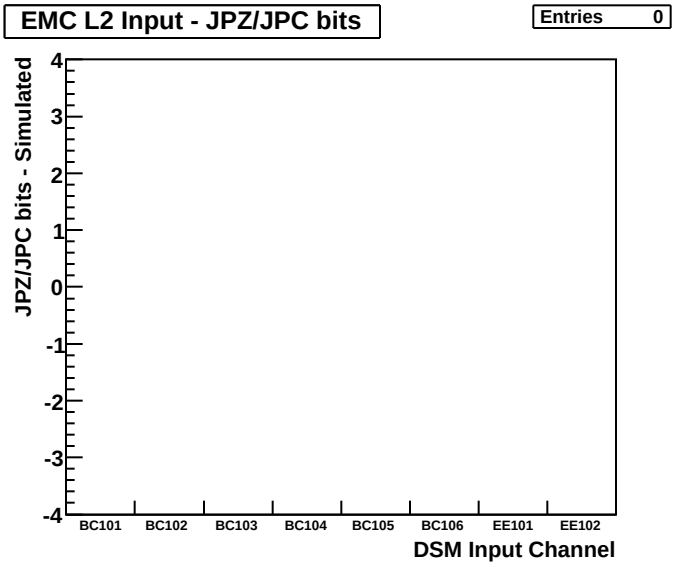

EMC L2 Input - JPX/JPA bits
Entries 2285496
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

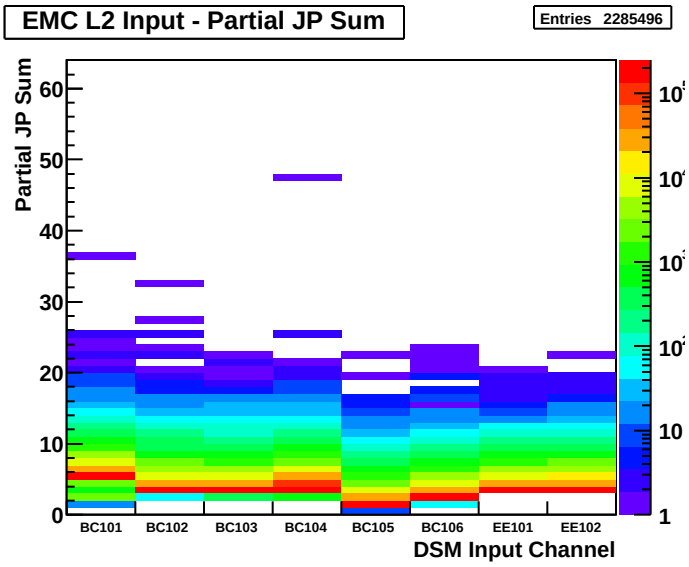

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

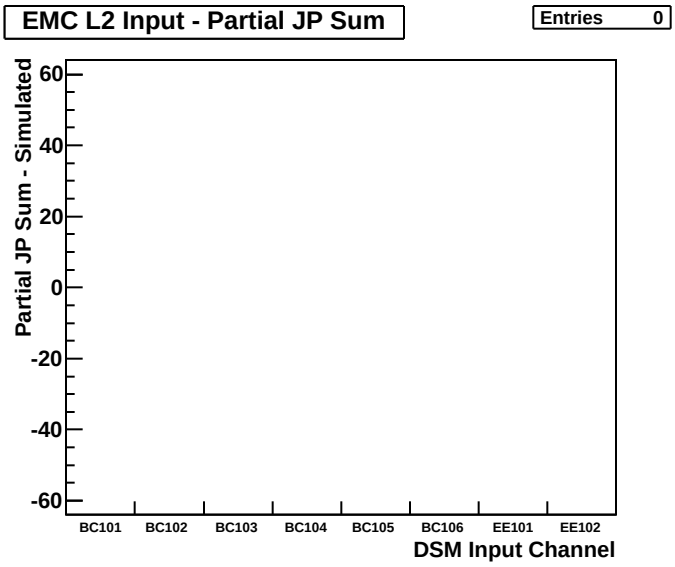

EMC L2 Input - JPY/JPB bits
Entries 2285496
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

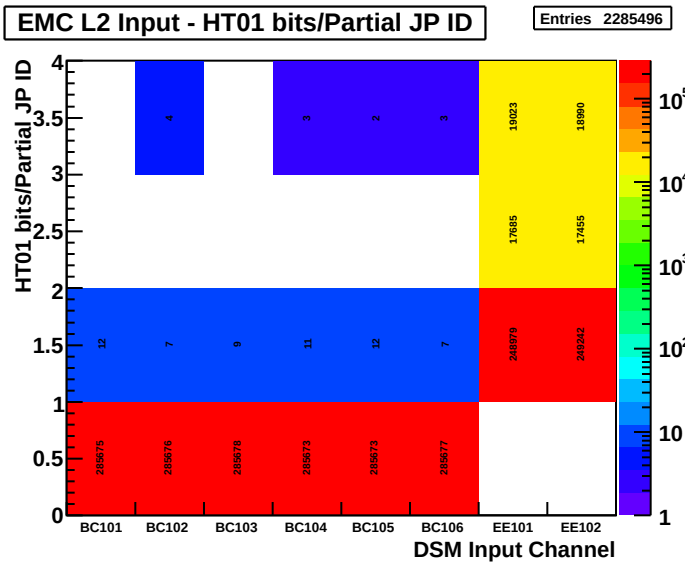

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

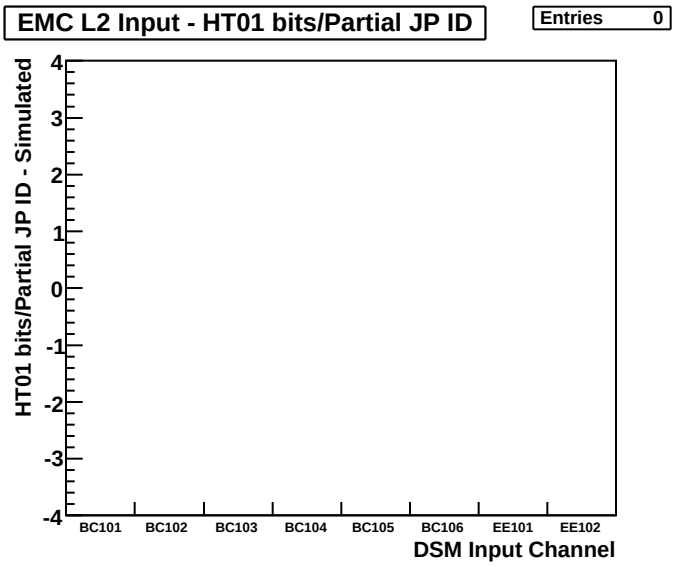

EMC L2 Input - JPZ/JPC bits
Entries 2285496
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

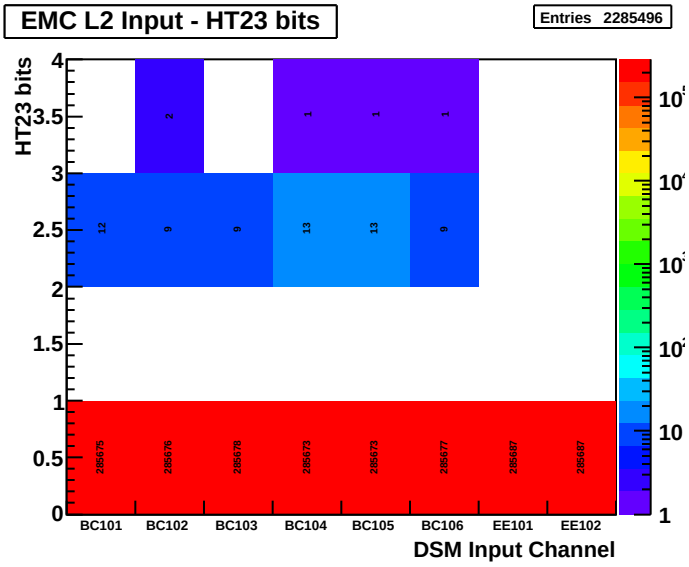

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

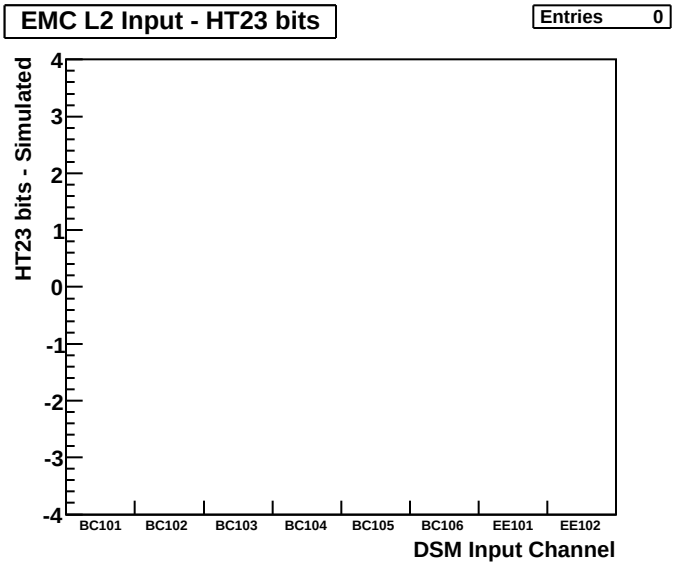

EMC L2 Input - Partial JP Sum
Entries 2285496
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

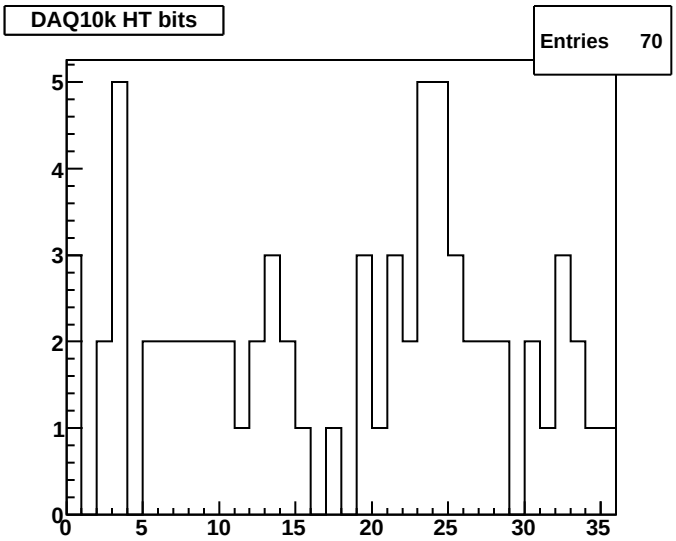

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

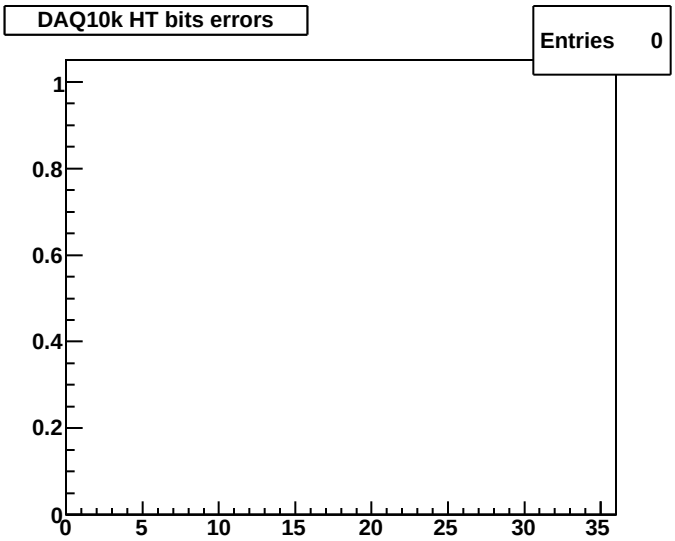

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2285496
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

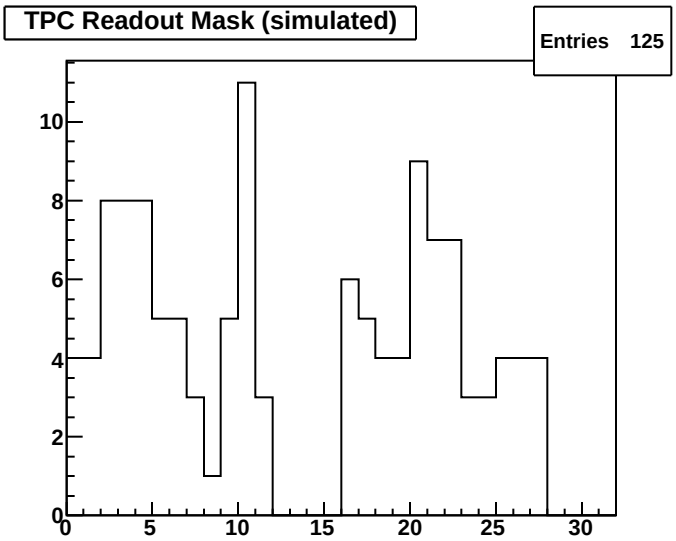

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

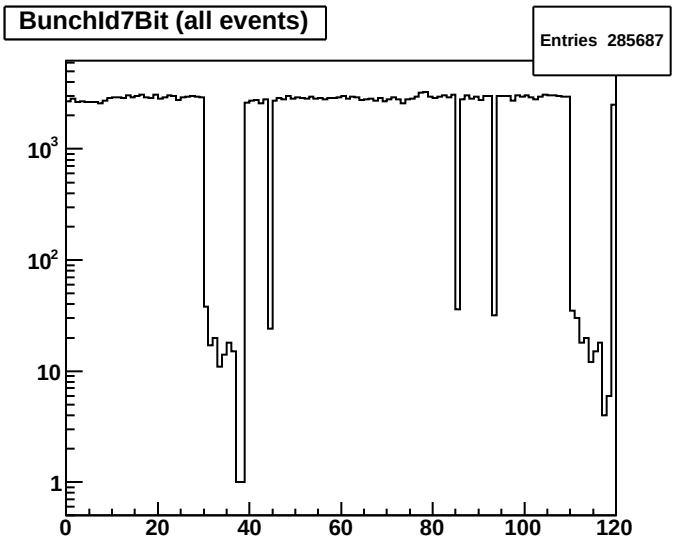

EMC L2 Input - HT23 bits
Entries 2285496
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

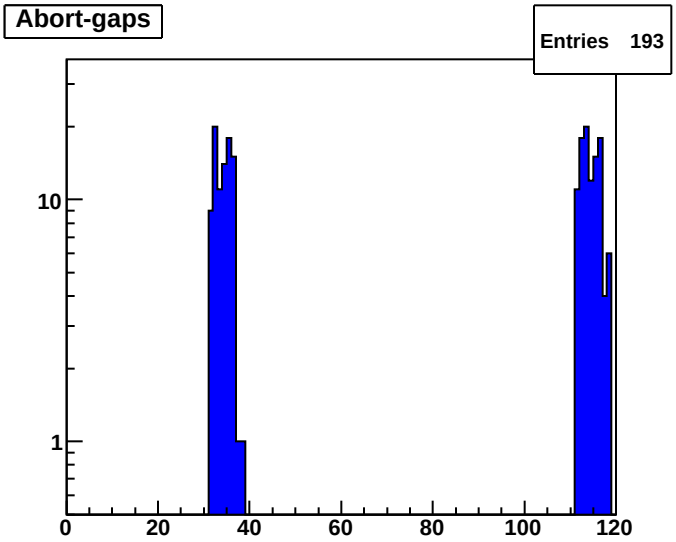

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

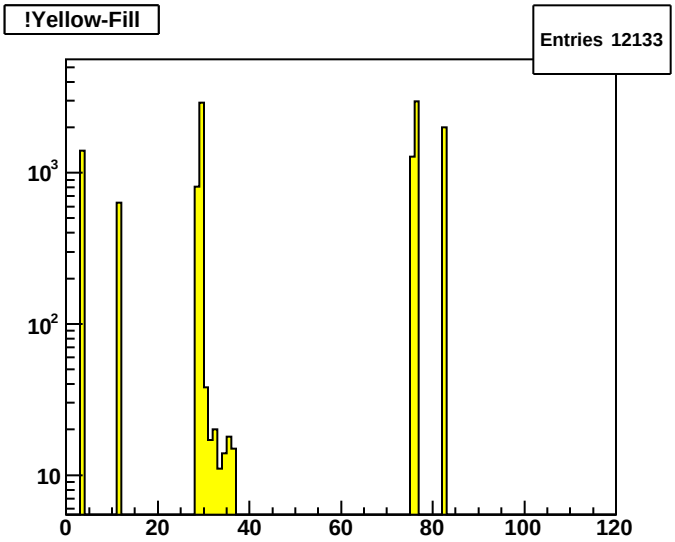

DAQ10k HT bits
Entries 70

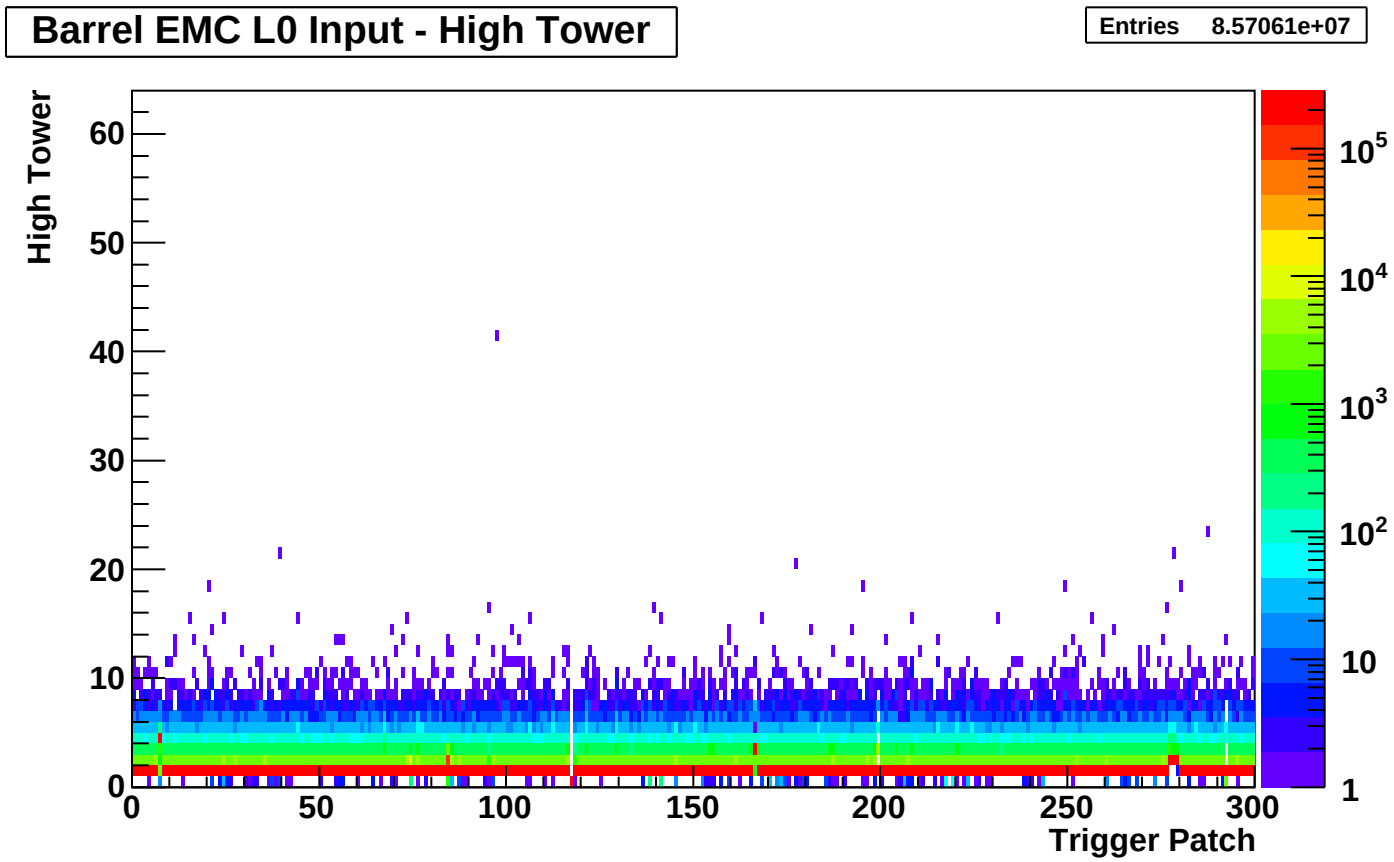

DAQ10k HT bits errors
Entries 0

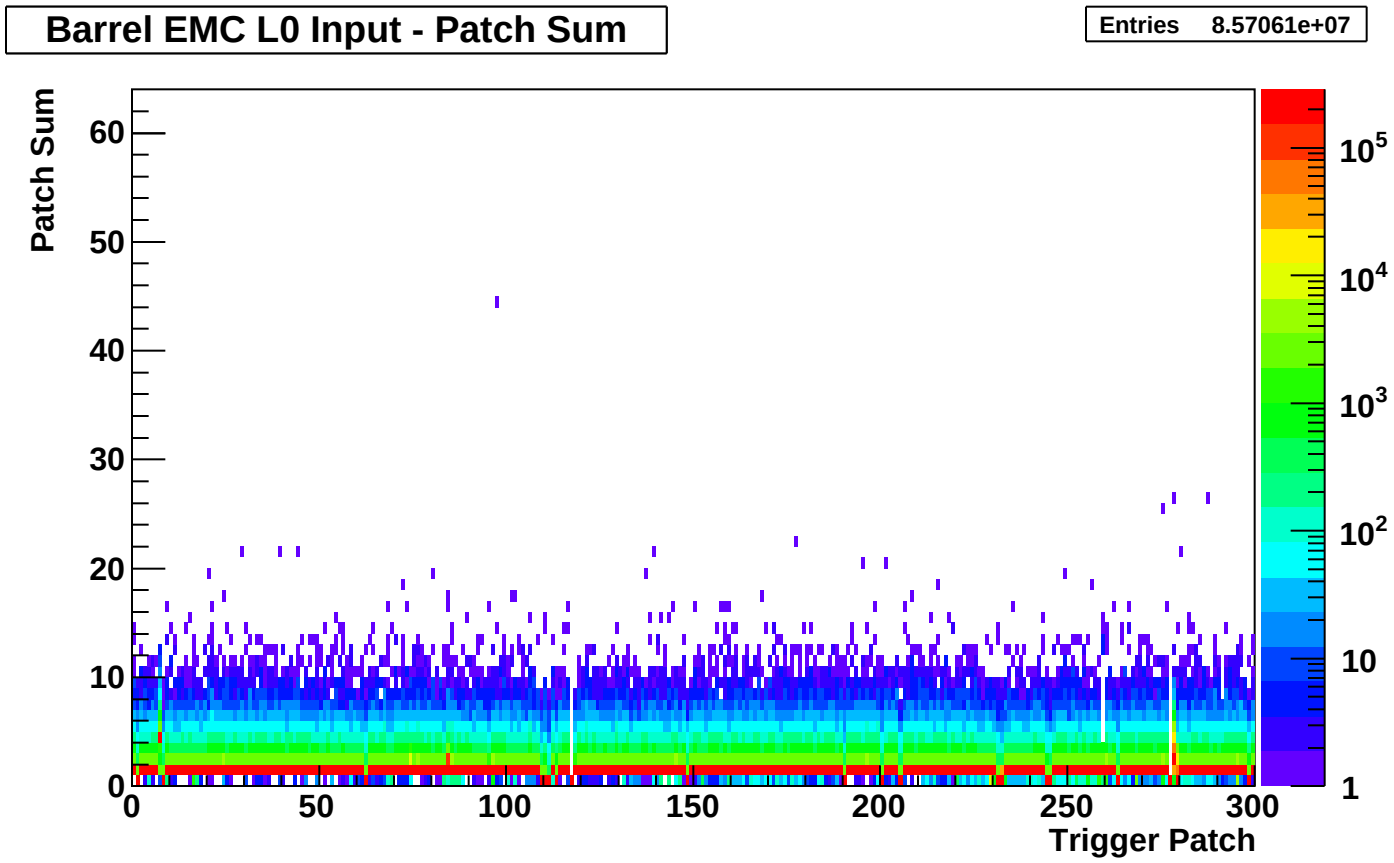

TPC Readout Mask (simulated)
Entries 125

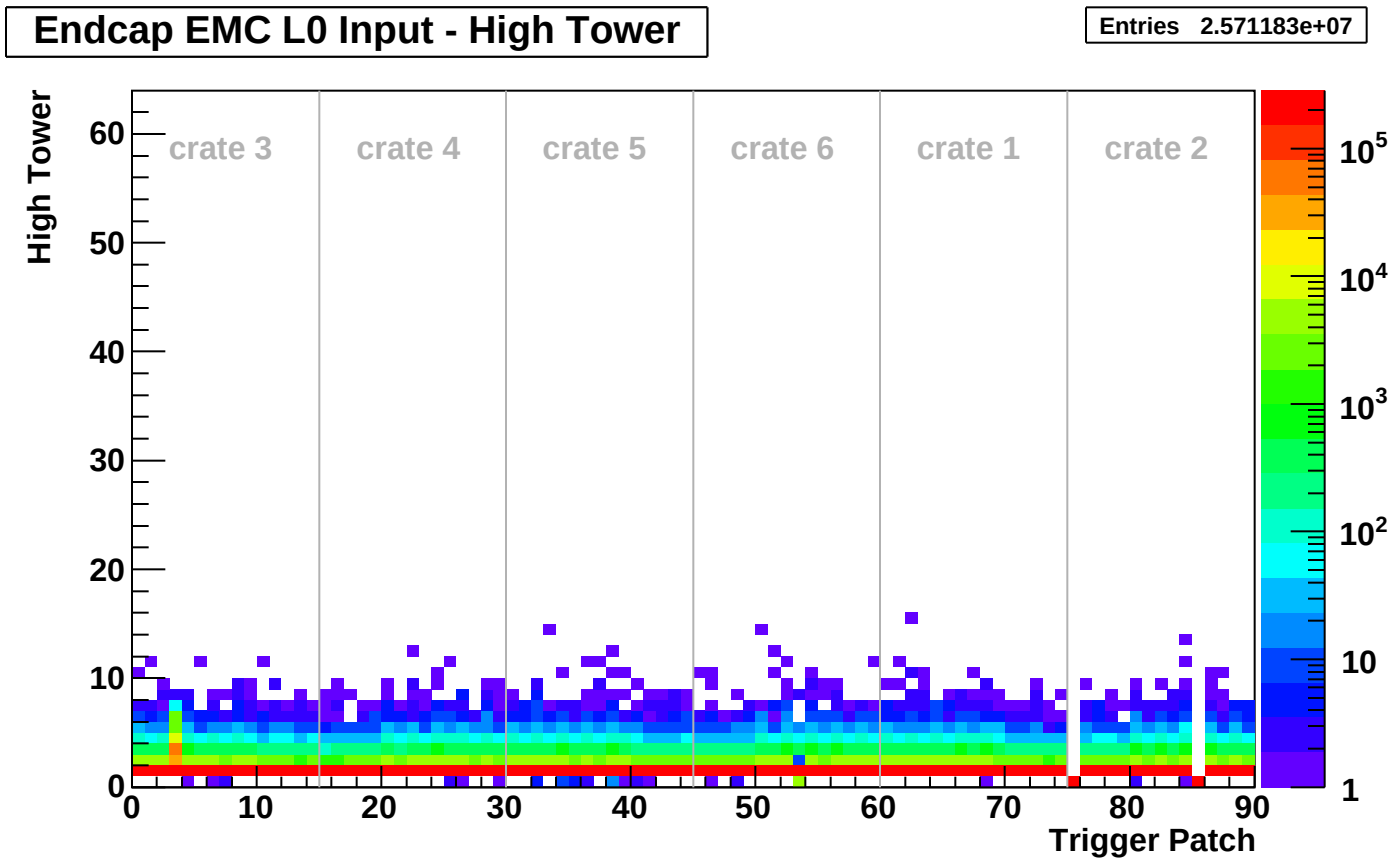

BunchId7Bit (all events)
Entries 285687

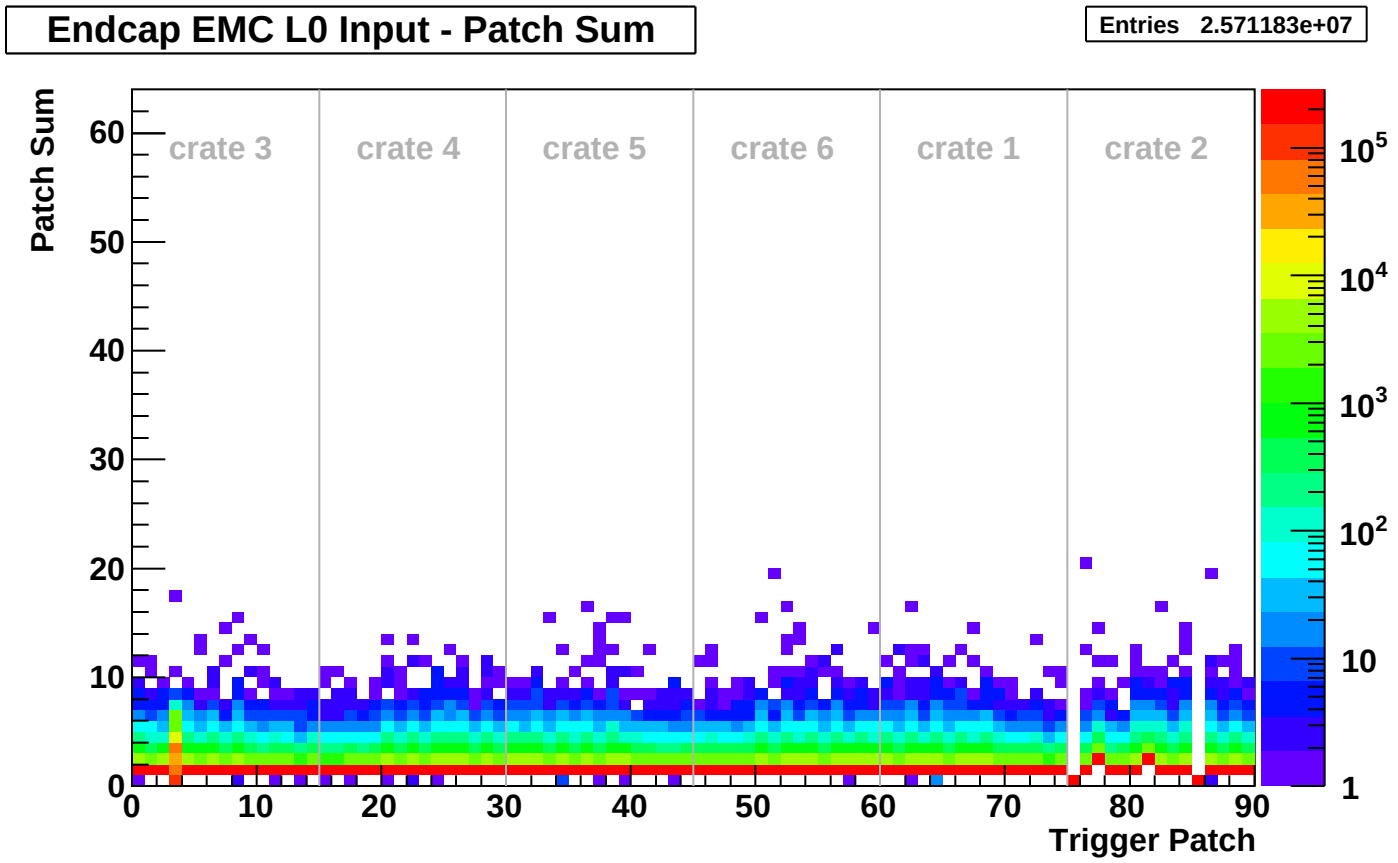

Abort-gaps
Entries 193

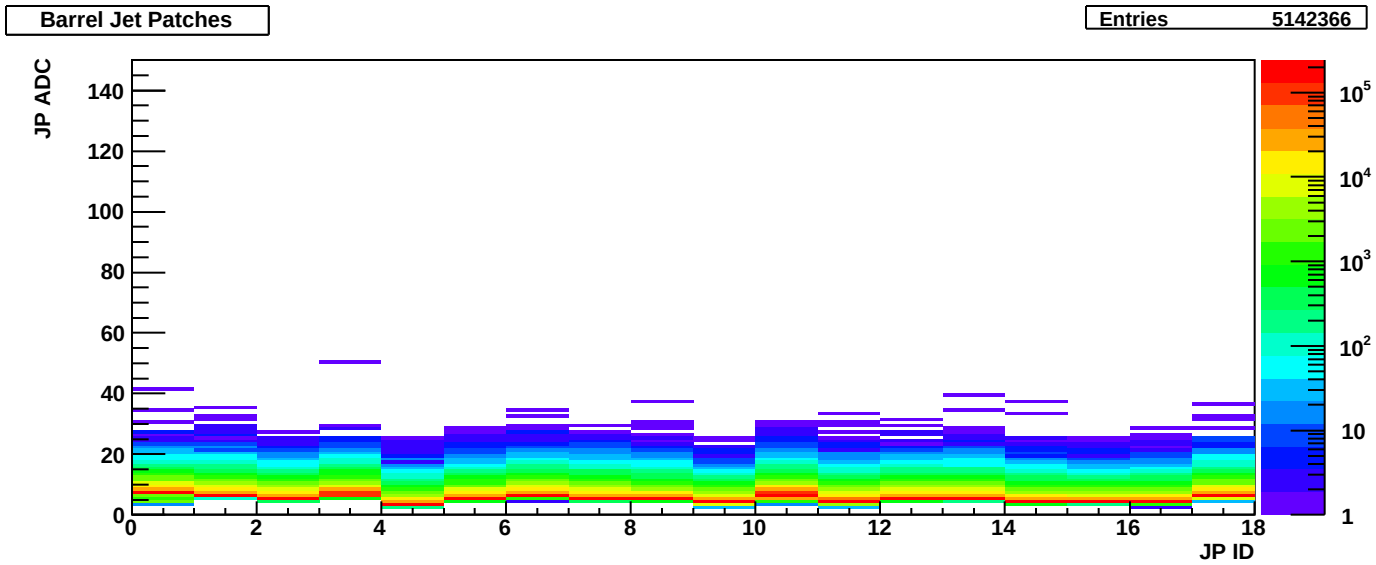

!Yellow-Fill
Entries 12133

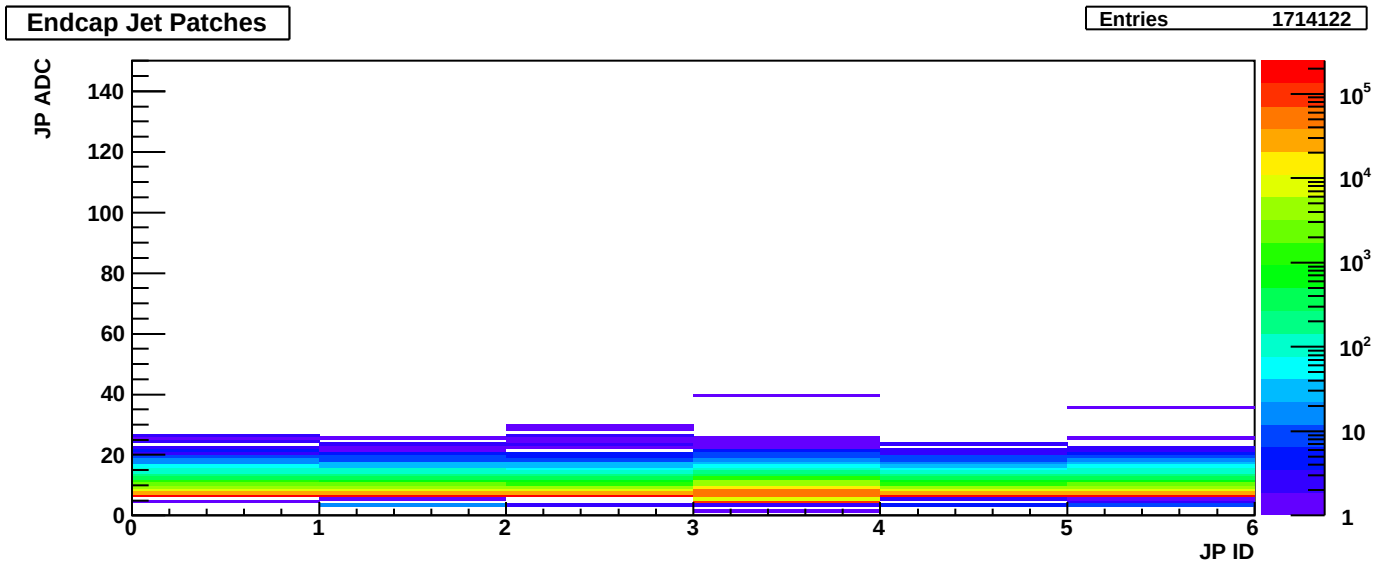

Barrel EMC L0 Input - High Tower
Entries 8.57061e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

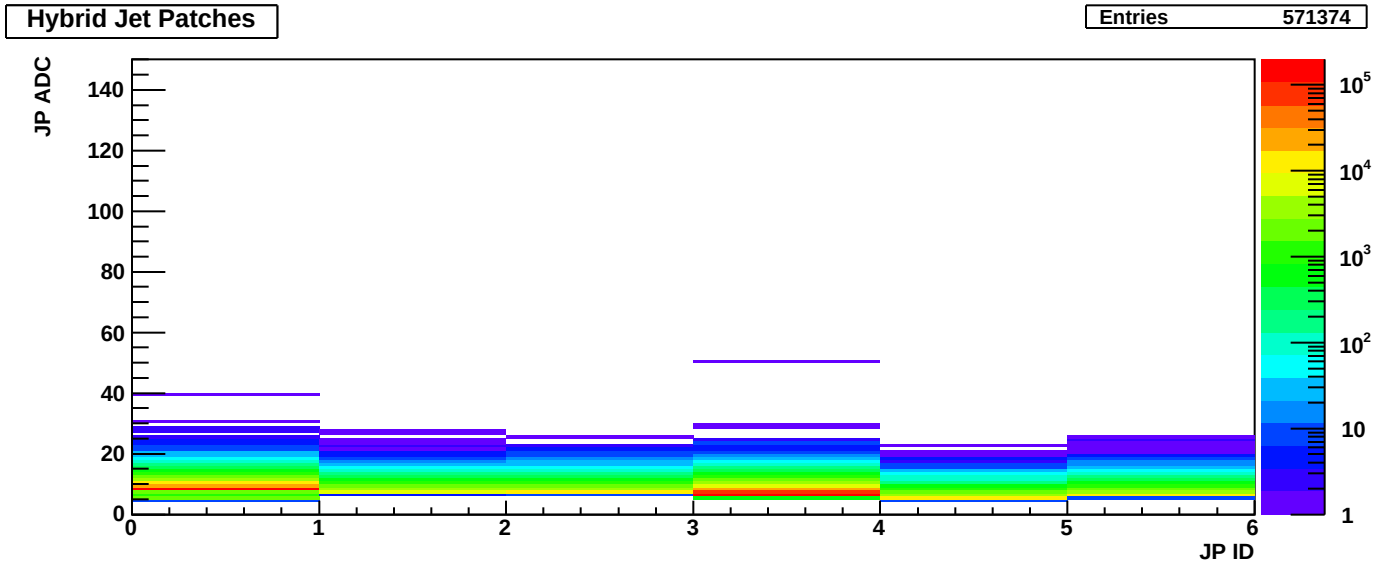

Barrel EMC L0 Input - Patch Sum
Entries 8.57061e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

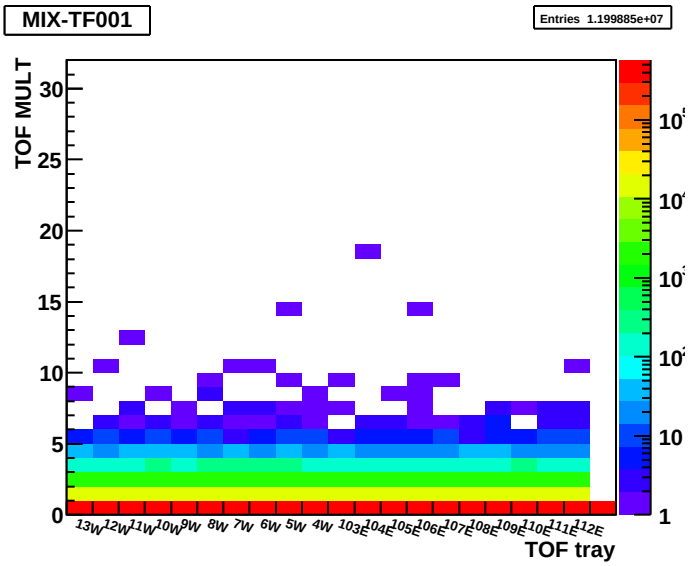

Endcap EMC L0 Input - High Tower
Entries 2.571183e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

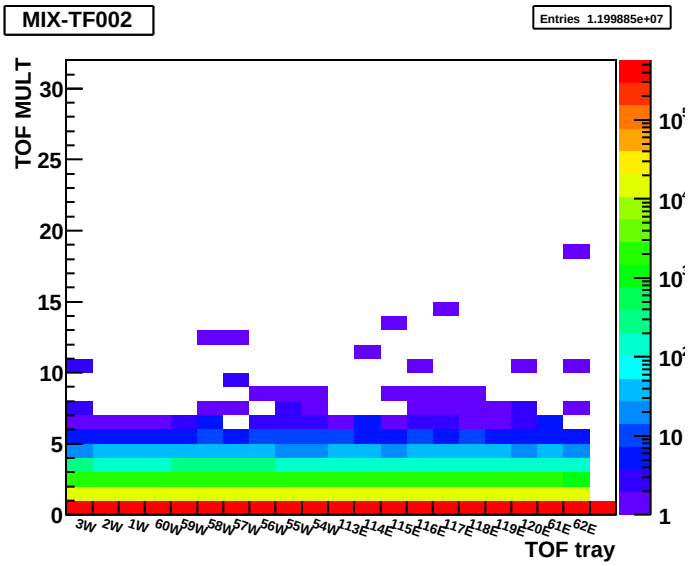

Endcap EMC L0 Input - Patch Sum
Entries 2.571183e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

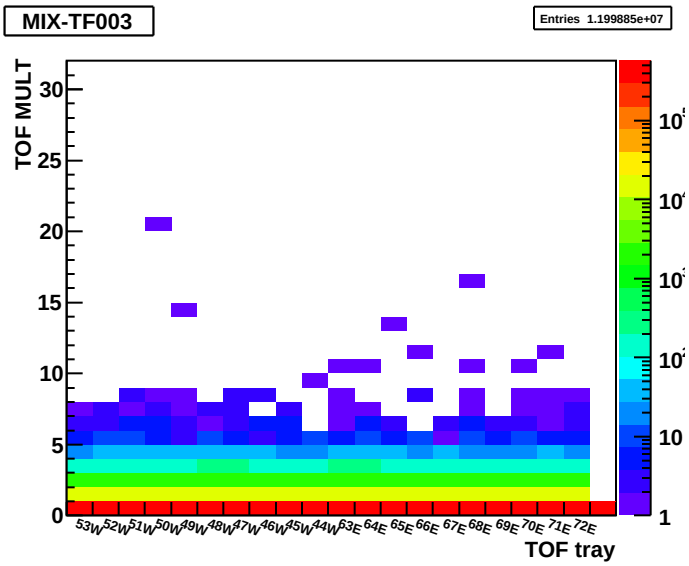

Barrel Jet Patches
Entries 5142366
JP ADC
JP ID

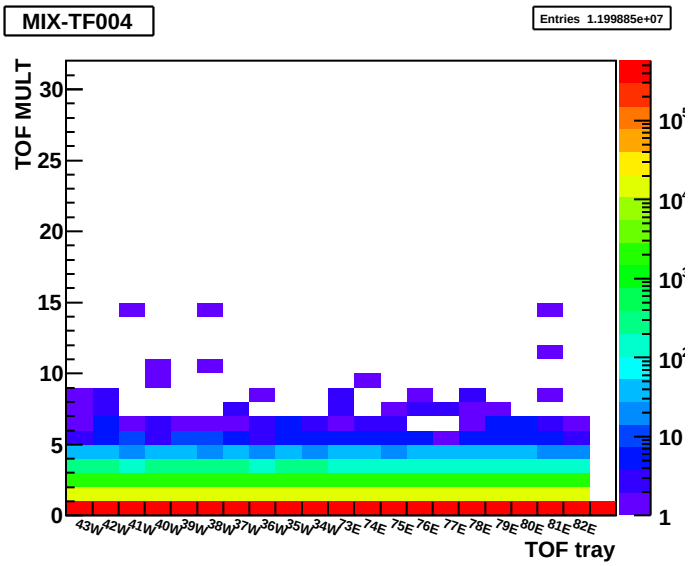

Endcap Jet Patches
Entries 1714122
JP ADC
JP ID

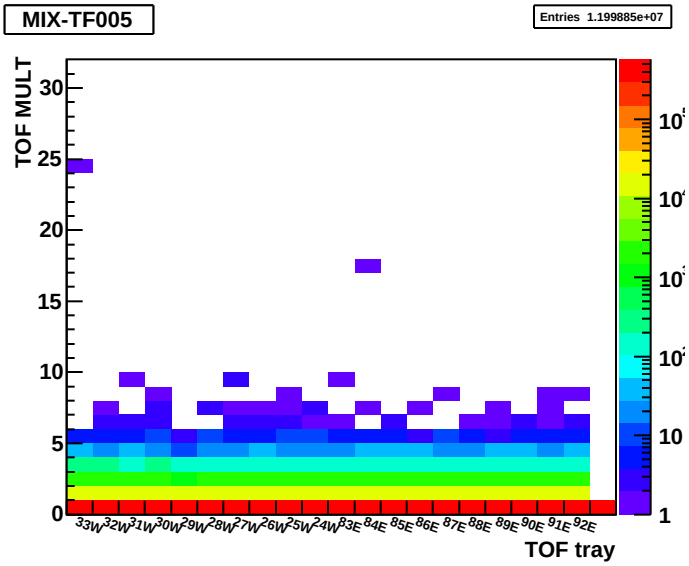

Hybrid Jet Patches
Entries 571374
JP ADC
JP ID

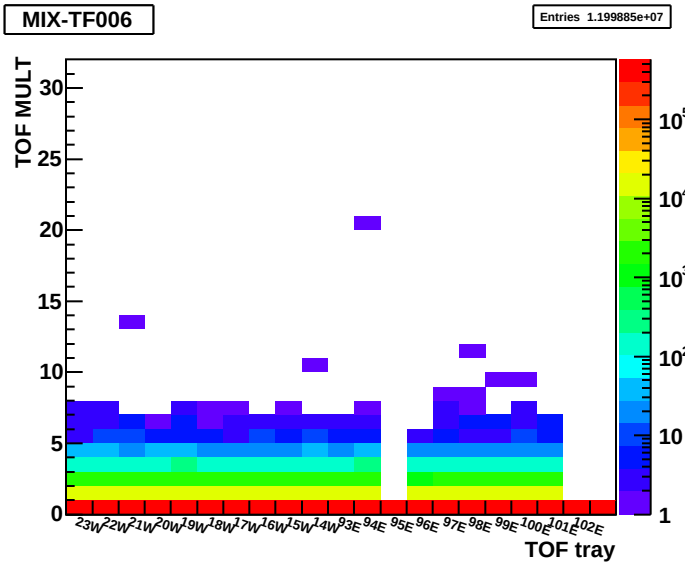

MIX-TF001
Entries 1.199885e+07
TOF MULT
TOF tray

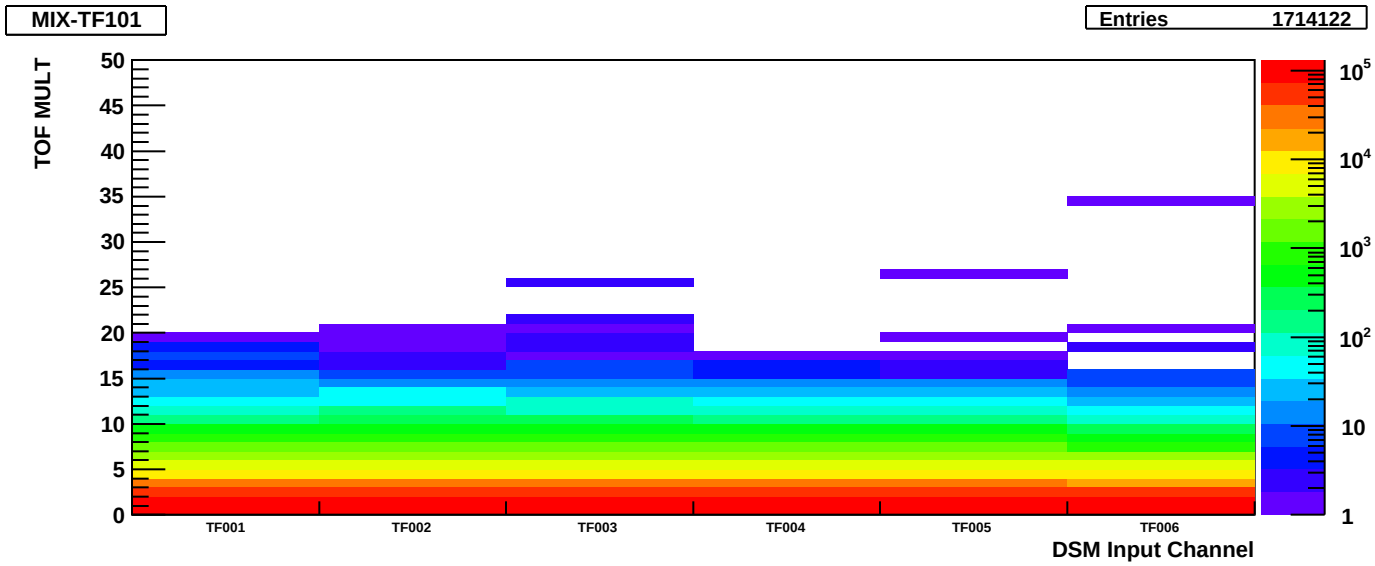

MIX-TF002
Entries 1.199885e+07
TOF MULT
TOF tray

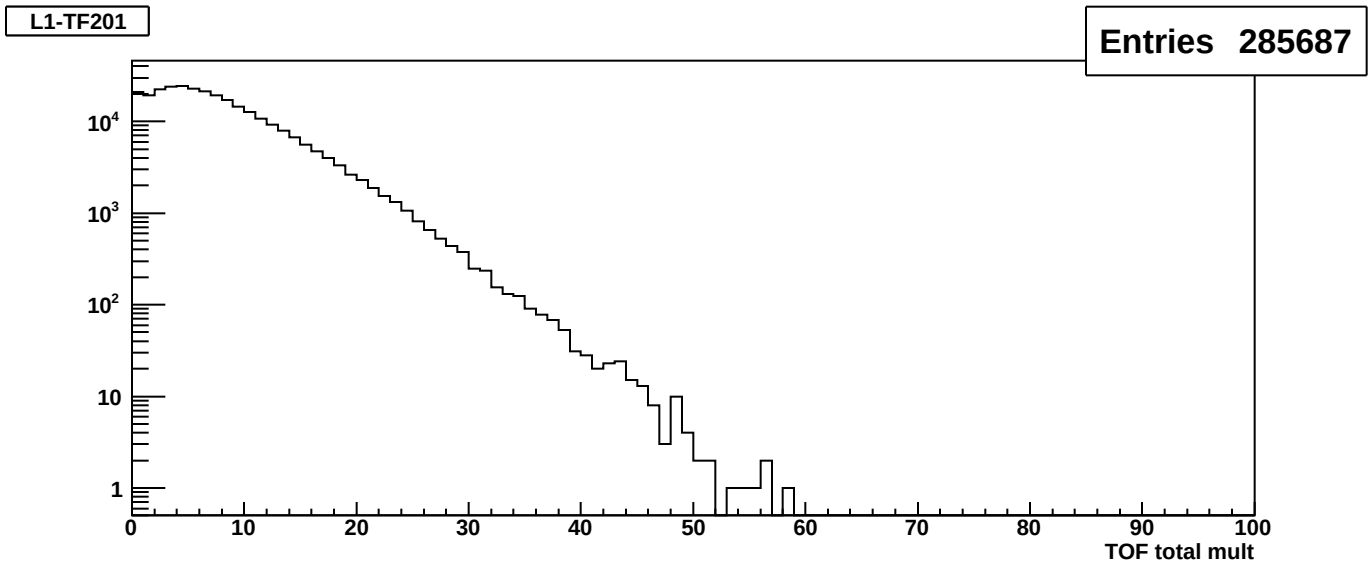

MIX-TF003
Entries 1.199885e+07
TOF MULT
TOF tray

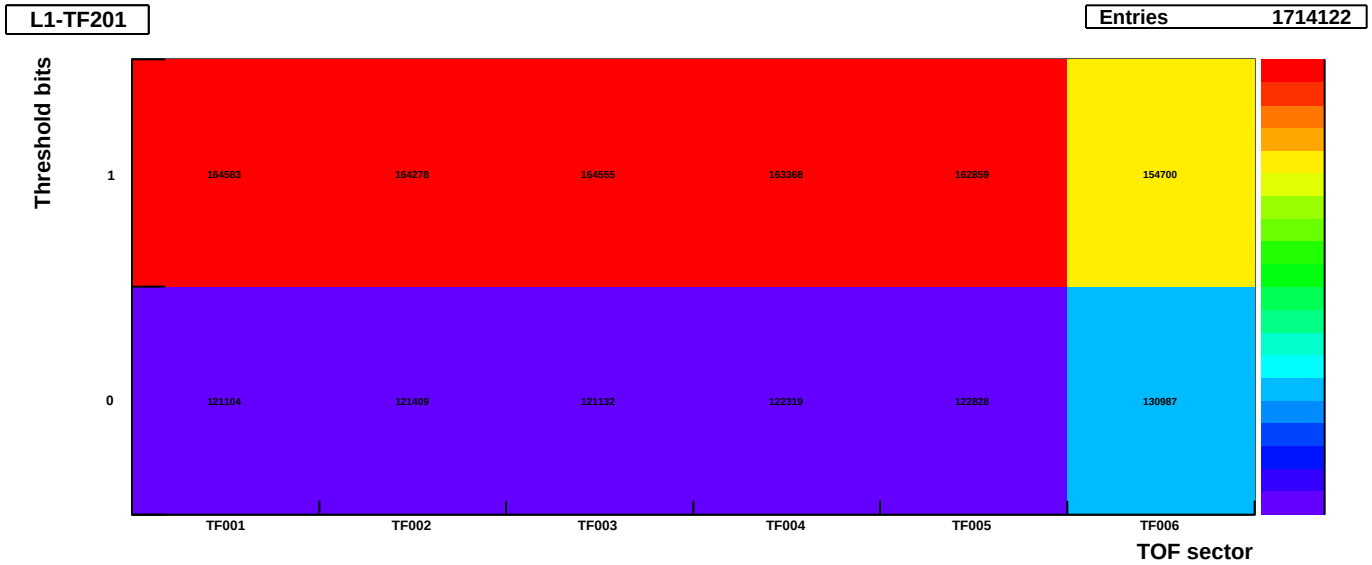

MIX-TF004
Entries 1.199885e+07
TOF MULT
TOF tray

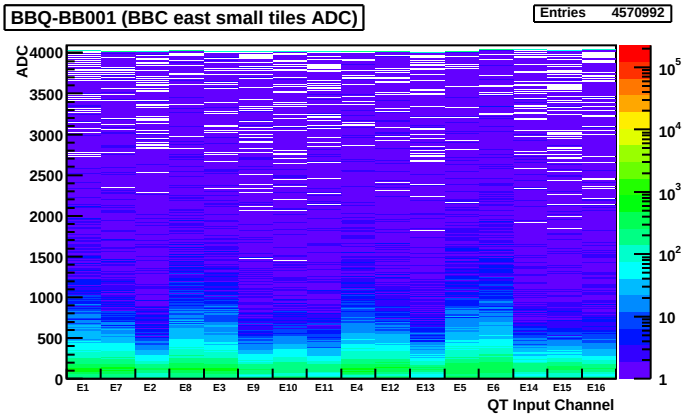

MIX-TF005
Entries 1.199885e+07
TOF MULT
TOF tray

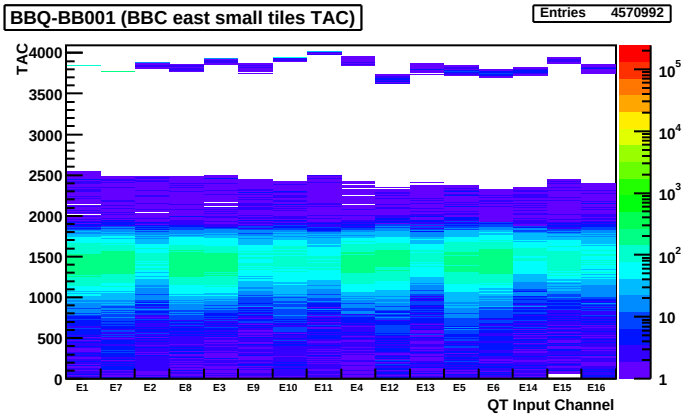

MIX-TF006
Entries 1.199885e+07
TOF MULT
TOF tray

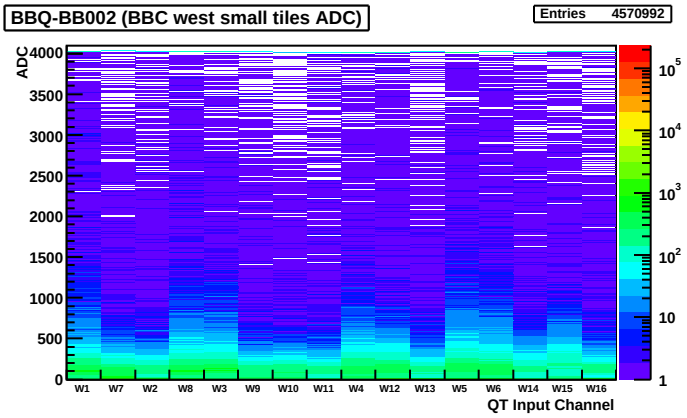

MIX-TF101
Entries 1714122
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

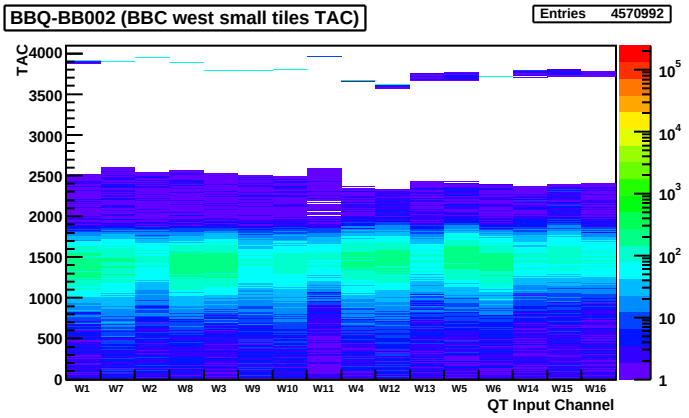

L1-TF201
Entries 285687
TOF total mult

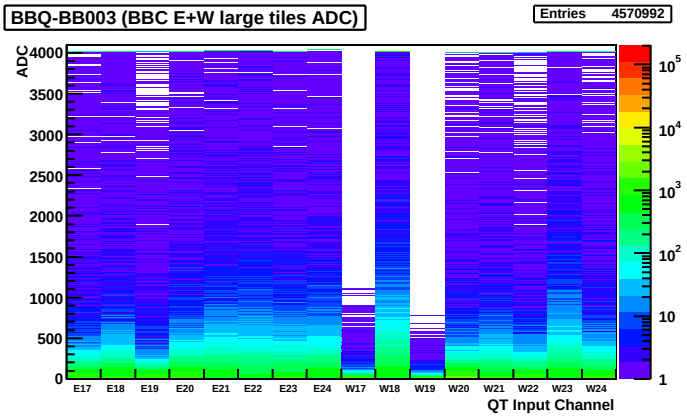

L1-TF201
Entries 1714122
Threshold bits
164583
164278
164555
163368
162859
154700
121104
121409
121132
122319
122828
130987
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

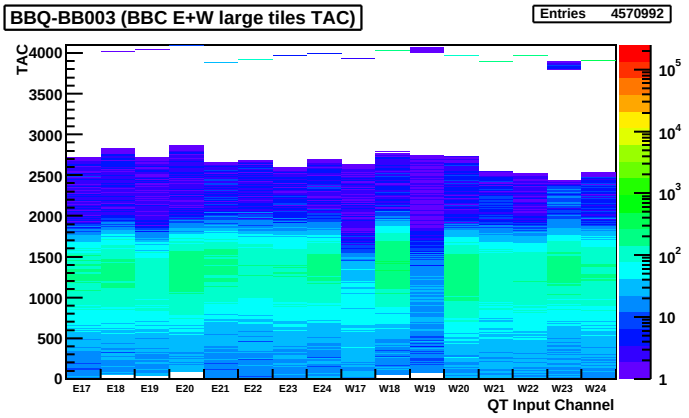

BBQ-BB001 (BBC east small tiles ADC)
Entries 4570992
ADC
QT Input Channel

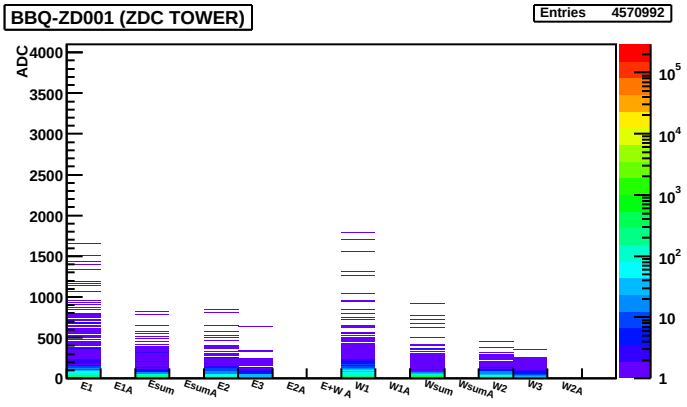

BBQ-BB001 (BBC east small tiles TAC)
Entries 4570992
TAC
QT Input Channel

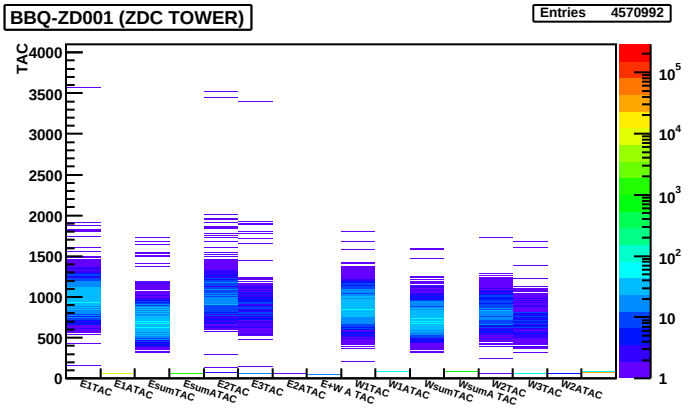

BBQ-BB002 (BBC west small tiles ADC)
Entries 4570992
ADC
QT Input Channel

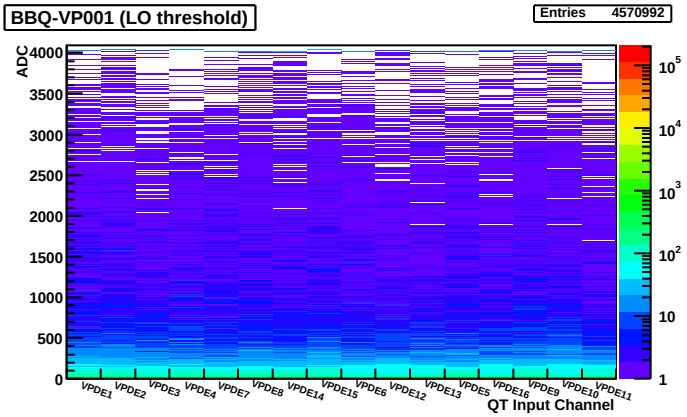

BBQ-BB002 (BBC west small tiles TAC)
Entries 4570992
TAC
QT Input Channel

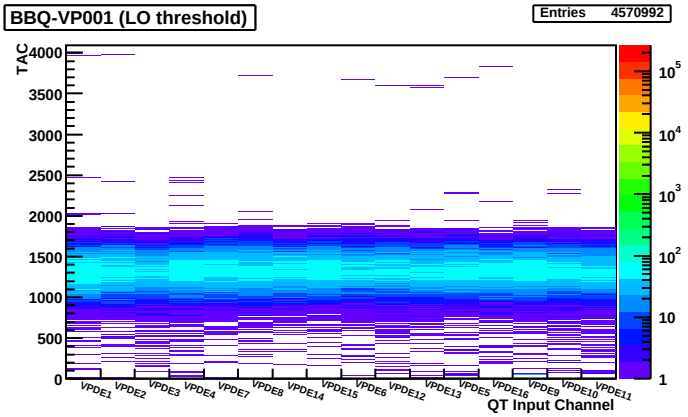

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4570992
ADC
QT Input Channel

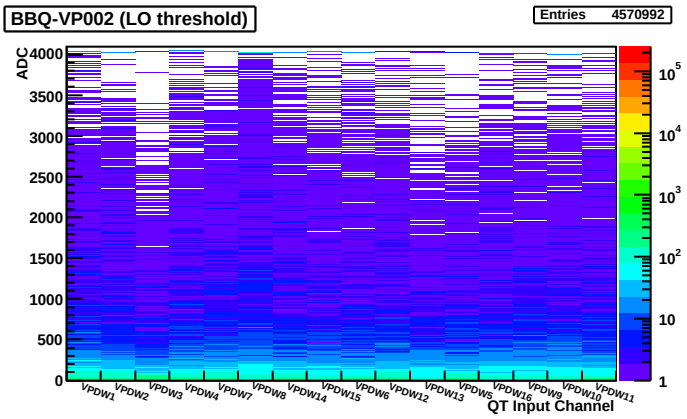

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4570992
TAC
QT Input Channel

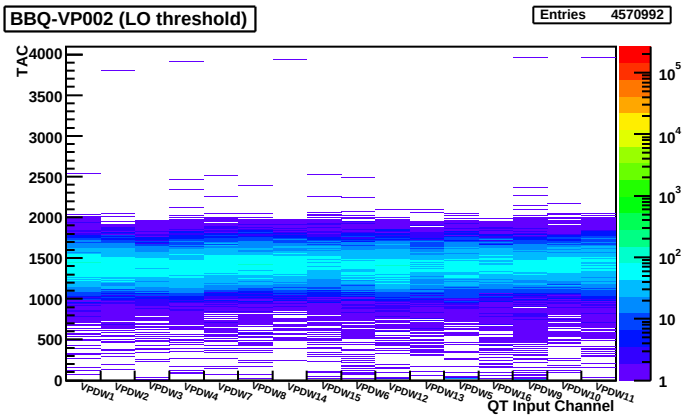

BBQ-ZD001 (ZDC TOWER)
Entries 4570992
ADC

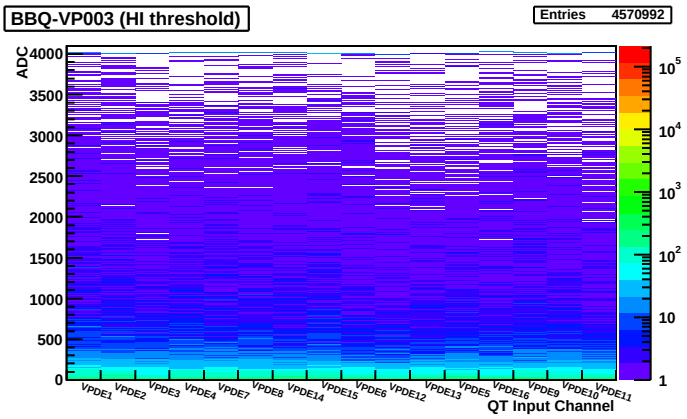

BBQ-ZD001 (ZDC TOWER)
Entries 4570992
TAC

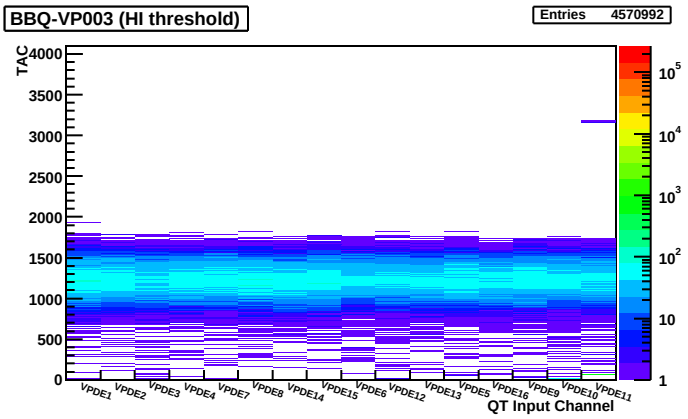

BBQ-VP001 (LO threshold)
Entries 4570992
ADC
QT Input Channel

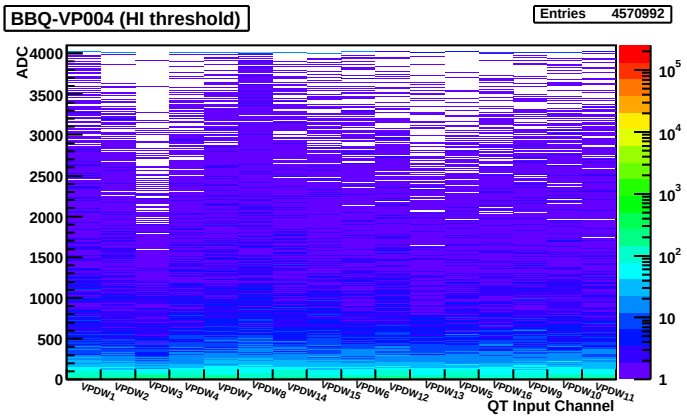

BBQ-VP001 (LO threshold)
Entries 4570992
TAC
QT Input Channel

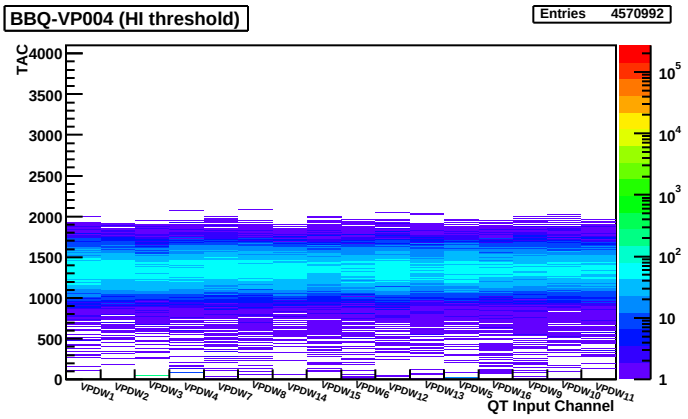

BBQ-VP002 (LO threshold)
Entries 4570992
ADC
QT Input Channel

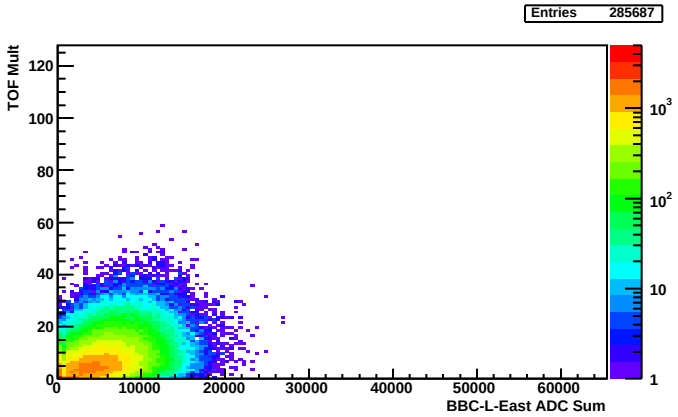

BBQ-VP002 (LO threshold)
Entries 4570992
TAC
QT Input Channel

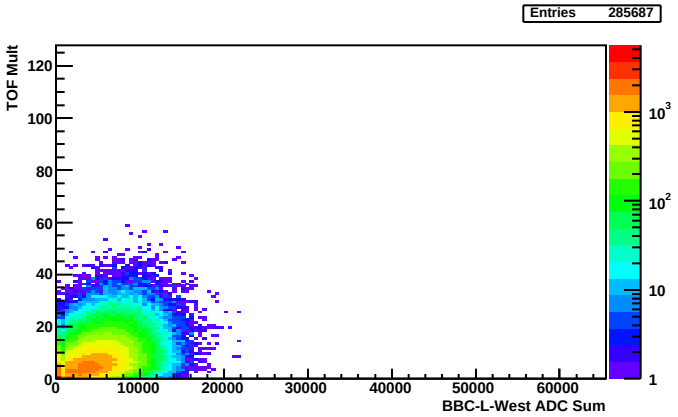

BBQ-VP003 (HI threshold)
Entries 4570992
ADC
QT Input Channel

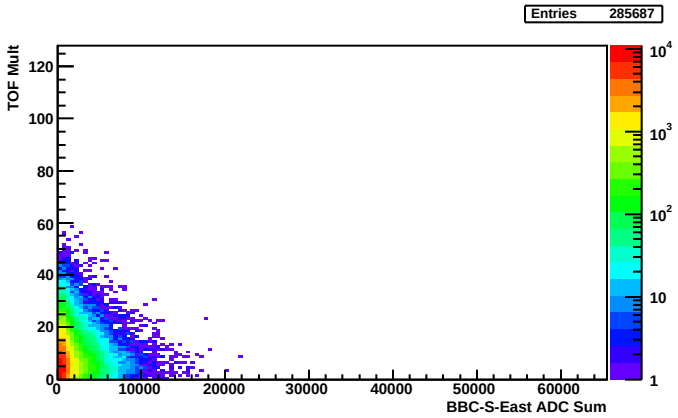

BBQ-VP003 (HI threshold)
Entries 4570992
TAC
QT Input Channel

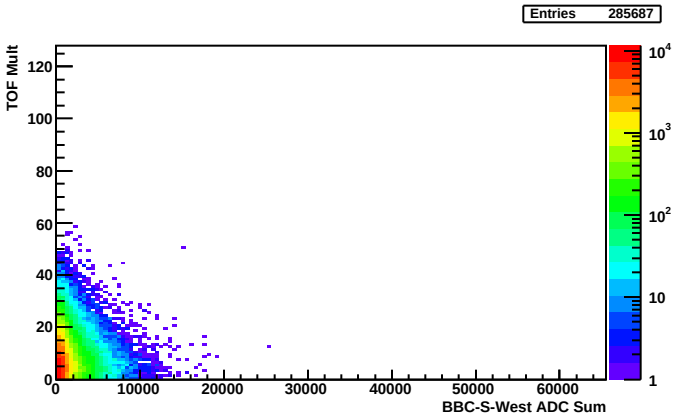

BBQ-VP004 (HI threshold)
Entries 4570992
ADC
QT Input Channel

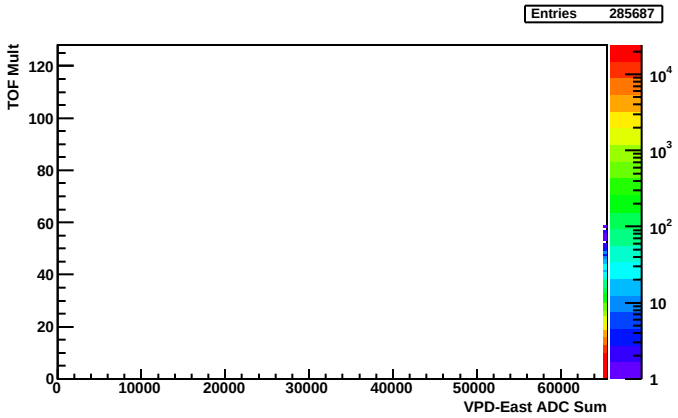

BBQ-VP004 (HI threshold)
Entries 4570992
TAC
QT Input Channel

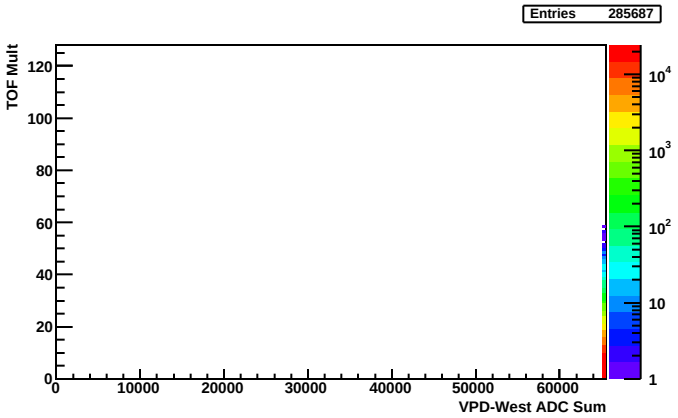

Entries 285687
TOF Mult
BBC-L-East ADC Sum

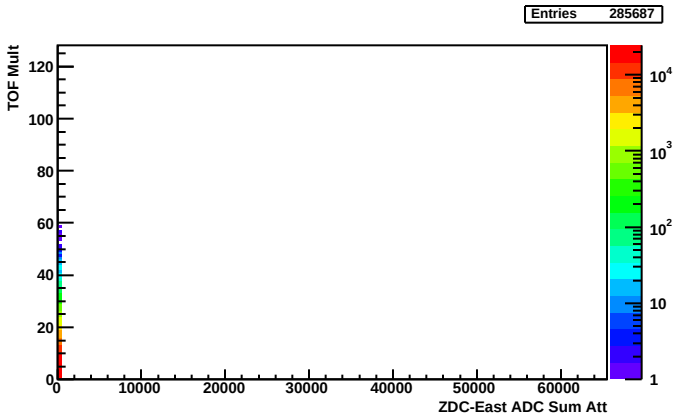

Entries 285687
TOF Mult
BBC-L-West ADC Sum

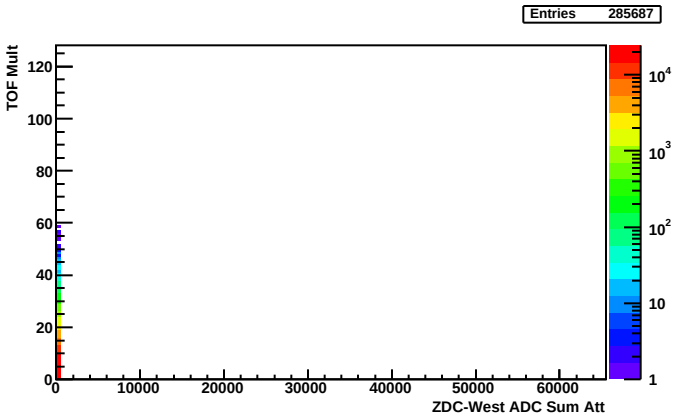

Entries 285687
TOF Mult
BBC-S-East ADC Sum

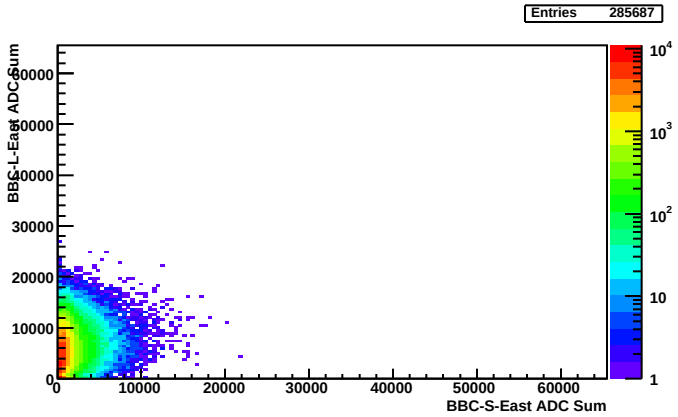

Entries 285687
TOF Mult
BBC-S-West ADC Sum

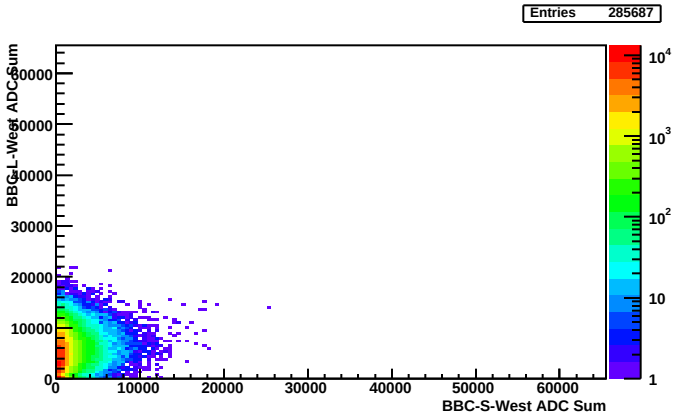

Entries 285687
TOF Mult
VPD-East ADC Sum

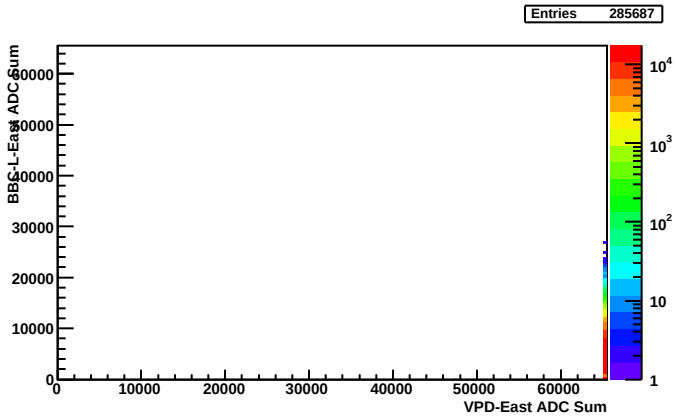

Entries 285687
TOF Mult
VPD-West ADC Sum

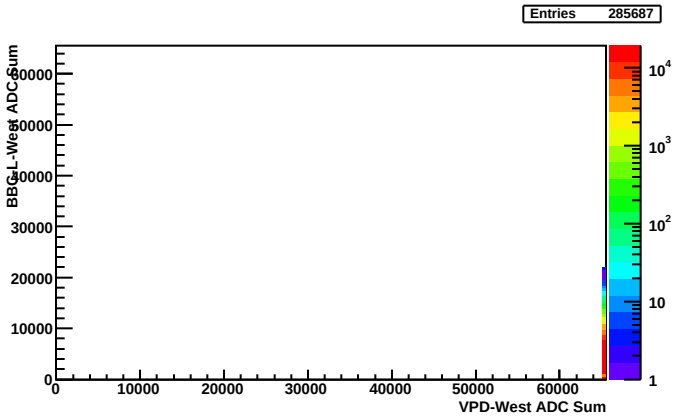

Entries 285687
TOF Mult
ZDC-East ADC Sum Att


Entries 285687
TOF Mult
ZDC-West ADC Sum Att


Entries 285687
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 285687
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 285687
BBC-L-East ADC Sum
VPD-East ADC Sum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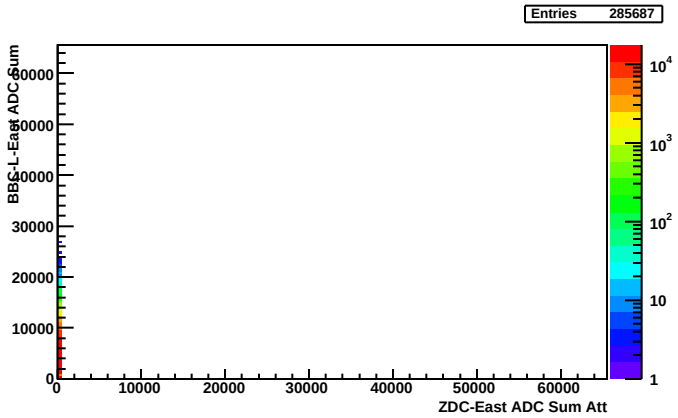
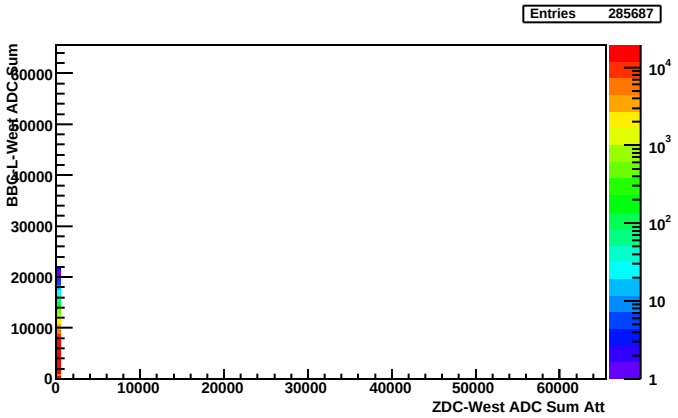
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 285687
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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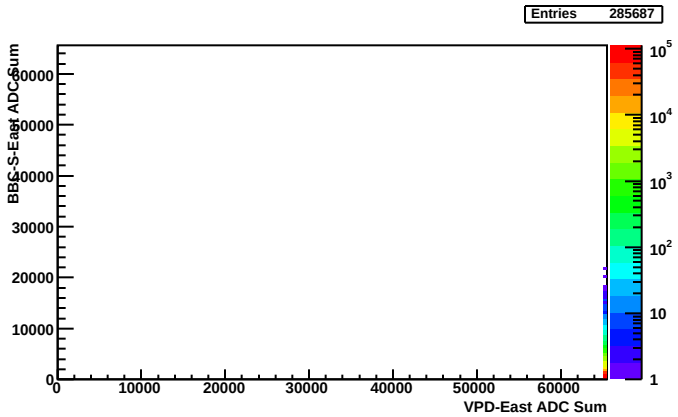
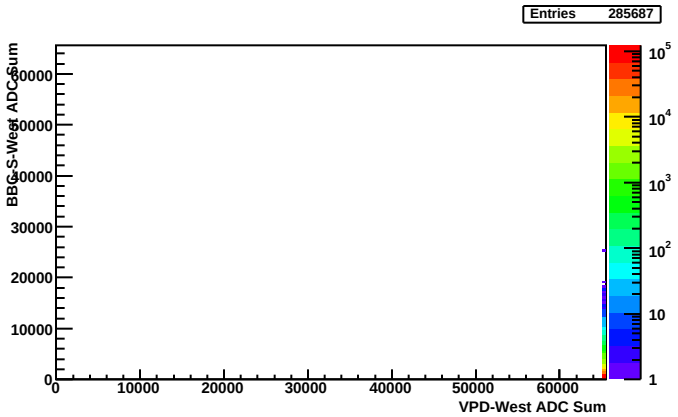
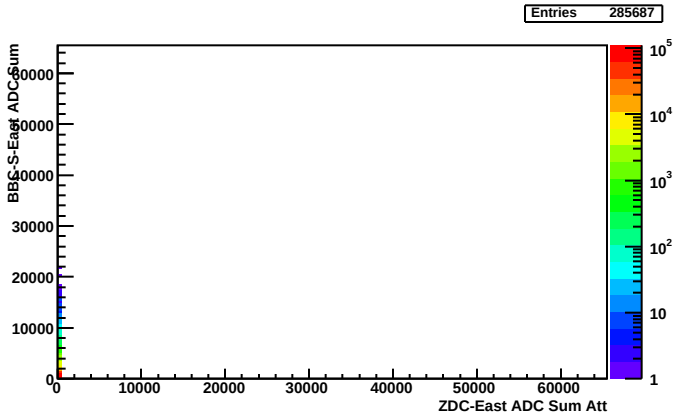
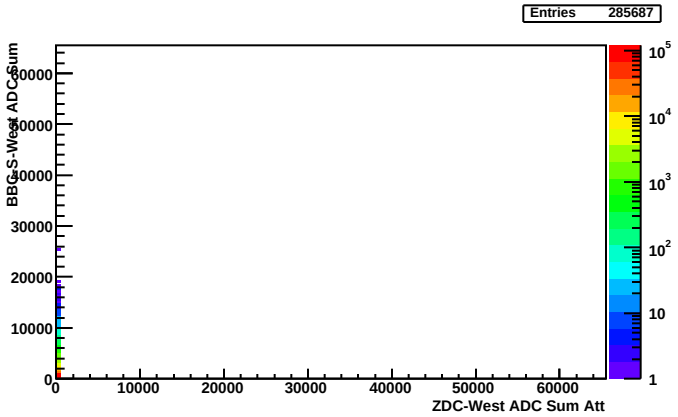
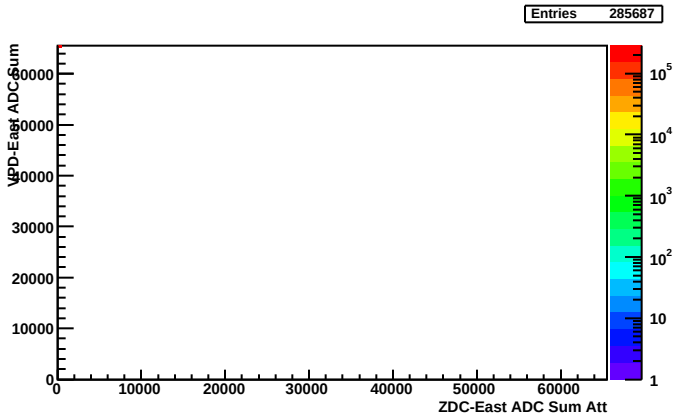
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

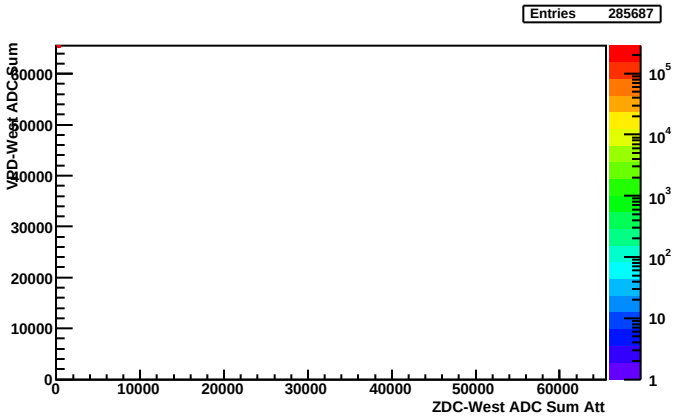

Entries 285687
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 285687
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 285687
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 285687
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 285687
VPD-East ADC Sum
ZDC-East ADC Sum Att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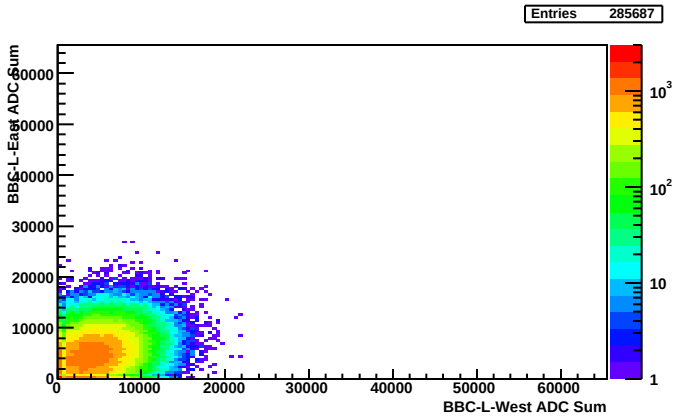
VPD-West ADC Sum
ZDC-West ADC Sum Att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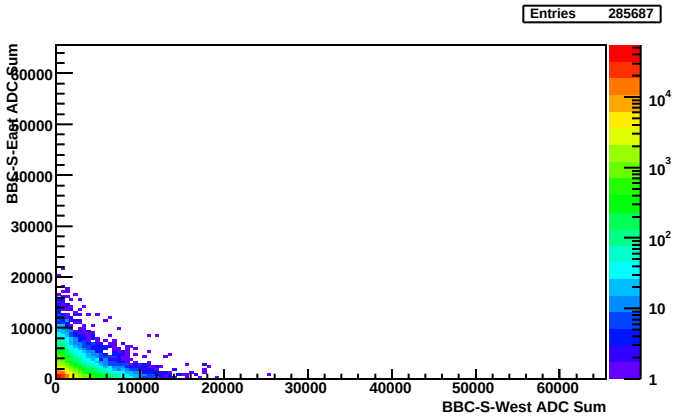
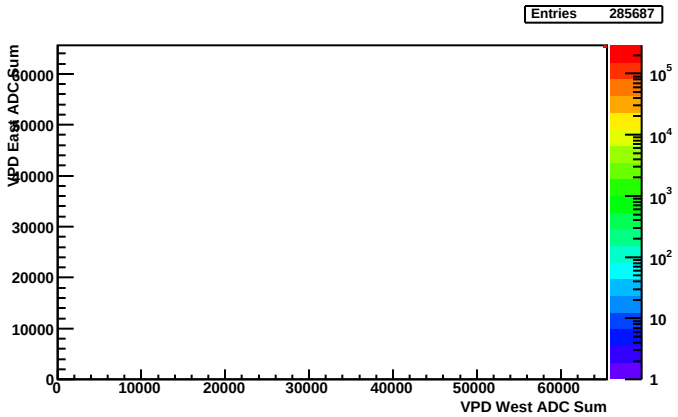
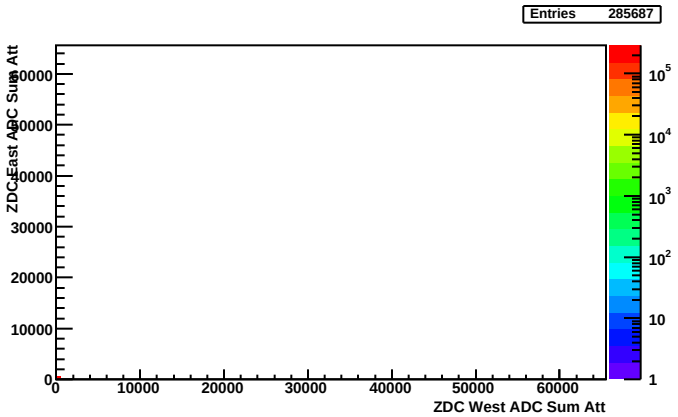
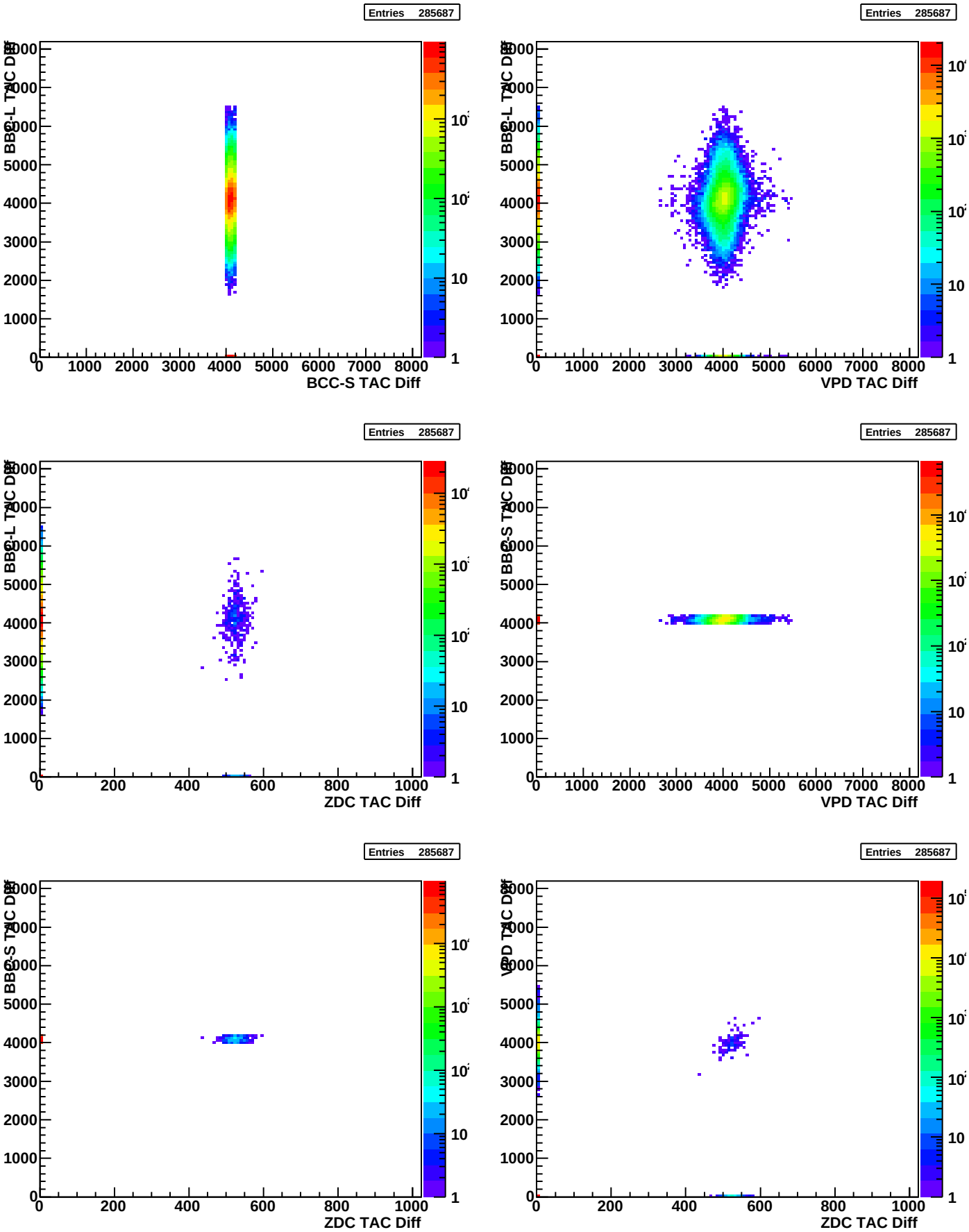
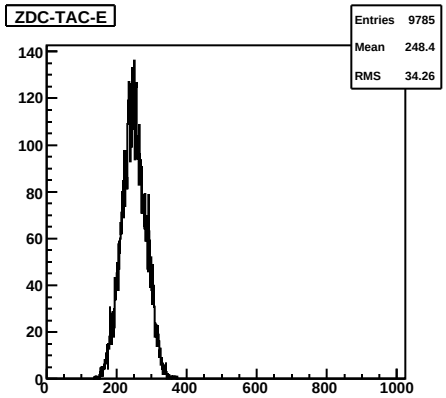
BBC-L-East ADC Sum
BBC-L-West ADC Sum

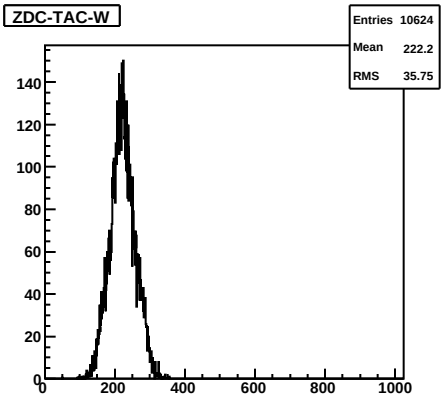

Entries 285687
BBC-S-East ADC Sum
BBC-S-West ADC Sum

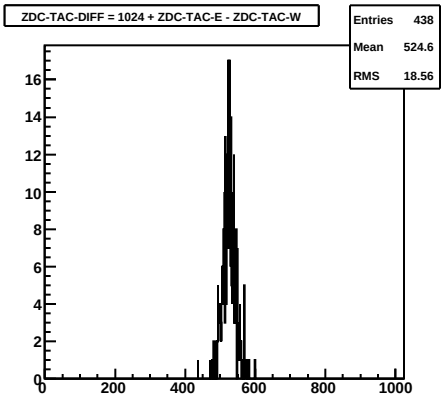

Entries 285687
VPD East ADC Sum
VPD West ADC Sum

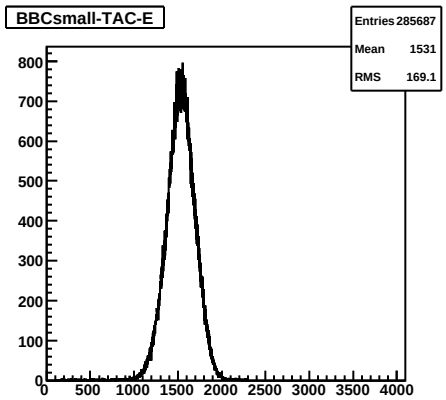

Entries 285687
ZDC East ADC Sum Att
ZDC West ADC Sum Att

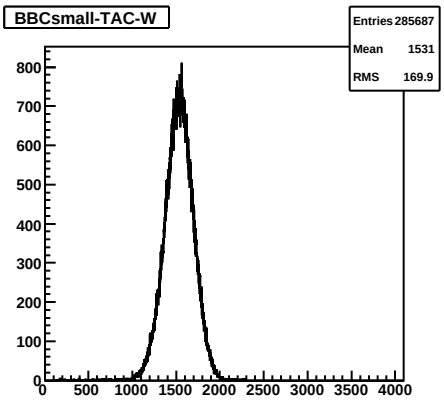

Entries 285687
BBC-L TAC Diff
BCC-S TAC Diff
Entries 285687
BBC-L TAC Diff
VPD TAC Diff
Entries 285687
BBC-L TAC Diff
ZDC TAC Diff
Entries 285687
BBC-S TAC Diff
VPD TAC Diff
Entries 285687
BBC-S TAC Diff
ZDC TAC Diff
Entries 285687
VPD TAC Diff
ZDC TAC Diff

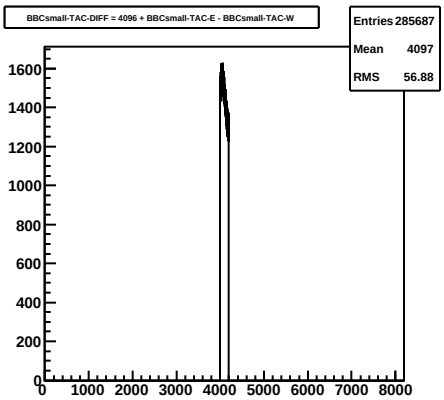

ZDC-TAC-E
Entries 9785
Mean 248.4
RMS 34.26

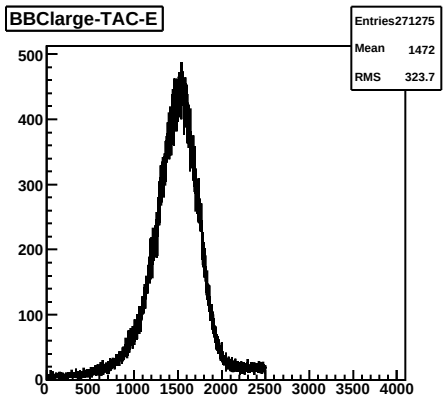

ZDC-TAC-W
Entries 10624
Mean 222.2
RMS 35.75

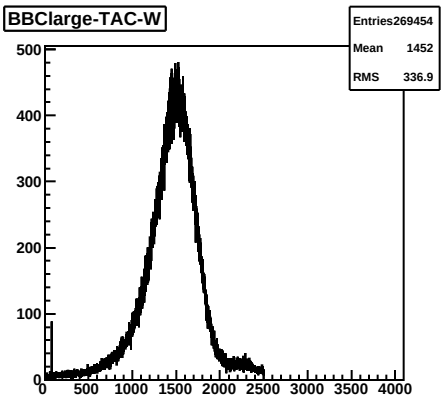

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 438
Mean 524.6
RMS 18.56

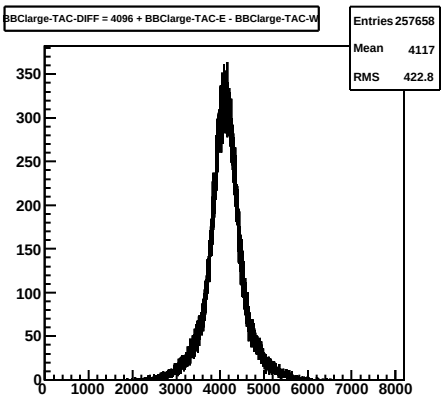

BBCsmall-TAC-E
Entries 285687
Mean 1531
RMS 169.1

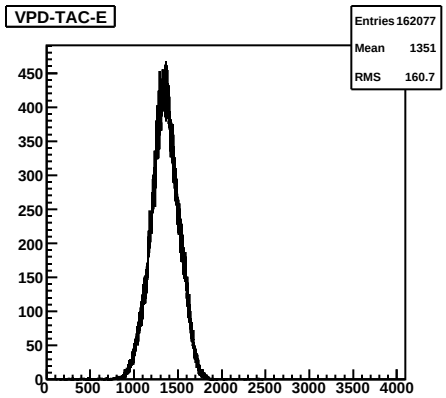

BBCsmall-TAC-W
Entries 285687
Mean 1531
RMS 169.9

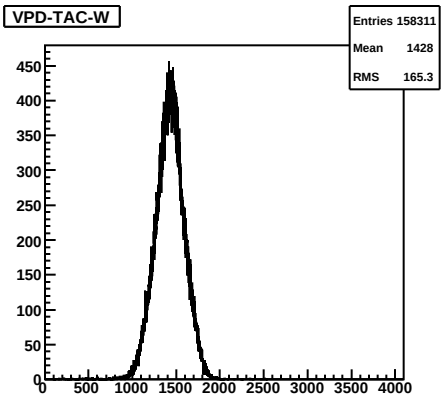

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 285687
Mean 4097
RMS 56.88

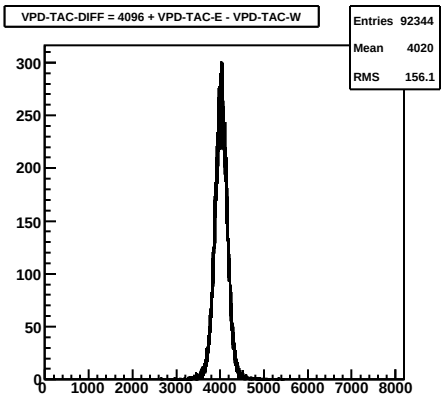

BBClarge-TAC-E
Entries 271275
Mean 1472
RMS 323.7

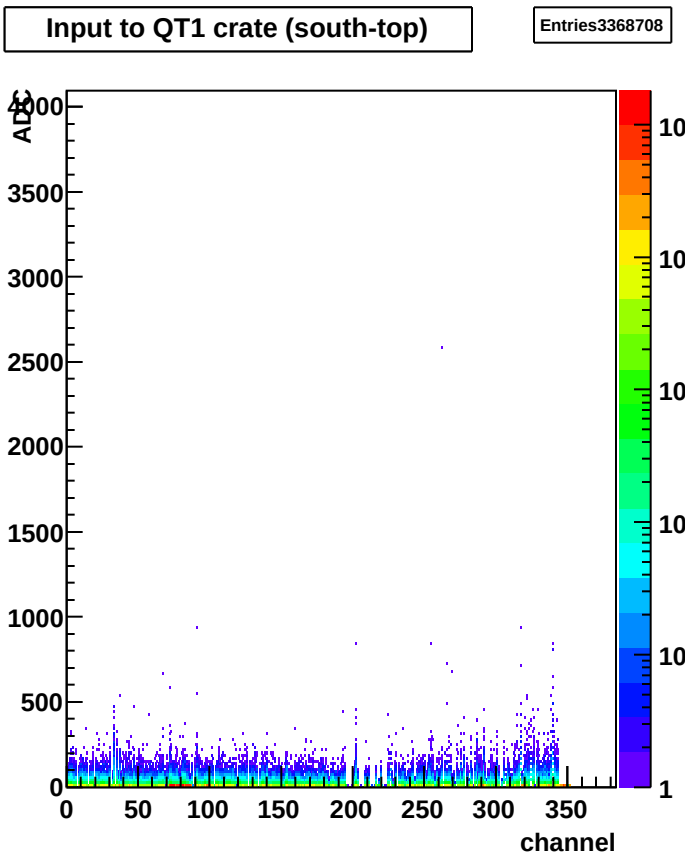

BBClarge-TAC-W
Entries 269454
Mean 1452
RMS 336.9

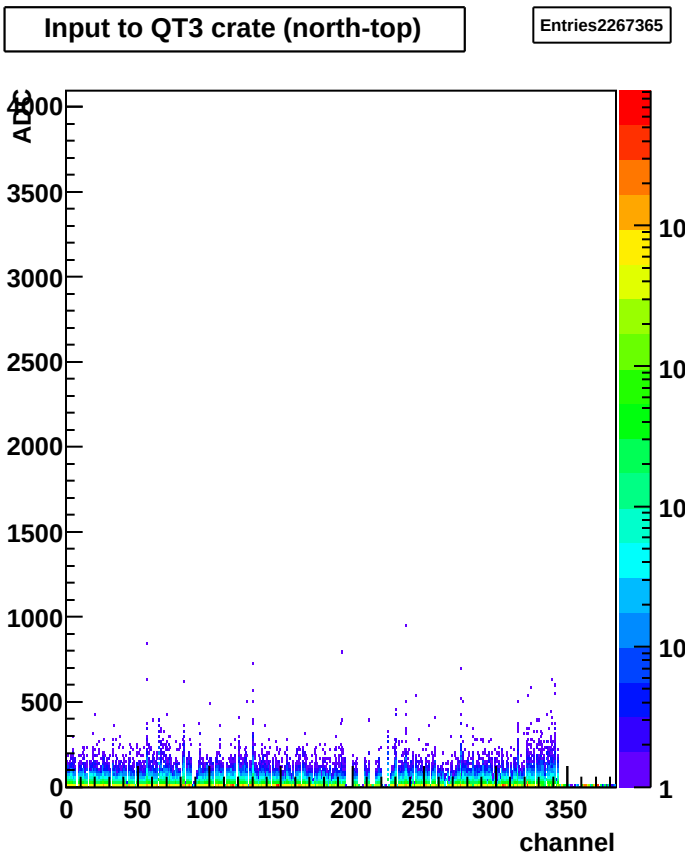

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 257658
Mean 4117
RMS 422.8

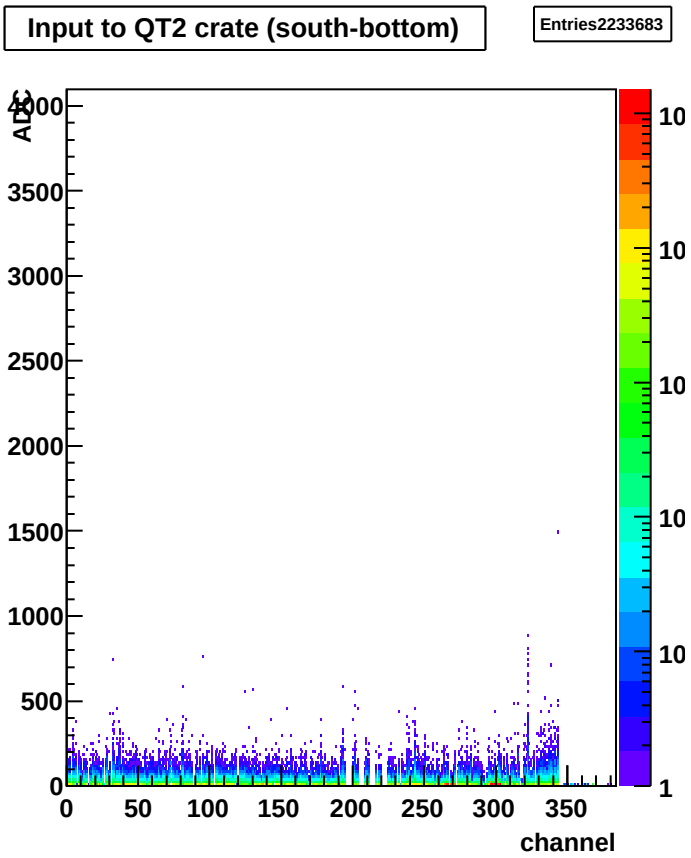

VPD-TAC-E
Entries 162077
Mean 1351
RMS 160.7

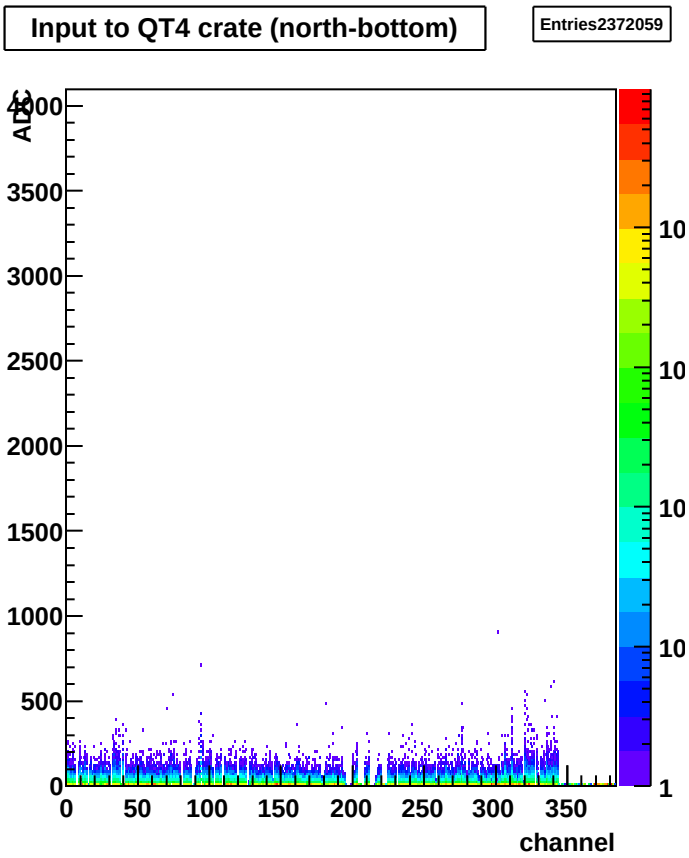

VPD-TAC-W
Entries 158311
Mean 1428
RMS 165.3

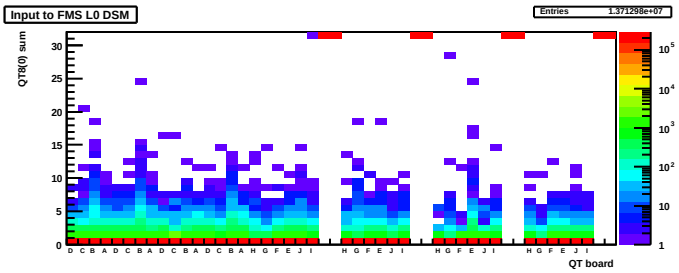

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 92344
Mean 4020
RMS 156.1

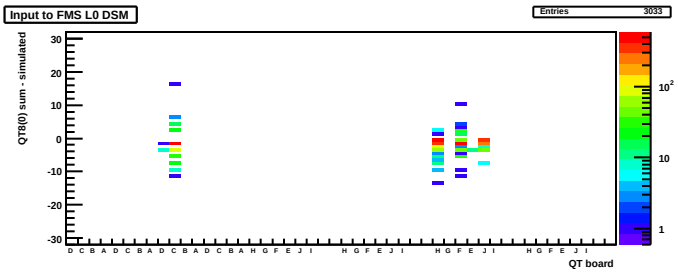

Input to QT1 crate (south-top)
Entries3368708
ADC
channel

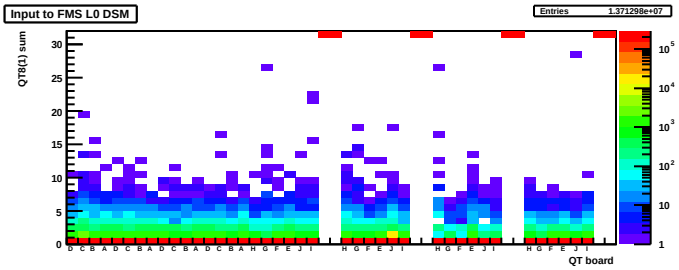

Input to QT3 crate (north-top)
Entries2267365
ADC
channel

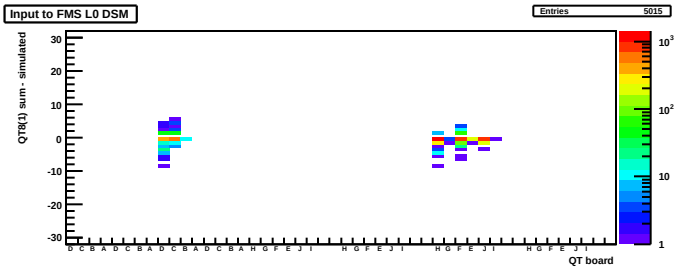

Input to QT2 crate (south-bottom)
Entries2233683
ADC
channel

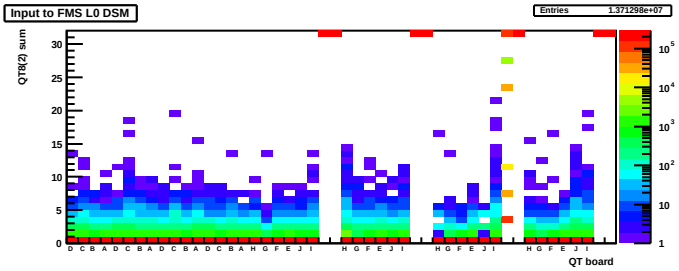

Input to QT4 crate (north-bottom)
Entries2372059
ADC
channel

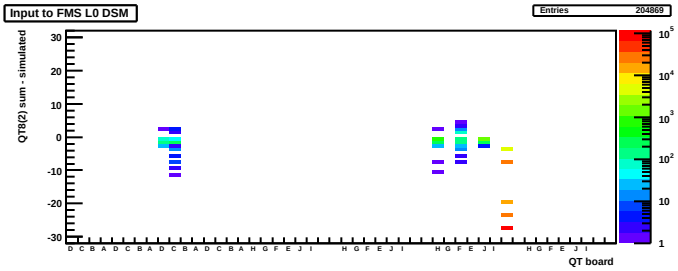

Input to FMS L0 DSM
Entries 1.371298e+07
QT8(0) sum
QT board

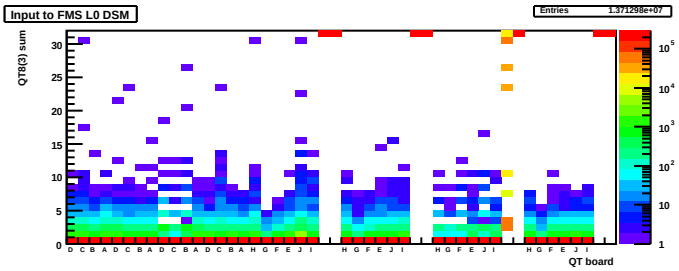

Input to FMS L0 DSM
Entries 3033
QT8(0) sum - simulated
QT board

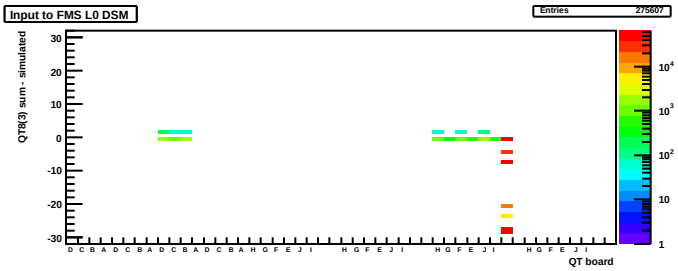

Input to FMS L0 DSM
Entries 1.371298e+07
QT8(1) sum
QT board

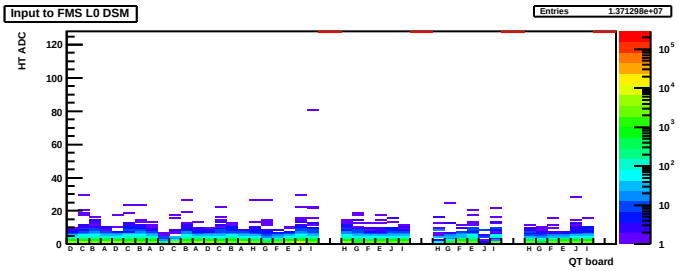

Input to FMS L0 DSM
Entries 5015
QT8(1) sum - simulated
QT board

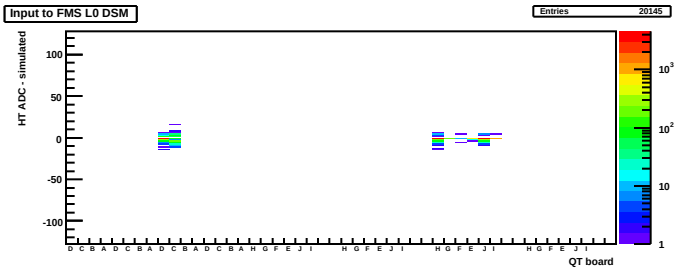

Input to FMS L0 DSM
Entries 1.371298e+07
QT8(2) sum
QT board

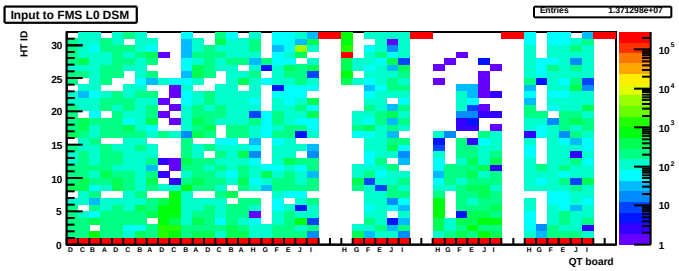

Input to FMS L0 DSM
Entries 204869
QT8(2) sum - simulated
QT board

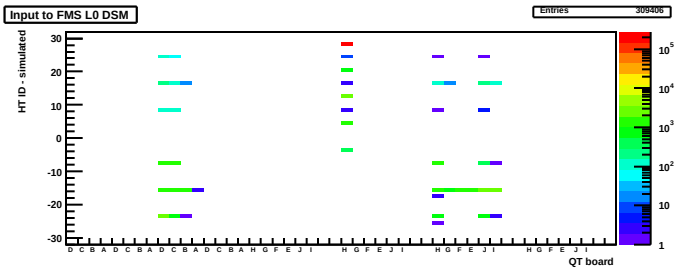

Input to FMS L0 DSM
Entries 1.371298e+07
QT8(3) sum
QT board

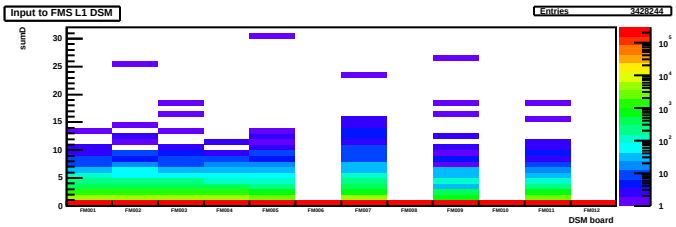

Input to FMS L0 DSM
Entries 275607
QT8(3) sum - simulated
QT board

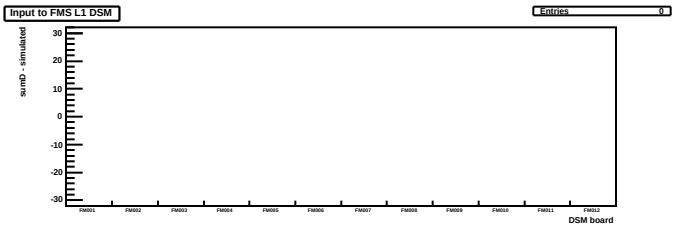

Input to FMS L0 DSM
Entries 1.371298e+07
HT ADC
QT board

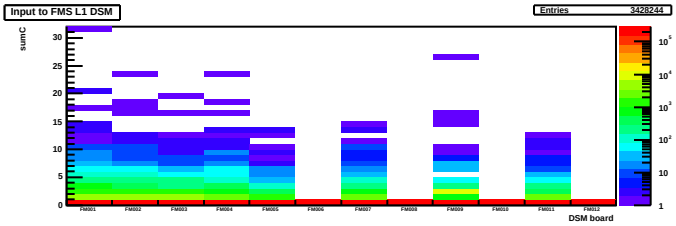

Input to FMS L0 DSM
Entries 20145
HT ADC - simulated
QT board

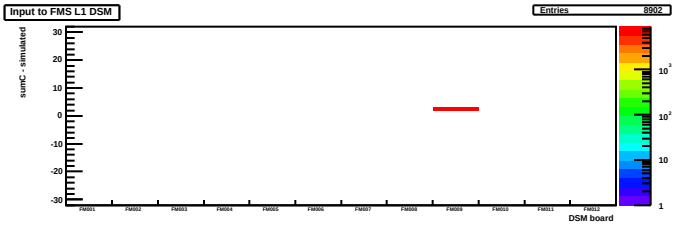

Input to FMS L0 DSM
Entries 1.371298e+07
HT ID
QT board

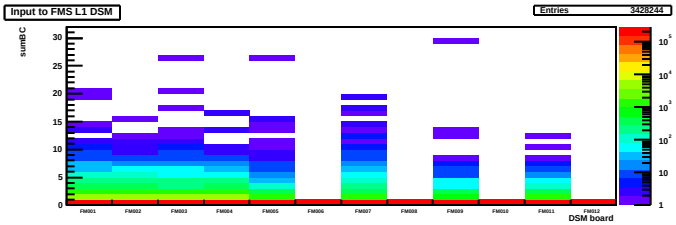

Input to FMS L0 DSM
Entries 309406
HT ID - simulated
QT board

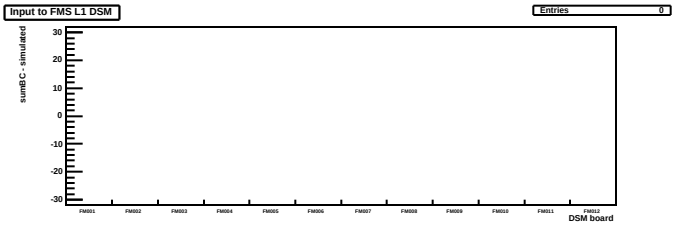

Input to FMS L1 DSM
Entries 3428244
sumD
DSM board

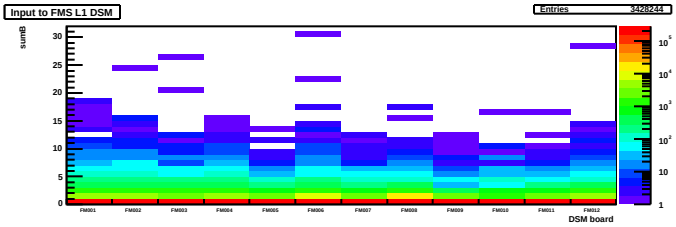

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

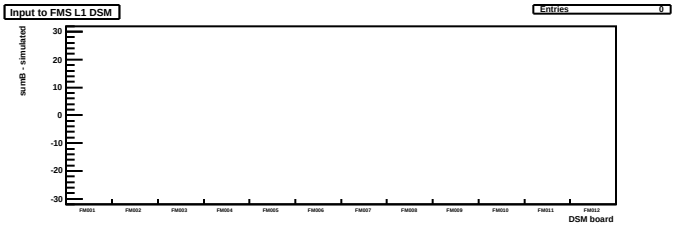

Input to FMS L1 DSM
Entries 3428244
sumC
DSM board

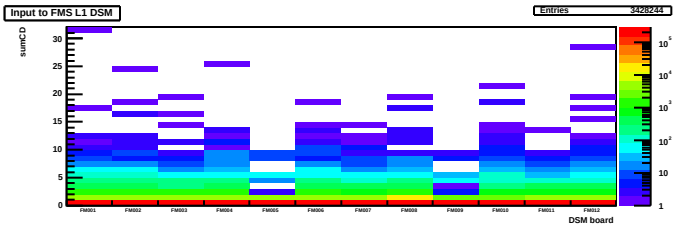

Input to FMS L1 DSM
Entries 8902
sumC - simulated
DSM board

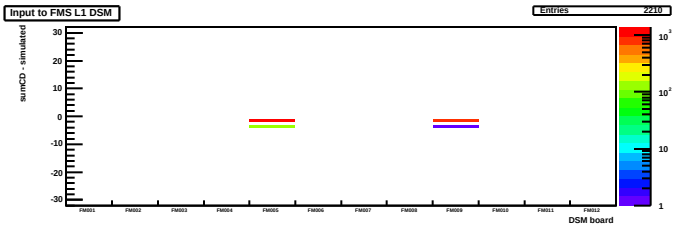

Input to FMS L1 DSM
Entries 3428244
sumBC
DSM board

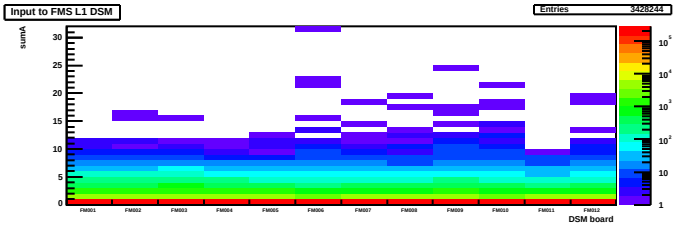

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

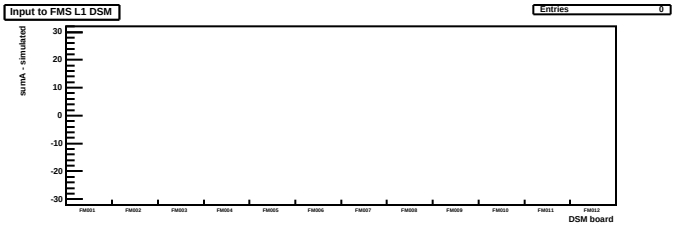

Input to FMS L1 DSM
Entries 3428244
sumB
DSM board

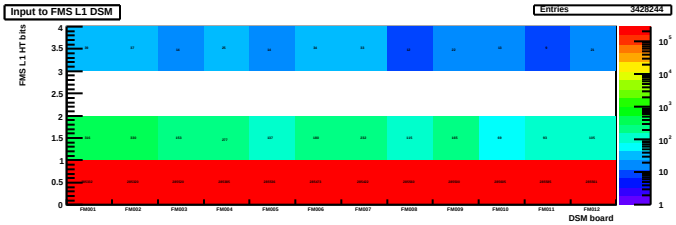

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

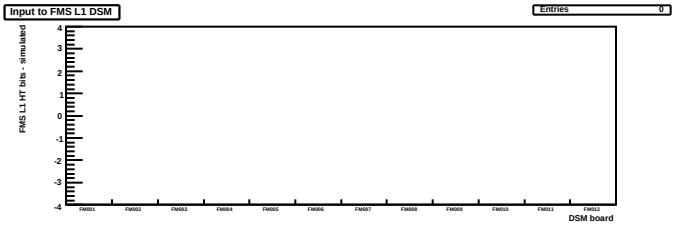

Input to FMS L1 DSM
Entries 3428244
sumCD
DSM board

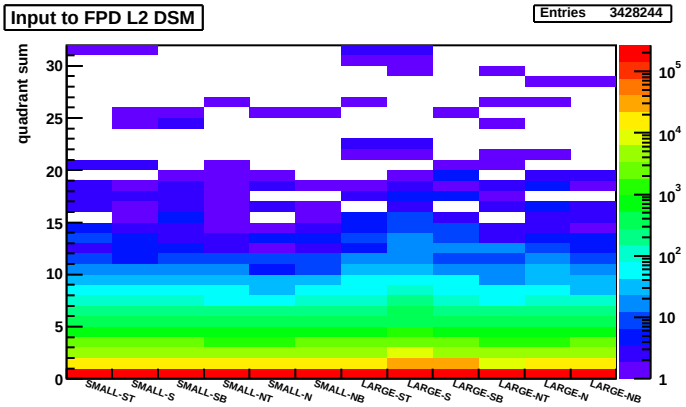

Input to FMS L1 DSM
Entries 2210
sumCD - simulated
DSM board

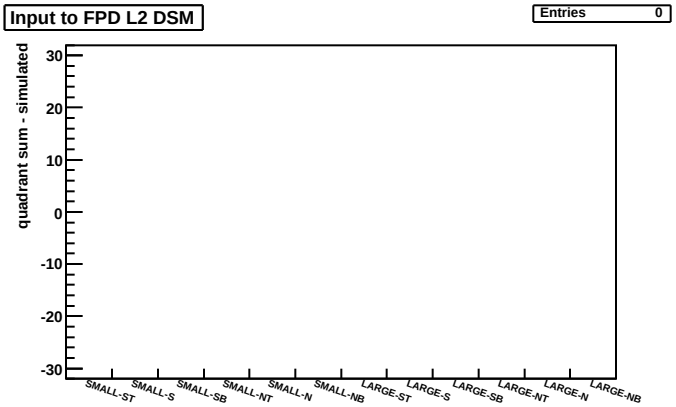

Input to FMS L1 DSM
Entries 3428244
sumA
DSM board

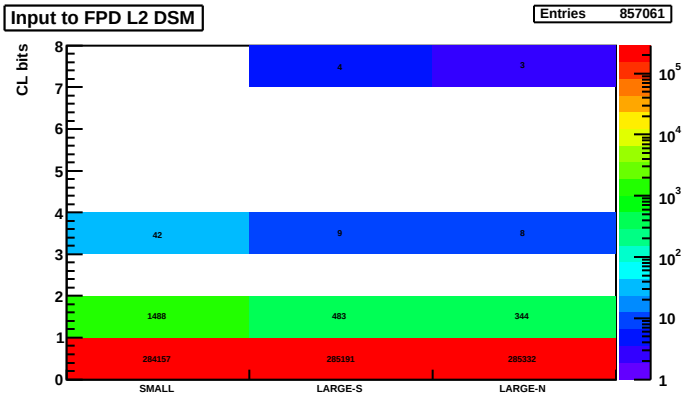

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

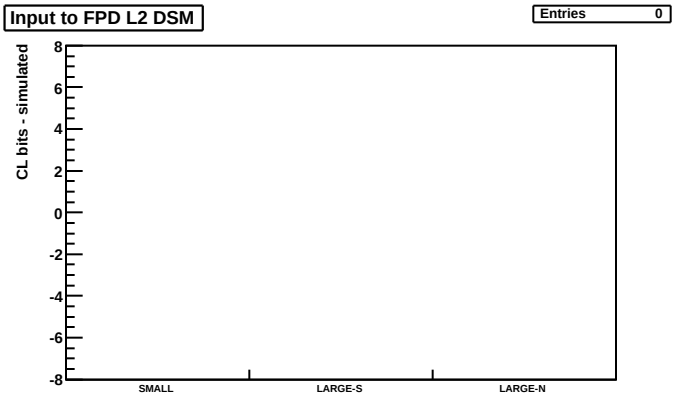

Input to FMS L1 DSM
Entries 3428244
FMS L1 HT bits
DSM board

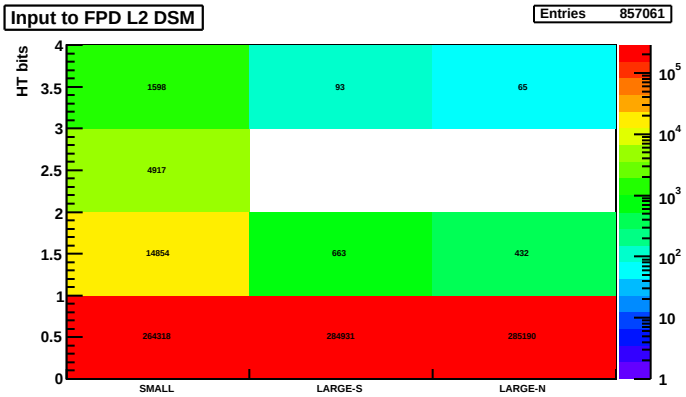

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

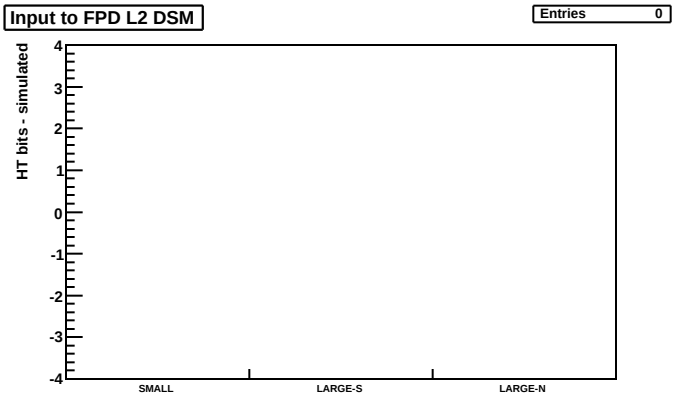

Input to FPD L2 DSM
Entries 3428244
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

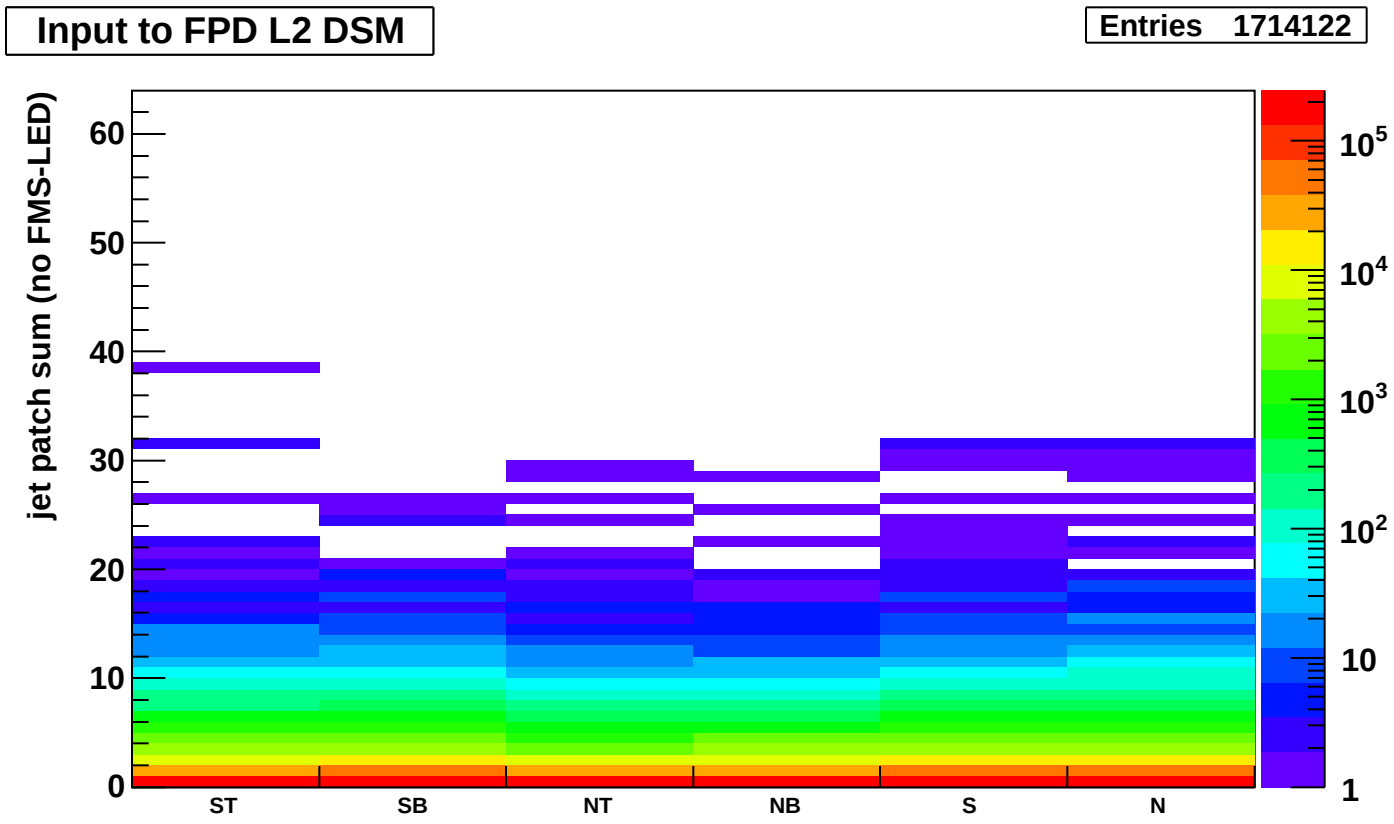

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

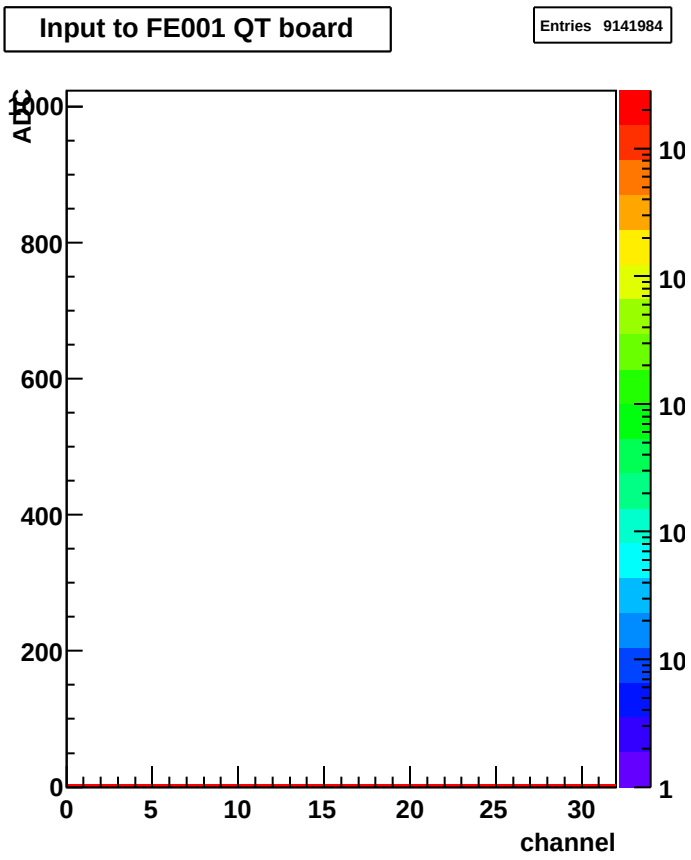

Input to FPD L2 DSM
Entries 857061
CL bits
4
3
42
9
8
1488
483
344
284157
285191
285332
SMALL
LARGE-S
LARGE-N

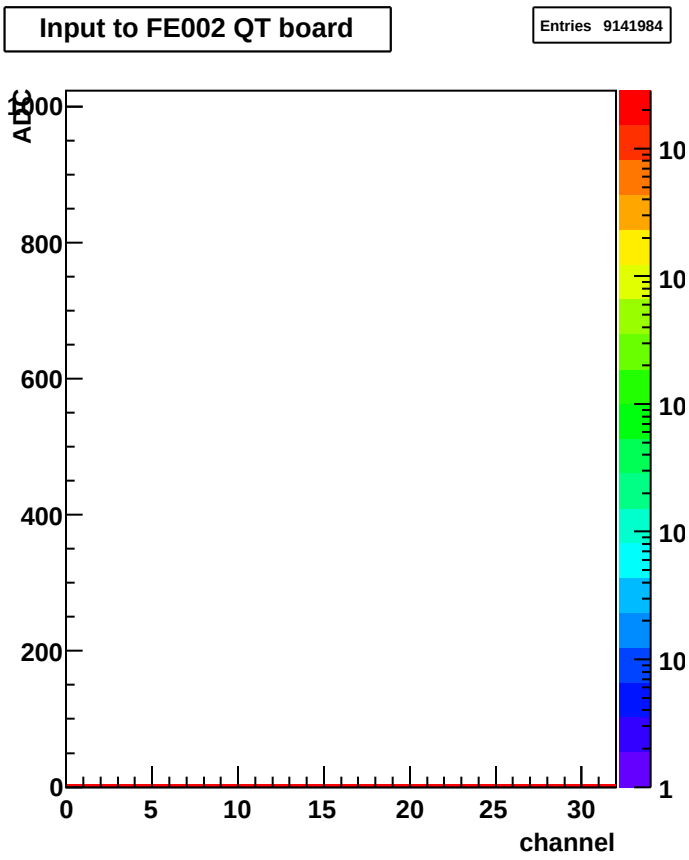

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

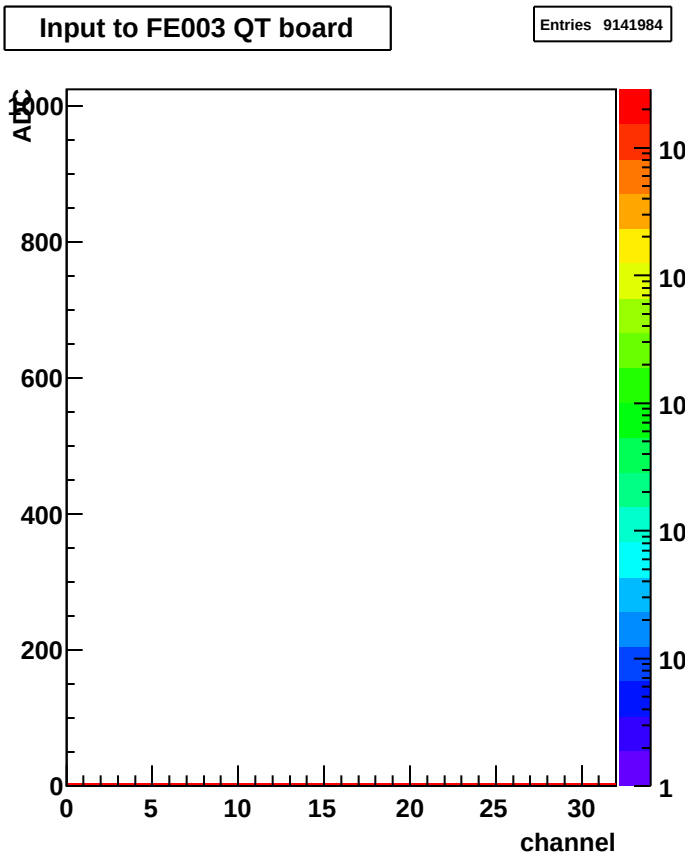

Input to FPD L2 DSM
Entries 857061
HT bits
1598
93
65
4917
14854
663
432
264318
284931
285190
SMALL
LARGE-S
LARGE-N

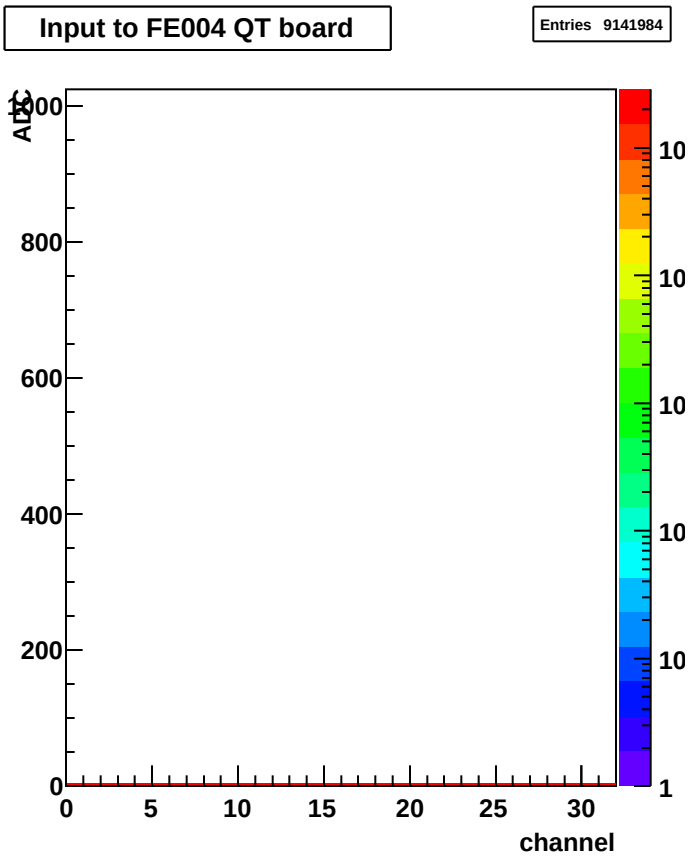

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

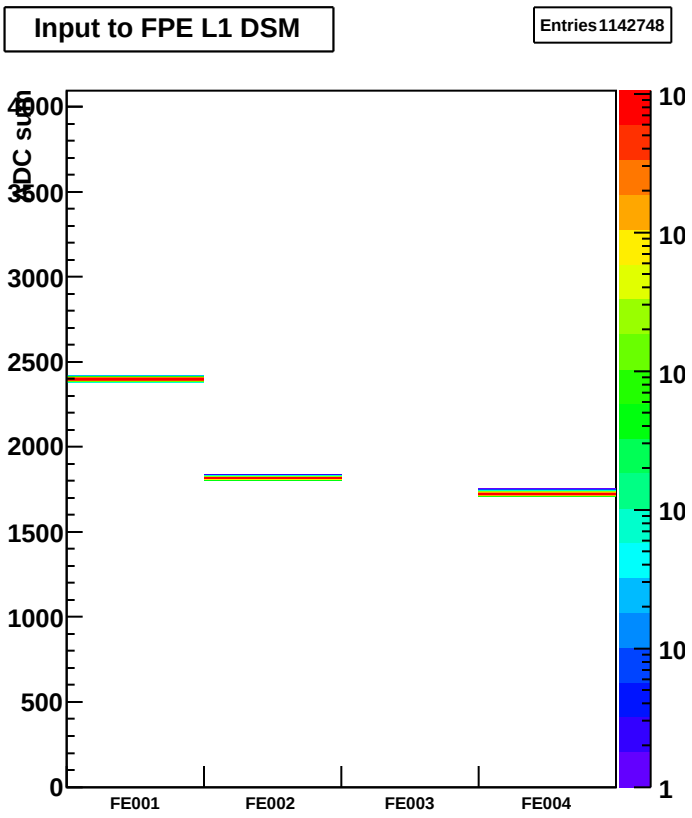

Input to FPD L2 DSM
Entries 1714122
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 9141984
ADC
channel

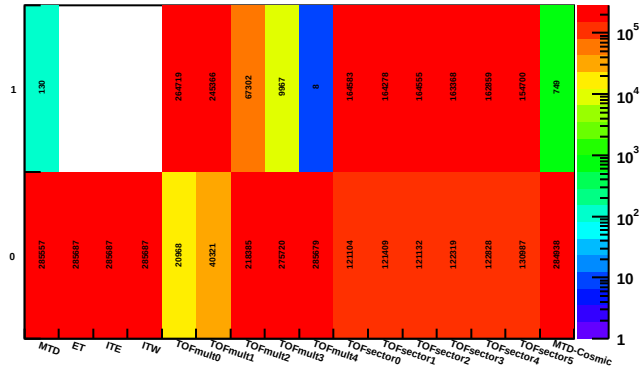

Input to FE002 QT board
Entries 9141984
ADC
channel


Input to FE003 QT board
Entries 9141984
ADC
channel

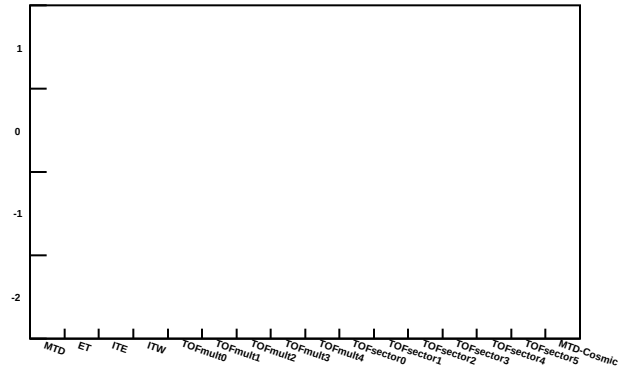

Input to FE004 QT board
Entries 9141984
ADC
channel


Input to FPE L1 DSM
Entries 1142748
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

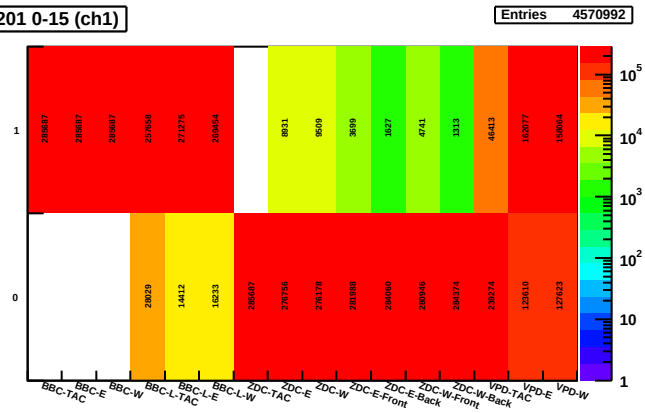
TF201 0-15 (ch0)



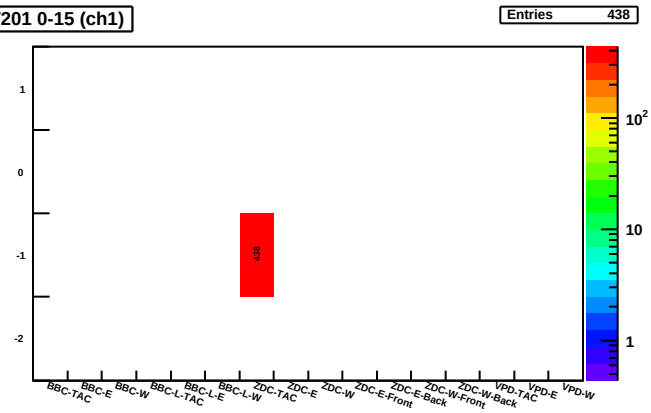
TF201 0-15 (ch0)



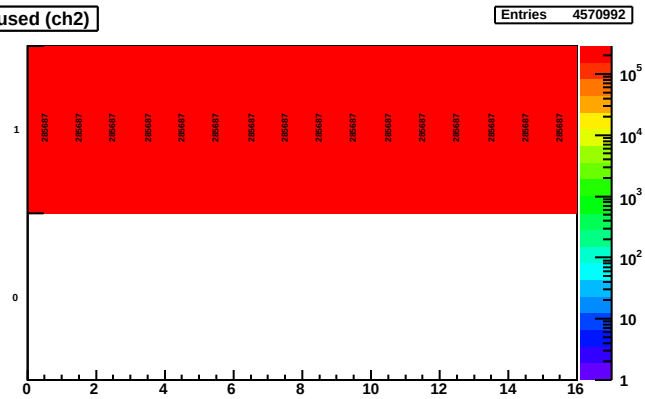
VT201 0-15 (ch1)

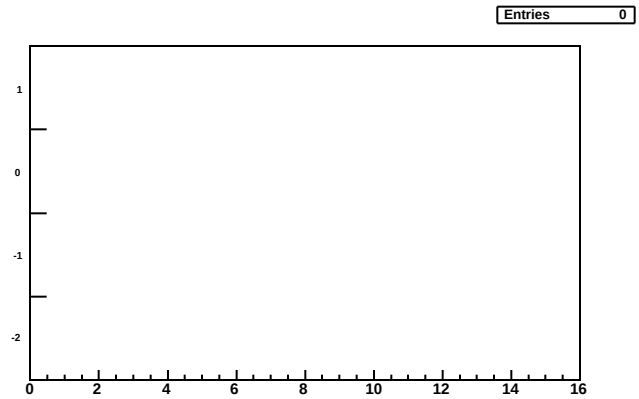


VT201 0-15 (ch1)

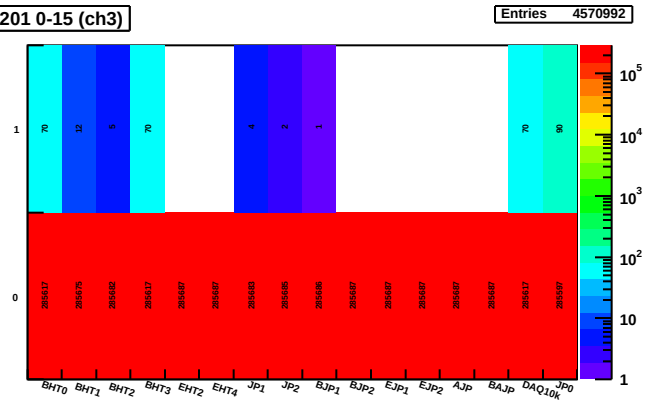


Unused (ch2)

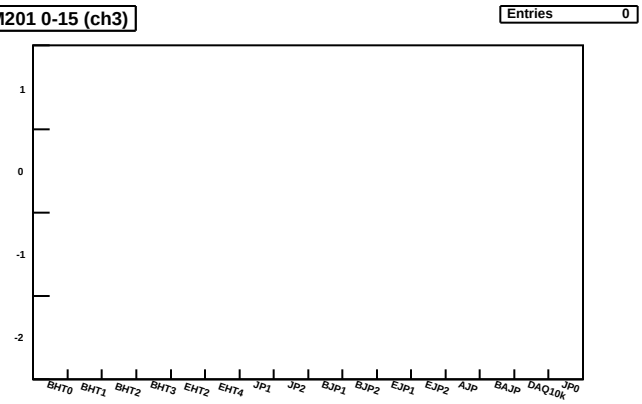




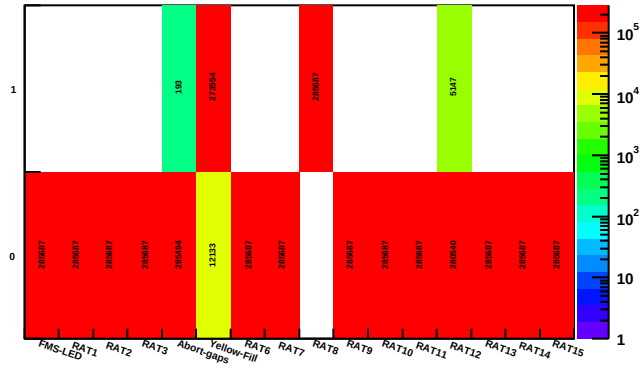
EM201 0-15 (ch3)

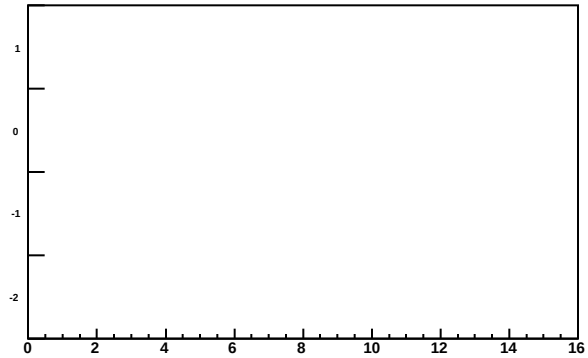


EM201 0-15 (ch3)

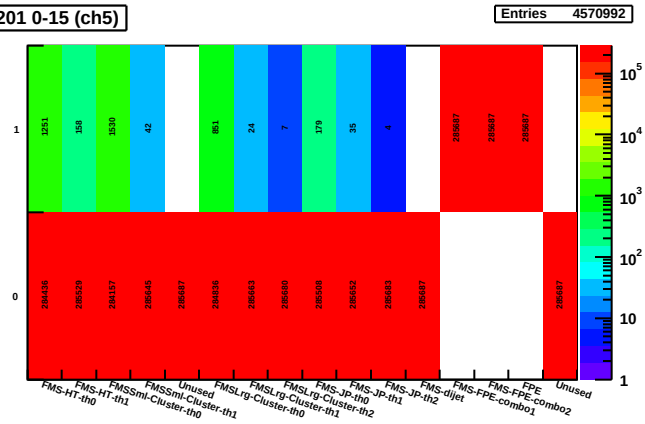


RAT board (ch4)





FP201 0-15 (ch5)



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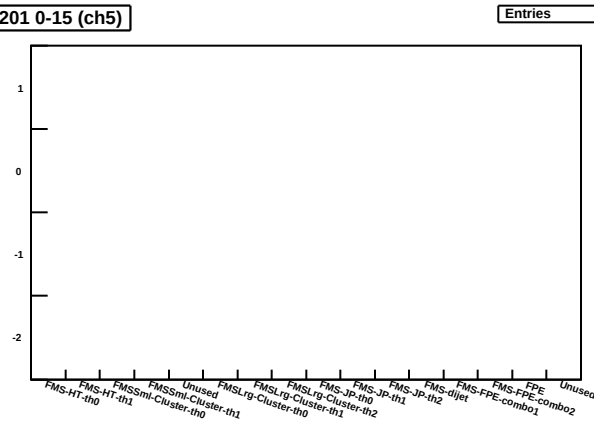

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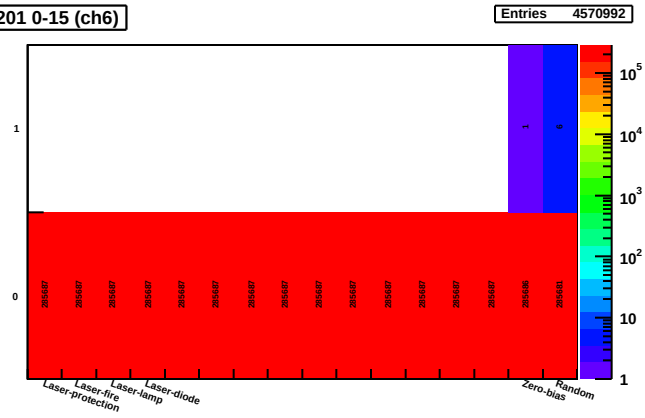

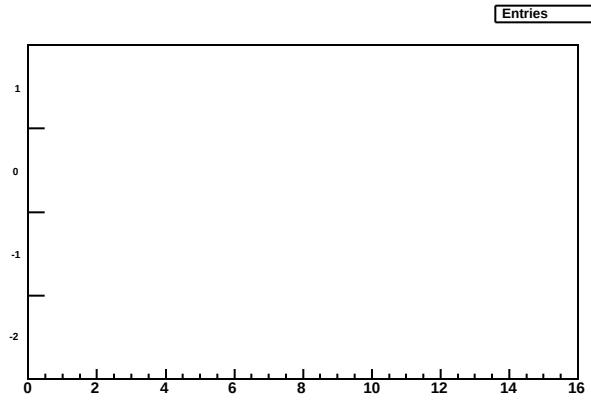
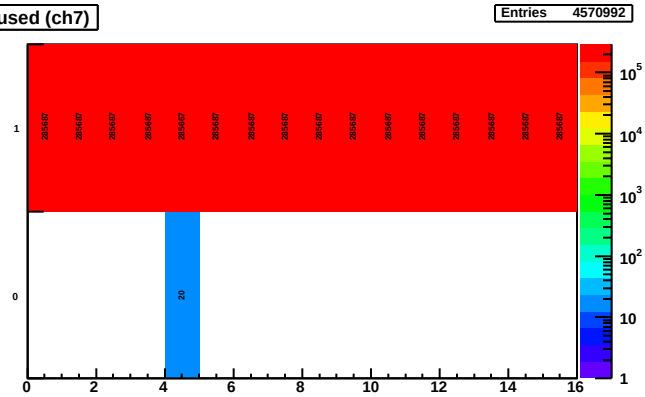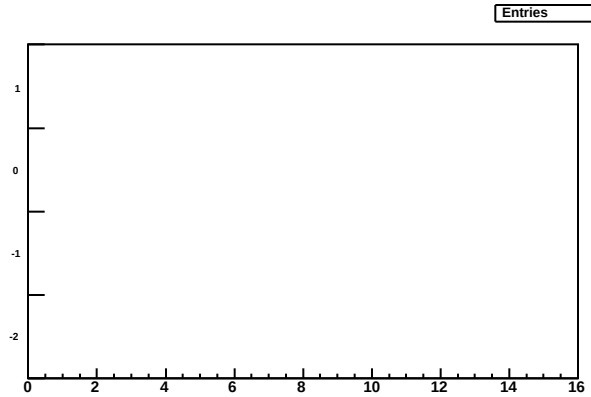
Unused (ch7)