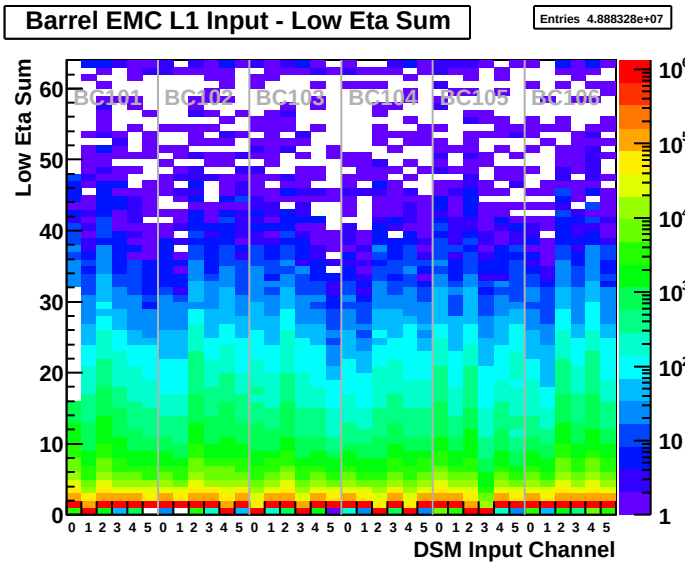
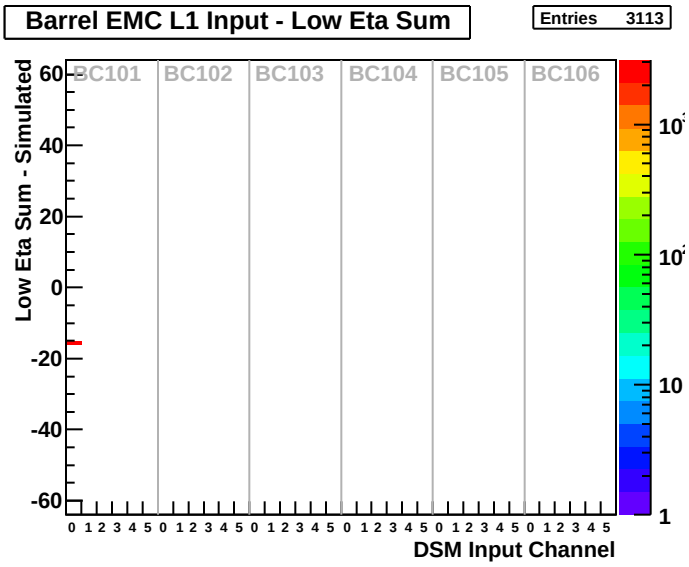
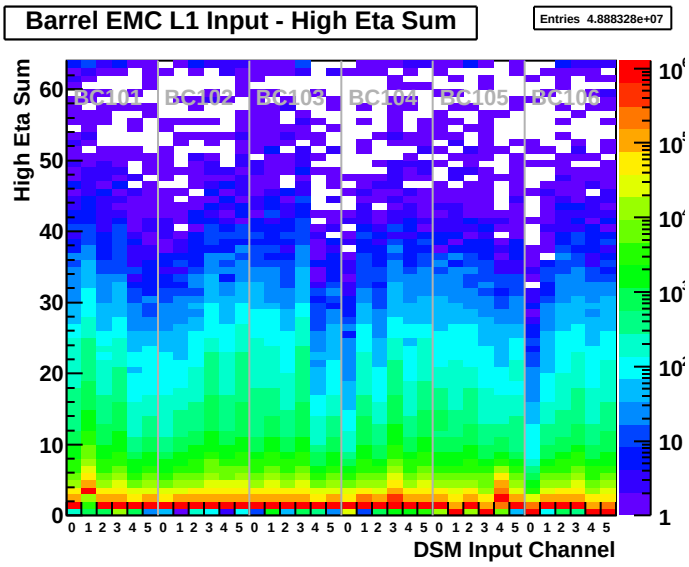
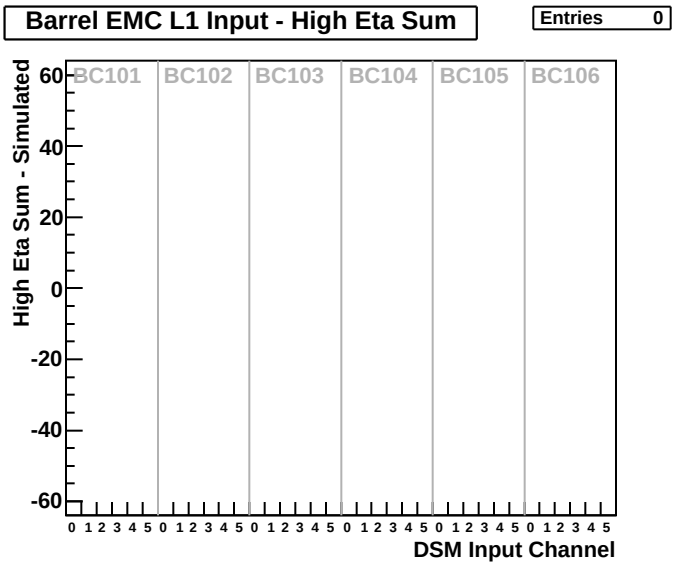
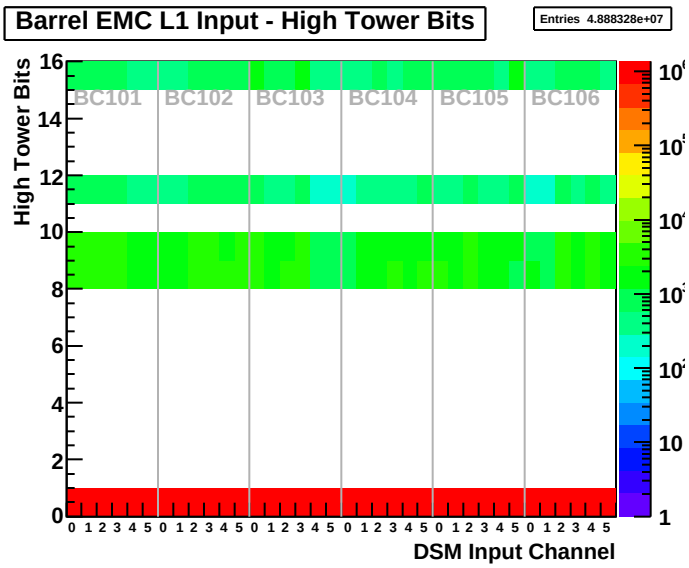
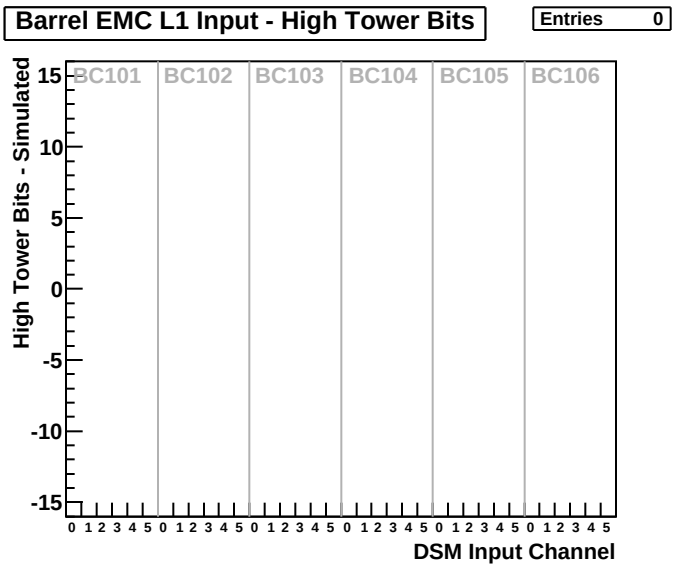

Barrel EMC L1 Input - Low Eta Sum
Entries 4.888328e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

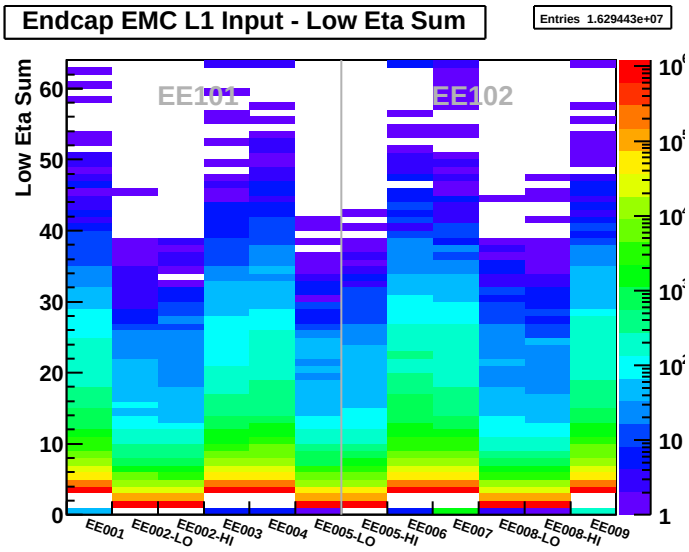

Barrel EMC L1 Input - Low Eta Sum
Entries 3113
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

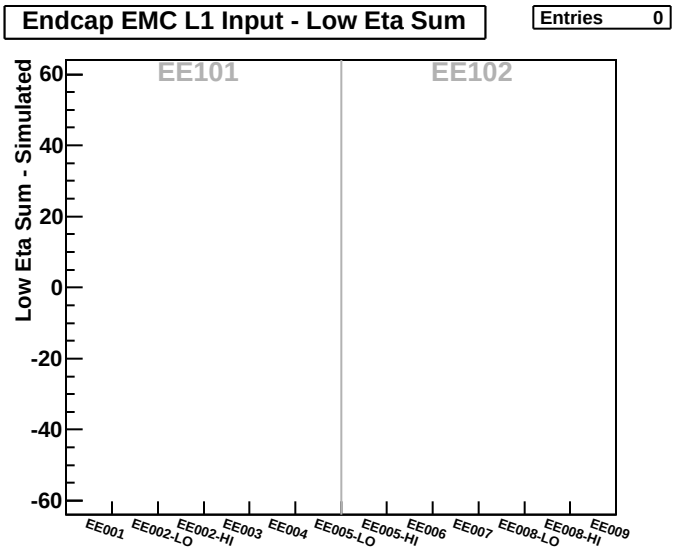

Barrel EMC L1 Input - High Eta Sum
Entries 4.888328e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

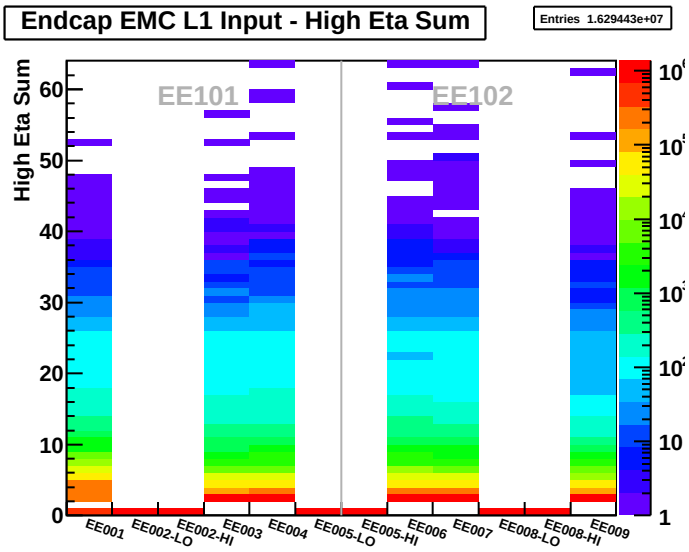

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

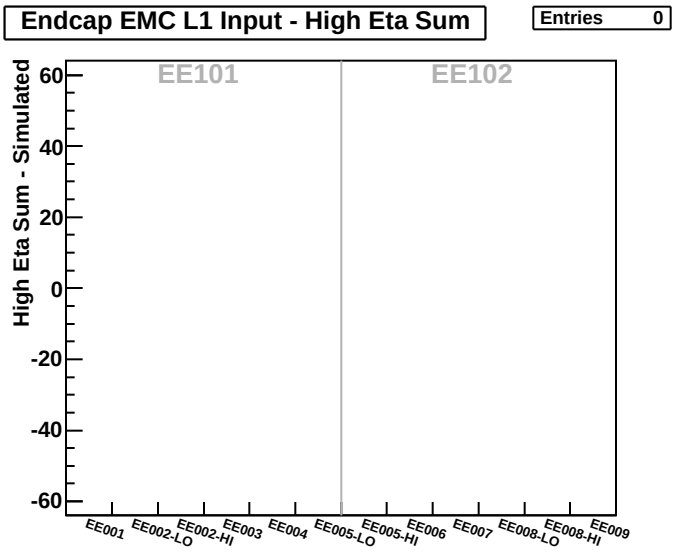

Barrel EMC L1 Input - High Tower Bits
Entries 4.888328e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

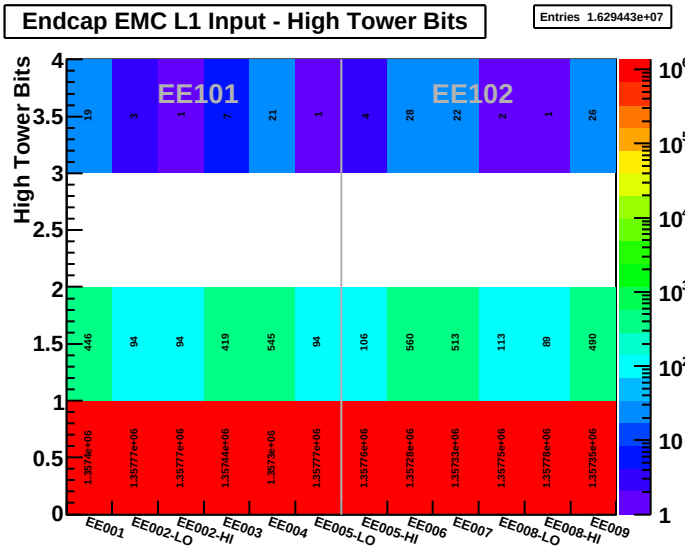

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

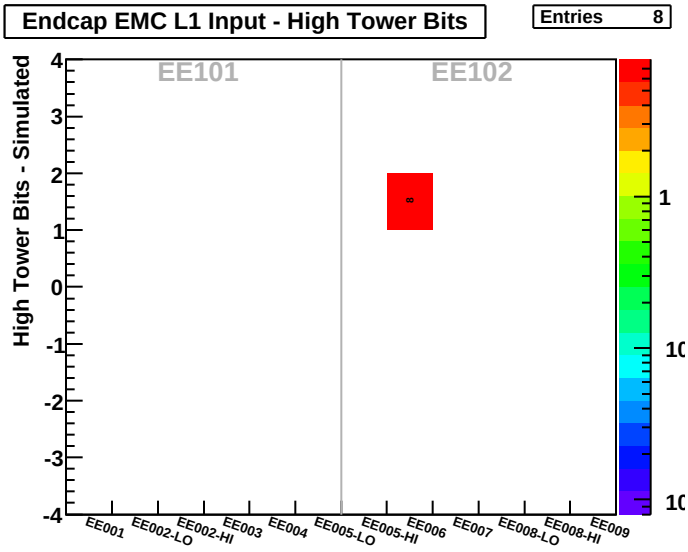

Endcap EMC L1 Input - Low Eta Sum
Entries 1.629443e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

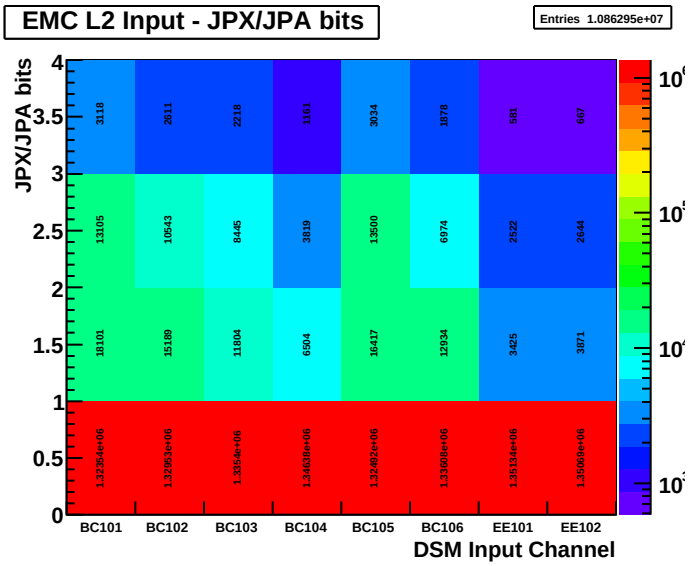

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

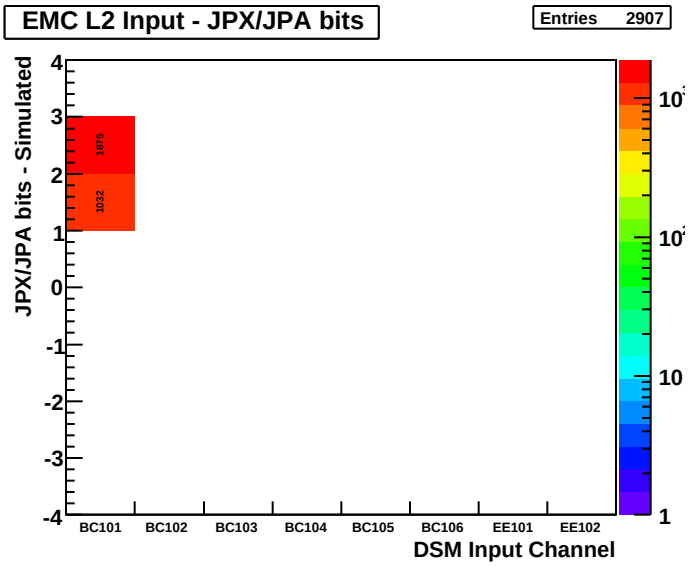

Endcap EMC L1 Input - High Eta Sum
Entries 1.629443e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

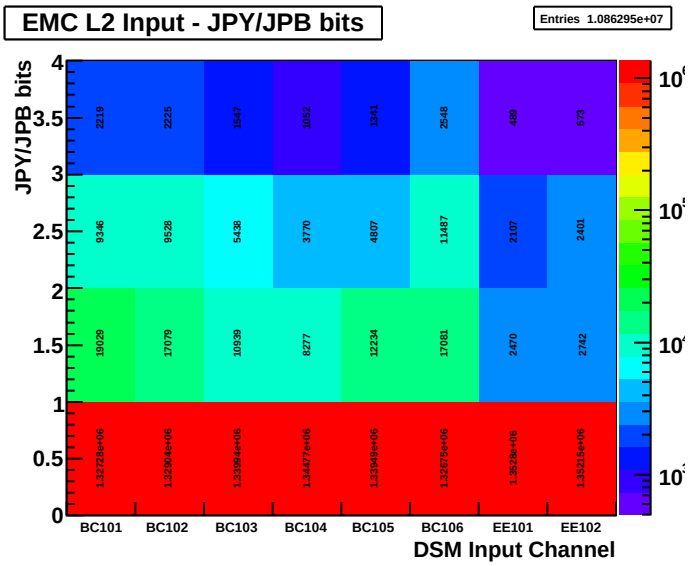

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

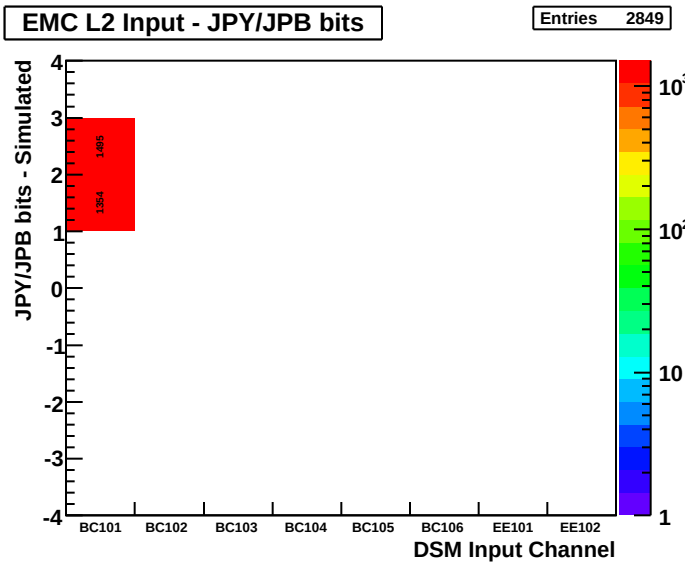

Endcap EMC L1 Input - High Tower Bits
Entries 1.629443e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

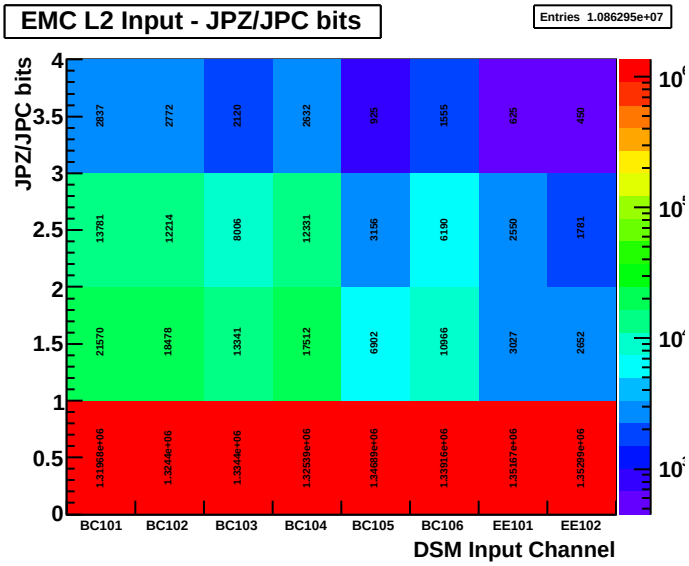

Endcap EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

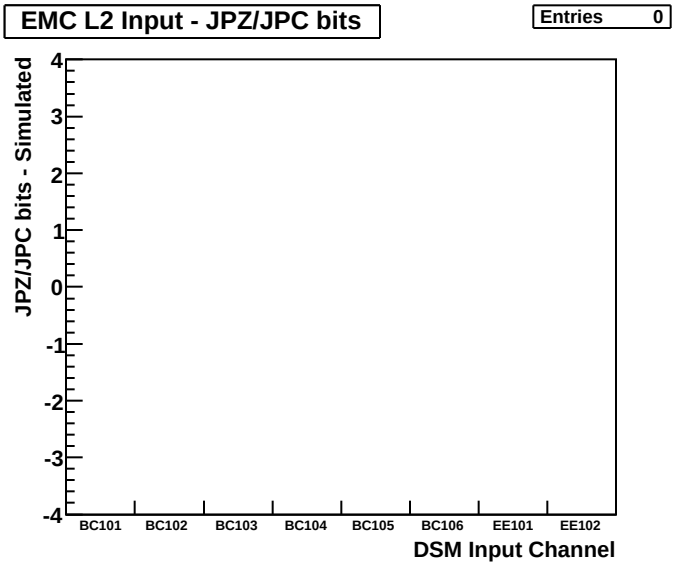

EMC L2 Input - JPX/JPA bits
Entries 1.086295e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3118 2611 2218 1161 3034 1878 581 667
13105 10543 8445 3819 13500 6974 2522 2644
18101 15189 11804 6504 16417 12934 3425 3871
1.32354e+06 1.32953e+06 1.3354e+06 1.34638e+06 1.32492e+06 1.33808e+06 1.35134e+06 1.35063e+06

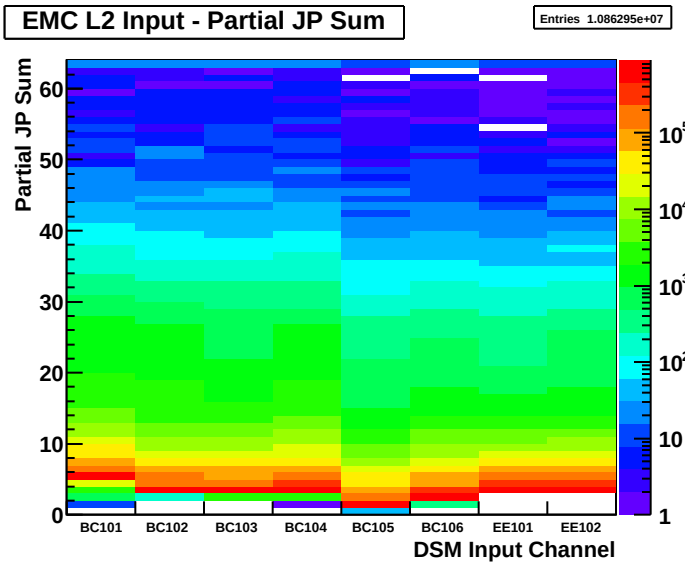

EMC L2 Input - JPX/JPA bits
Entries 2907
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1875
1032

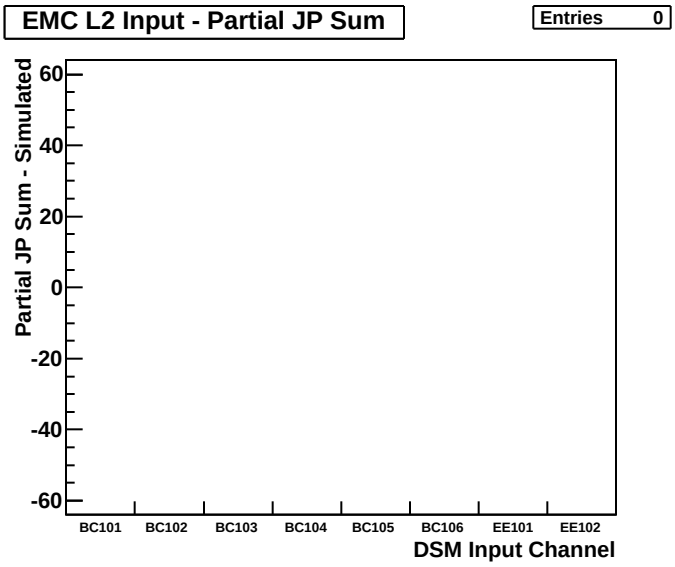

EMC L2 Input - JPY/JPB bits
Entries 1.086295e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2219 2225 1547 1052 1341 2548 489 573
9346 9528 5438 3770 4807 11487 2107 2401
19029 17079 10939 8277 12234 17081 2470 2742
1.32728e+06 1.32904e+06 1.33394e+06 1.34477e+06 1.33949e+06 1.32675e+06 1.3528e+06 1.35215e+06

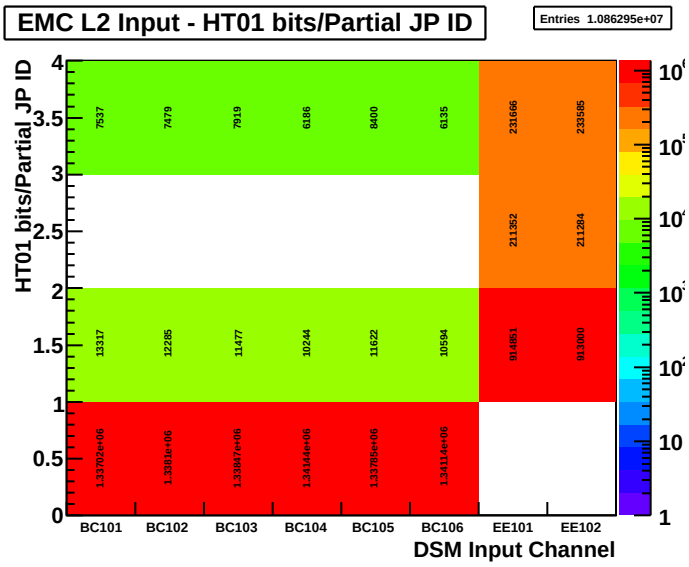

EMC L2 Input - JPY/JPB bits
Entries 2849
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1495
1354

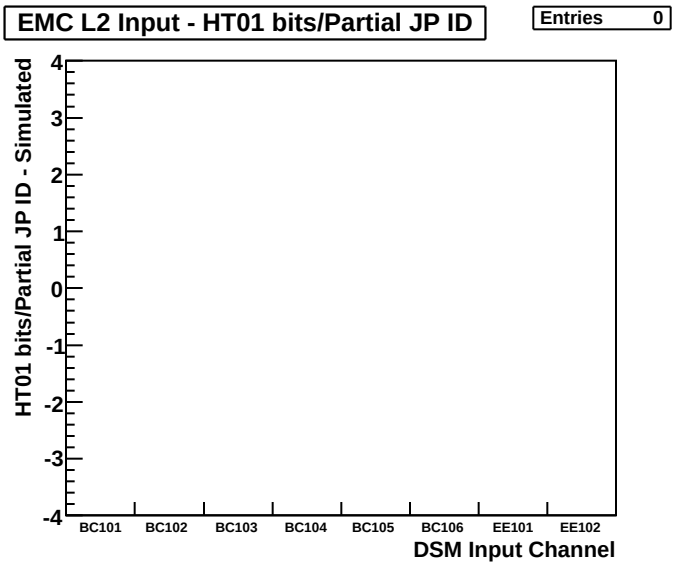

EMC L2 Input - JPZ/JPC bits
Entries 1.086295e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2837 2772 2120 2632 925 1555 625 450
13781 12214 8006 12331 3156 6190 2550 1781
21570 18478 13341 17512 6902 10966 3027 2652
1.31968e+06 1.3244e+06 1.3344e+06 1.32539e+06 1.34689e+06 1.33916e+06 1.35167e+06 1.35299e+06

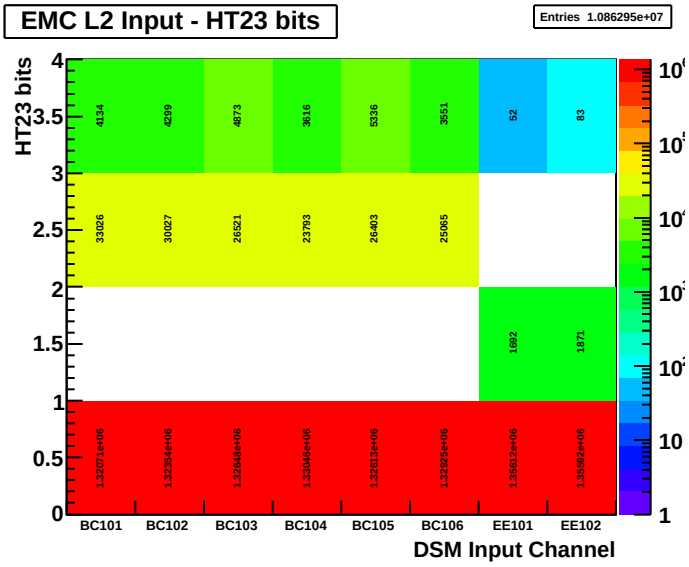

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

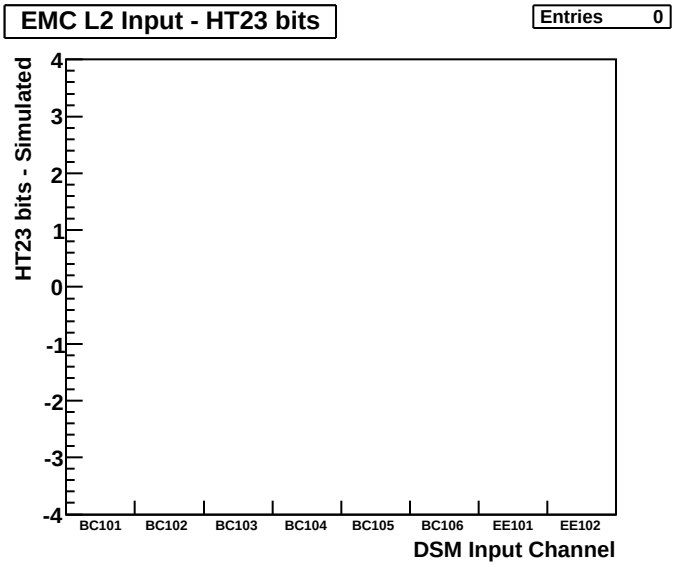

EMC L2 Input - Partial JP Sum
Entries 1.086295e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

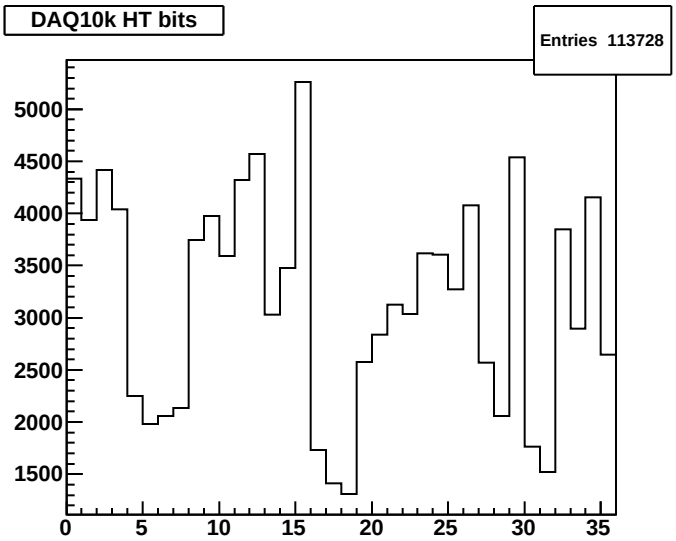

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

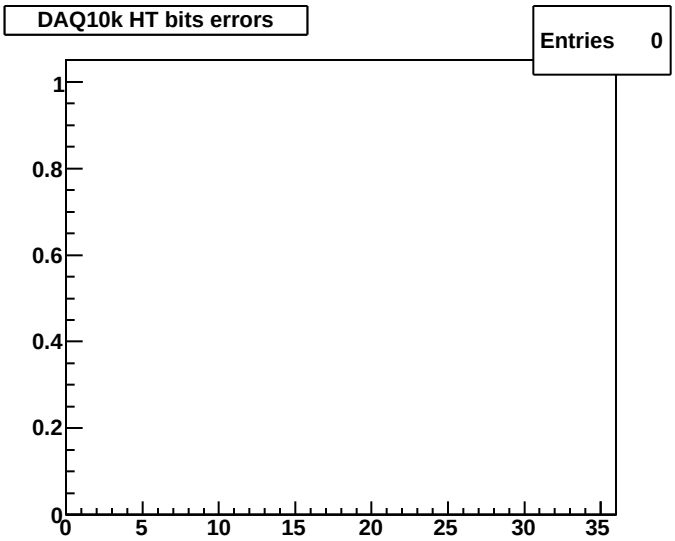

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.086295e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

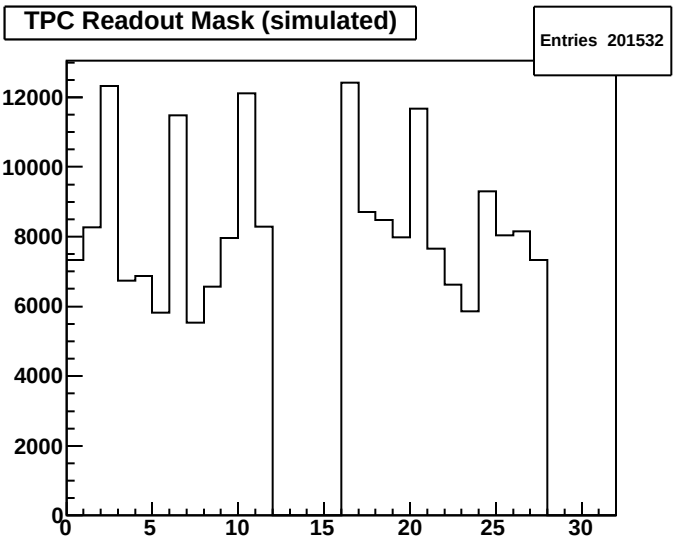

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

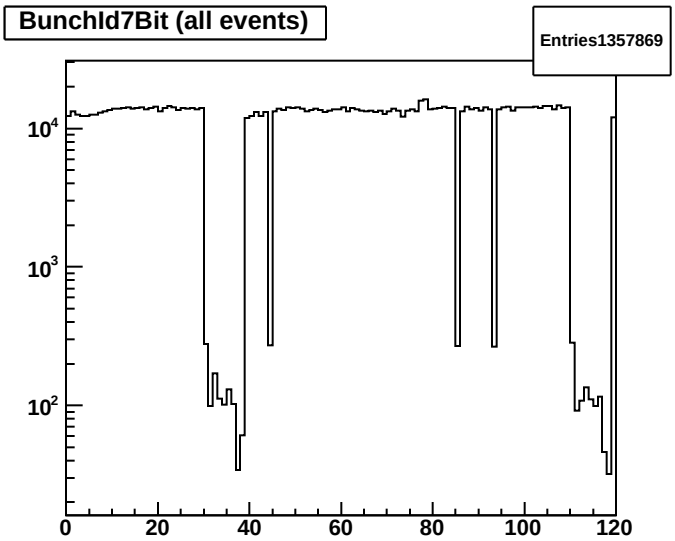

EMC L2 Input - HT23 bits
Entries 1.086295e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

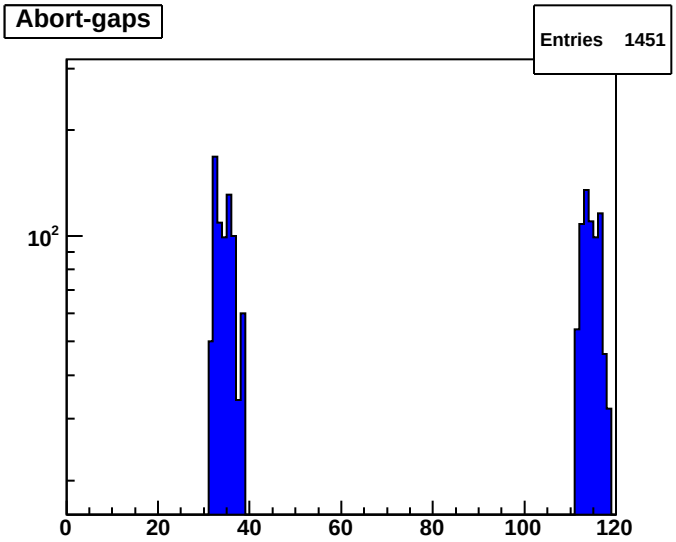

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

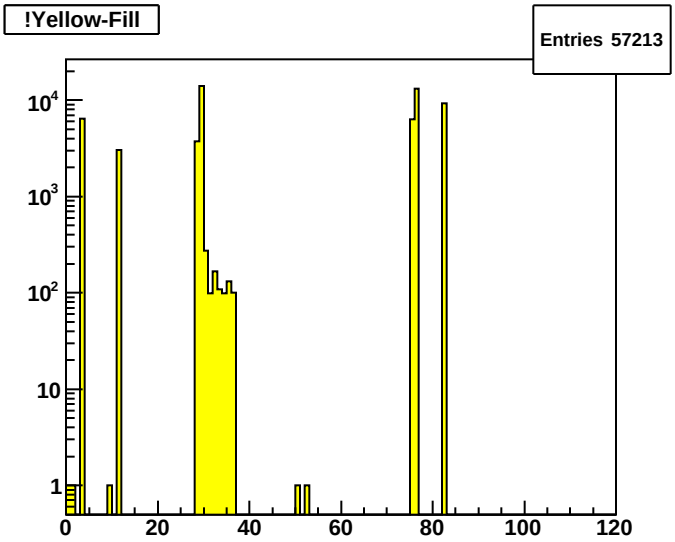

DAQ10k HT bits
Entries 113728

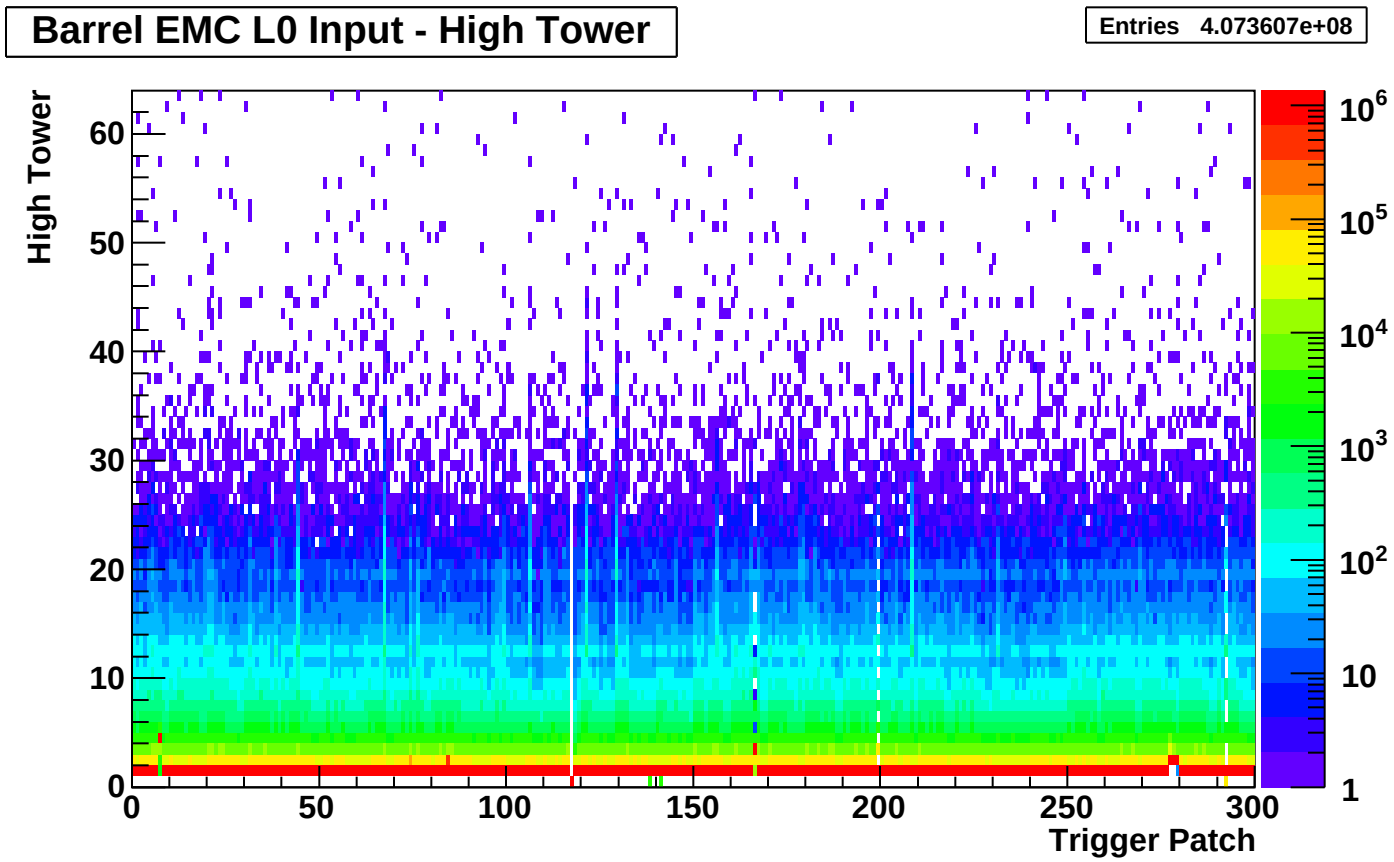

DAQ10k HT bits errors
Entries 0

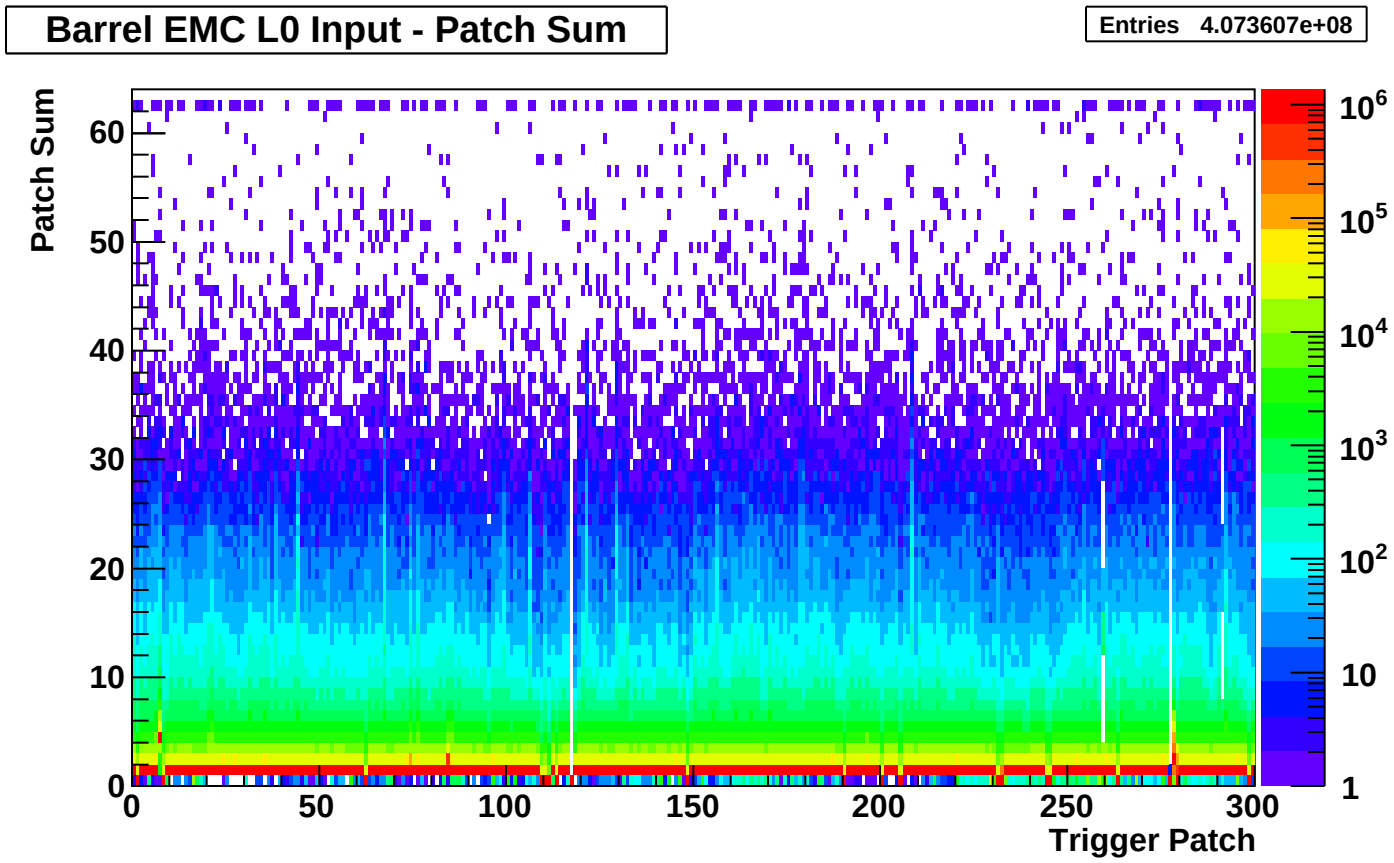

TPC Readout Mask (simulated)
Entries 201532

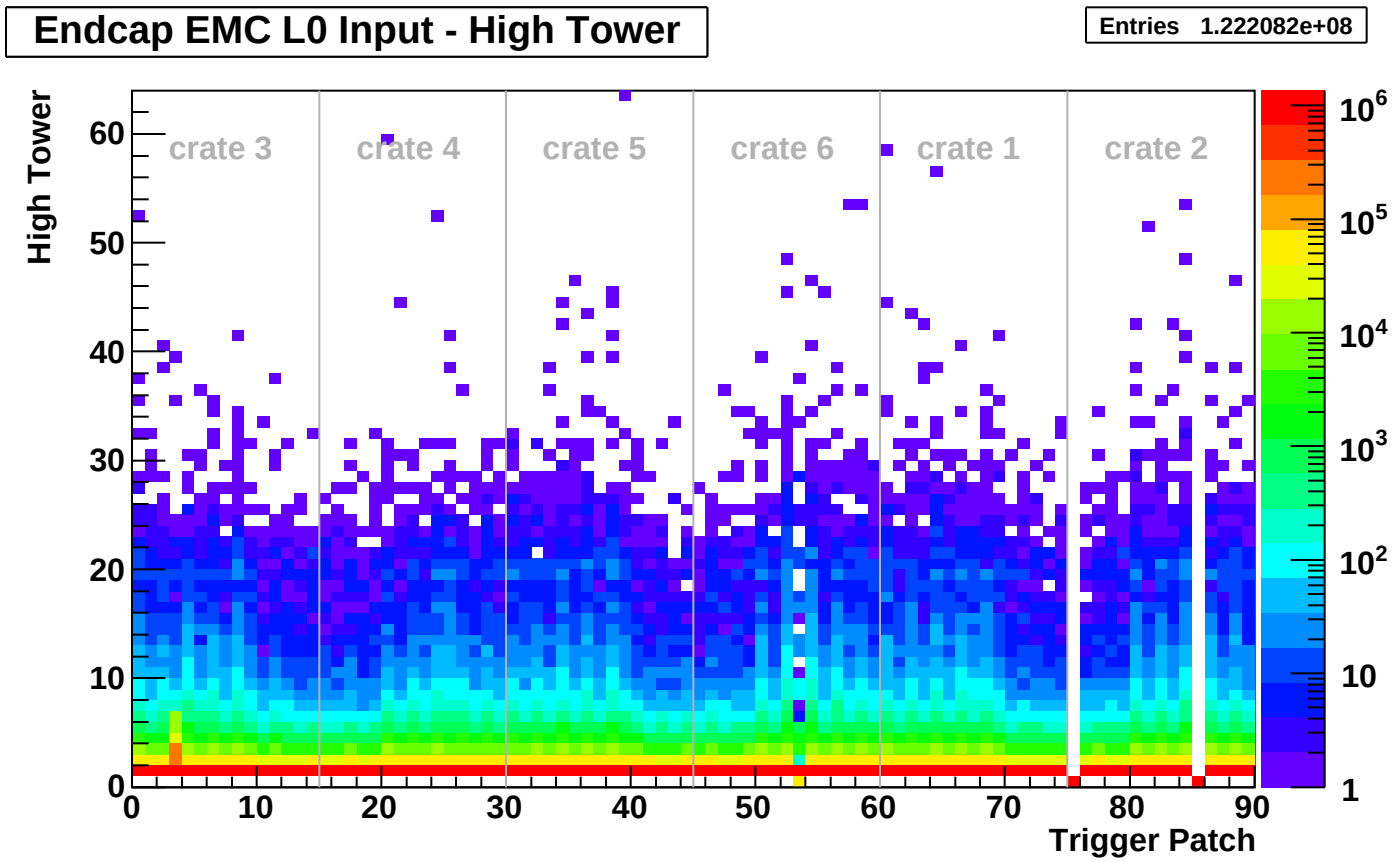

BunchId7Bit (all events)
Entries1357869

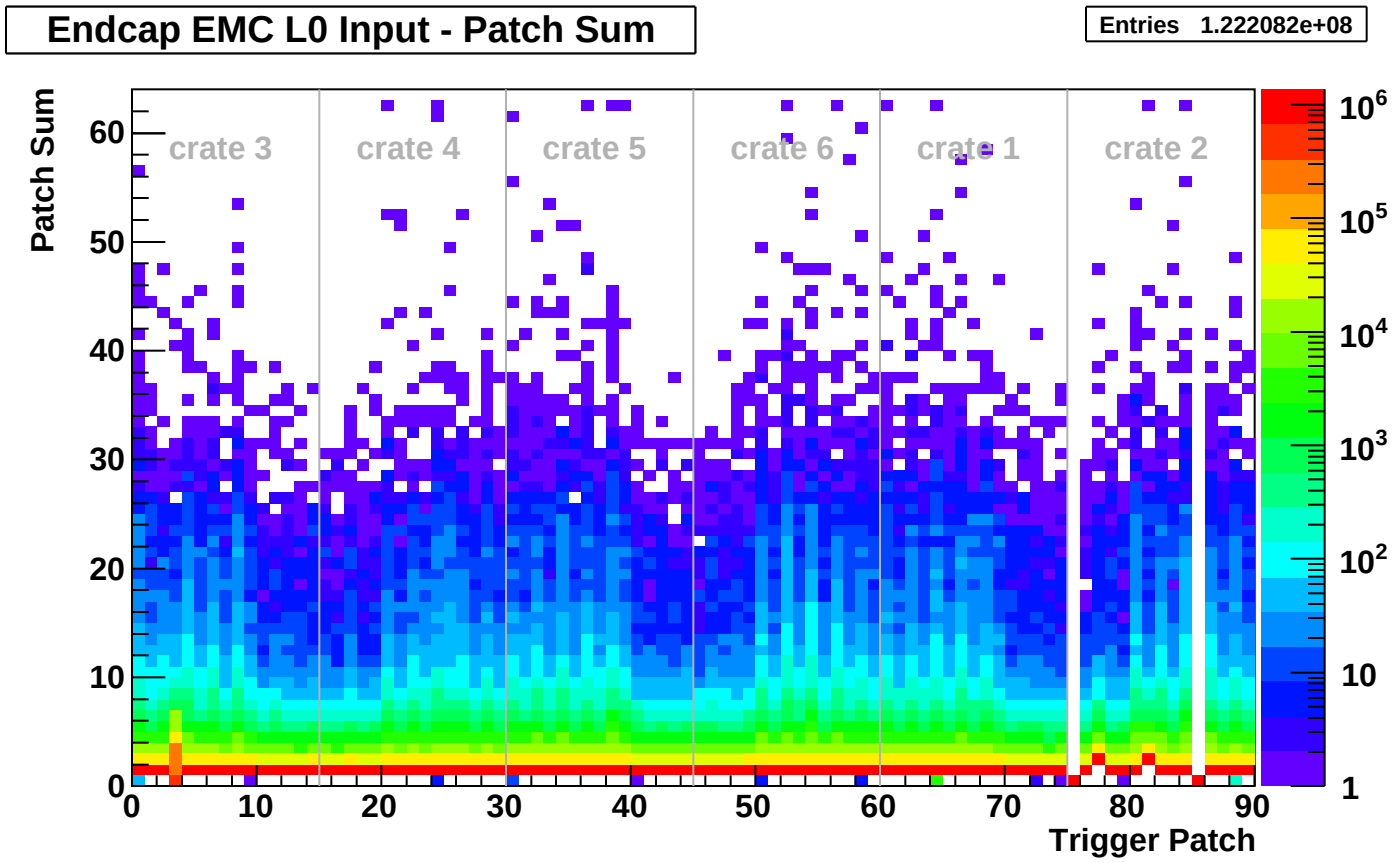

Abort-gaps
Entries 1451

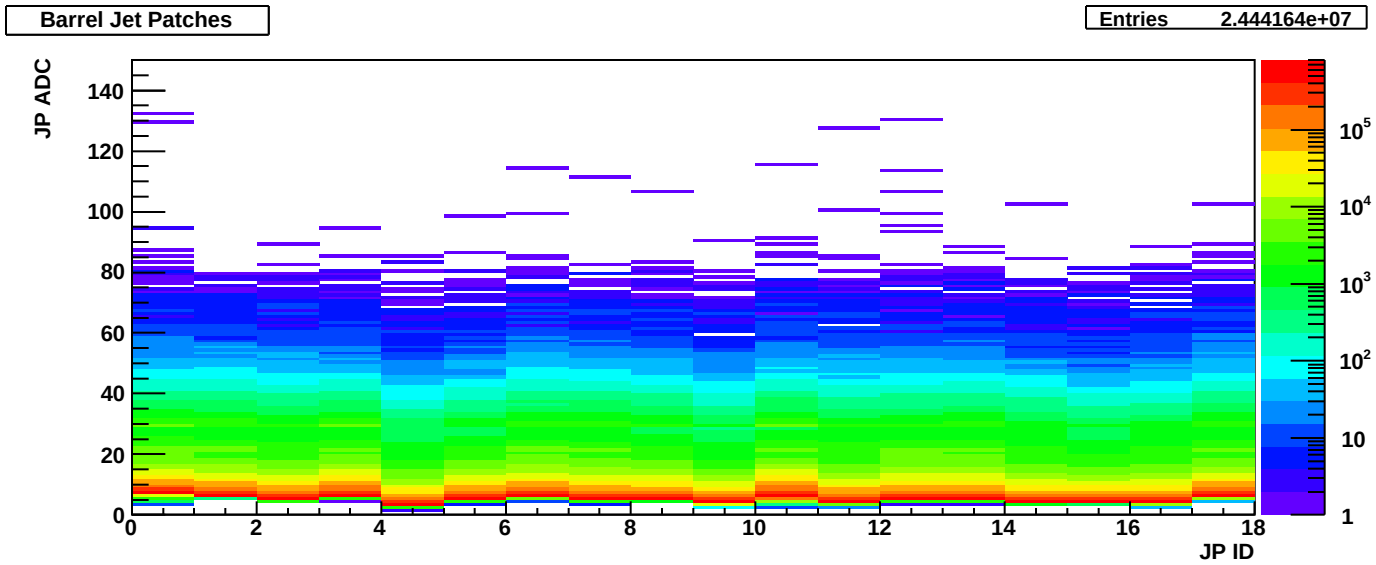

!Yellow-Fill
Entries 57213

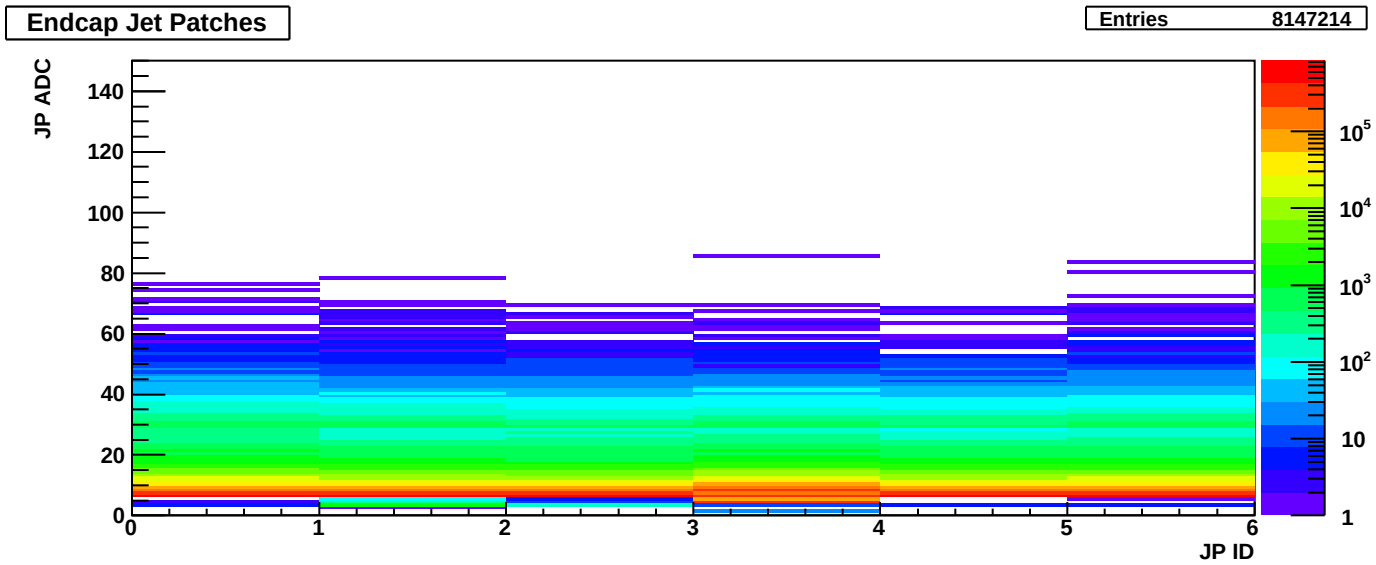

Barrel EMC L0 Input - High Tower
Entries 4.073607e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

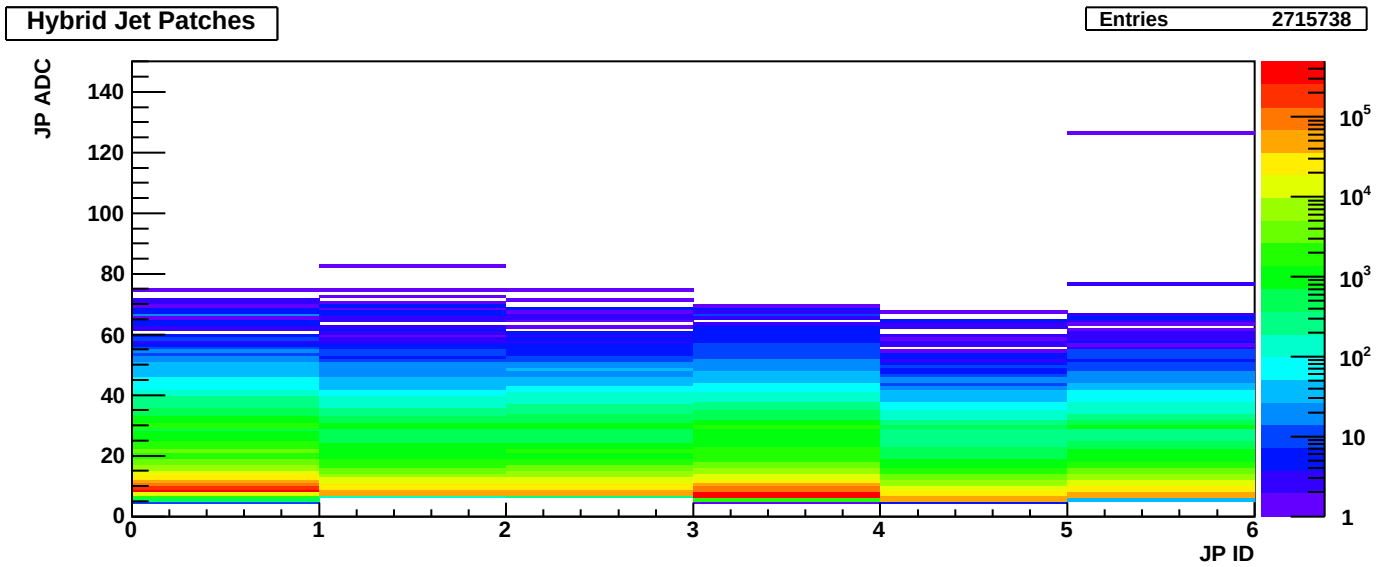

Barrel EMC L0 Input - Patch Sum
Entries 4.073607e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

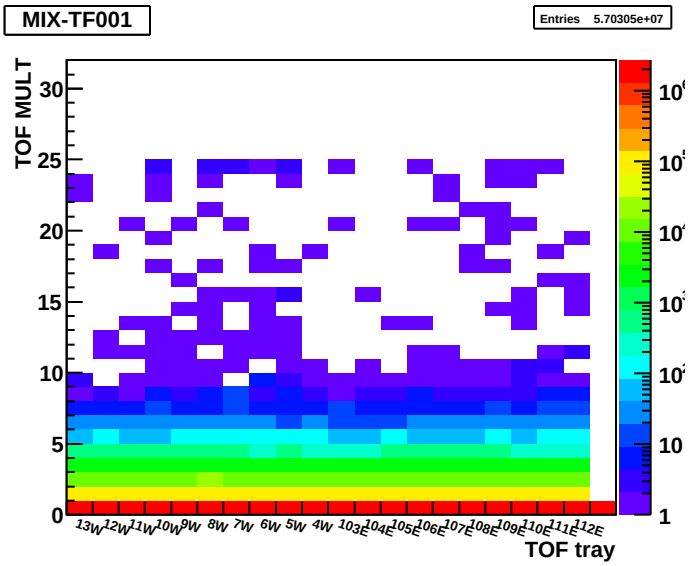

Endcap EMC L0 Input - High Tower
Entries 1.222082e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

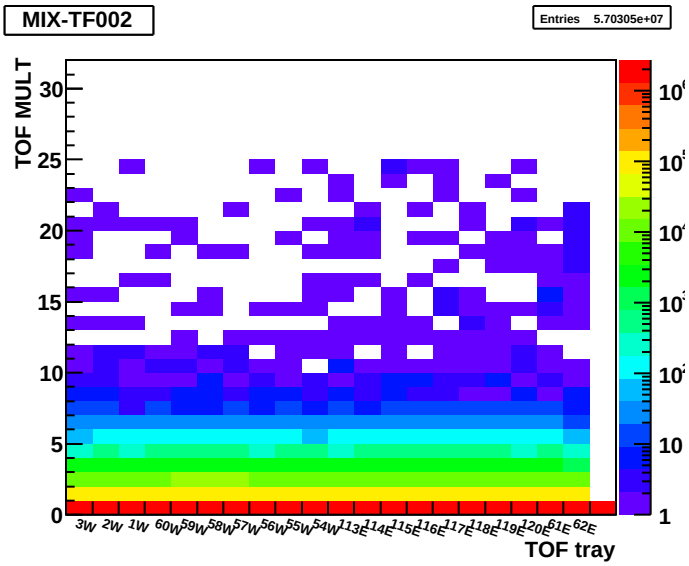

Endcap EMC L0 Input - Patch Sum
Entries 1.222082e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

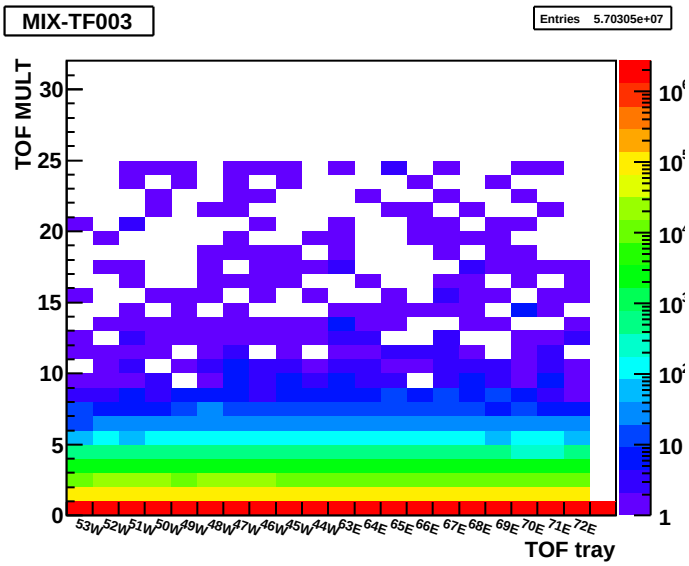

Barrel Jet Patches
Entries 2.444164e+07
JP ADC
JP ID

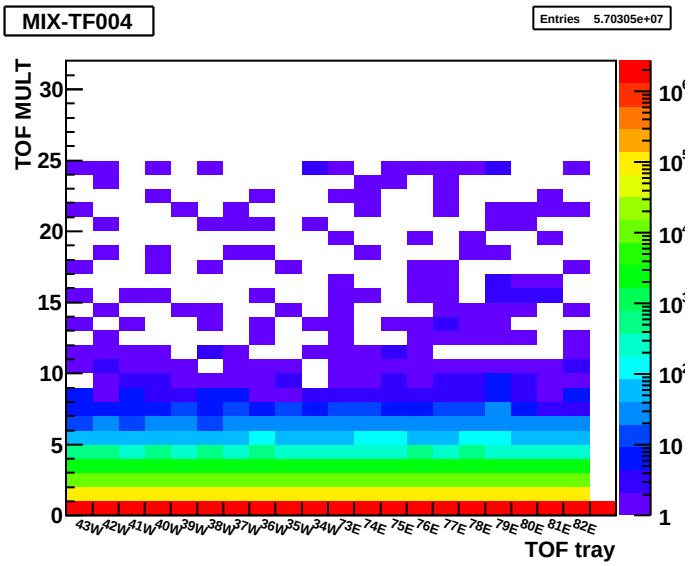

Endcap Jet Patches
Entries 8147214
JP ADC
JP ID

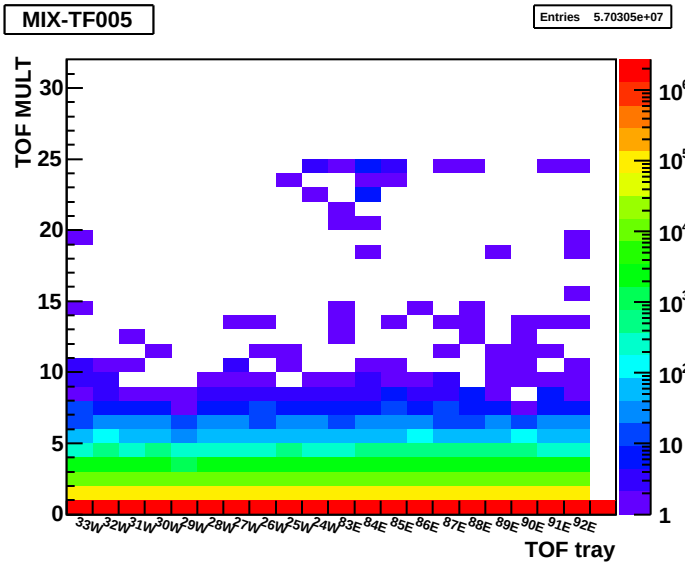

Hybrid Jet Patches
Entries 2715738
JP ADC
JP ID

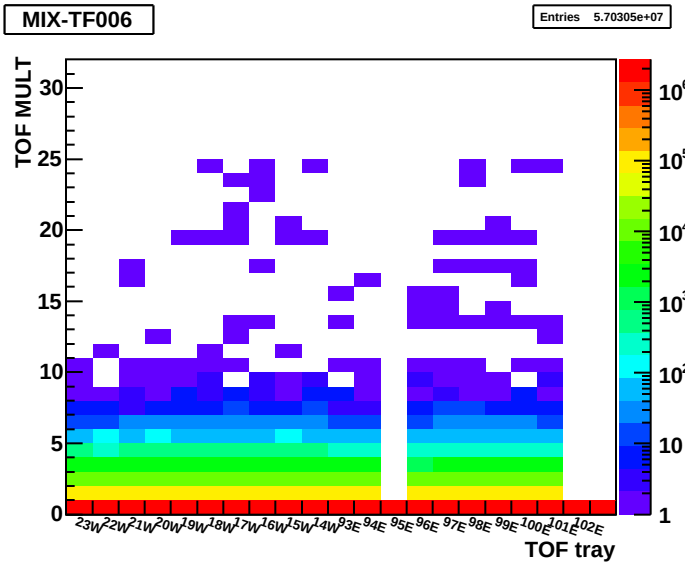

MIX-TF001
Entries 5.70305e+07
TOF MULT
TOF tray

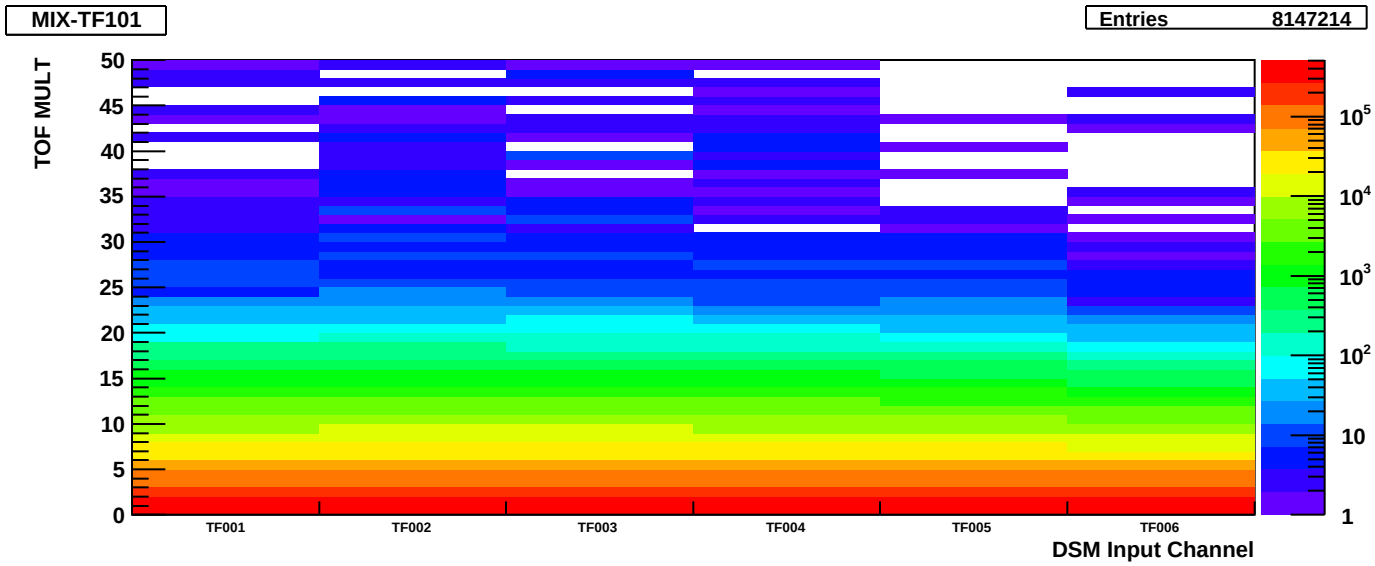

MIX-TF002
Entries 5.70305e+07
TOF MULT
TOF tray

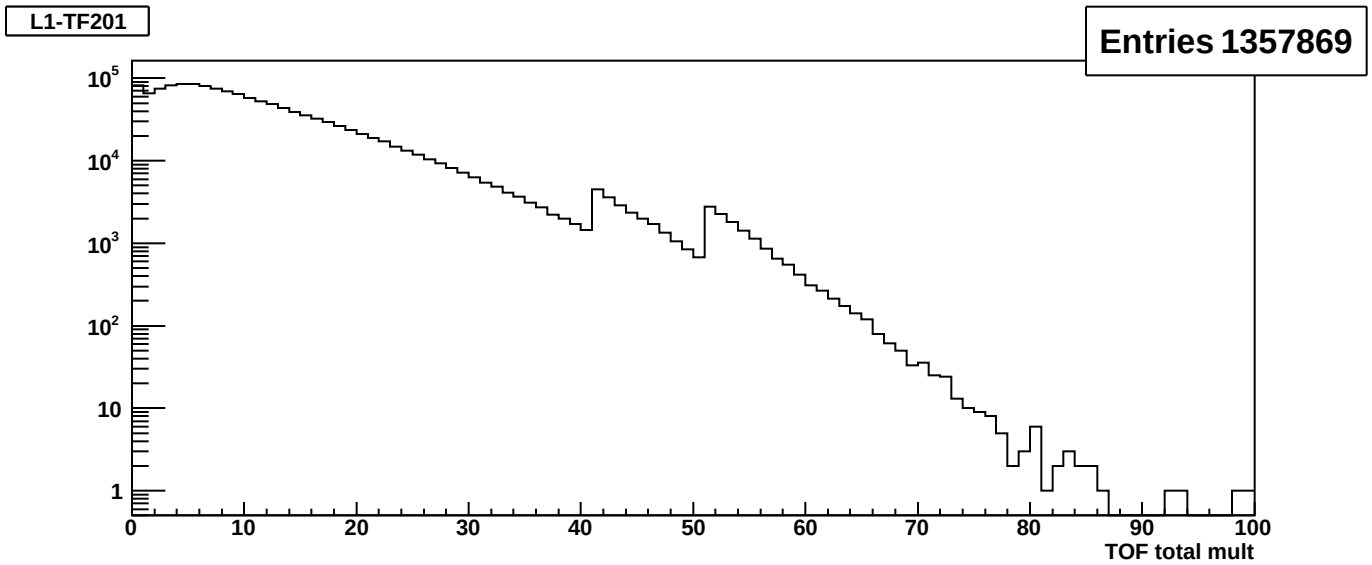

MIX-TF003
Entries 5.70305e+07
TOF MULT
TOF tray

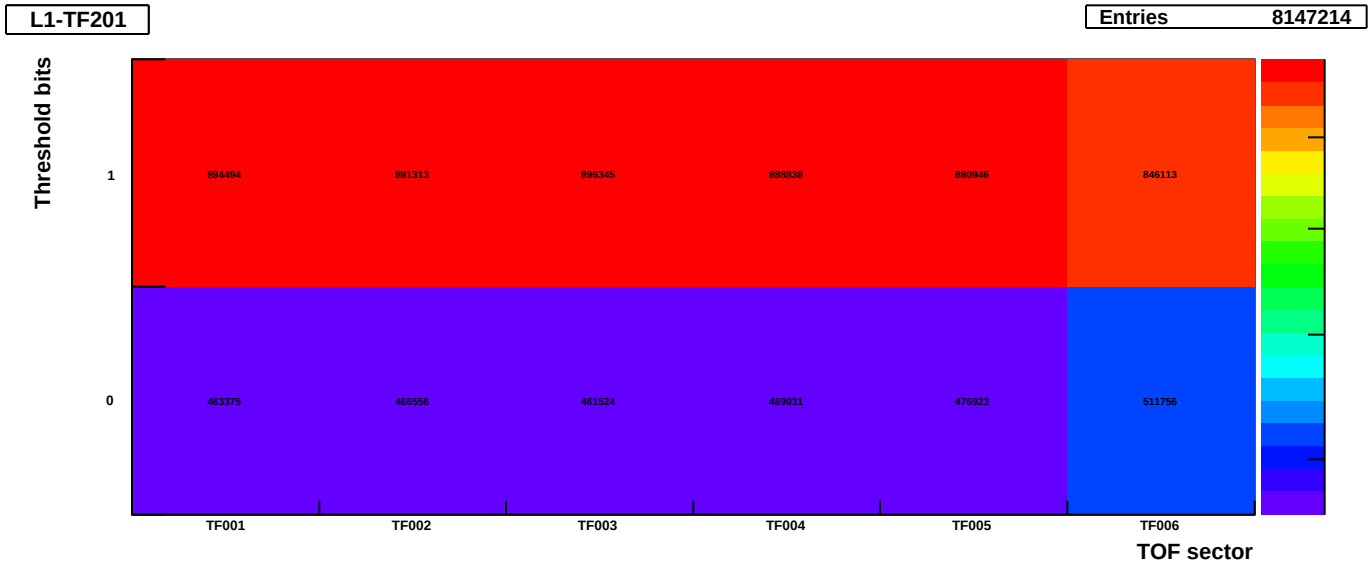

MIX-TF004
Entries 5.70305e+07
TOF MULT
TOF tray

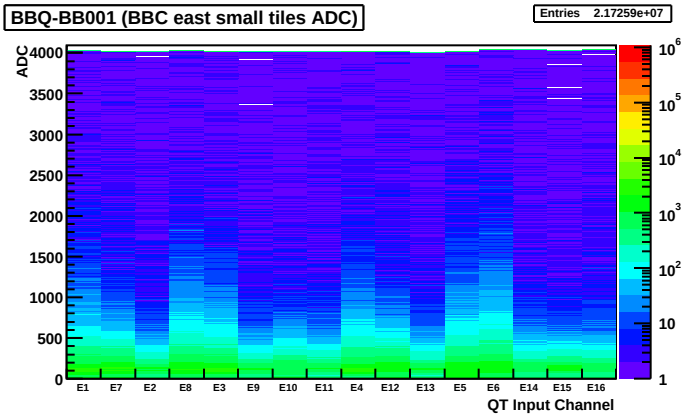

MIX-TF005
Entries 5.70305e+07
TOF MULT
TOF tray

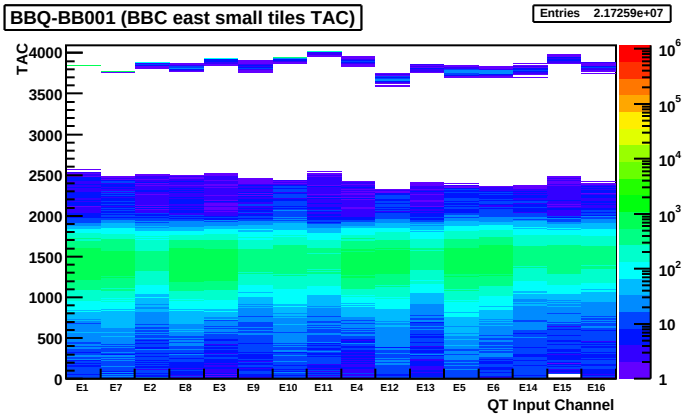

MIX-TF006
Entries 5.70305e+07
TOF MULT
TOF tray

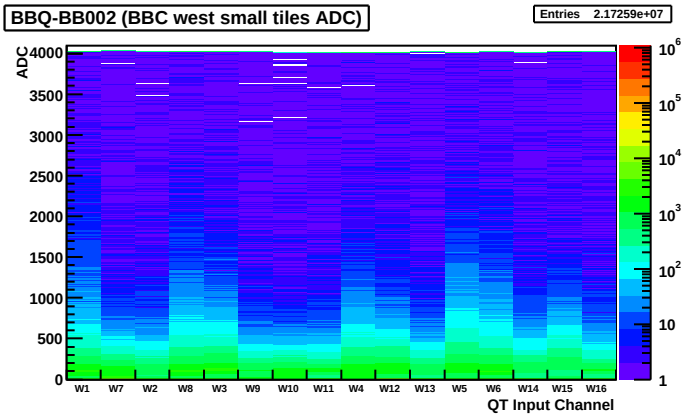

MIX-TF101
Entries 8147214
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

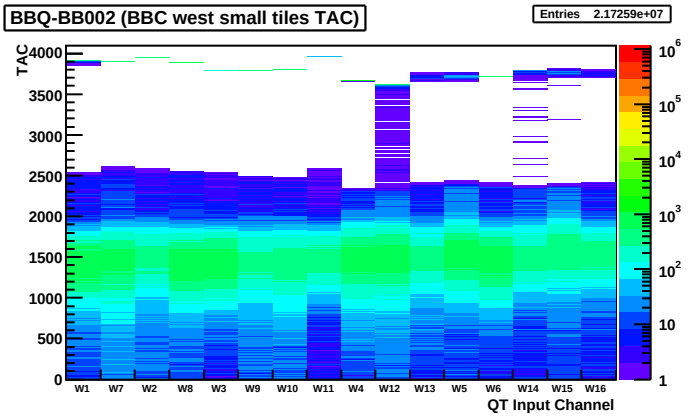

L1-TF201
Entries 1357869
TOF total mult

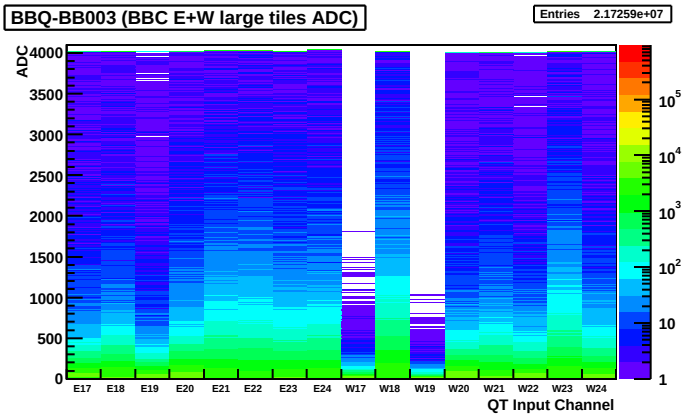

L1-TF201
Entries 8147214
Threshold bits
894494
891313
896345
888838
880946
846113
463375
466556
461524
469031
476923
511756
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

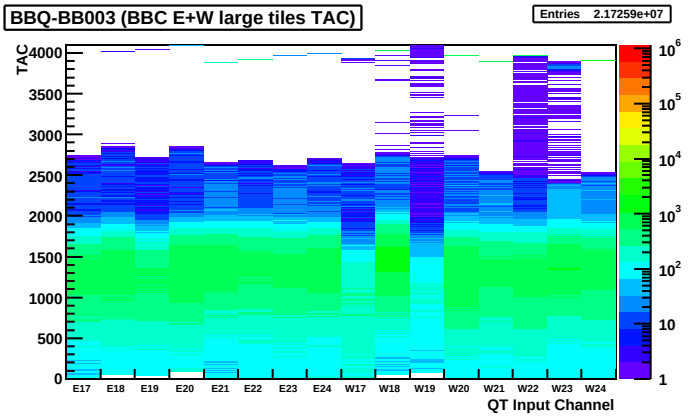

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.17259e+07
ADC
QT Input Channel

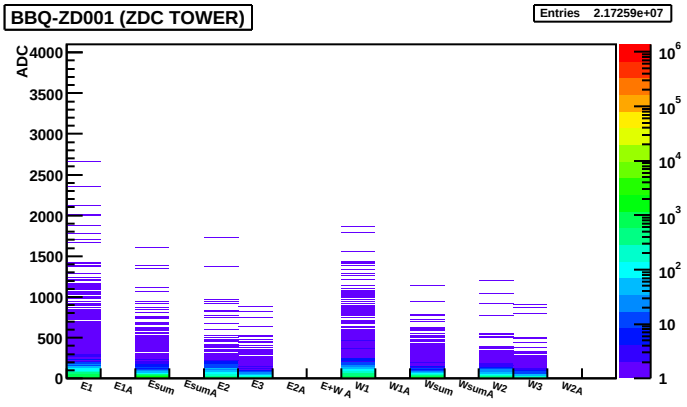

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.17259e+07
TAC
QT Input Channel

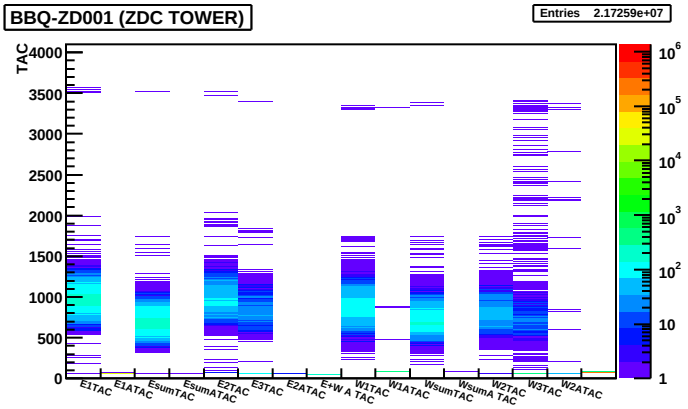

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.17259e+07
ADC
QT Input Channel

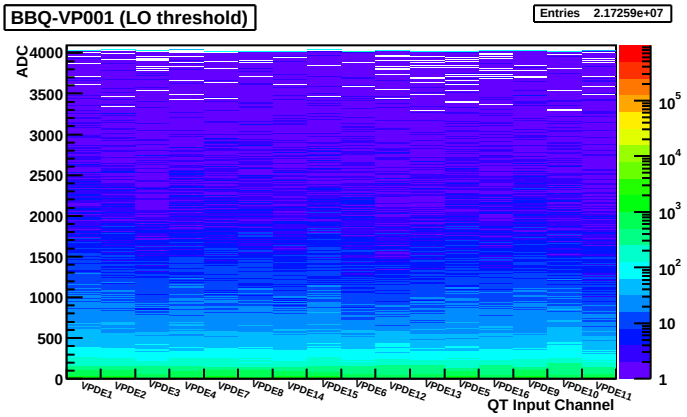

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.17259e+07
TAC
QT Input Channel

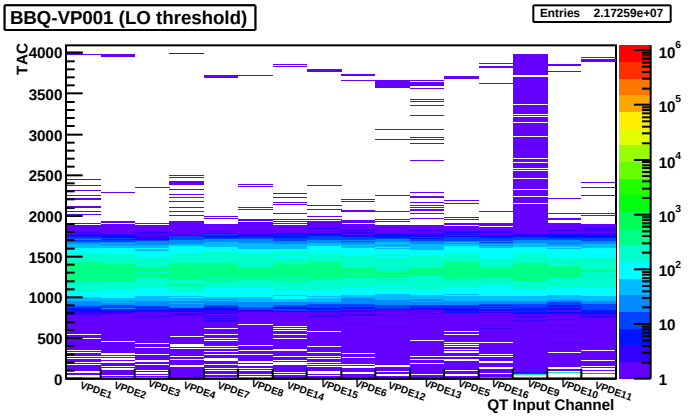

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.17259e+07
ADC
QT Input Channel

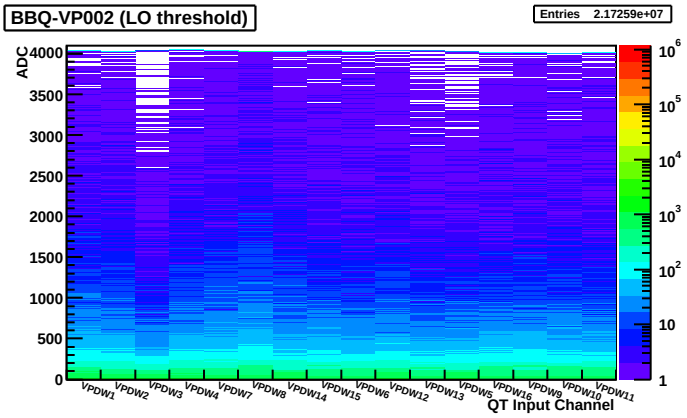

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.17259e+07
TAC
QT Input Channel

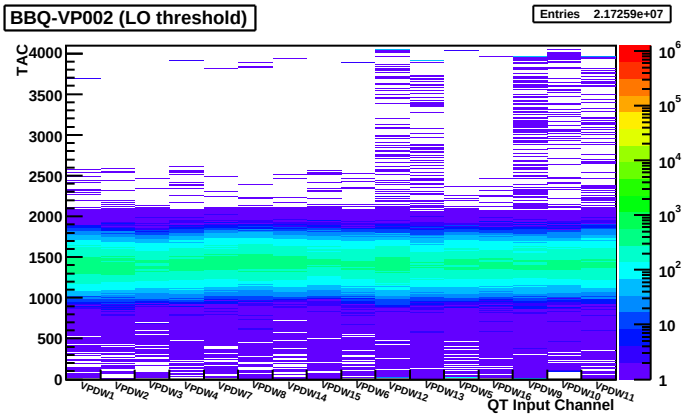

BBQ-ZD001 (ZDC TOWER)
Entries 2.17259e+07
ADC

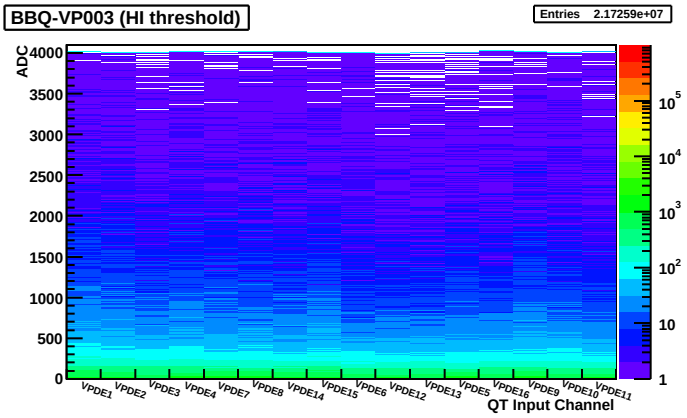

BBQ-ZD001 (ZDC TOWER)
Entries 2.17259e+07
TAC

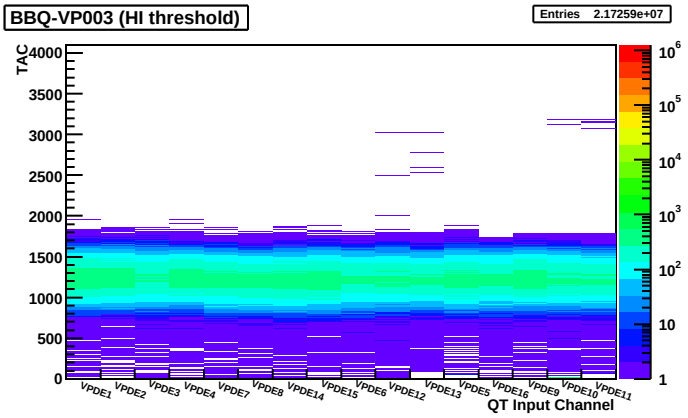

BBQ-VP001 (LO threshold)
Entries 2.17259e+07
ADC
QT Input Channel

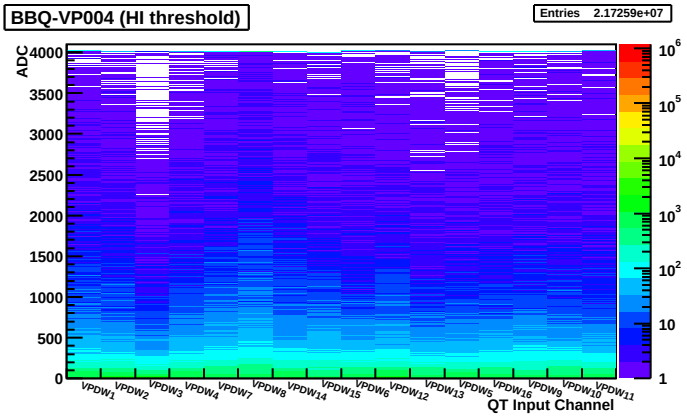

BBQ-VP001 (LO threshold)
Entries 2.17259e+07
TAC
QT Input Channel

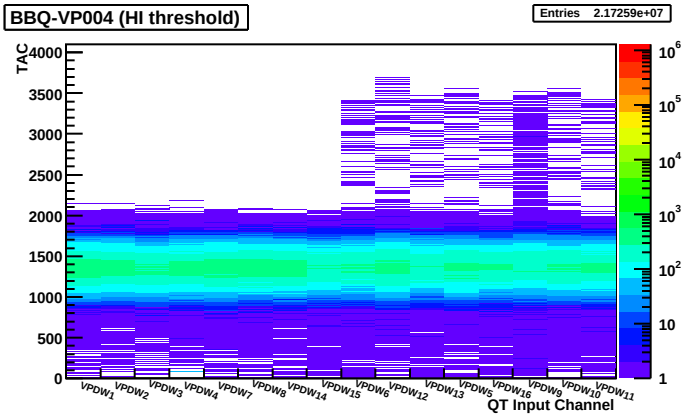

BBQ-VP002 (LO threshold)
Entries 2.17259e+07
ADC
QT Input Channel

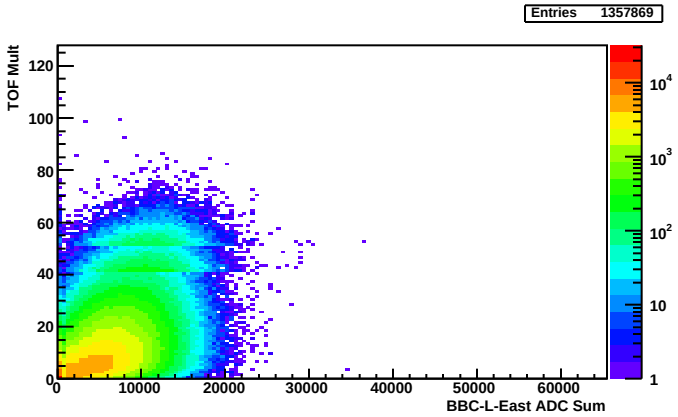

BBQ-VP002 (LO threshold)
Entries 2.17259e+07
TAC
QT Input Channel

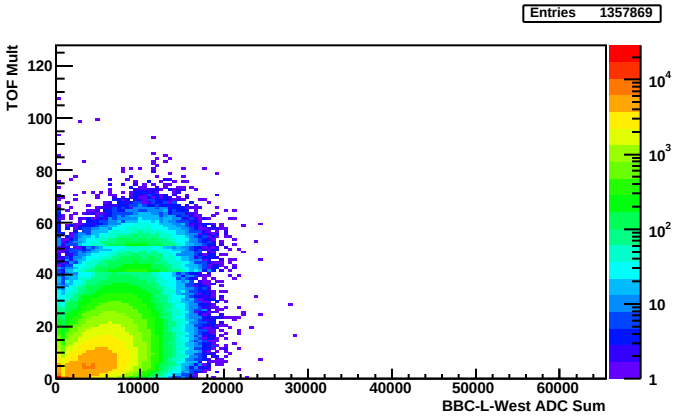

BBQ-VP003 (HI threshold)
Entries 2.17259e+07
ADC
QT Input Channel

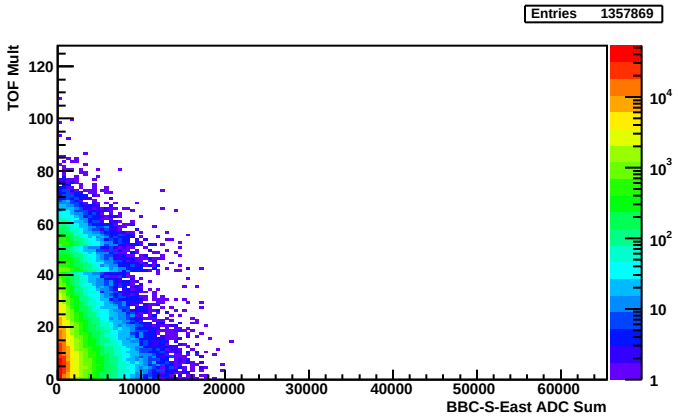

BBQ-VP003 (HI threshold)
Entries 2.17259e+07
TAC
QT Input Channel

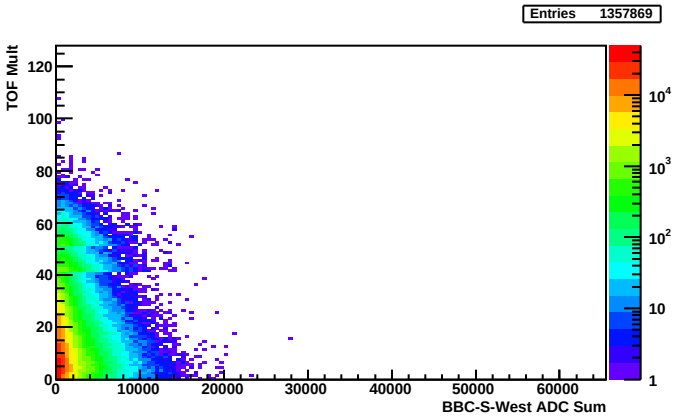

BBQ-VP004 (HI threshold)
Entries 2.17259e+07
ADC
QT Input Channel

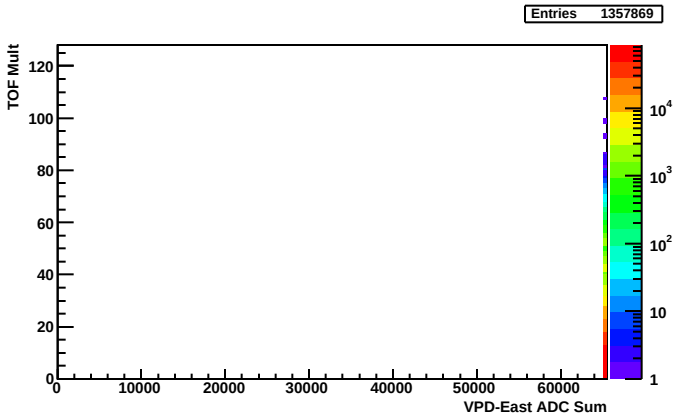

BBQ-VP004 (HI threshold)
Entries 2.17259e+07
TAC
QT Input Channel

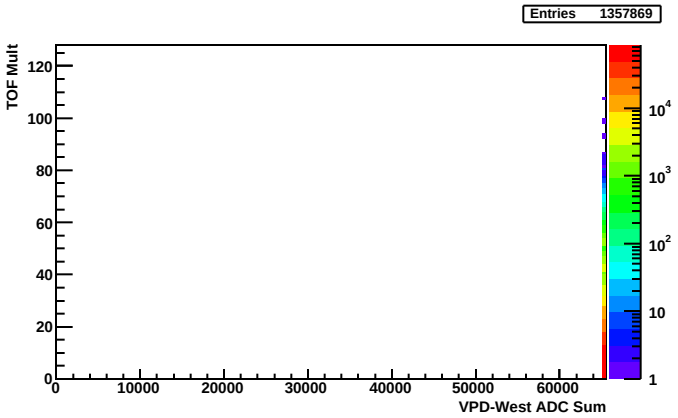

Entries 1357869
TOF Mult
BBC-L-East ADC Sum

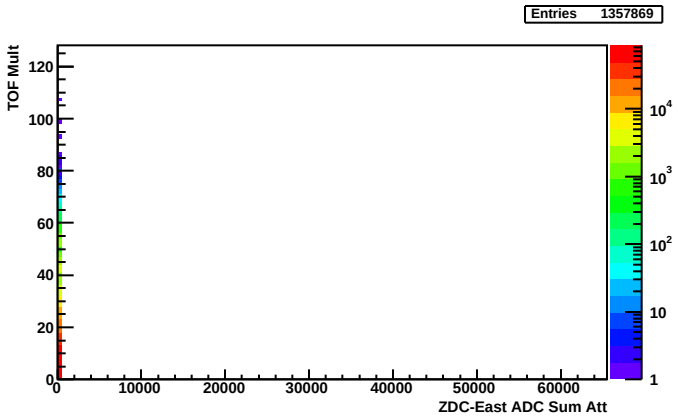

Entries 1357869
TOF Mult
BBC-L-West ADC Sum

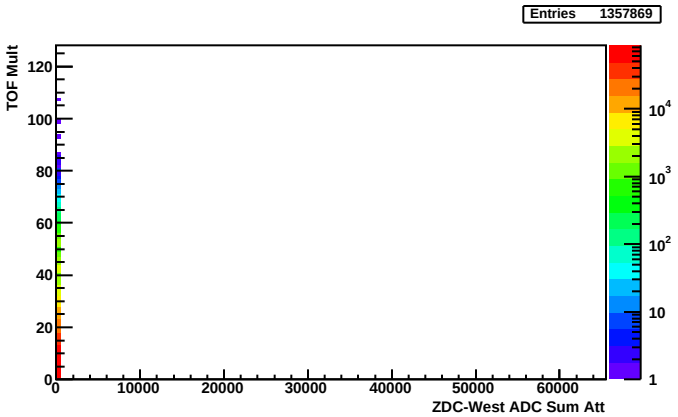

Entries 1357869
TOF Mult
BBC-S-East ADC Sum

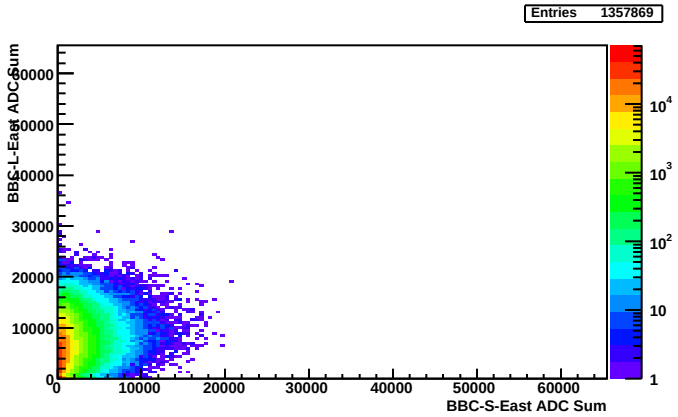

Entries 1357869
TOF Mult
BBC-S-West ADC Sum

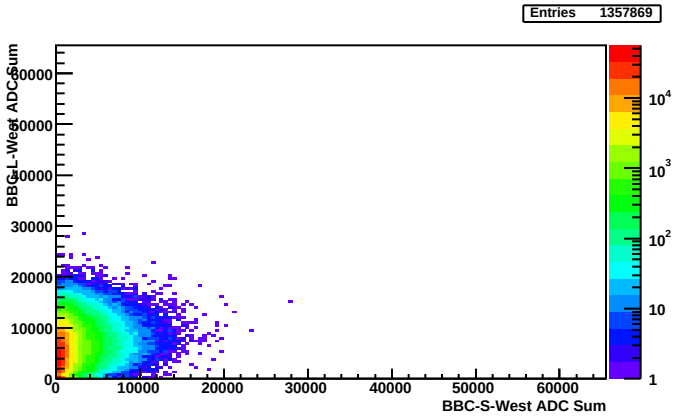

Entries 1357869
TOF Mult
VPD-East ADC Sum

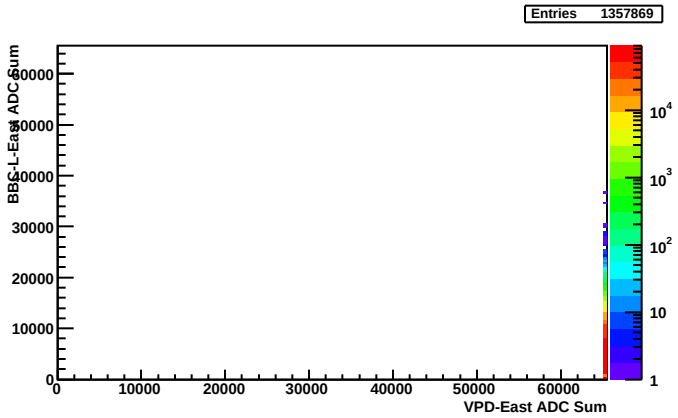

Entries 1357869
TOF Mult
VPD-West ADC Sum

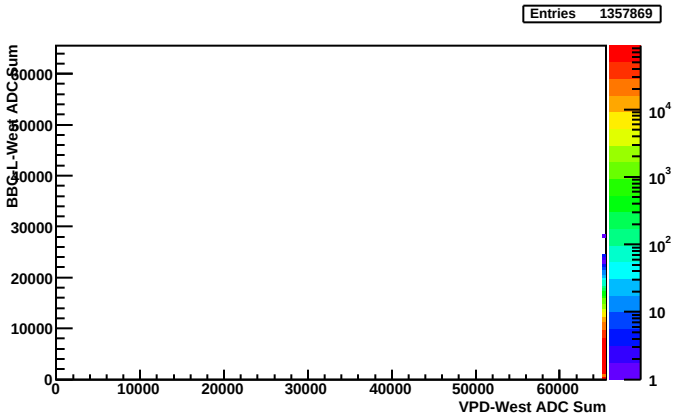

Entries 1357869
TOF Mult
ZDC-East ADC Sum Att

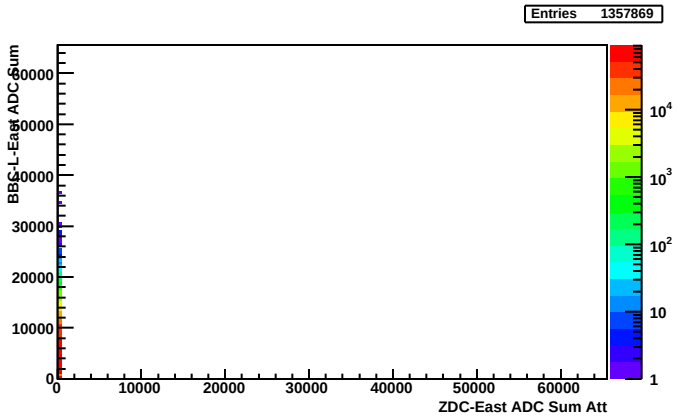

Entries 1357869
TOF Mult
ZDC-West ADC Sum Att

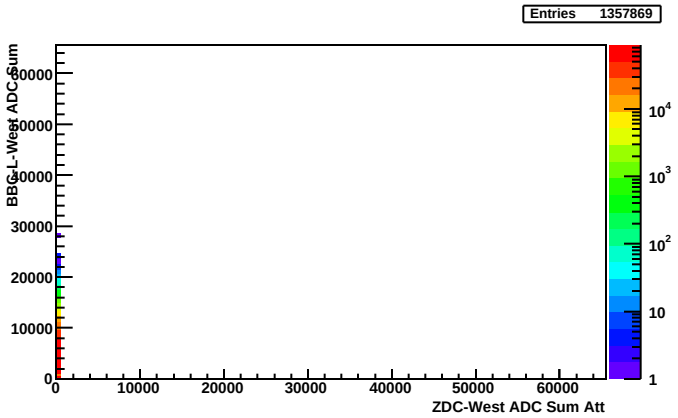

Entries 1357869
BBC-L-East ADC Sum
BBC-S-East ADC Sum

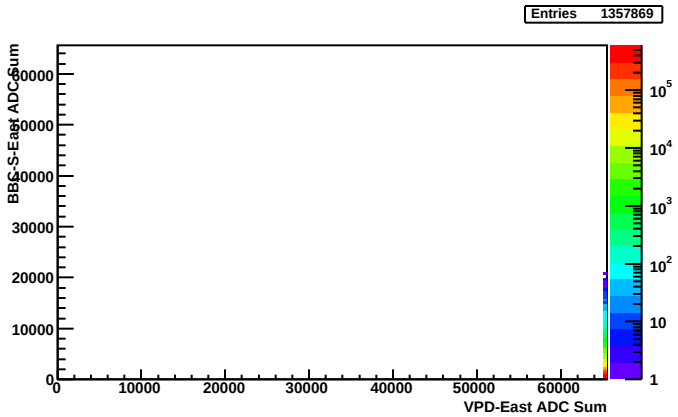

Entries 1357869
BBC-L-West ADC Sum
BBC-S-West ADC Sum

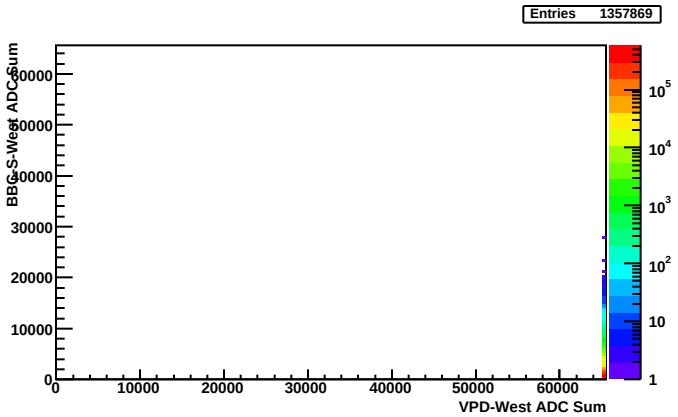

Entries 1357869
BBC-L-East ADC Sum
VPD-East ADC Sum

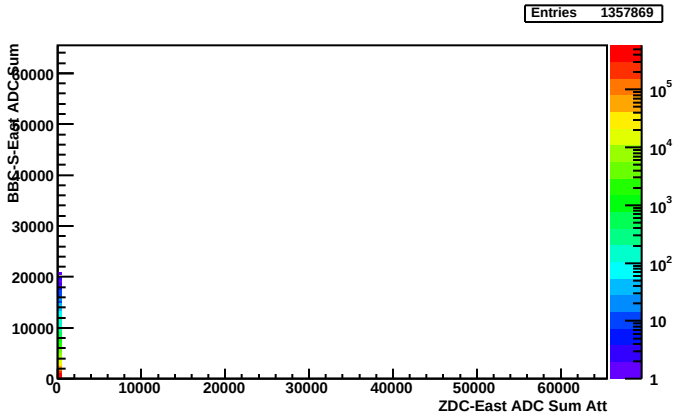

Entries 1357869
BBC-L-West ADC Sum
VPD-West ADC Sum

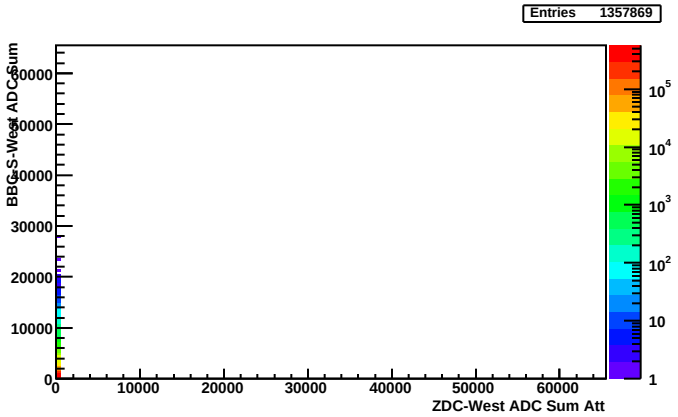

Entries 1357869
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

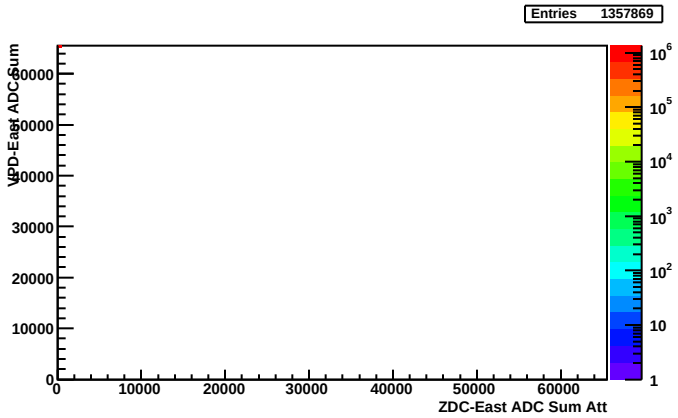

Entries 1357869
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

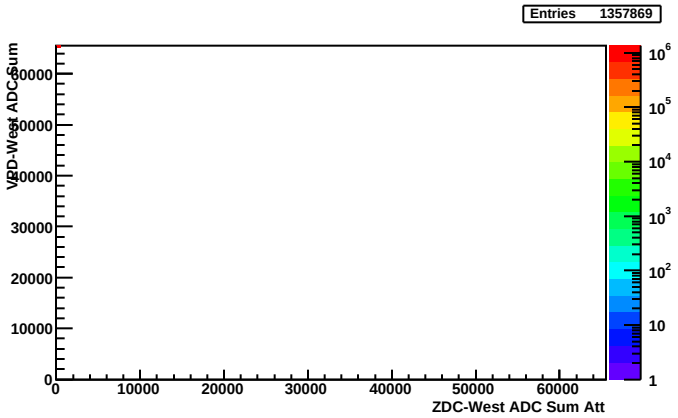

Entries 1357869
BBC-S-East ADC Sum
VPD-East ADC Sum

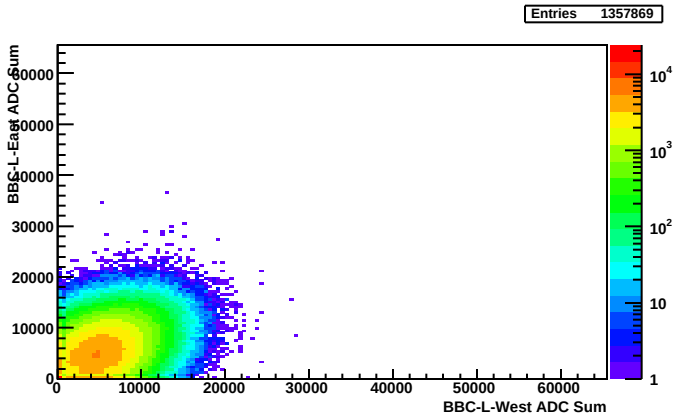

Entries 1357869
BBC-S-West ADC Sum
VPD-West ADC Sum

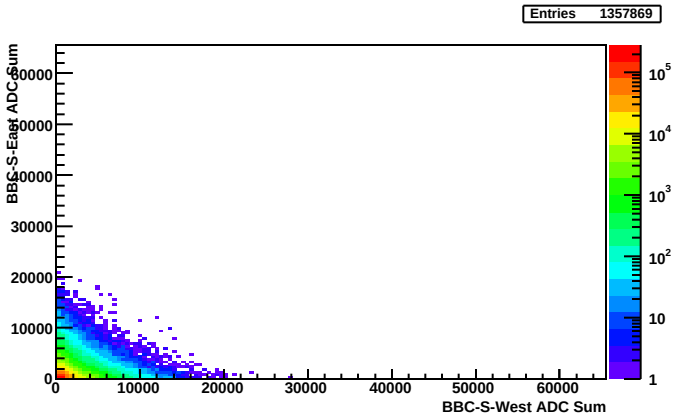

Entries 1357869
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

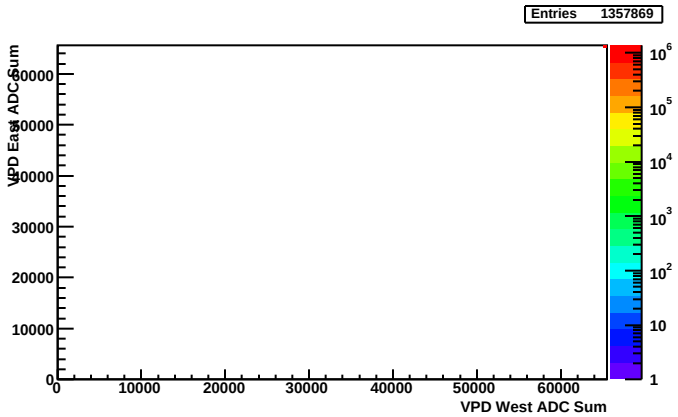

Entries 1357869
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

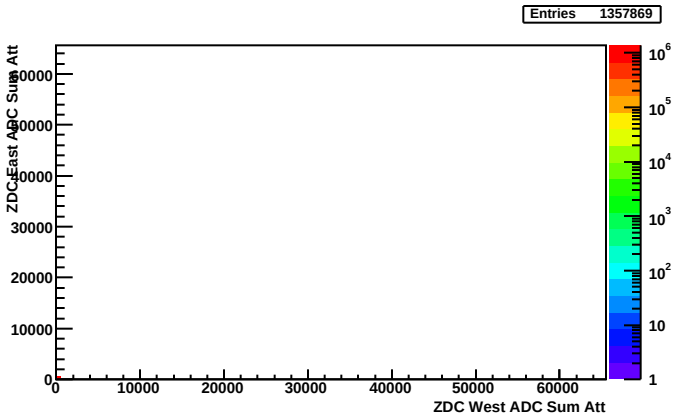

Entries 1357869
VPD-East ADC Sum
ZDC-East ADC Sum Att

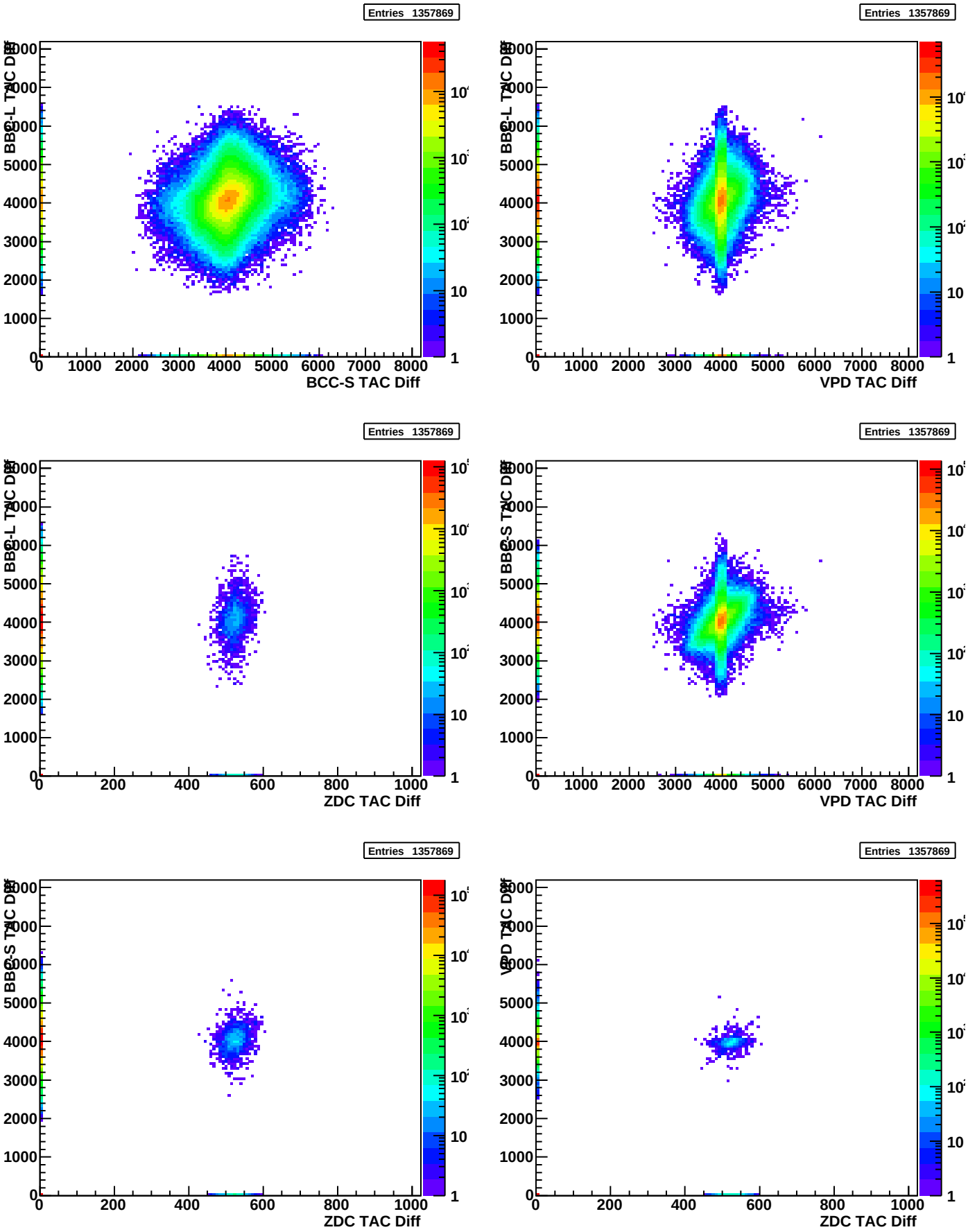

Entries 1357869
VPD-West ADC Sum
ZDC-West ADC Sum Att

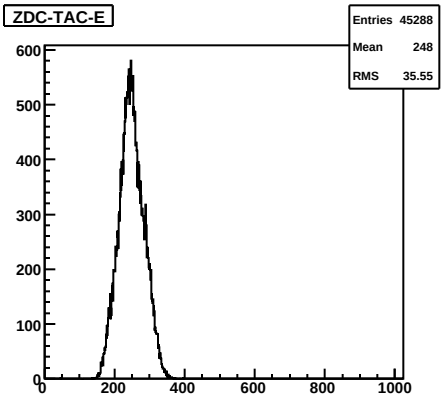

Entries 1357869
BBC-L-East ADC Sum
BBC-L-West ADC Sum

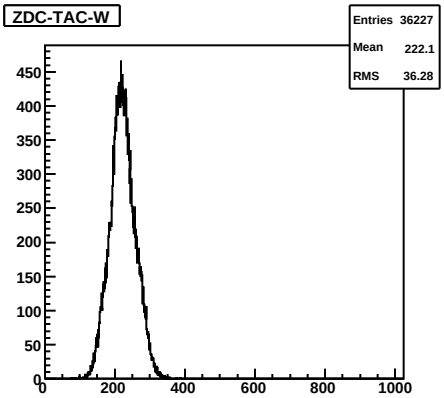

Entries 1357869
BBC-S-East ADC Sum
BBC-S-West ADC Sum

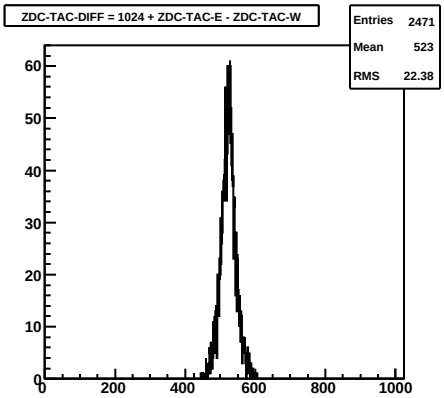

Entries 1357869
VPD East ADC Sum
VPD West ADC Sum

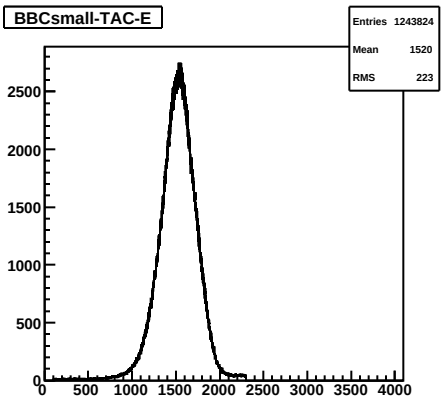

Entries 1357869
ZDC East ADC Sum Att
ZDC West ADC Sum Att

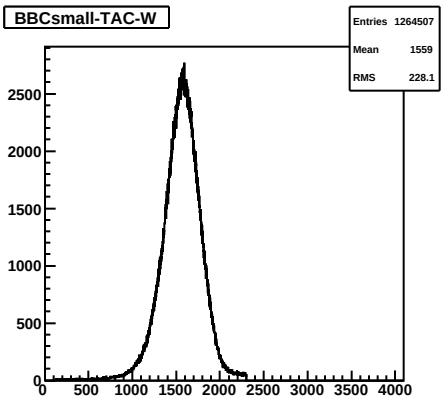

Entries 1357869
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1357869
BBC-L TAC Diff
VPD TAC Diff
Entries 1357869
BBC-L TAC Diff
ZDC TAC Diff
Entries 1357869
BBC-S TAC Diff
VPD TAC Diff
Entries 1357869
BBC-S TAC Diff
ZDC TAC Diff
Entries 1357869
VPD TAC Diff
ZDC TAC Diff

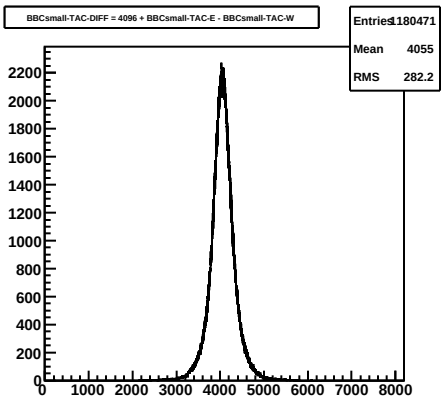

ZDC-TAC-E
Entries 45288
Mean 248
RMS 35.55

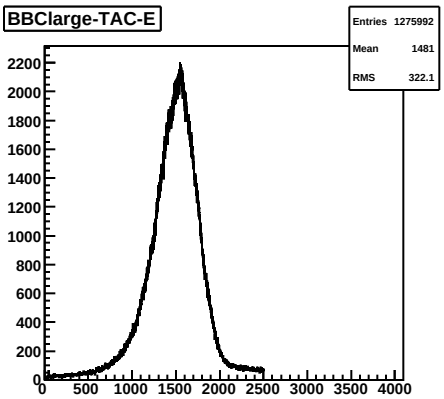

ZDC-TAC-W
Entries 36227
Mean 222.1
RMS 36.28

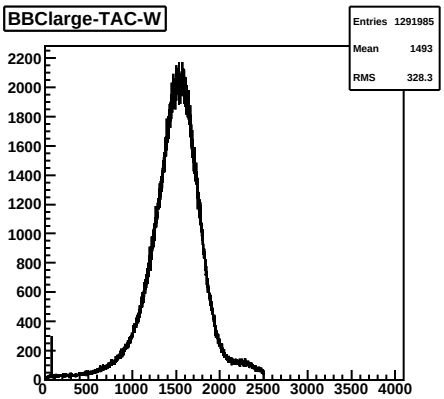

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2471
Mean 523
RMS 22.38

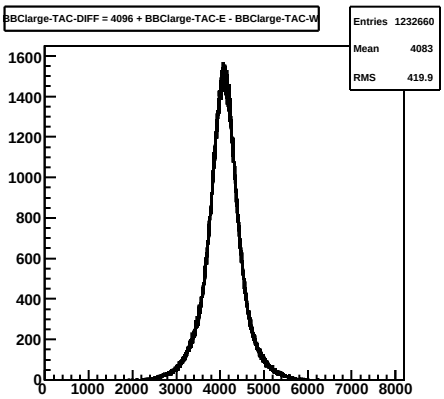

BBCsmall-TAC-E
Entries 1243824
Mean 1520
RMS 223

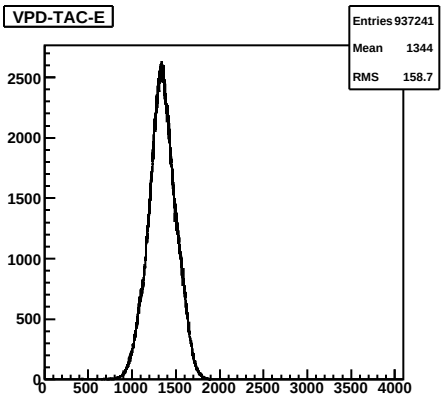

BBCsmall-TAC-W
Entries 1264507
Mean 1559
RMS 228.1

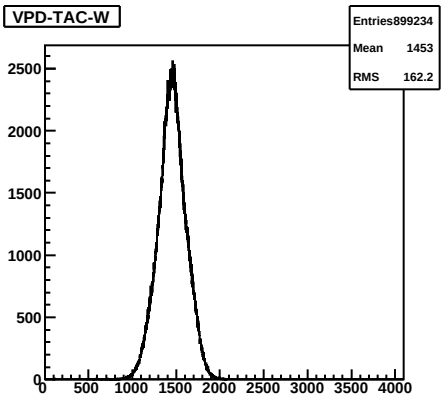

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1180471
Mean 4055
RMS 282.2

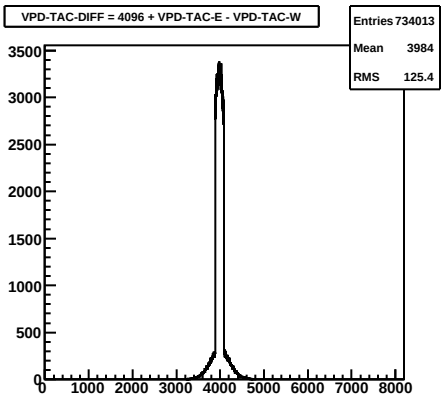

BBClarge-TAC-E
Entries 1275992
Mean 1481
RMS 322.1

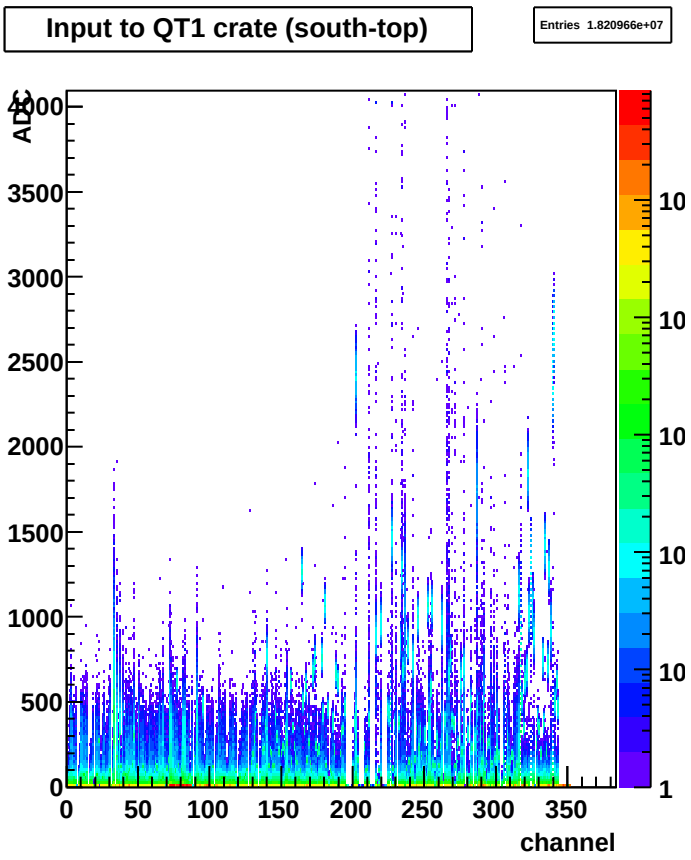

BBClarge-TAC-W
Entries 1291985
Mean 1493
RMS 328.3

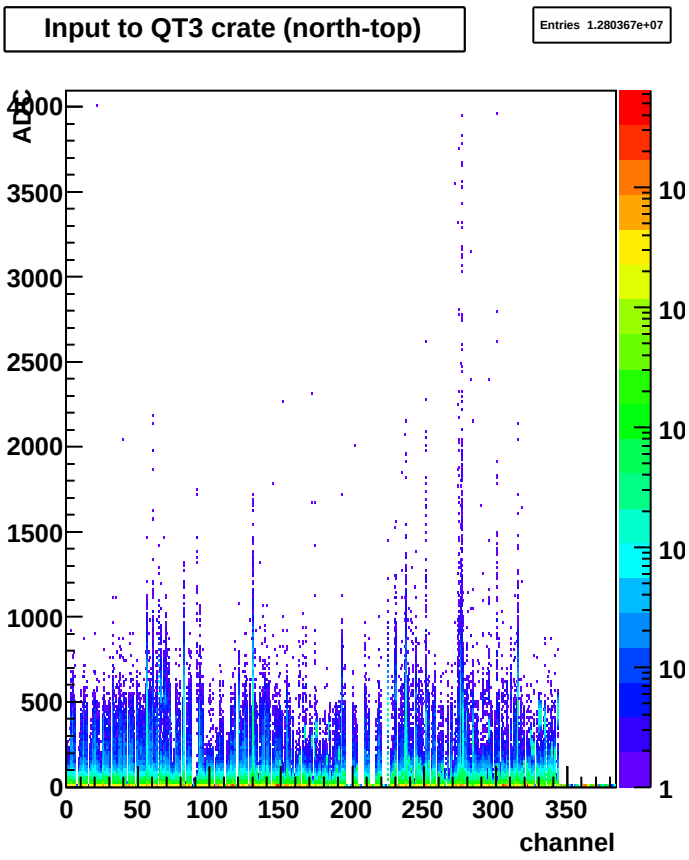

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1232660
Mean 4083
RMS 419.9

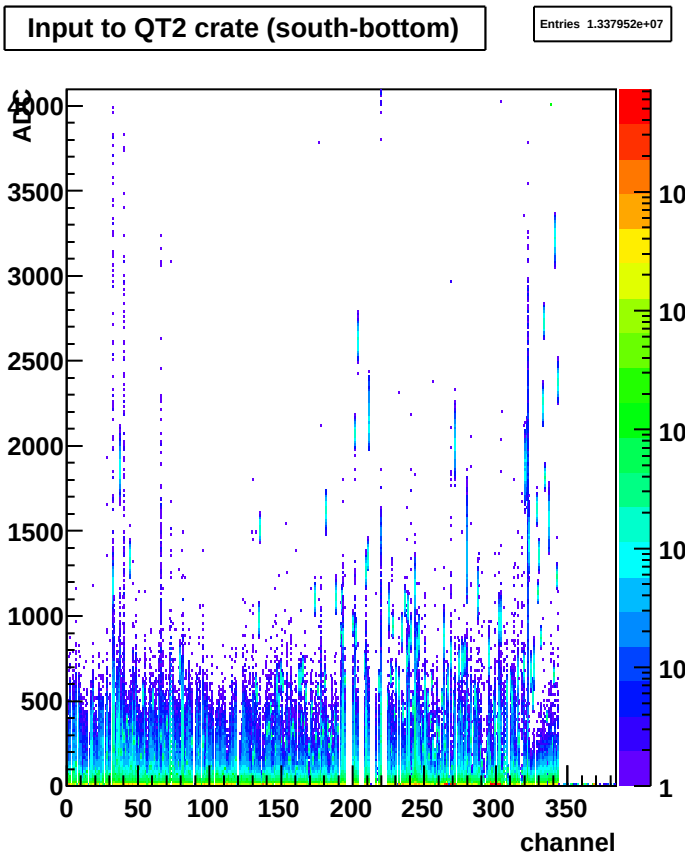

VPD-TAC-E
Entries 937241
Mean 1344
RMS 158.7

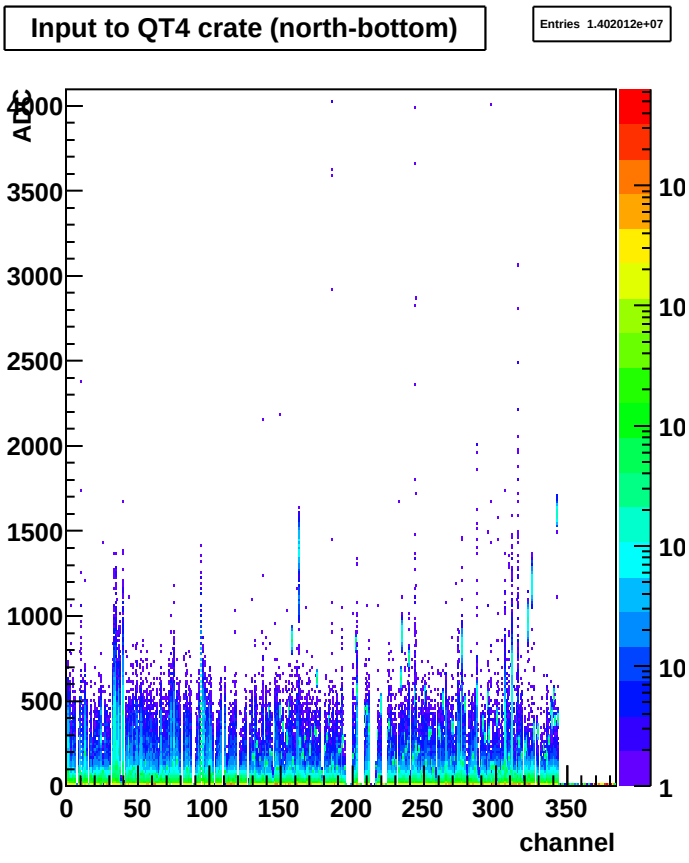

VPD-TAC-W
Entries 899234
Mean 1453
RMS 162.2

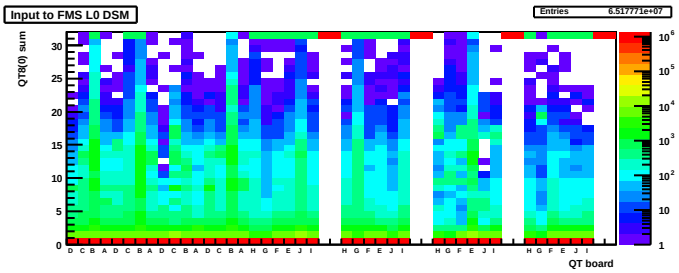

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 734013
Mean 3984
RMS 125.4

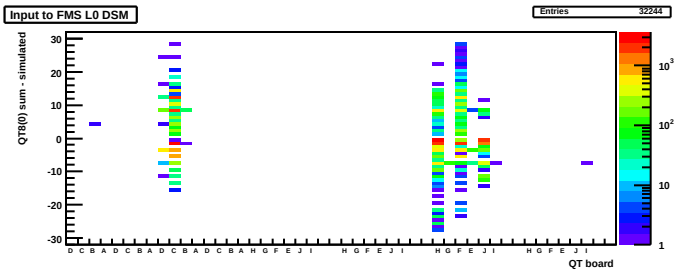

Input to QT1 crate (south-top)
Entries 1.820966e+07
ADC
channel

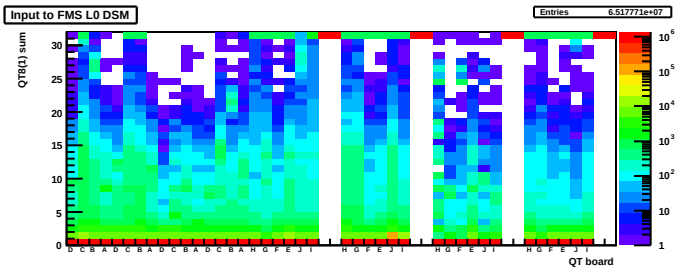

Input to QT3 crate (north-top)
Entries 1.280367e+07
ADC
channel

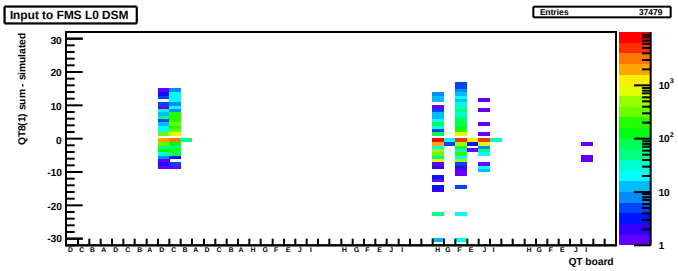

Input to QT2 crate (south-bottom)
Entries 1.337952e+07
ADC
channel

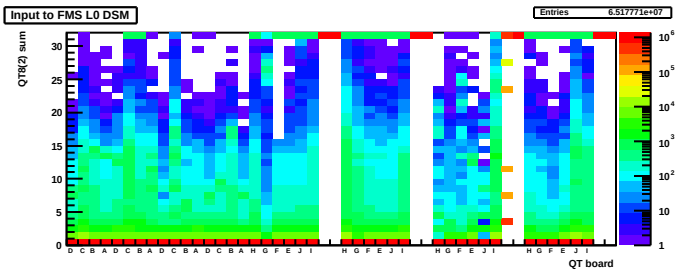

Input to QT4 crate (north-bottom)
Entries 1.402012e+07
ADC
channel

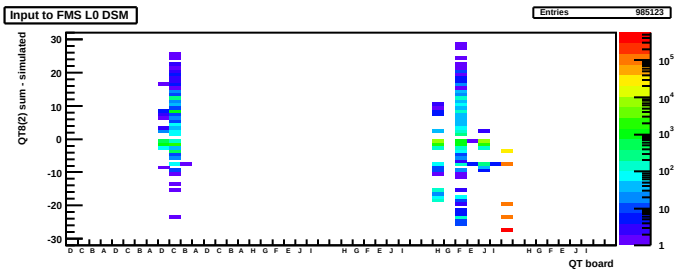

Input to FMS L0 DSM
Entries 6.517771e+07
QT8(0) sum
QT board

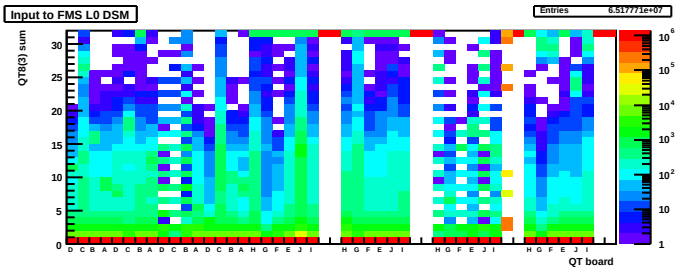

Input to FMS L0 DSM
Entries 32244
QT8(0) sum - simulated
QT board

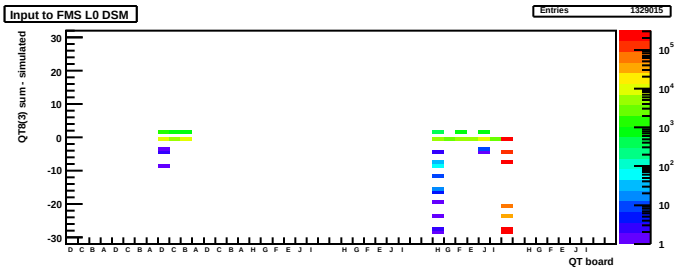

Input to FMS L0 DSM
Entries 6.517771e+07
QT8(1) sum
QT board

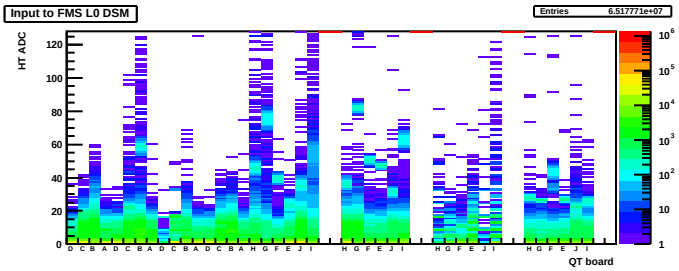

Input to FMS L0 DSM
Entries 37479
QT8(1) sum - simulated
QT board

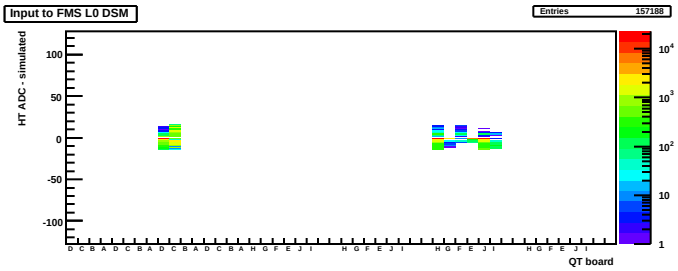

Input to FMS L0 DSM
Entries 6.517771e+07
QT8(2) sum
QT board

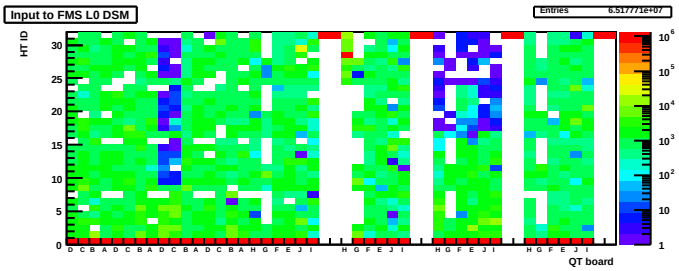

Input to FMS L0 DSM
Entries 985123
QT8(2) sum - simulated
QT board

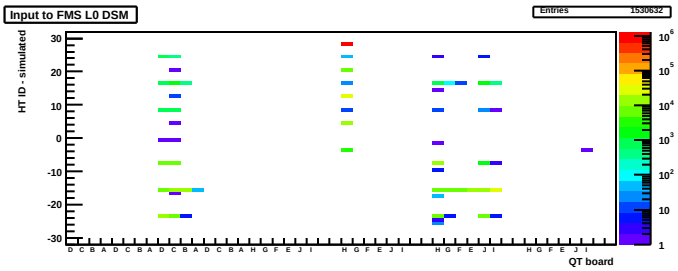

Input to FMS L0 DSM
Entries 6.517771e+07
QT8(3) sum
QT board

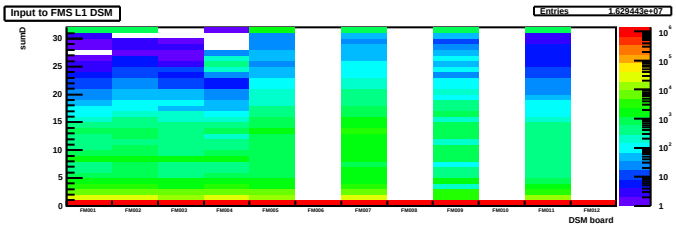

Input to FMS L0 DSM
Entries 1329015
QT8(3) sum - simulated
QT board

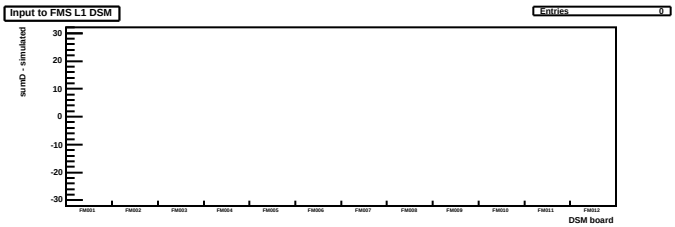

Input to FMS L0 DSM
Entries 6.517771e+07
HT ADC
QT board

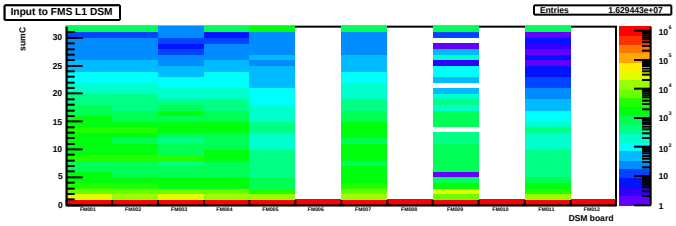

Input to FMS L0 DSM
Entries 157188
HT ADC - simulated
QT board

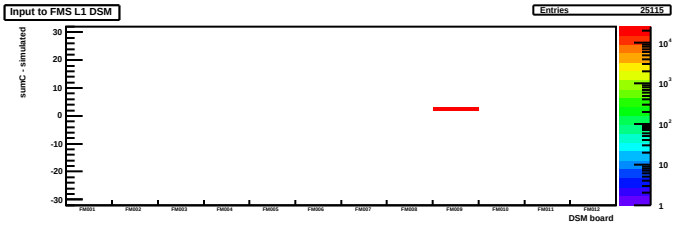

Input to FMS L0 DSM
Entries 6.517771e+07
HT ID
QT board

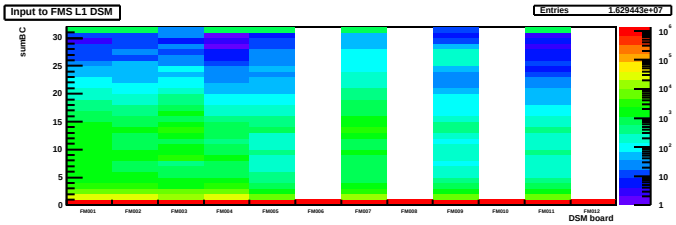

Input to FMS L0 DSM
Entries 1530632
HT ID - simulated
QT board

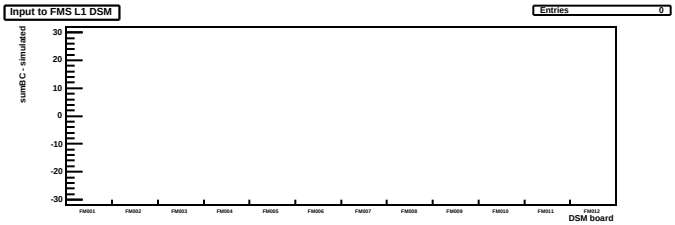

Input to FMS L1 DSM
Entries 1.629443e+07
sumD
DSM board

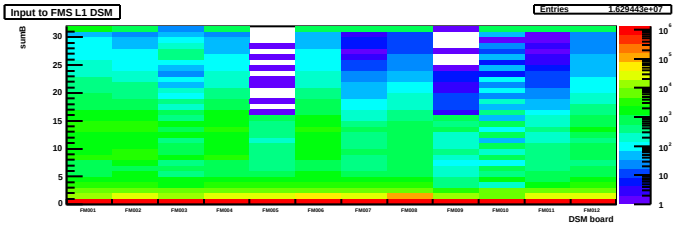

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

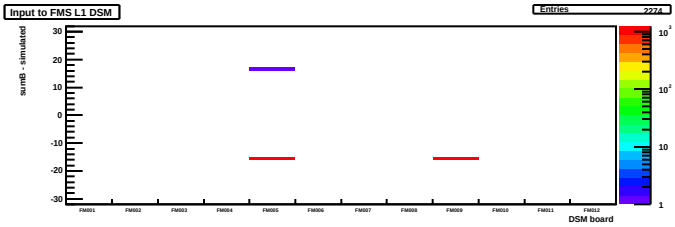

Input to FMS L1 DSM
Entries 1.629443e+07
sumC
DSM board

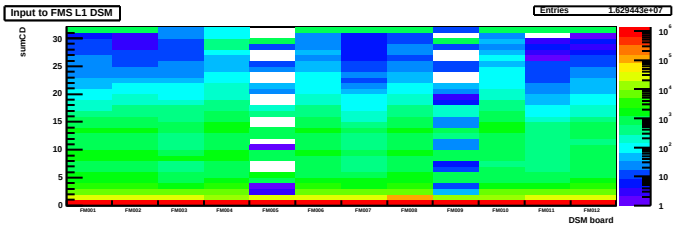

Input to FMS L1 DSM
Entries 25115
sumC - simulated
DSM board

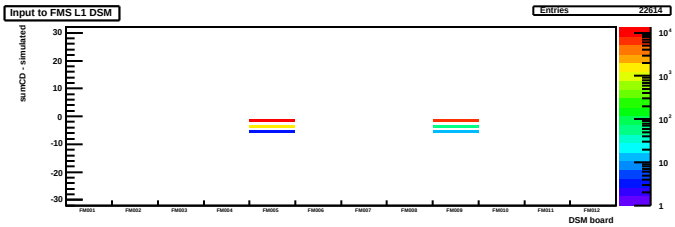

Input to FMS L1 DSM
Entries 1.629443e+07
sumBC
DSM board

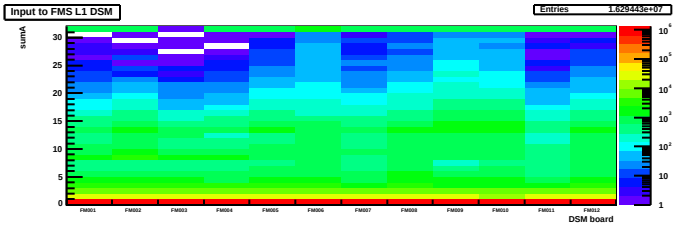

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

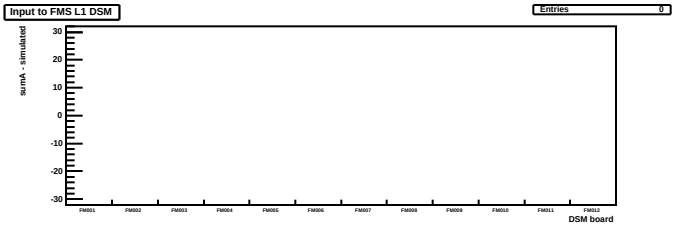

Input to FMS L1 DSM
Entries 1.629443e+07
sumB
DSM board

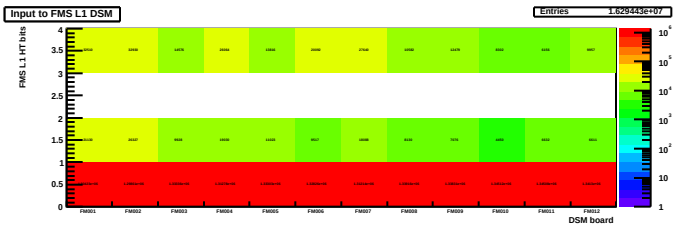

Input to FMS L1 DSM
Entries 2274
sumB - simulated
DSM board

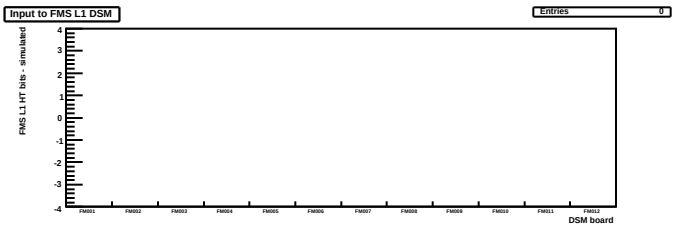

Input to FMS L1 DSM
Entries 1.629443e+07
sumCD
DSM board

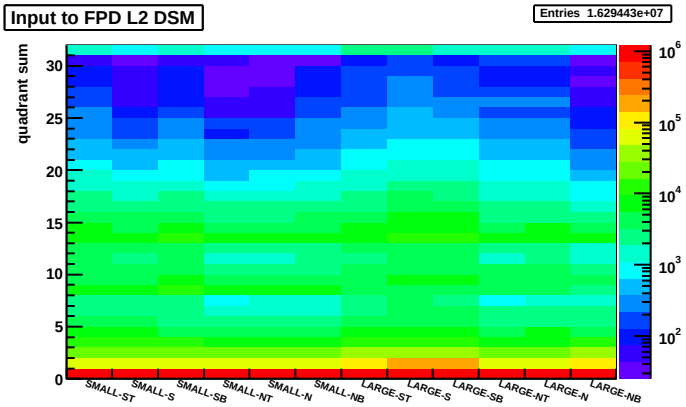

Input to FMS L1 DSM
Entries 22614
sumCD - simulated
DSM board

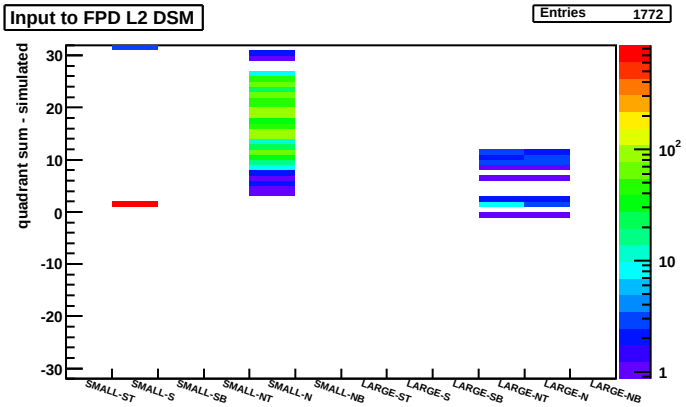

Input to FMS L1 DSM
Entries 1.629443e+07
sumA
DSM board

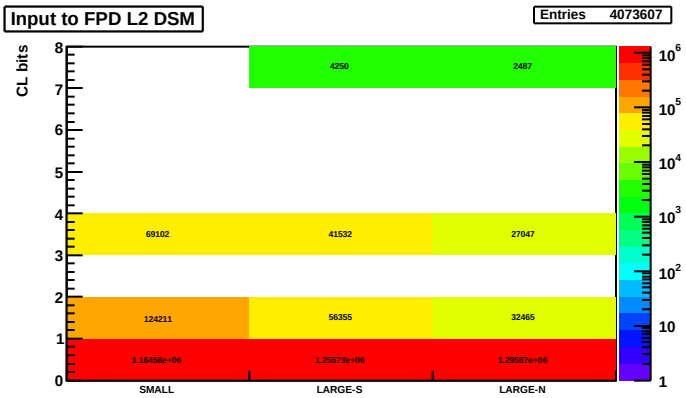

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

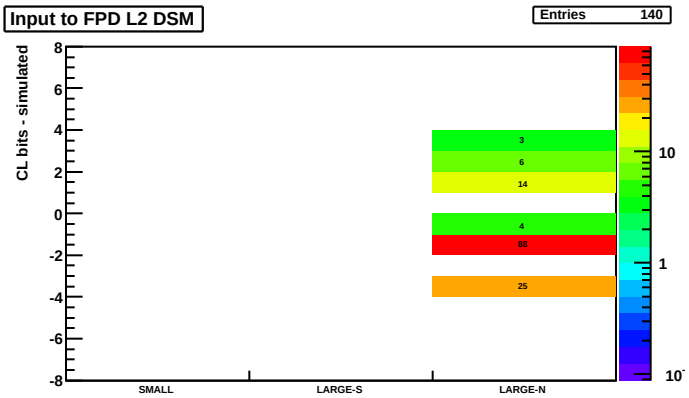

Input to FMS L1 DSM
Entries 1.629443e+07
FMS L1 HT bits
DSM board

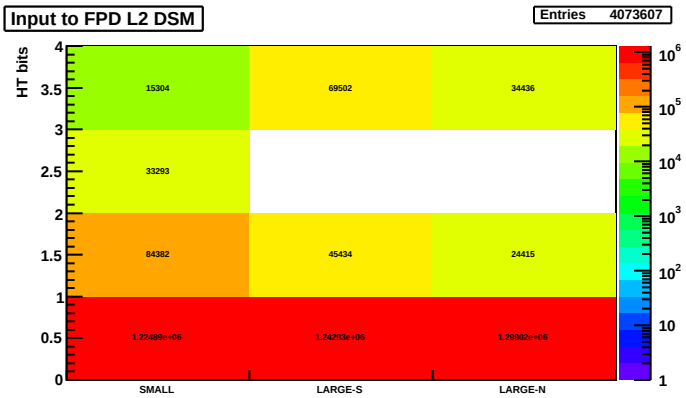

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

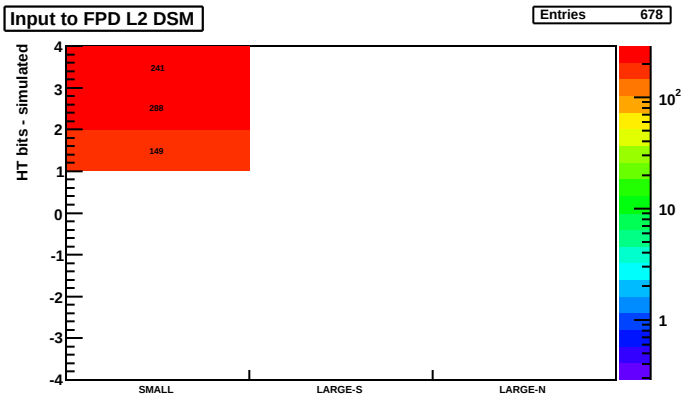

Input to FPD L2 DSM
Entries 1.629443e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

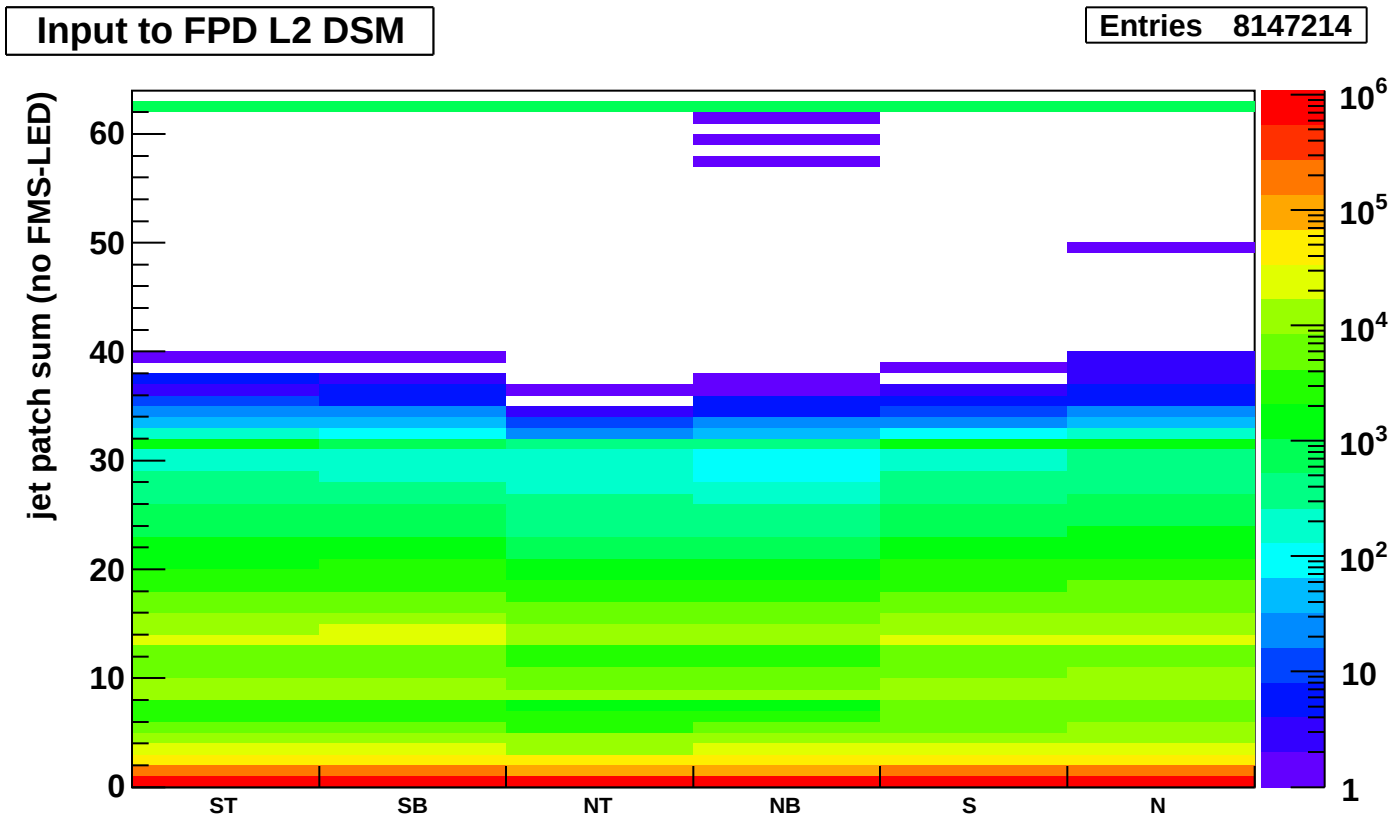

Input to FPD L2 DSM
Entries 1772
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

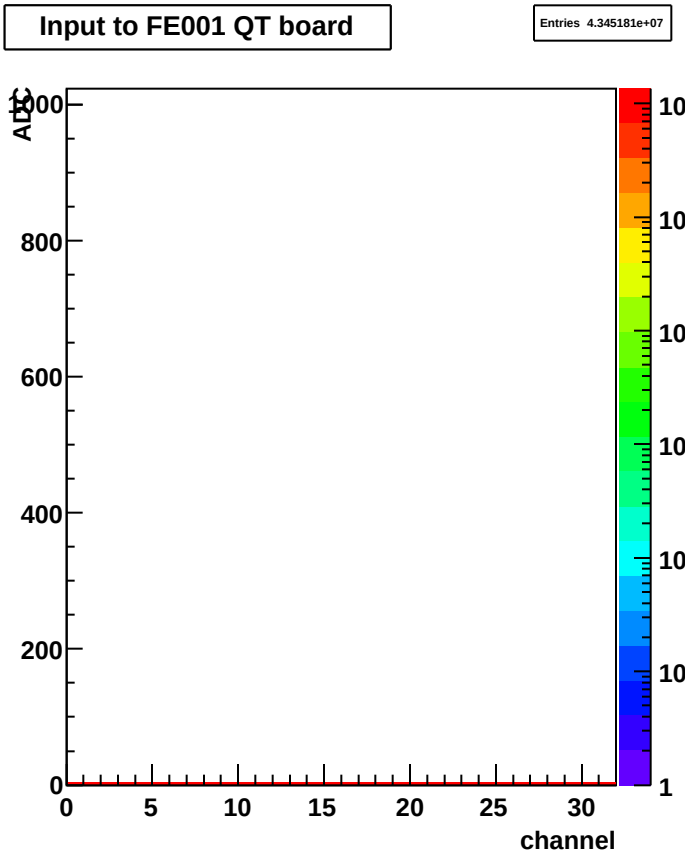

Input to FPD L2 DSM
Entries 4073607
CL bits
4250
2487
69102
41532
27047
124211
56355
32465
1.16456e+06
1.25573e+06
1.29587e+06
SMALL
LARGE-S
LARGE-N

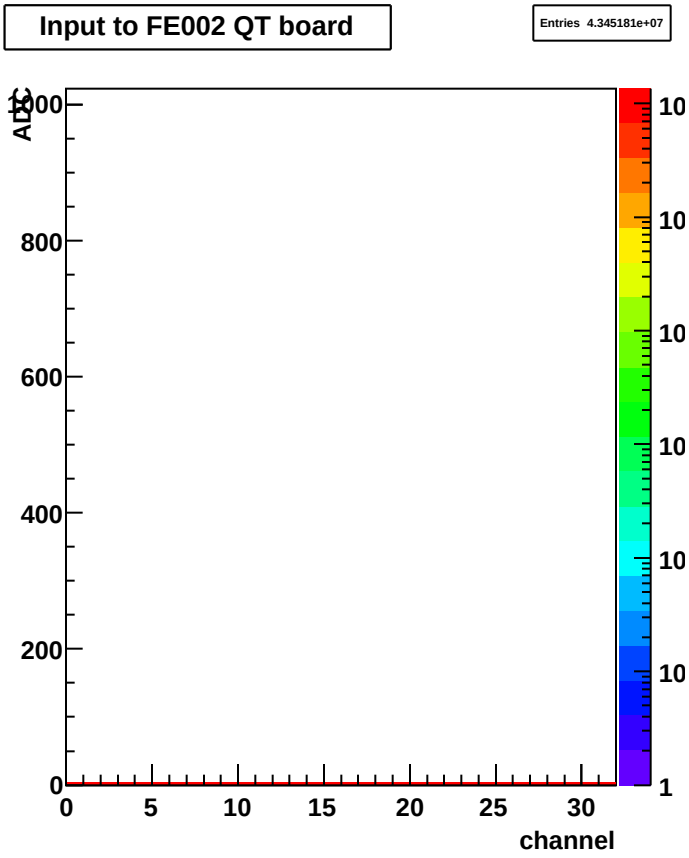

Input to FPD L2 DSM
Entries 140
CL bits - simulated
3
6
14
4
88
25
SMALL
LARGE-S
LARGE-N

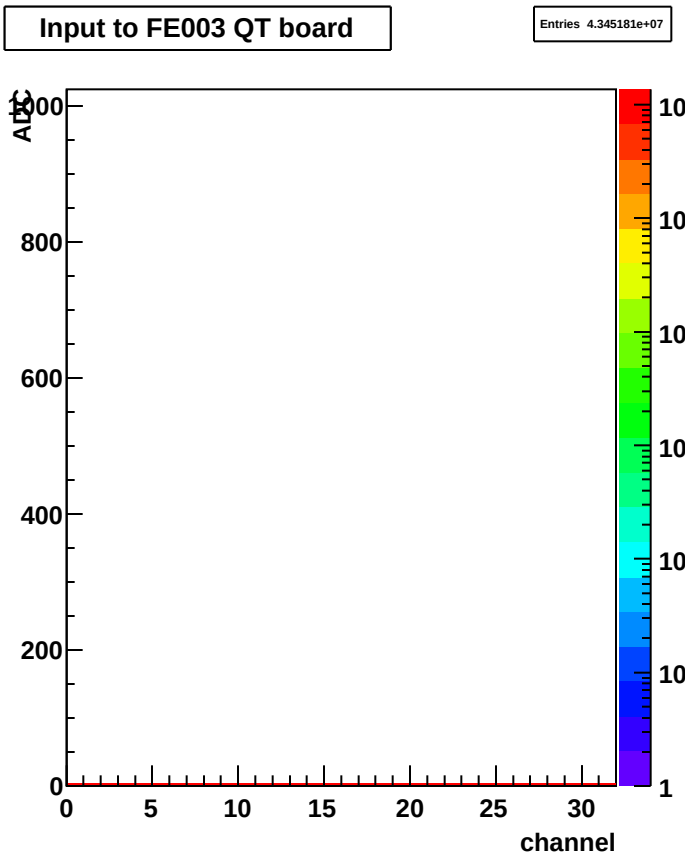

Input to FPD L2 DSM
Entries 4073607
HT bits
15304
69502
34436
33293
84382
45434
24415
1.22489e+06
1.24293e+06
1.29902e+06
SMALL
LARGE-S
LARGE-N

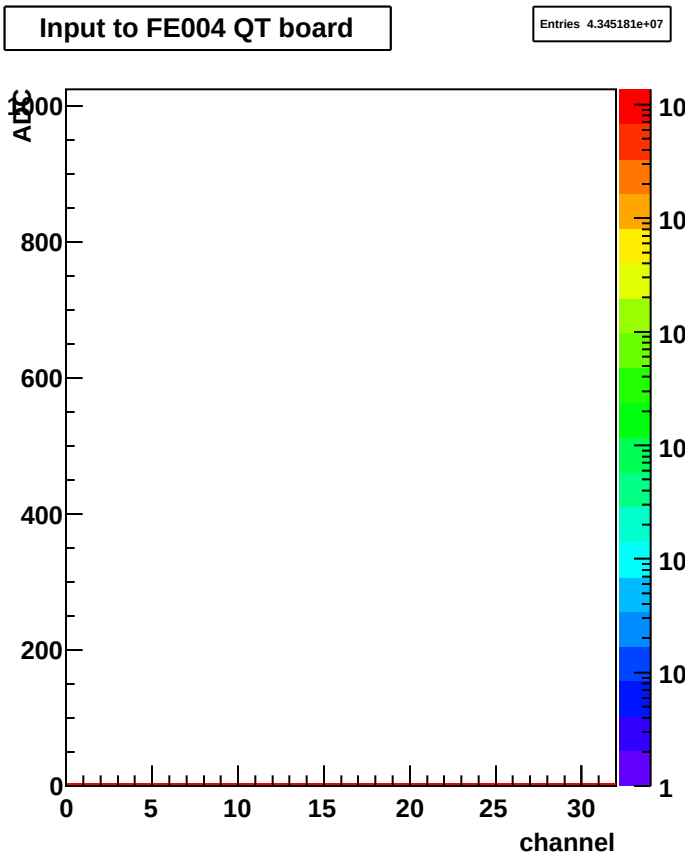

Input to FPD L2 DSM
Entries 678
HT bits - simulated
241
288
149
SMALL
LARGE-S
LARGE-N

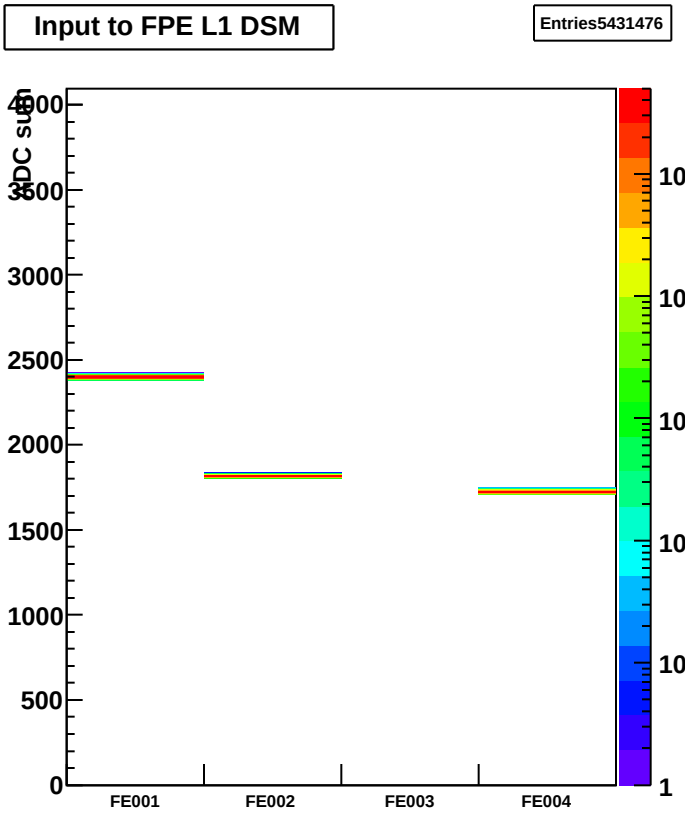

Input to FPD L2 DSM
Entries 8147214
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

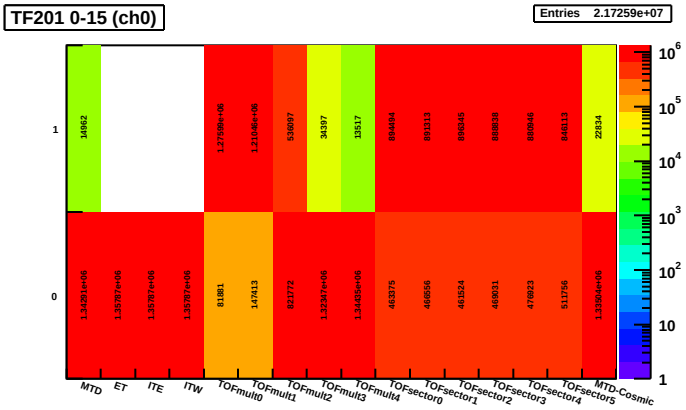

Input to FE001 QT board
Entries 4.345181e+07
ADC
channel

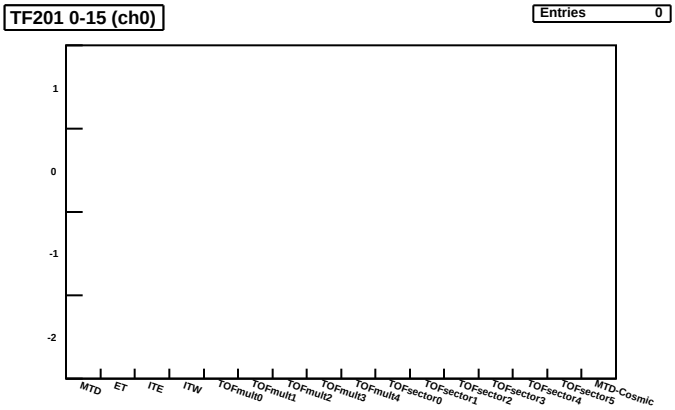

Input to FE002 QT board
Entries 4.345181e+07
ADC
channel

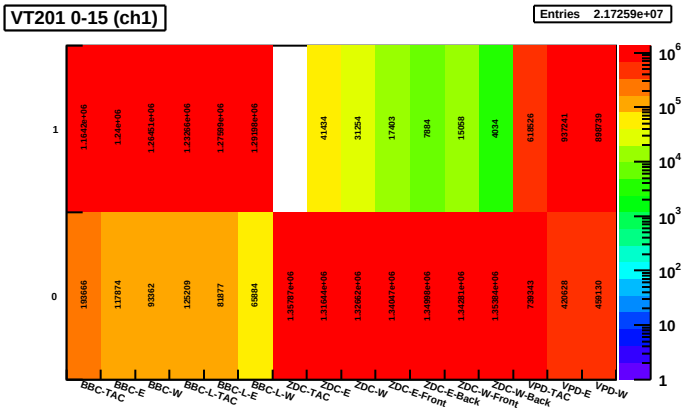

Input to FE003 QT board
Entries 4.345181e+07
ADC
channel


Input to FE004 QT board
Entries 4.345181e+07
ADC
channel


Input to FPE L1 DSM
Entries5431476
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.17259e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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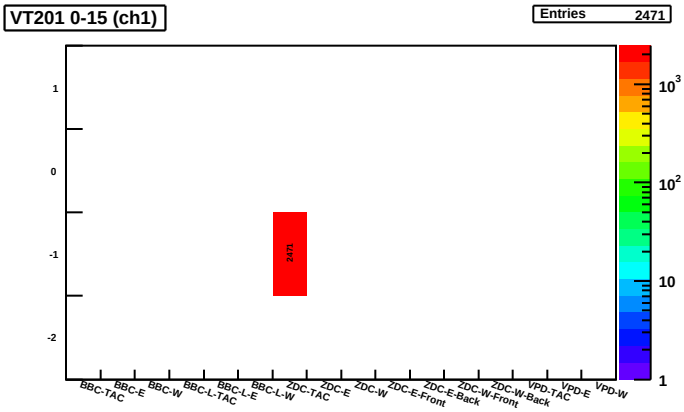
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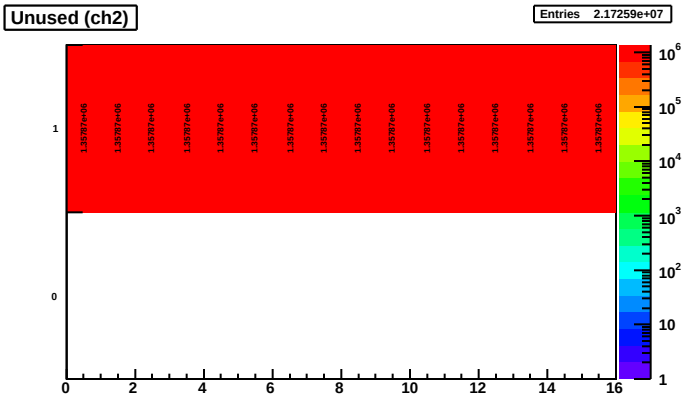
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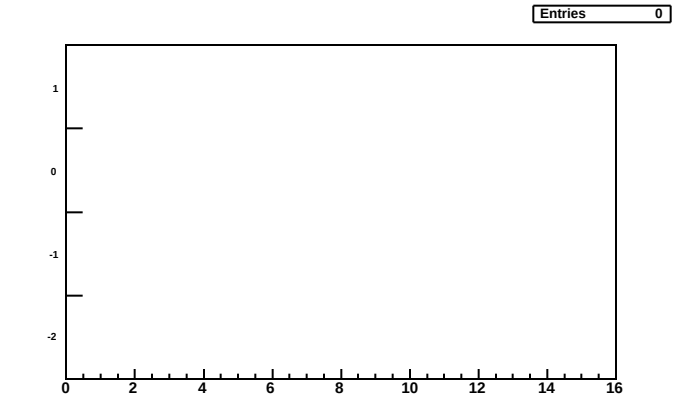
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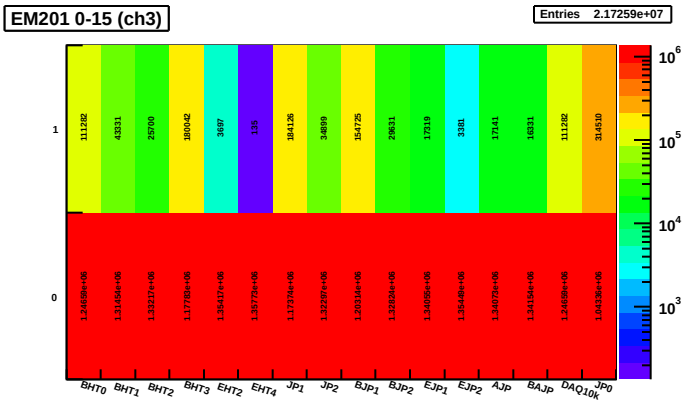
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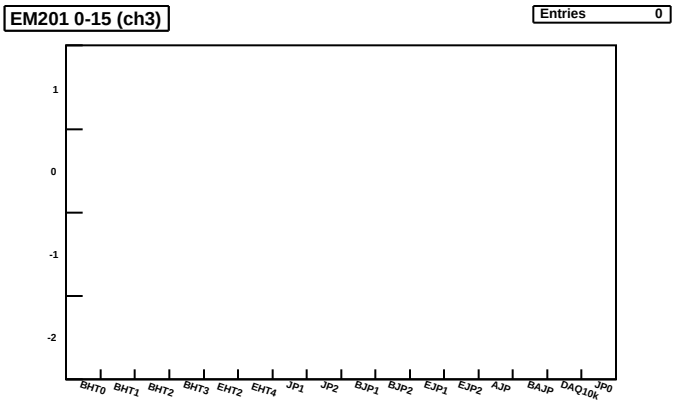
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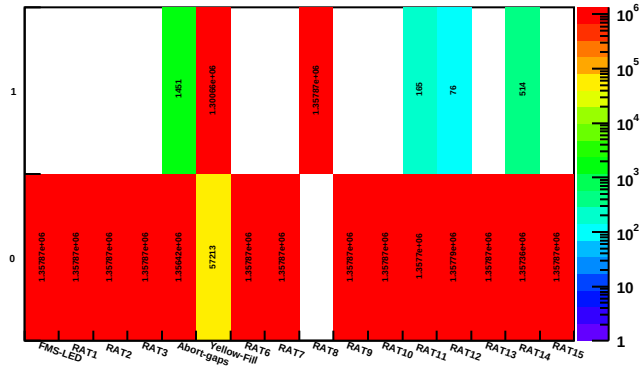
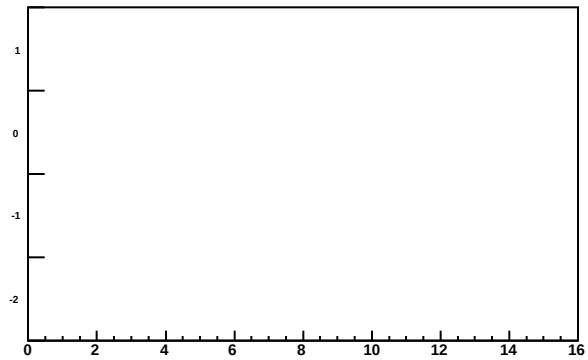
RAT board (ch4)





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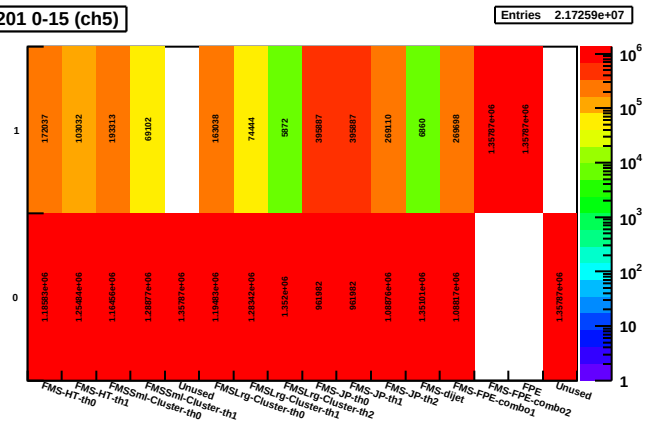

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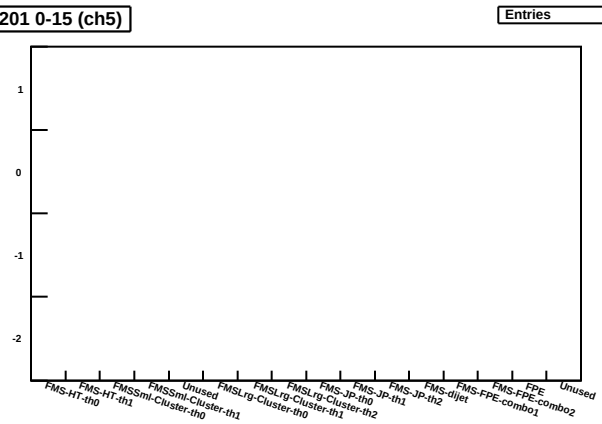
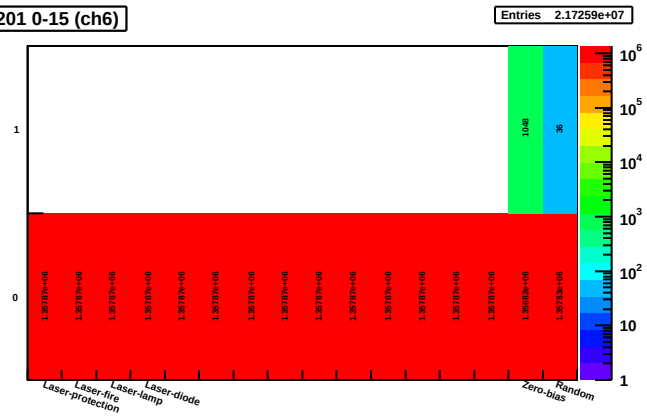
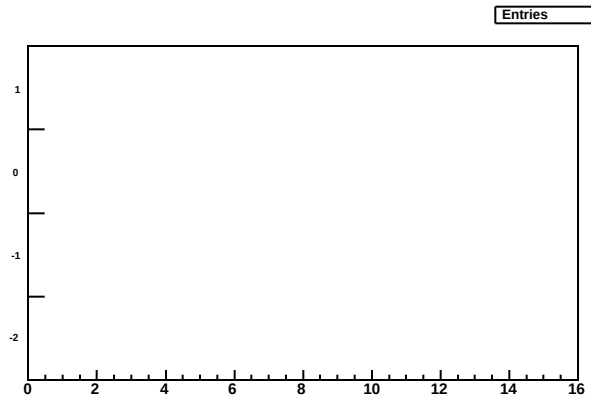
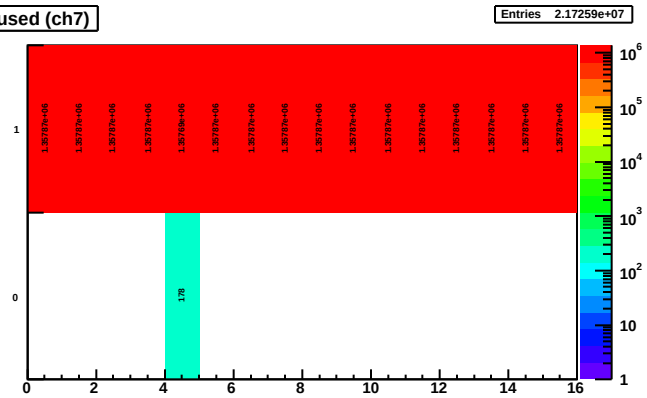
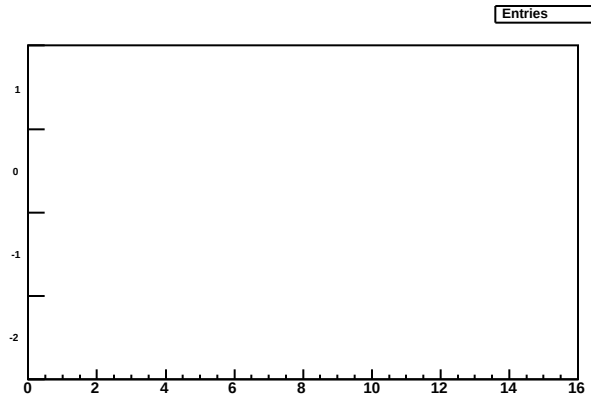


ST201 0-15 (ch6)





Unused (ch7)