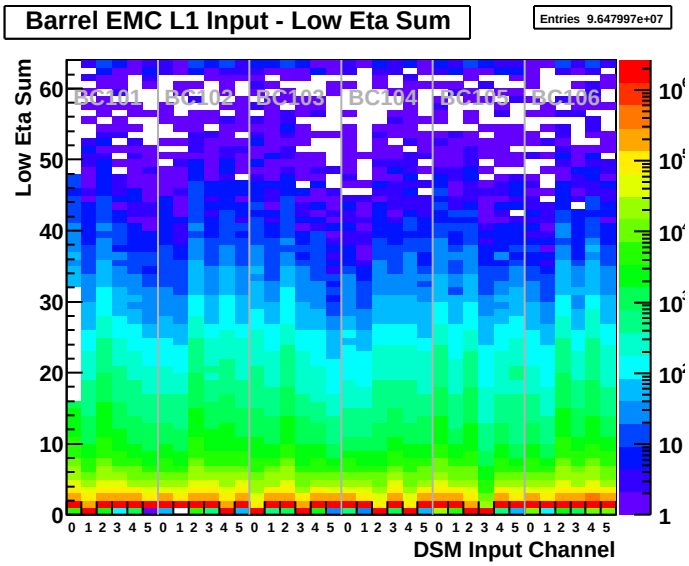
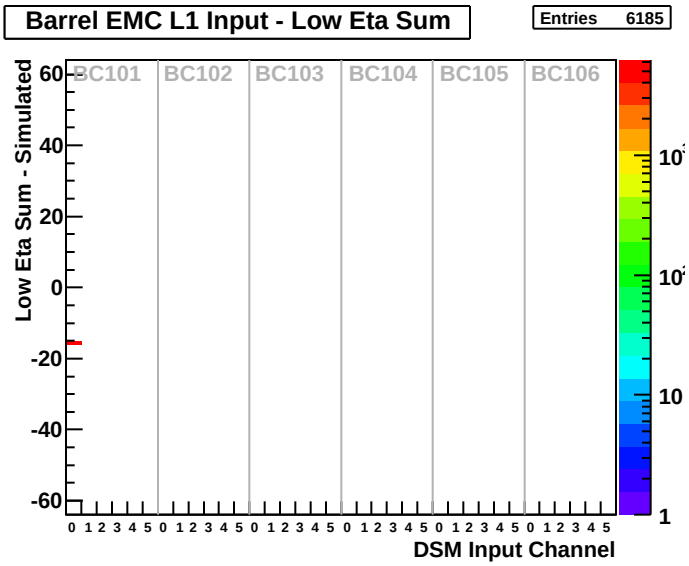
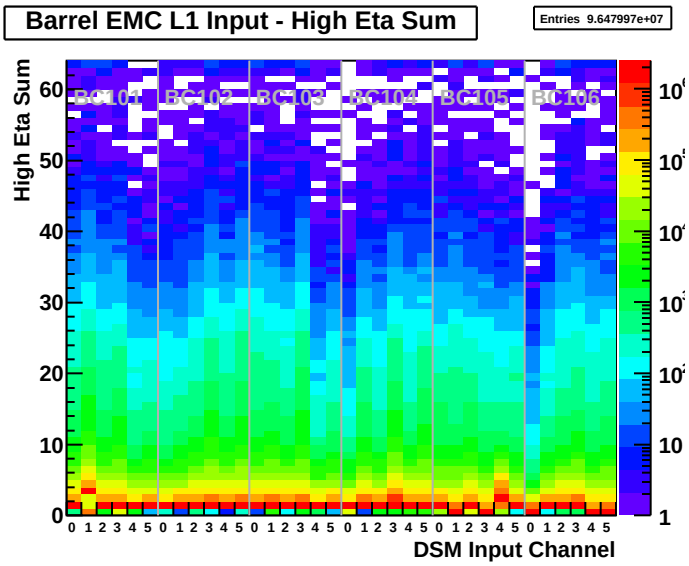
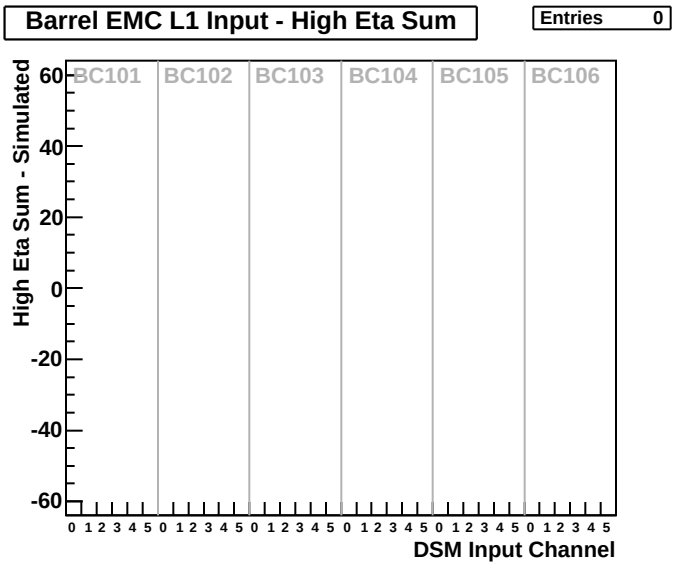
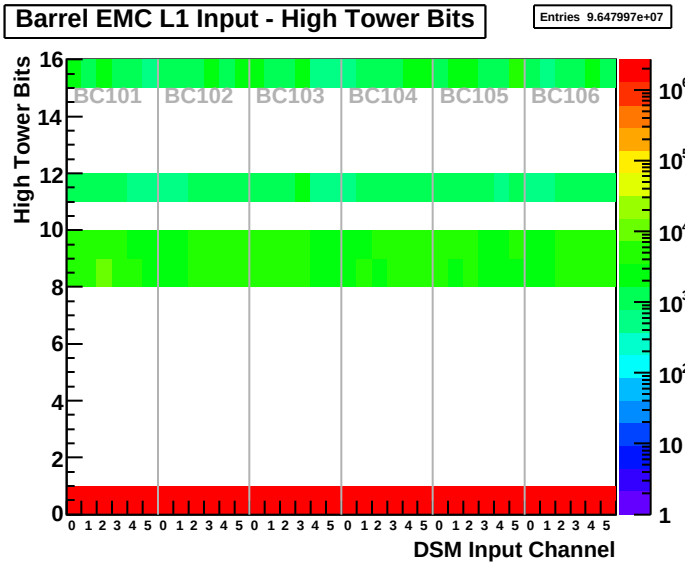
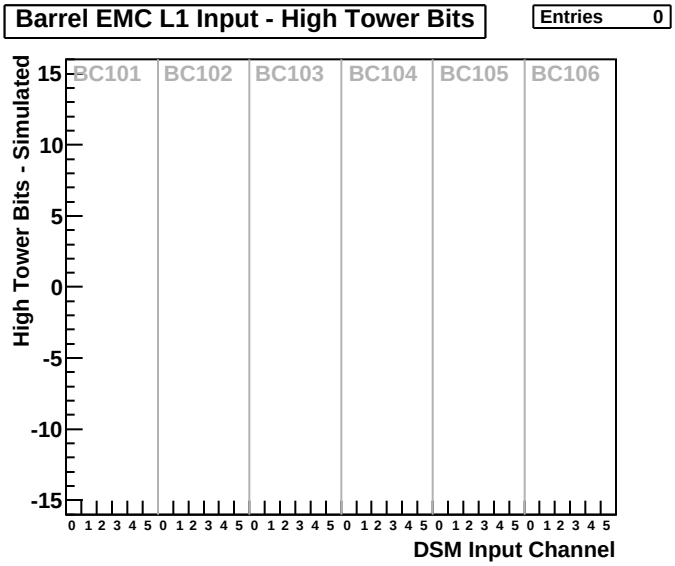

Barrel EMC L1 Input - Low Eta Sum
Entries 9.647997e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

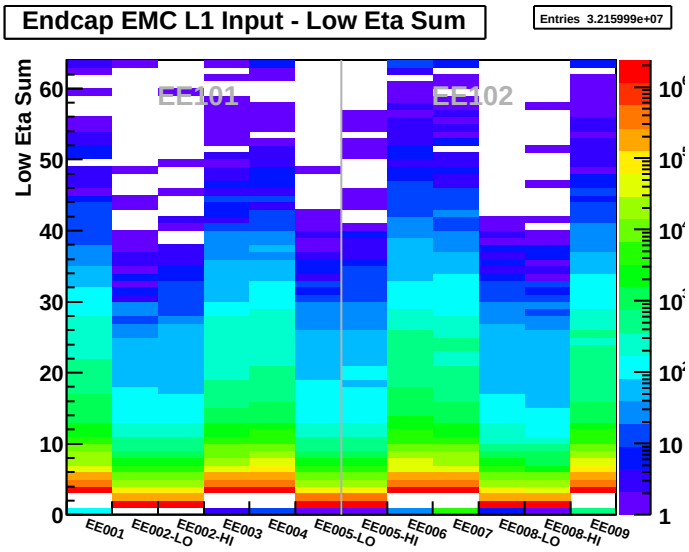

Barrel EMC L1 Input - Low Eta Sum
Entries 6185
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

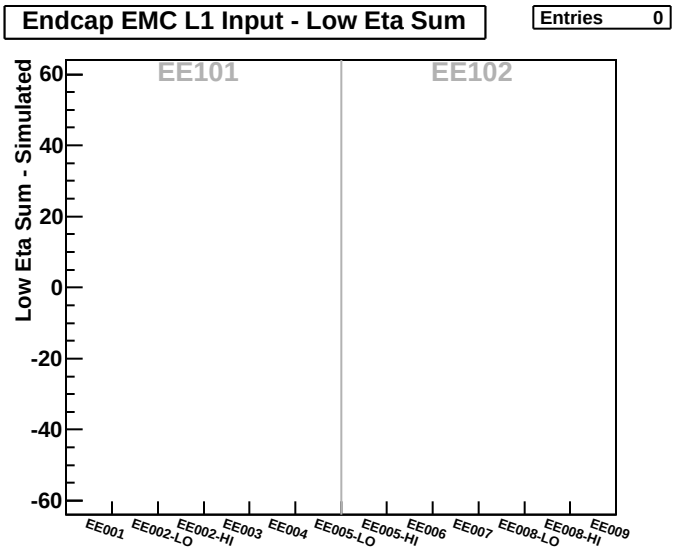

Barrel EMC L1 Input - High Eta Sum
Entries 9.647997e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

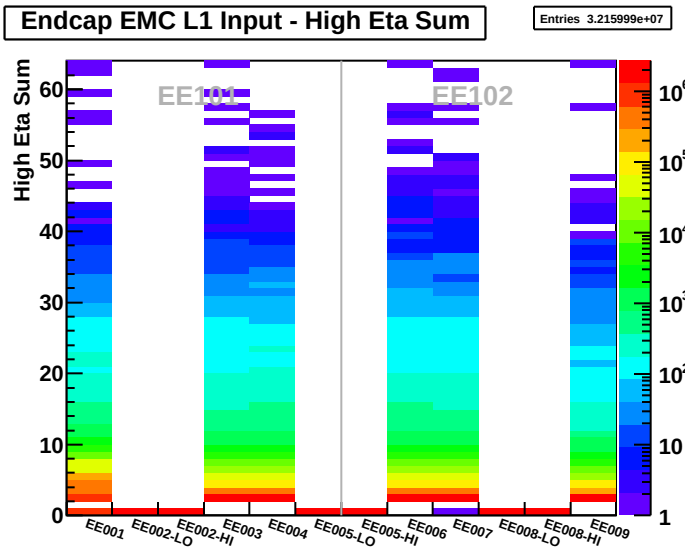

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

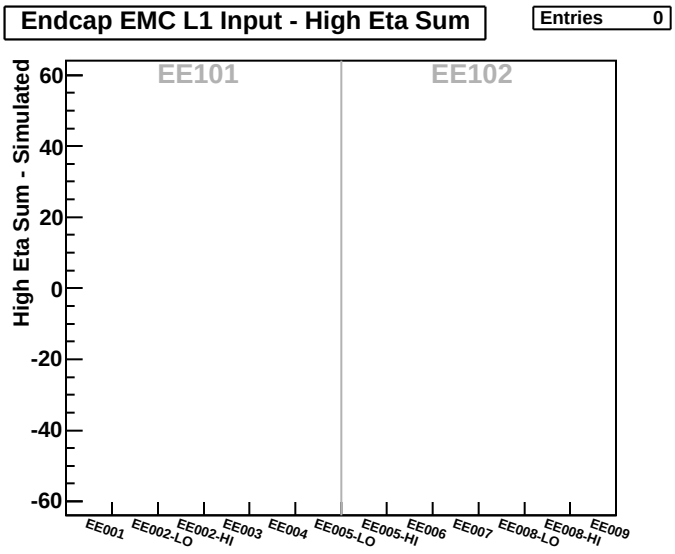

Barrel EMC L1 Input - High Tower Bits
Entries 9.647997e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

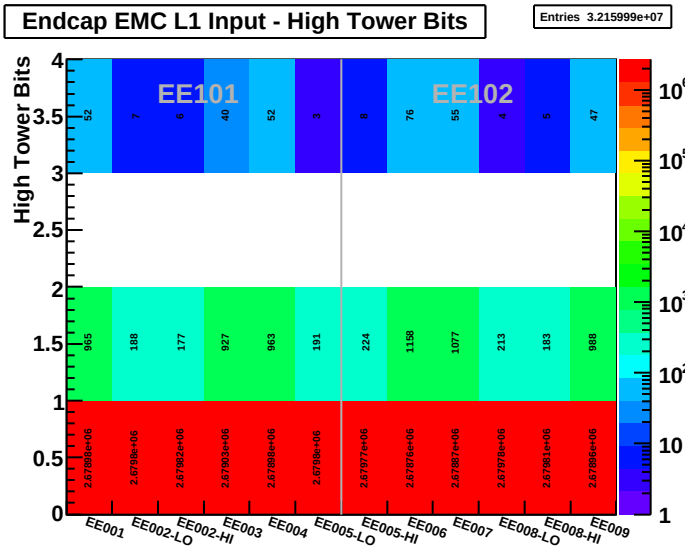

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

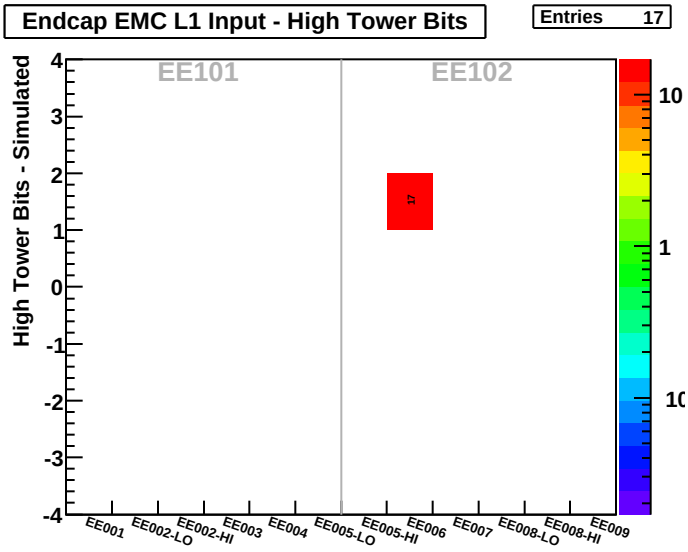

Endcap EMC L1 Input - Low Eta Sum
Entries 3.215999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

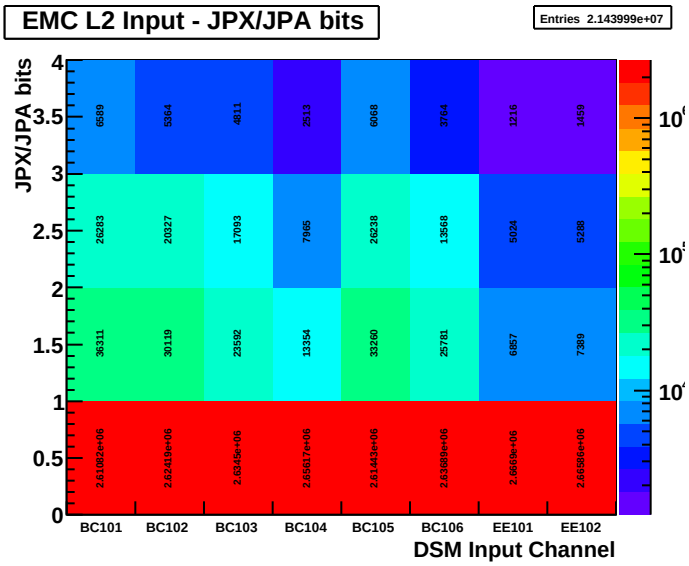

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

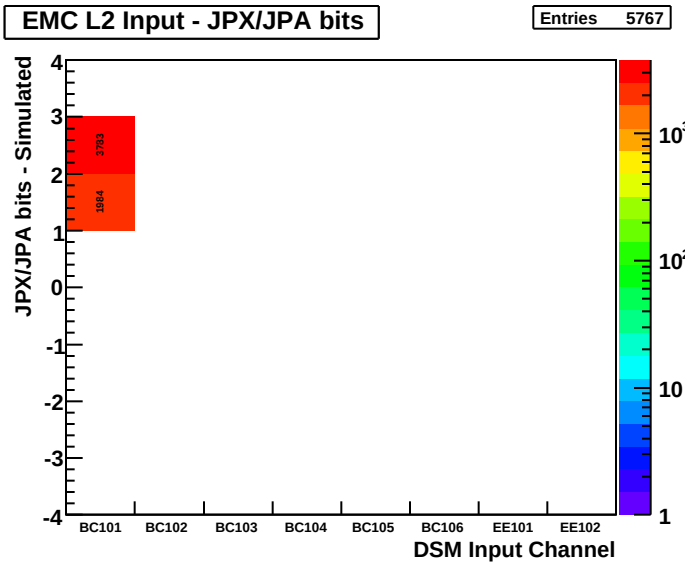

Endcap EMC L1 Input - High Eta Sum
Entries 3.215999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

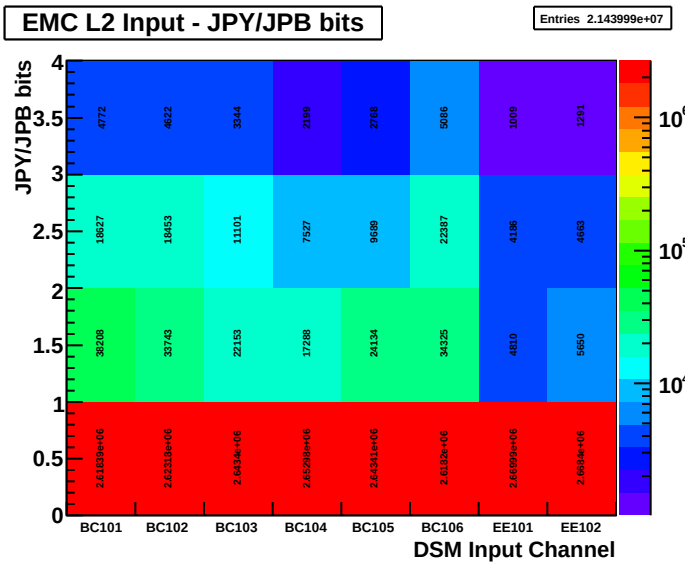

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

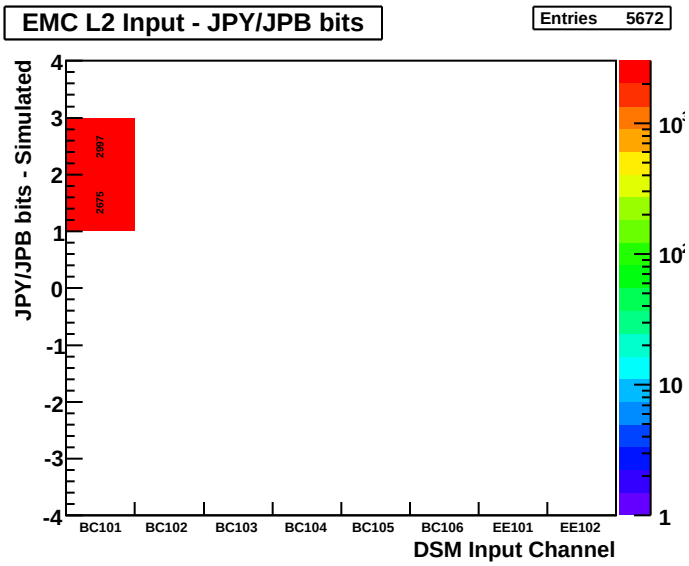

Endcap EMC L1 Input - High Tower Bits
Entries 3.215999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

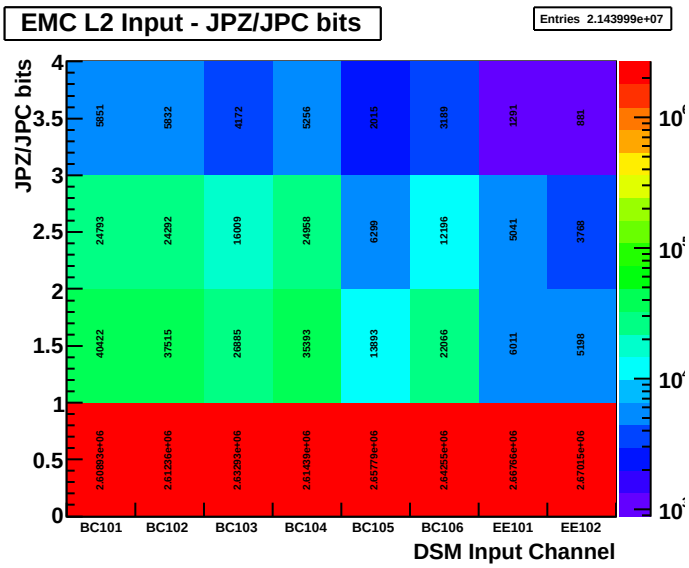

Endcap EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

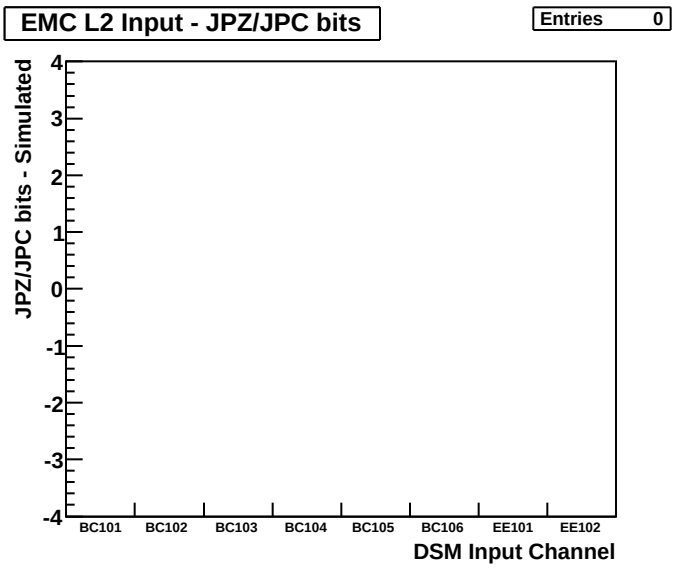

EMC L2 Input - JPX/JPA bits
Entries 2.143999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6589 5364 4811 2513 6068 3764 1216 1459
26283 20327 17093 7965 26238 13568 5024 5288
36311 30119 23592 13354 33260 25781 6857 7389
2.61082e+06 2.62419e+06 2.6345e+06 2.65617e+06 2.61443e+06 2.63889e+06 2.6669e+06 2.66586e+06

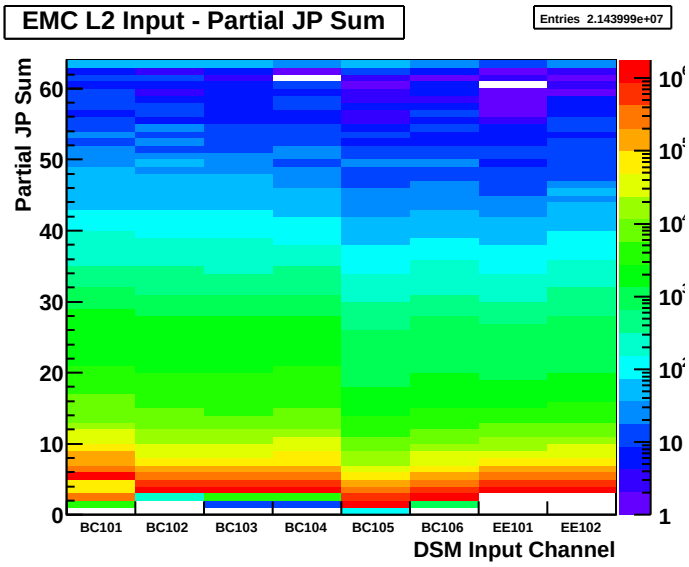

EMC L2 Input - JPX/JPA bits
Entries 5767
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3783
1984

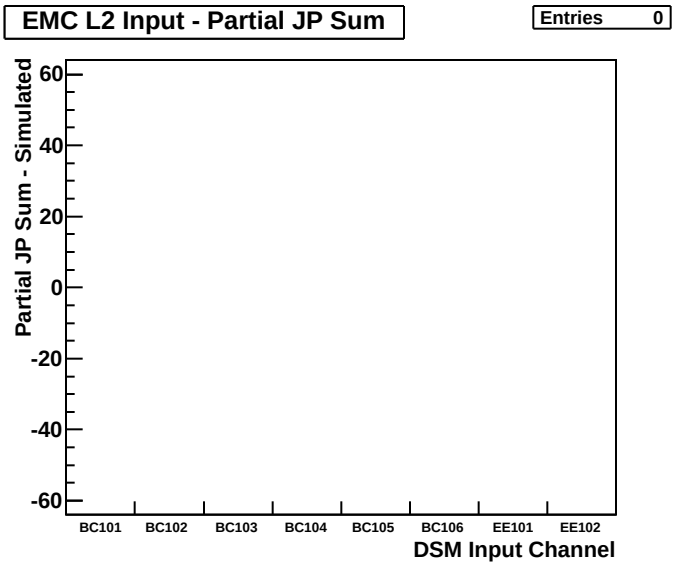

EMC L2 Input - JPY/JPB bits
Entries 2.143999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4772 4622 3344 2199 2768 5086 1009 1291
18627 19453 11101 7527 9689 22387 4186 4663
38208 33743 22153 17288 24134 34325 4810 5650
2.61639e+06 2.62318e+06 2.6434e+06 2.65288e+06 2.64341e+06 2.6182e+06 2.66999e+06 2.6684e+06

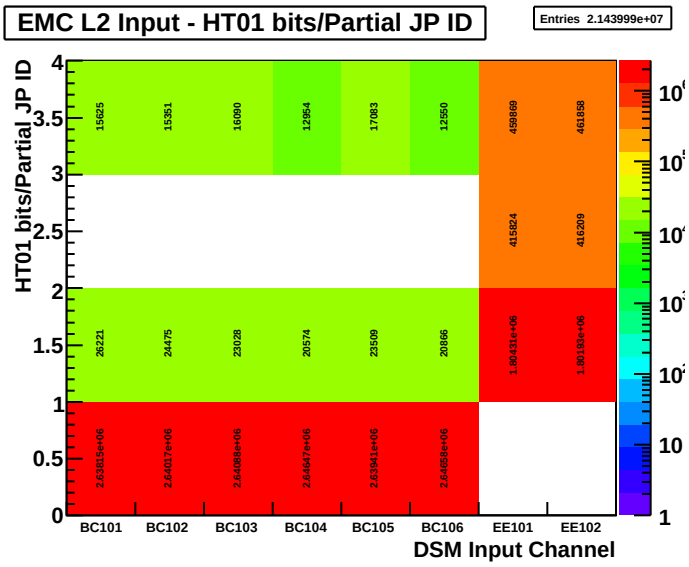

EMC L2 Input - JPY/JPB bits
Entries 5672
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2997
2675

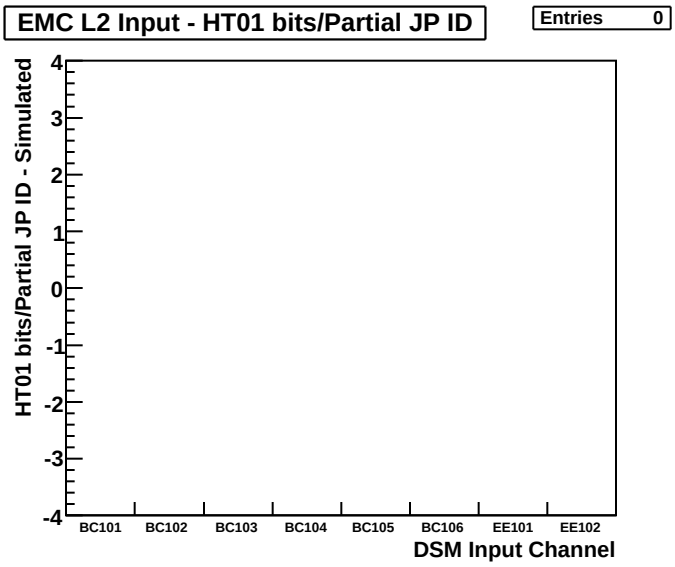

EMC L2 Input - JPZ/JPC bits
Entries 2.143999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5851 5832 4172 5256 2015 3189 1291 831
24793 24292 16009 24958 6299 12196 5041 3768
40422 37515 26885 35393 13893 22066 6011 5198
2.60893e+06 2.61236e+06 2.63293e+06 2.61439e+06 2.65779e+06 2.64255e+06 2.66765e+06 2.67015e+06

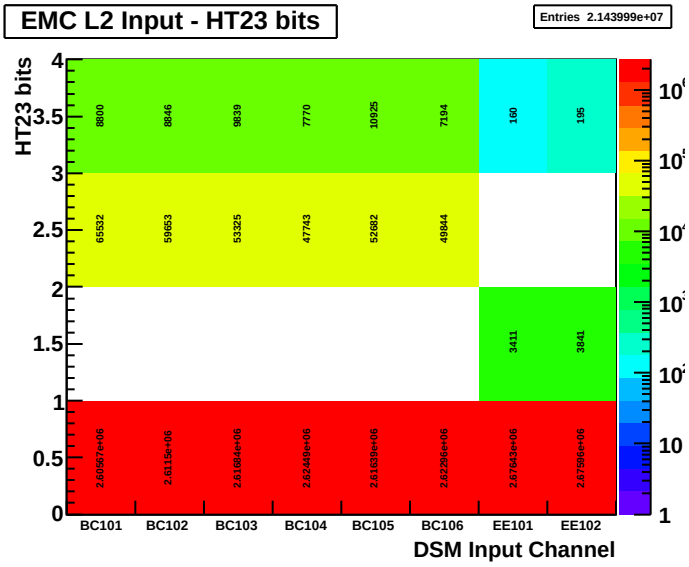

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

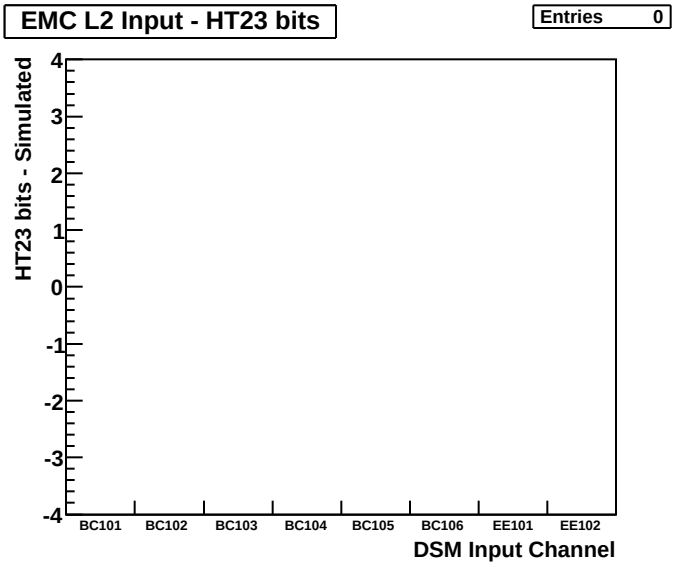

EMC L2 Input - Partial JP Sum
Entries 2.143999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

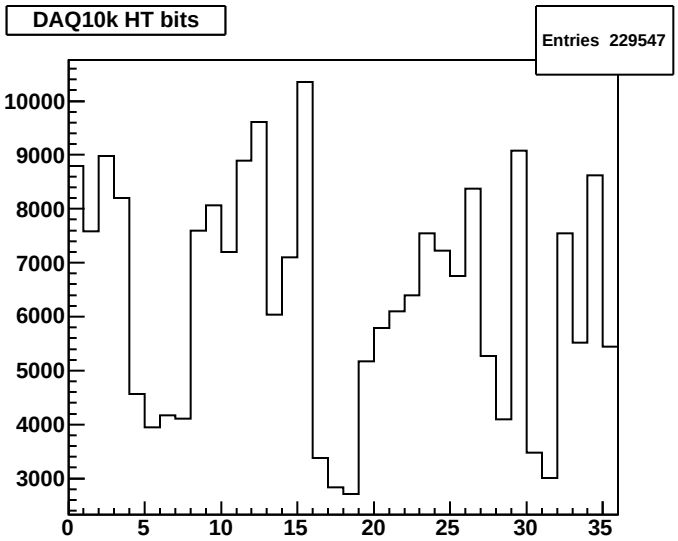

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

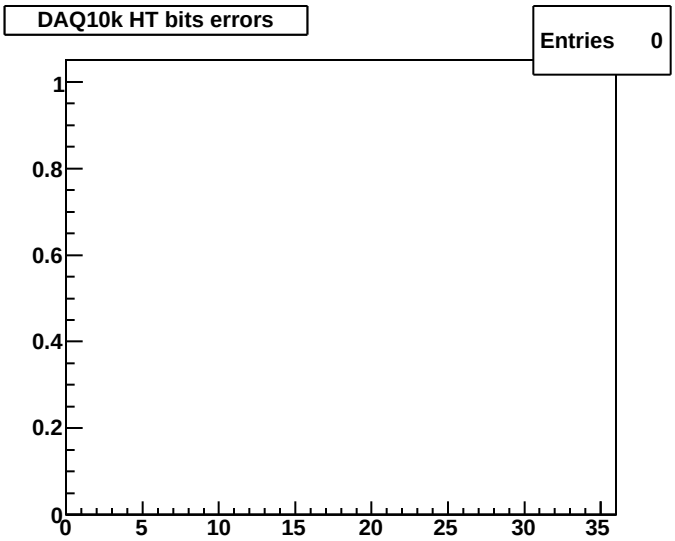

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.143999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

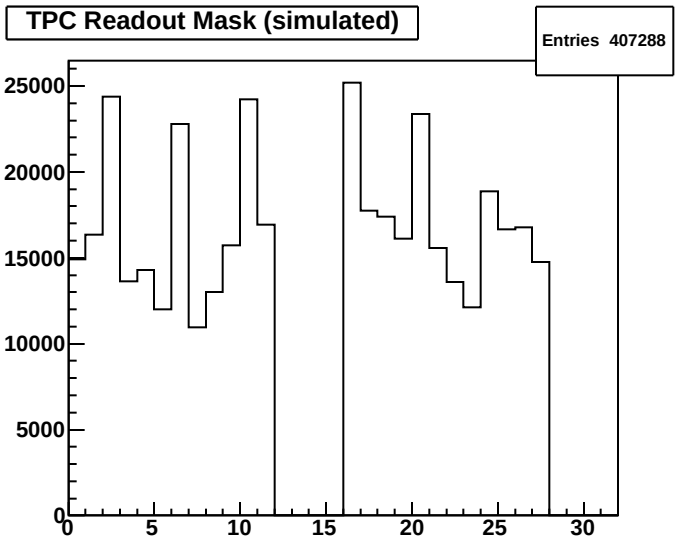

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

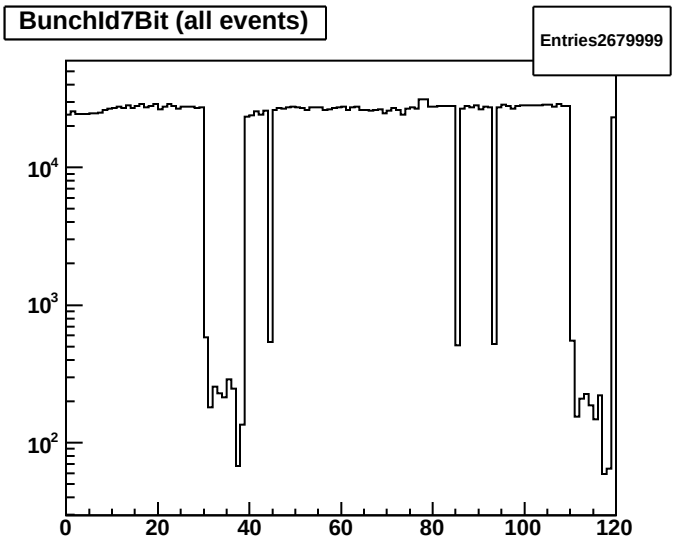

EMC L2 Input - HT23 bits
Entries 2.143999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

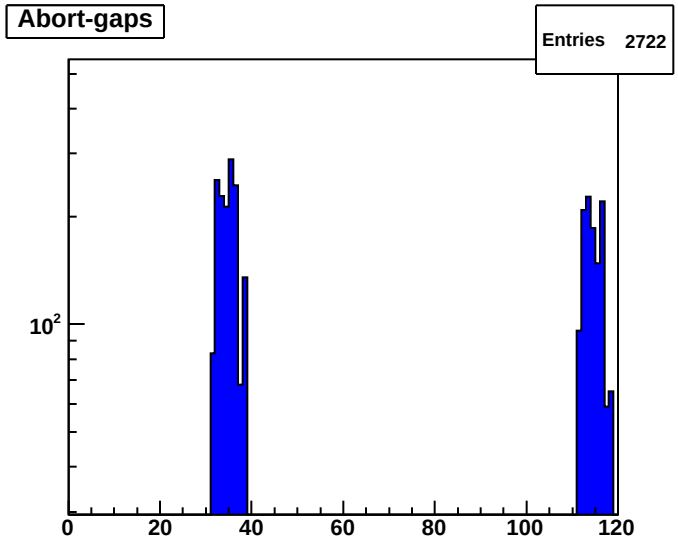

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

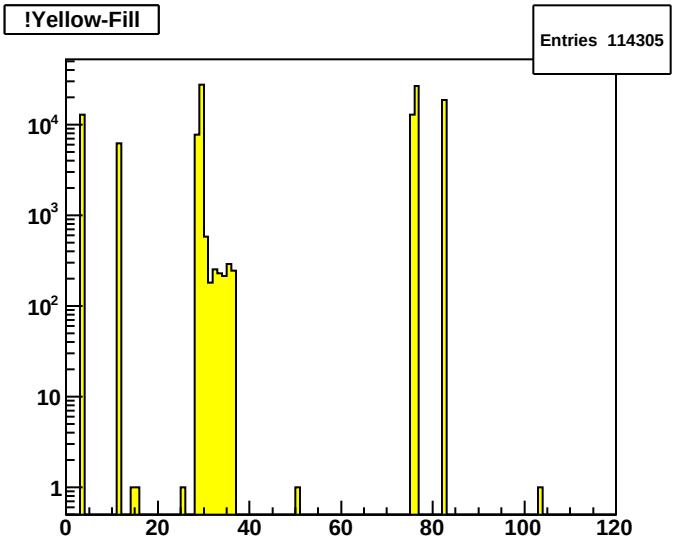

DAQ10k HT bits
Entries 229547

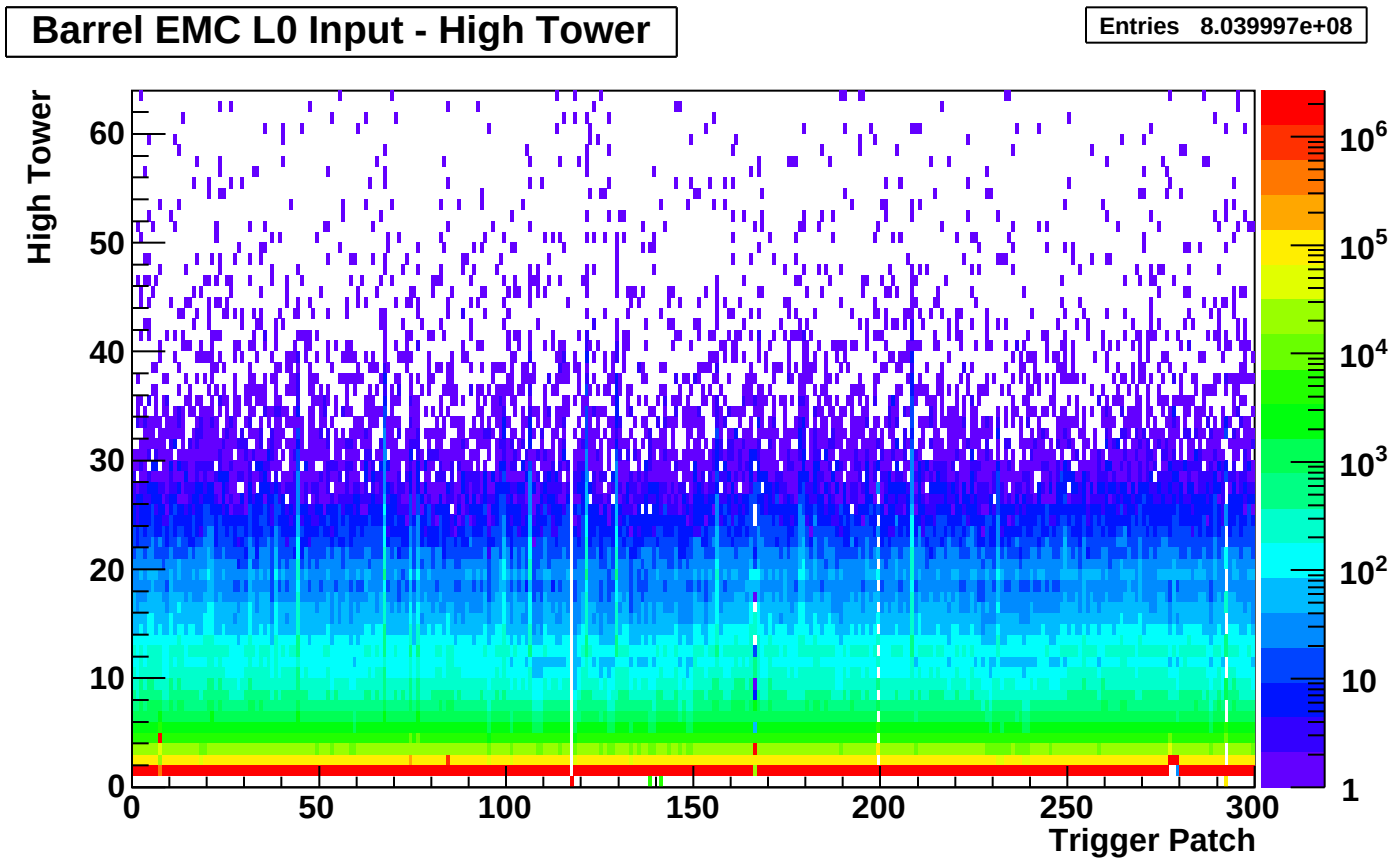

DAQ10k HT bits errors
Entries 0

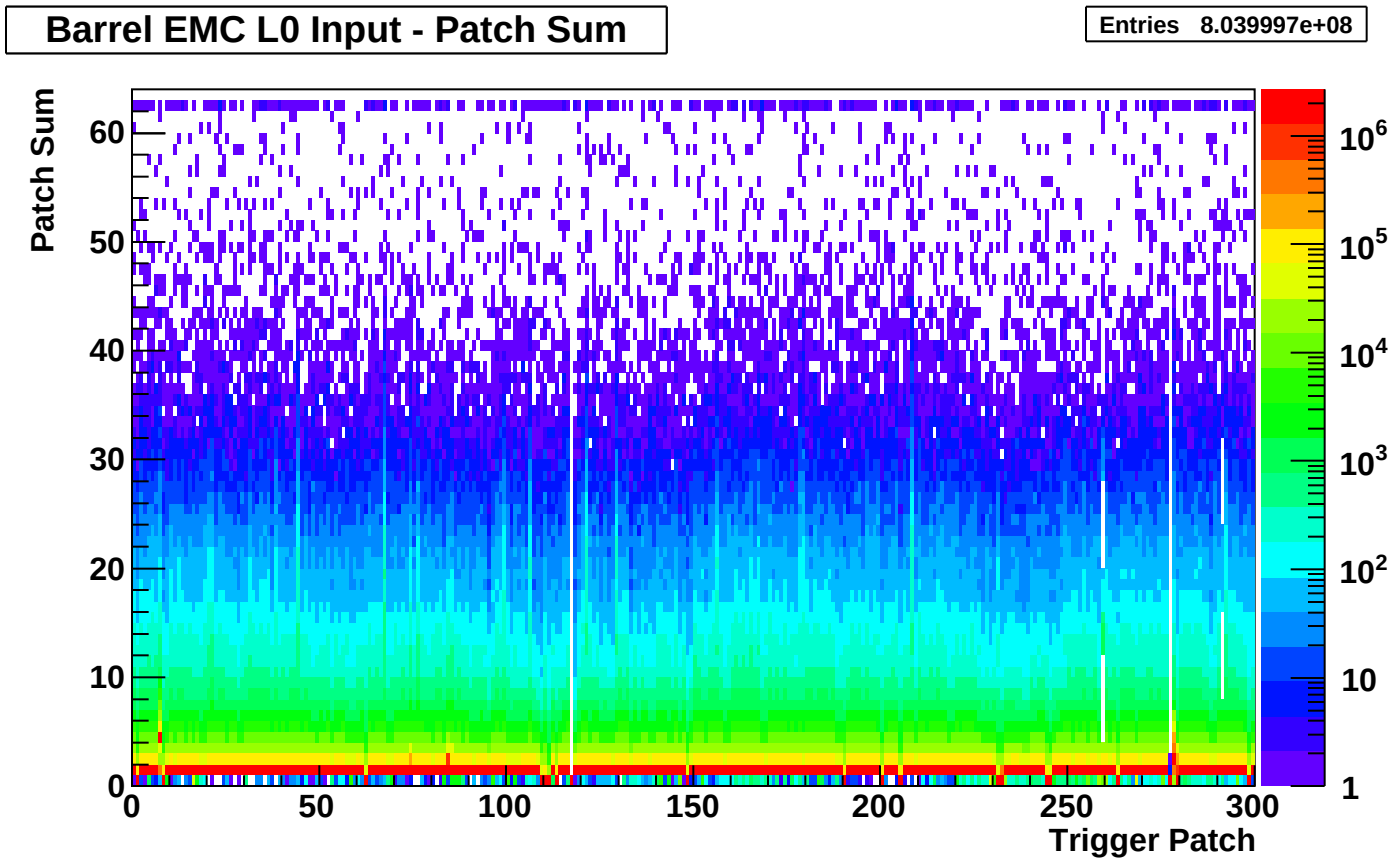

TPC Readout Mask (simulated)
Entries 407288

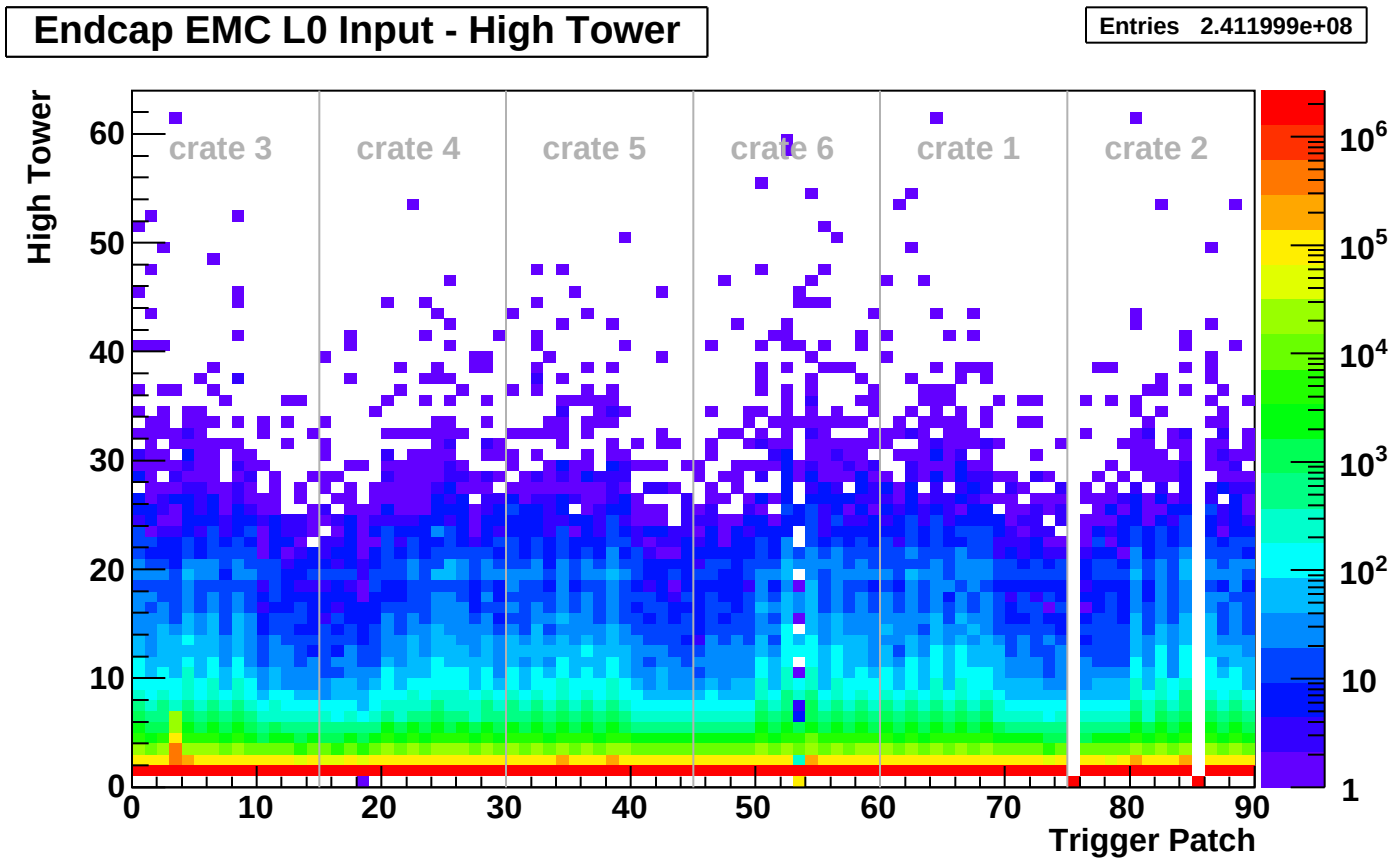

BunchId7Bit (all events)
Entries2679999

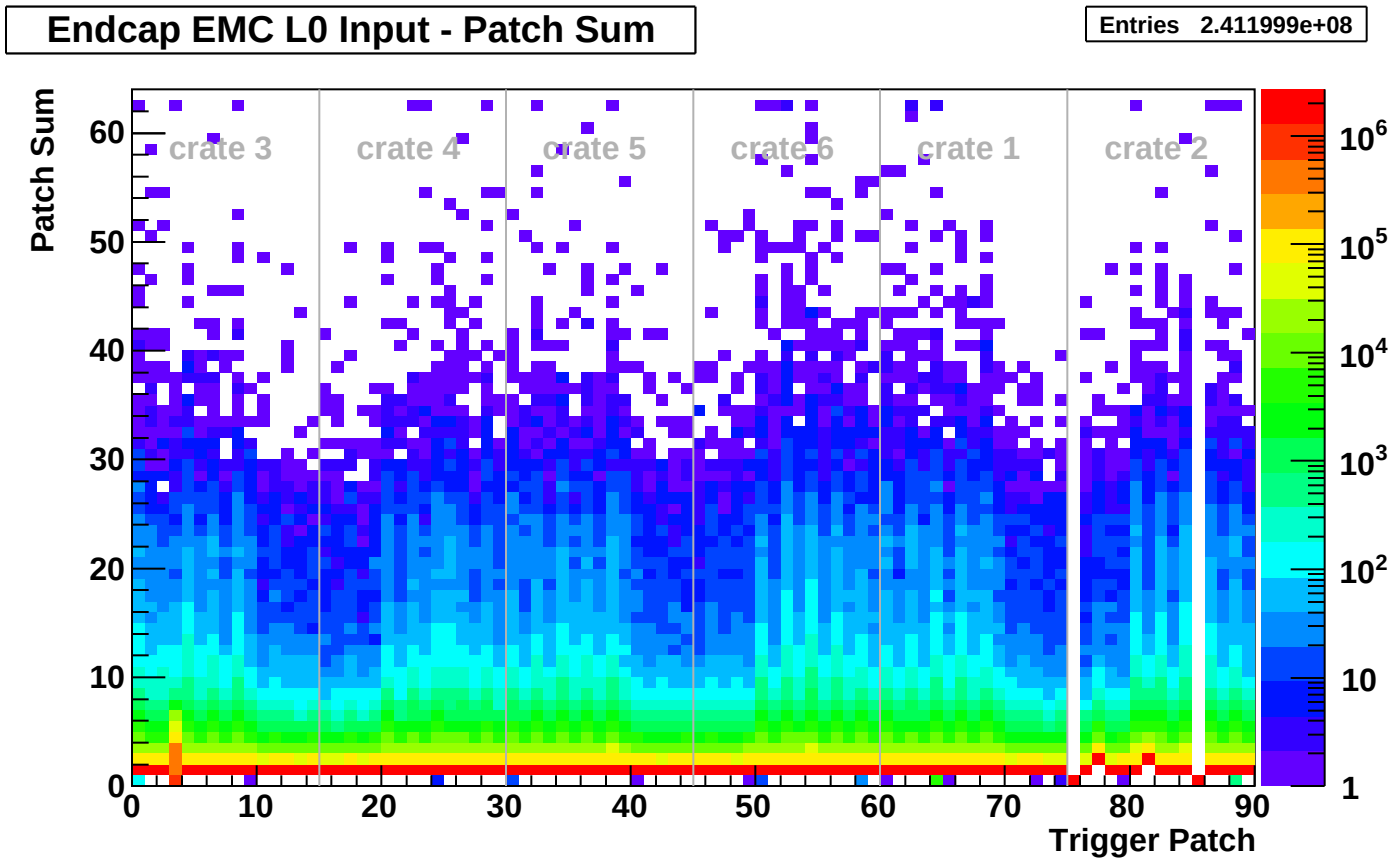

Abort-gaps
Entries 2722

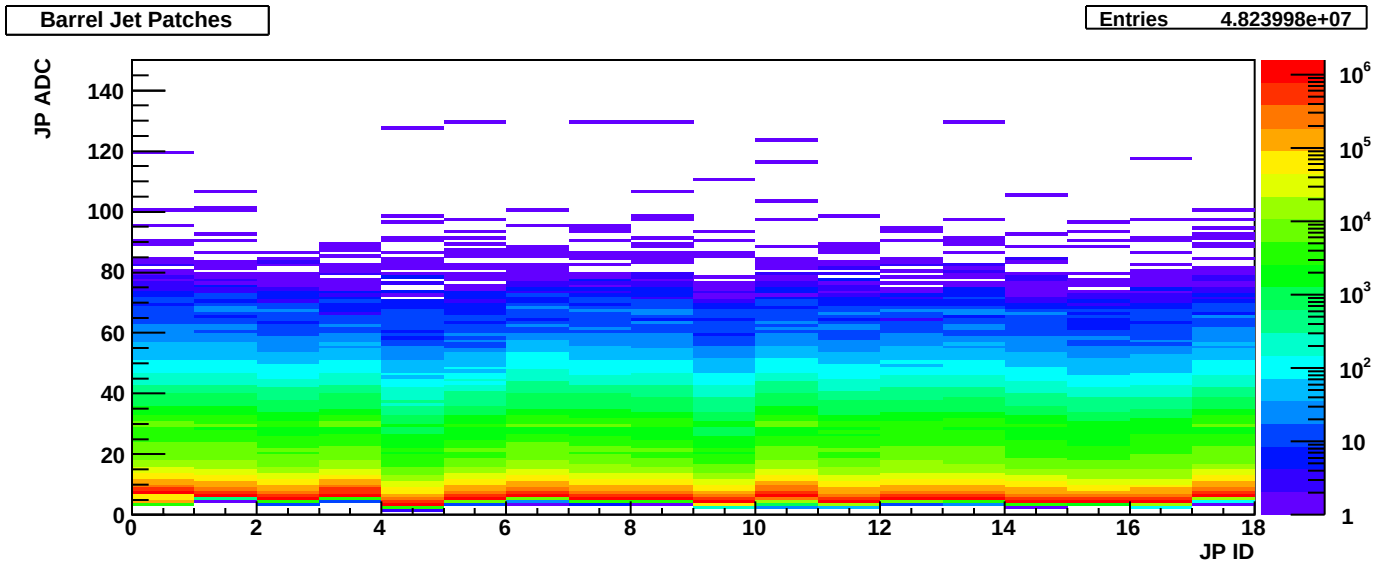

!Yellow-Fill
Entries 114305

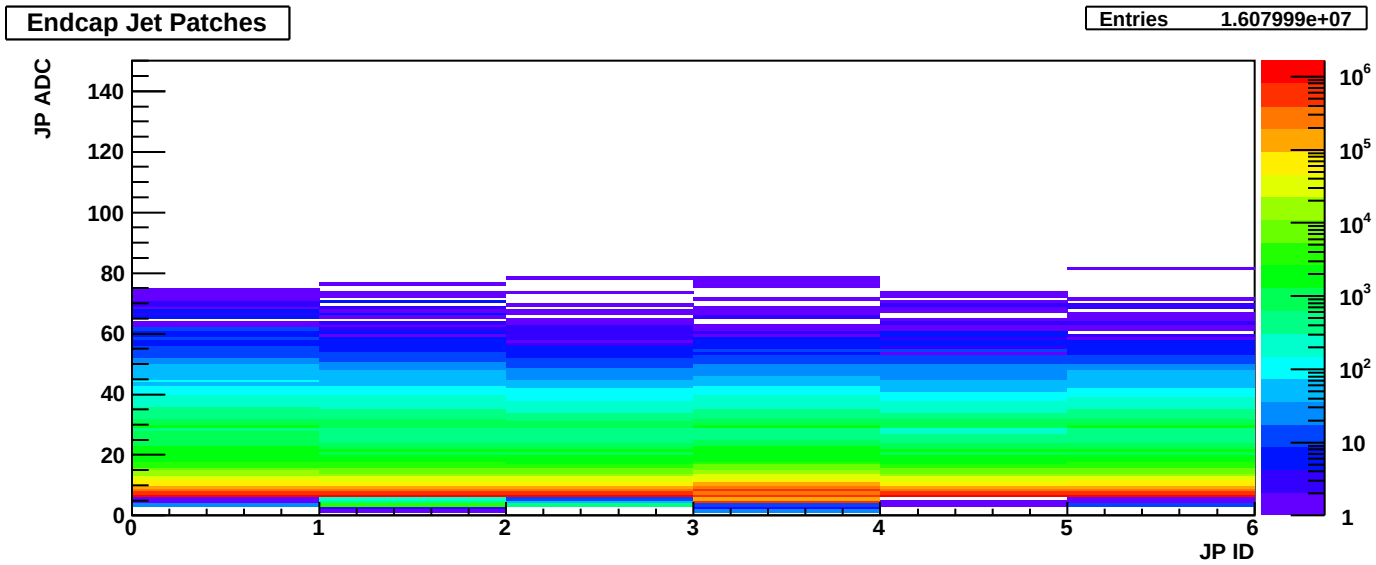

Barrel EMC L0 Input - High Tower
Entries 8.039997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

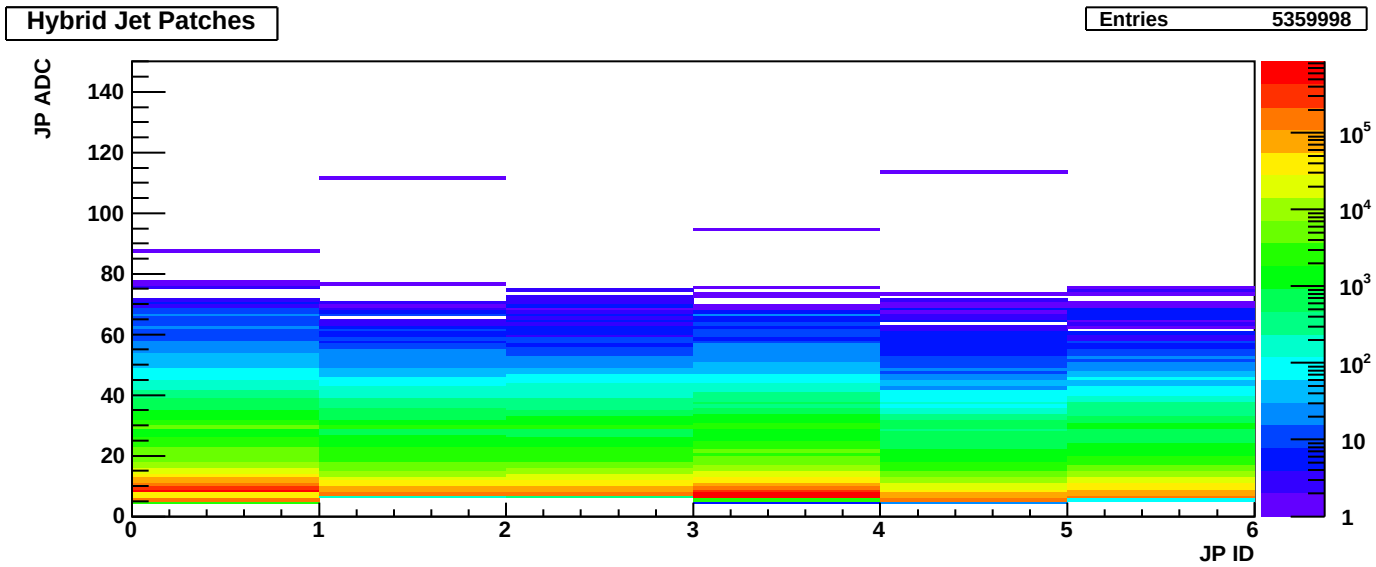

Barrel EMC L0 Input - Patch Sum
Entries 8.039997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

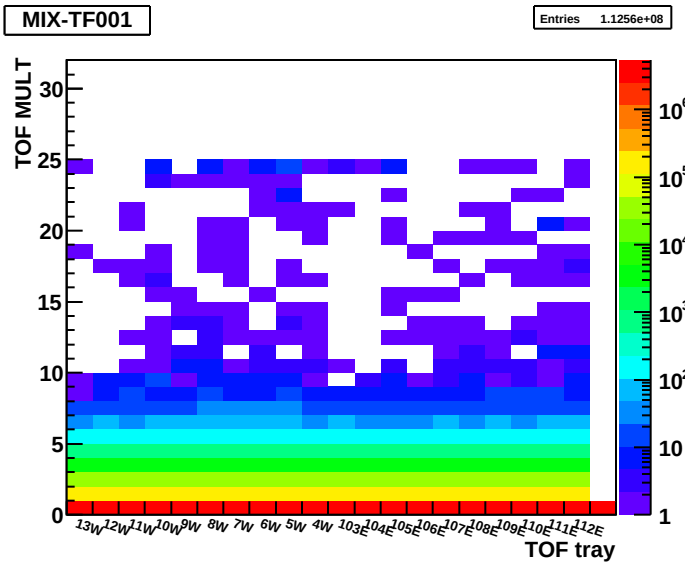

Endcap EMC L0 Input - High Tower
Entries 2.411999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

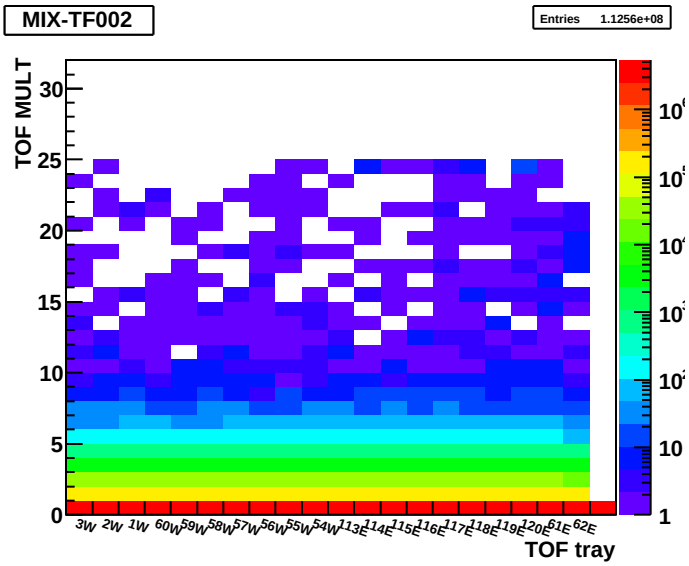

Endcap EMC L0 Input - Patch Sum
Entries 2.411999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

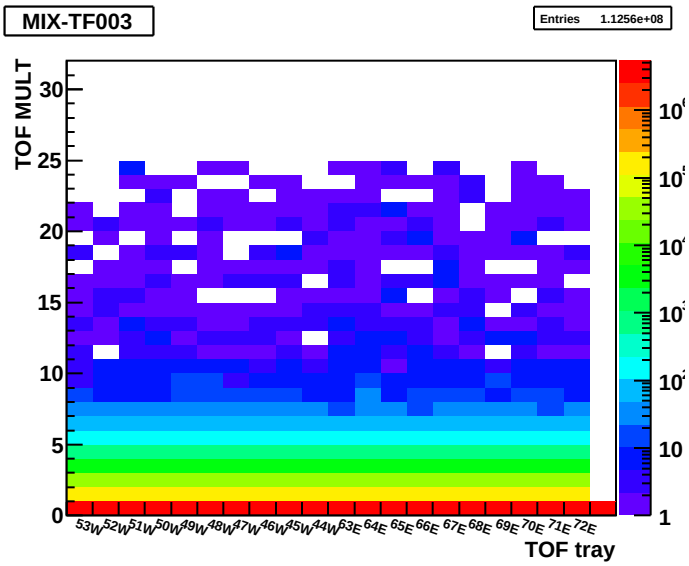

Barrel Jet Patches
Entries 4.823998e+07
JP ADC
JP ID

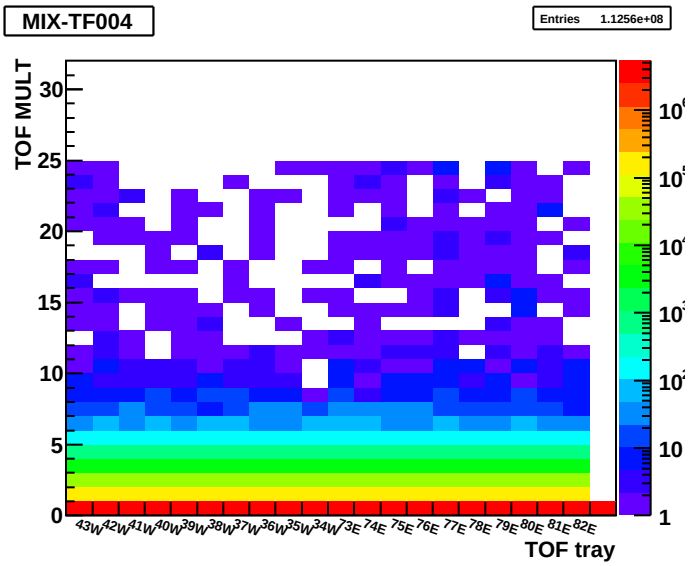

Endcap Jet Patches
Entries 1.607999e+07
JP ADC
JP ID

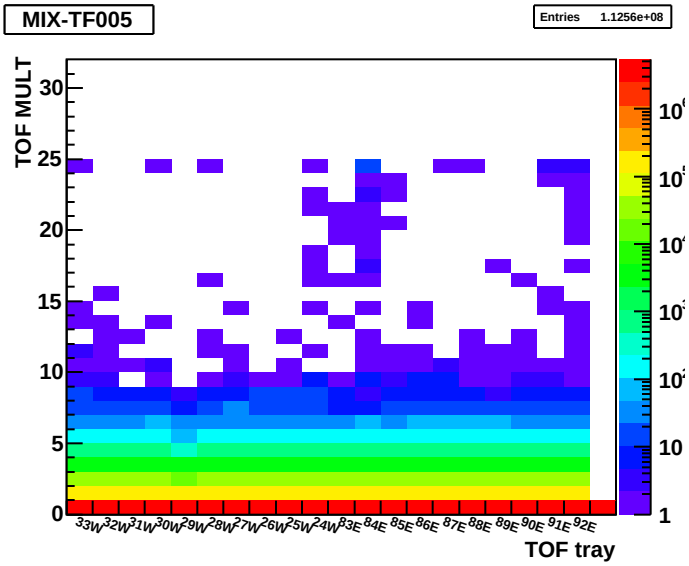

Hybrid Jet Patches
Entries 5359998
JP ADC
JP ID

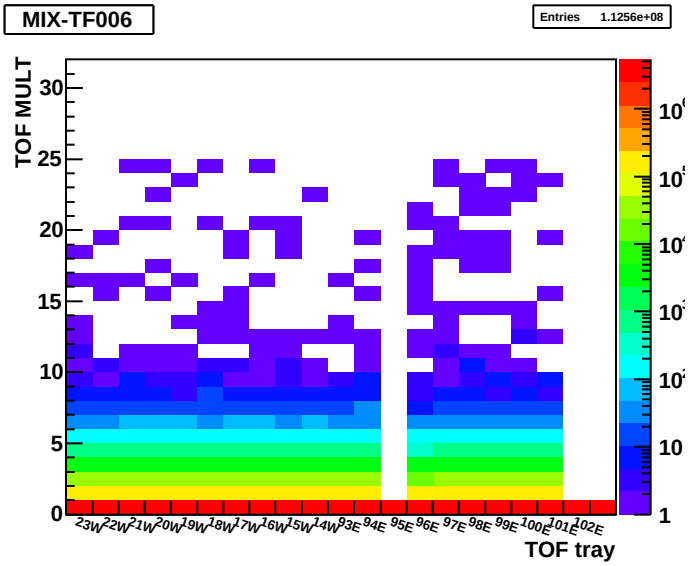

MIX-TF001
Entries 1.1256e+08
TOF MULT
TOF tray

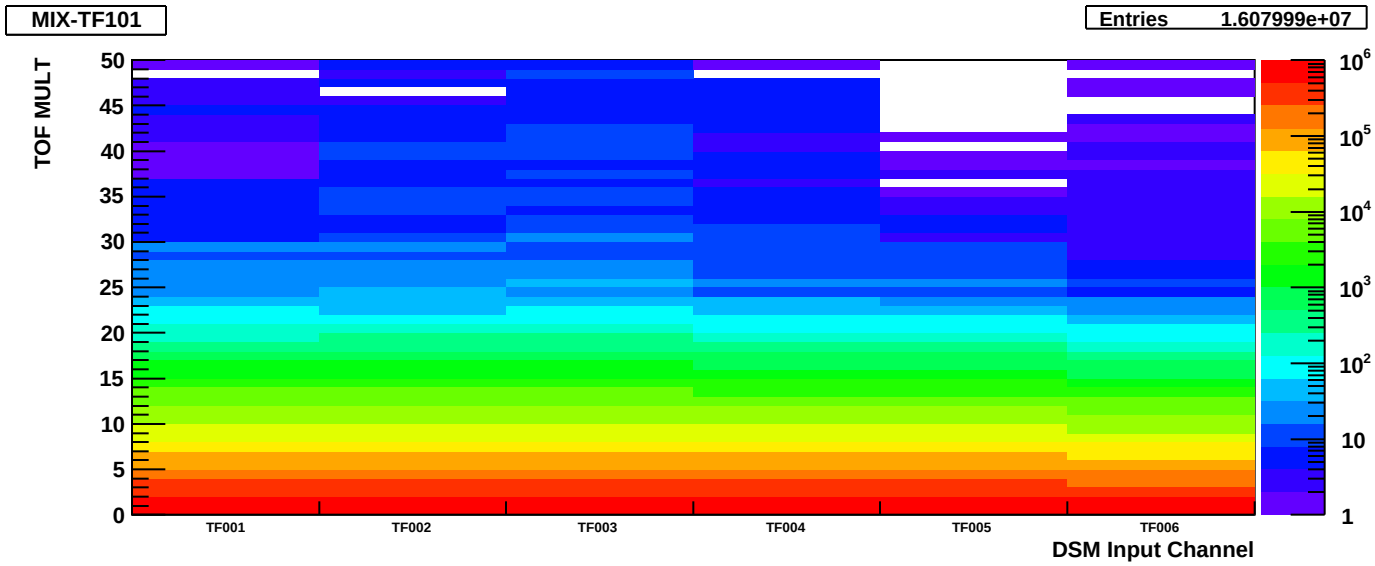

MIX-TF002
Entries 1.1256e+08
TOF MULT
TOF tray

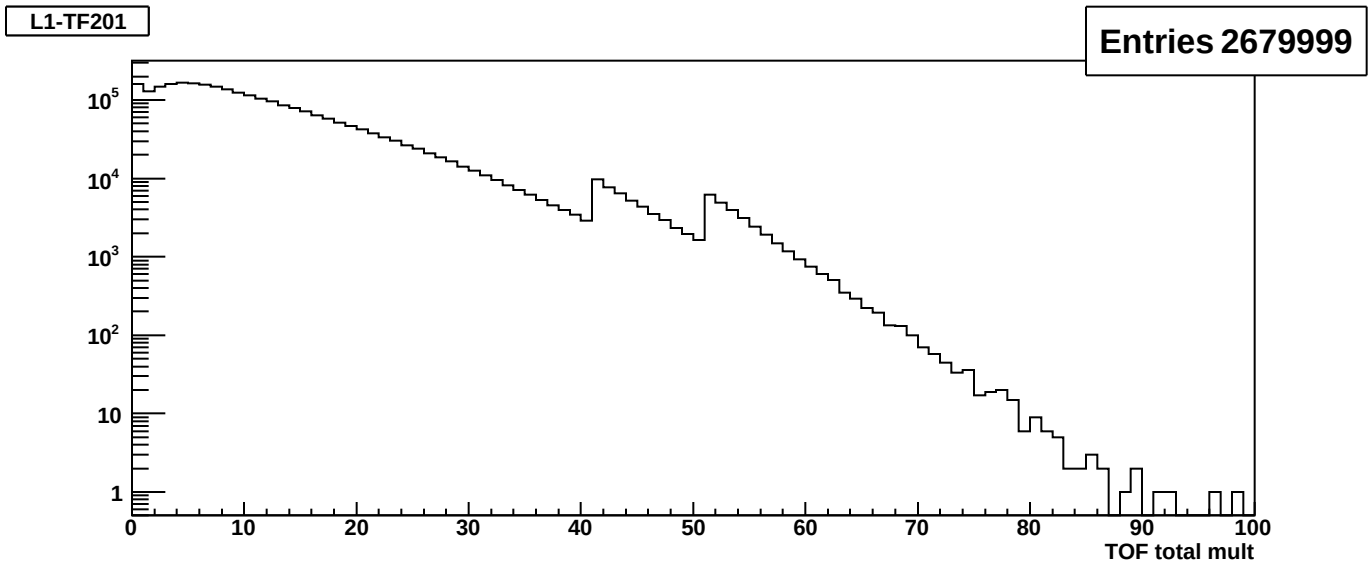

MIX-TF003
Entries 1.1256e+08
TOF MULT
TOF tray

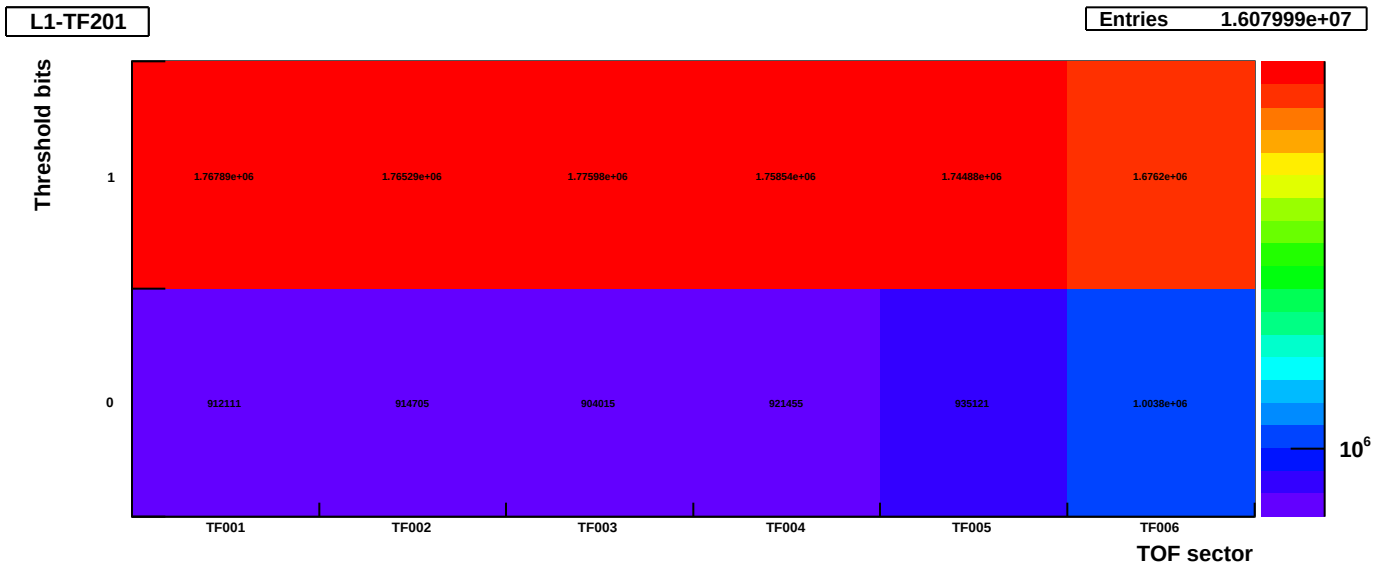

MIX-TF004
Entries 1.1256e+08
TOF MULT
TOF tray

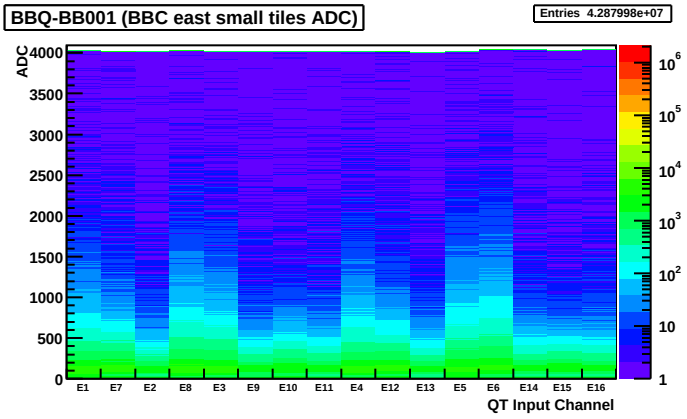

MIX-TF005
Entries 1.1256e+08
TOF MULT
TOF tray

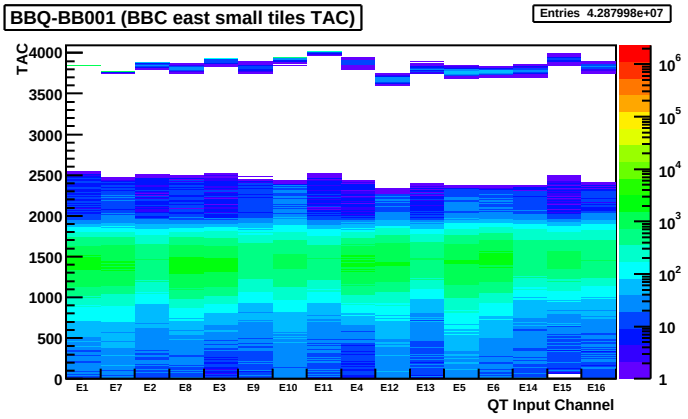

MIX-TF006
Entries 1.1256e+08
TOF MULT
TOF tray

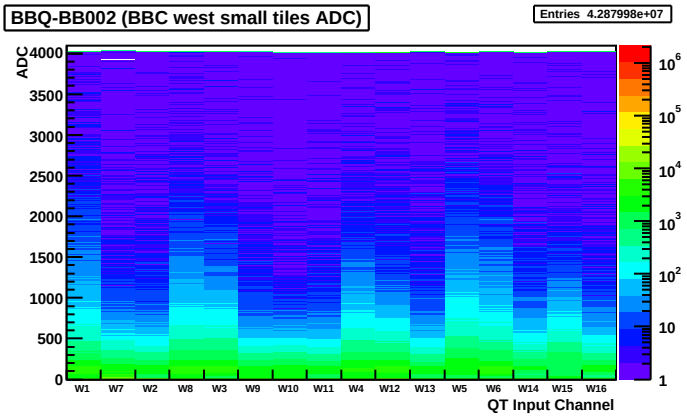

MIX-TF101
Entries 1.607999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^6
10^5
10^4
10^3
10^2
10
1

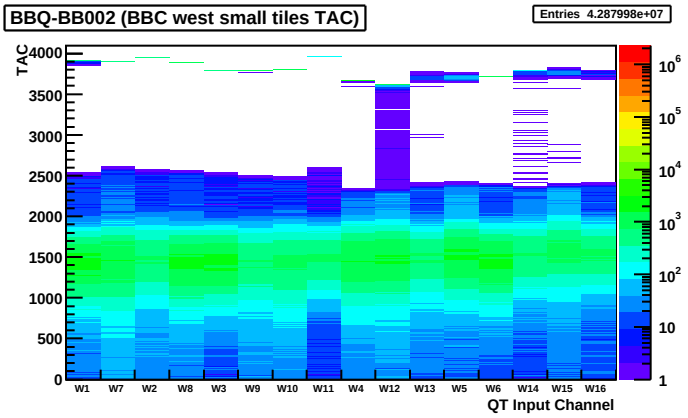

L1-TF201
Entries 2679999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

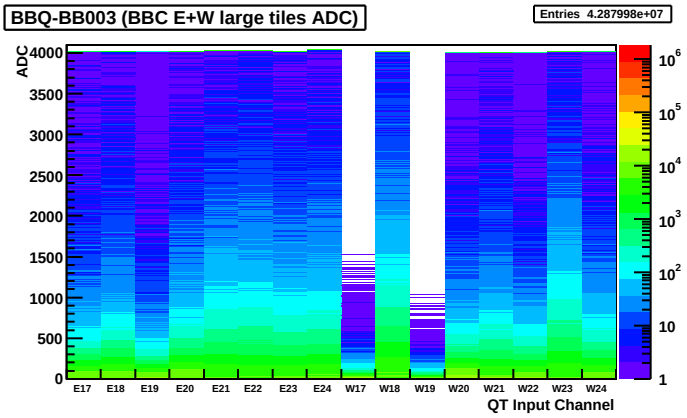

L1-TF201
Entries 1.607999e+07
Threshold bits
1
0
1.76789e+06
1.76529e+06
1.77598e+06
1.75854e+06
1.74488e+06
1.6762e+06
912111
914705
904015
921455
935121
1.0038e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

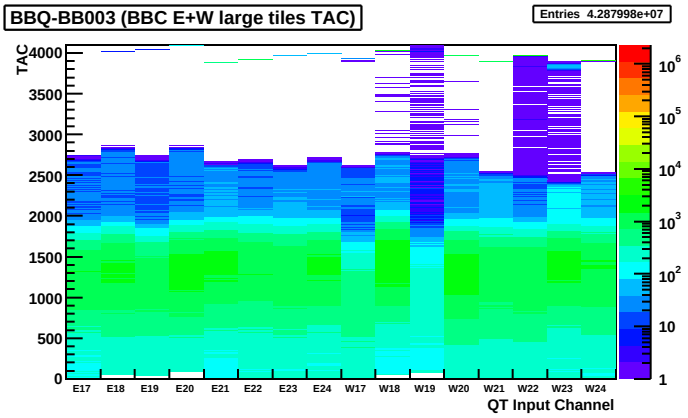

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.287998e+07
ADC
QT Input Channel

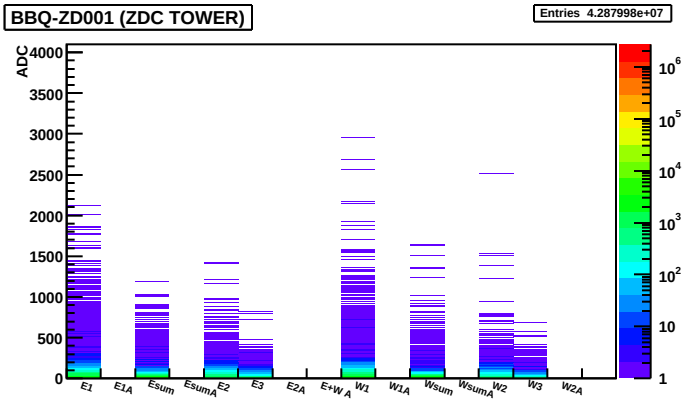

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.287998e+07
TAC
QT Input Channel

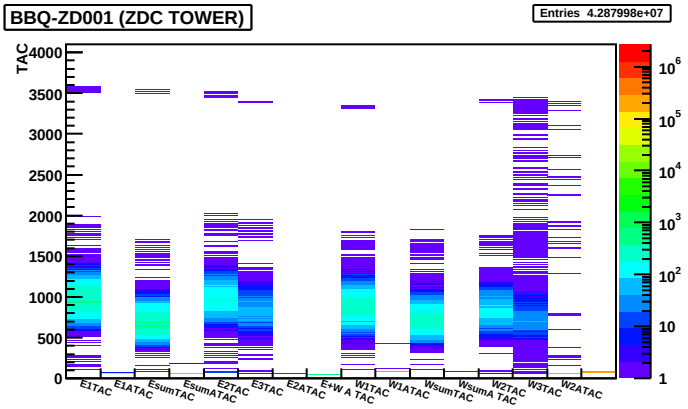

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.287998e+07
ADC
QT Input Channel

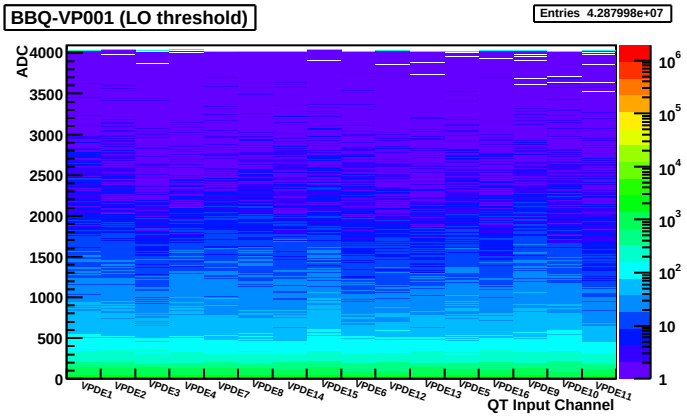

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.287998e+07
TAC
QT Input Channel

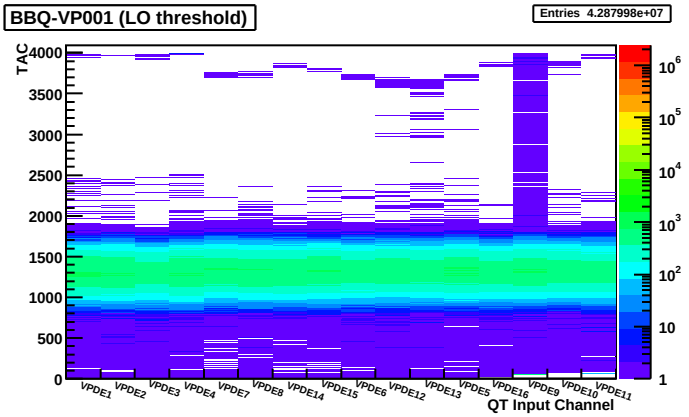

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.287998e+07
ADC
QT Input Channel

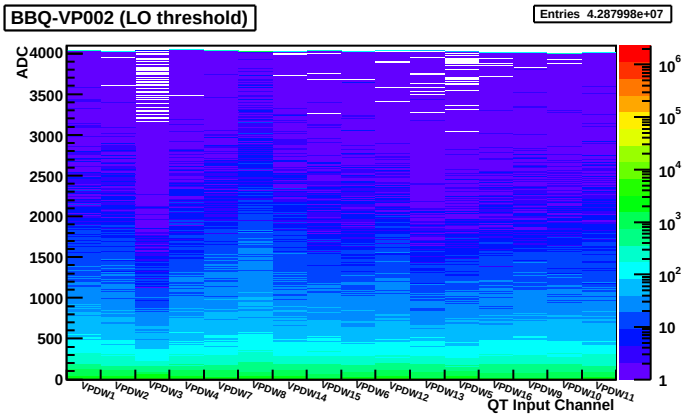

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.287998e+07
TAC
QT Input Channel

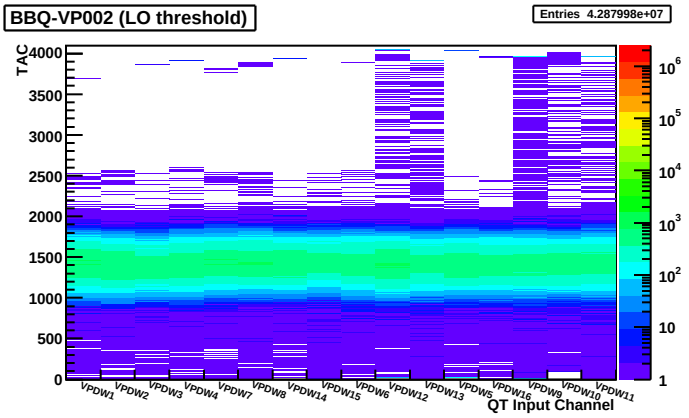

BBQ-ZD001 (ZDC TOWER)
Entries 4.287998e+07
ADC

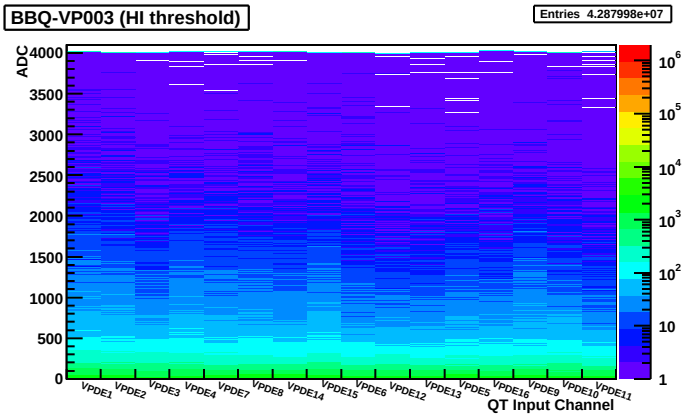

BBQ-ZD001 (ZDC TOWER)
Entries 4.287998e+07
TAC

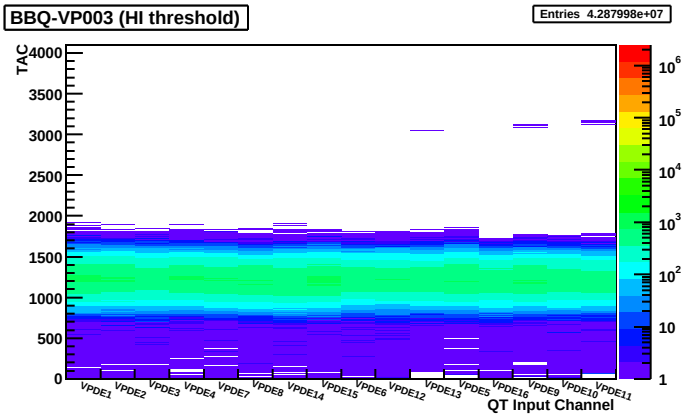

BBQ-VP001 (LO threshold)
Entries 4.287998e+07
ADC
QT Input Channel

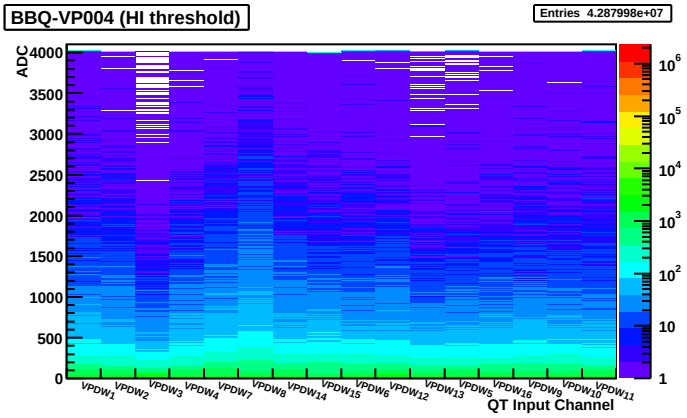

BBQ-VP001 (LO threshold)
Entries 4.287998e+07
TAC
QT Input Channel

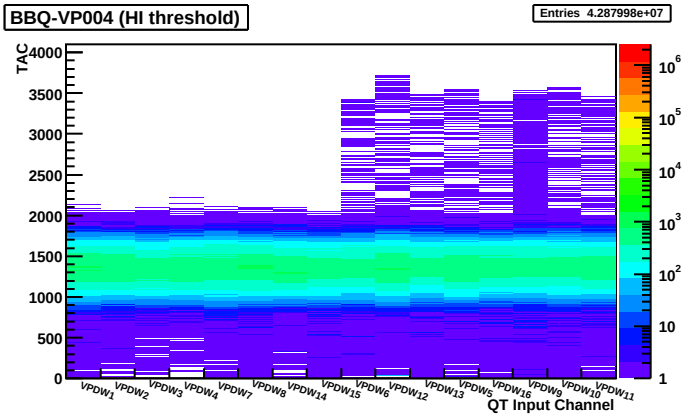

BBQ-VP002 (LO threshold)
Entries 4.287998e+07
ADC
QT Input Channel

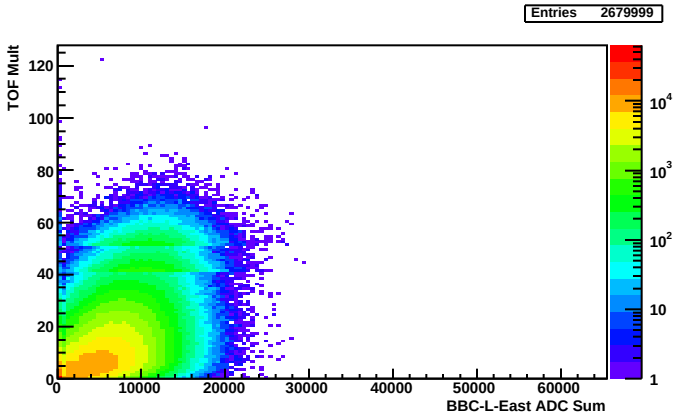

BBQ-VP002 (LO threshold)
Entries 4.287998e+07
TAC
QT Input Channel

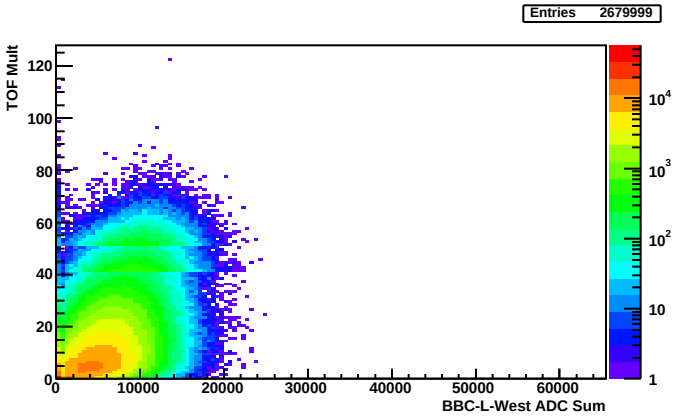

BBQ-VP003 (HI threshold)
Entries 4.287998e+07
ADC
QT Input Channel

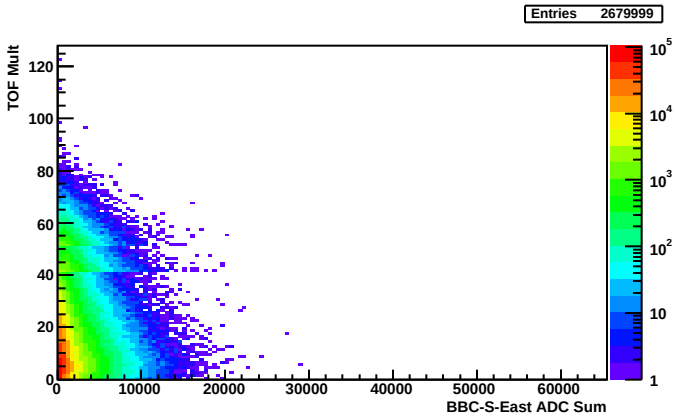

BBQ-VP003 (HI threshold)
Entries 4.287998e+07
TAC
QT Input Channel

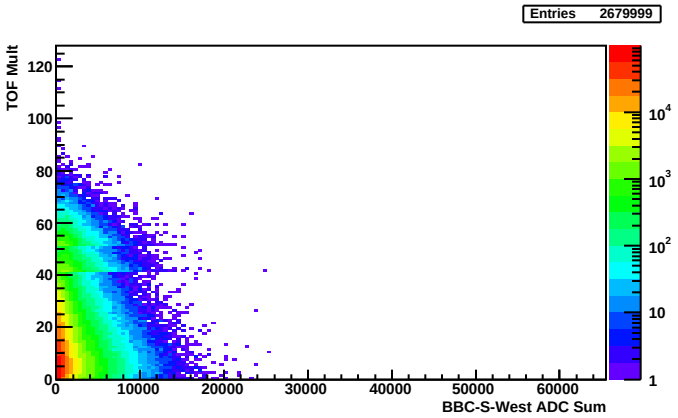

BBQ-VP004 (HI threshold)
Entries 4.287998e+07
ADC
QT Input Channel

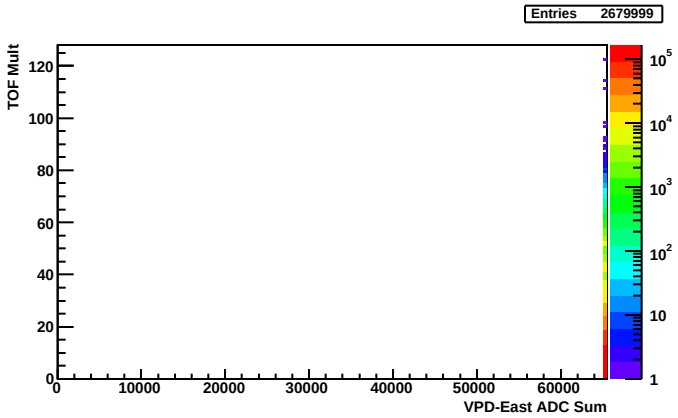

BBQ-VP004 (HI threshold)
Entries 4.287998e+07
TAC
QT Input Channel

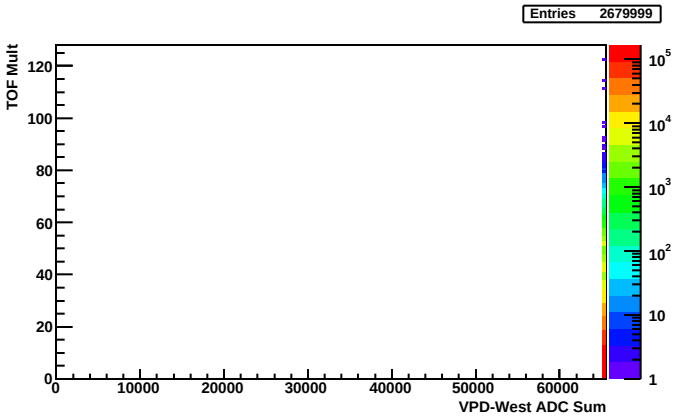

Entries 2679999
TOF Mult
BBC-L-East ADC Sum

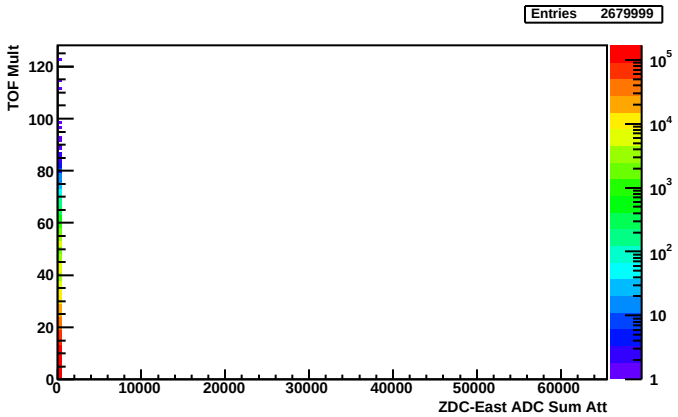

Entries 2679999
TOF Mult
BBC-L-West ADC Sum

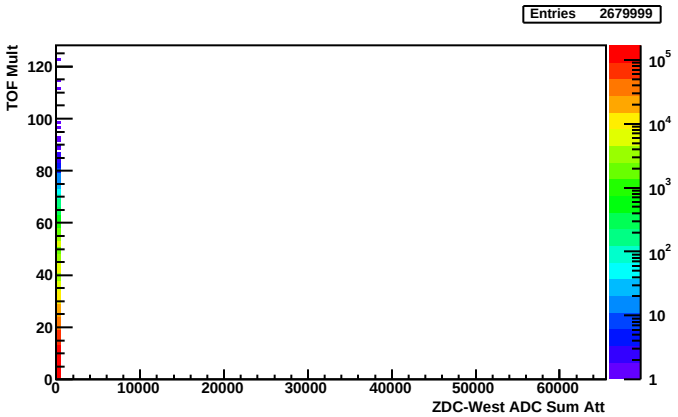

Entries 2679999
TOF Mult
BBC-S-East ADC Sum

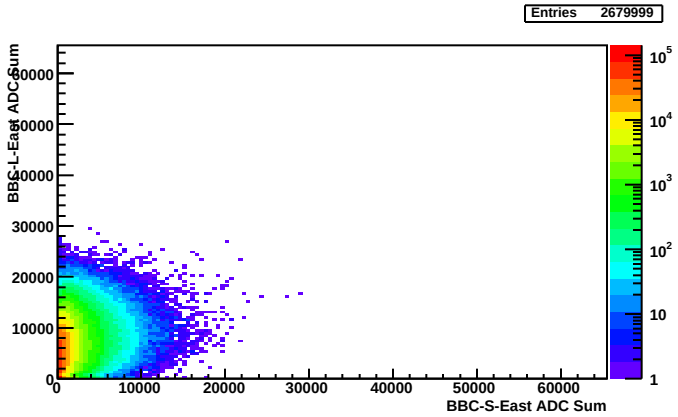

Entries 2679999
TOF Mult
BBC-S-West ADC Sum

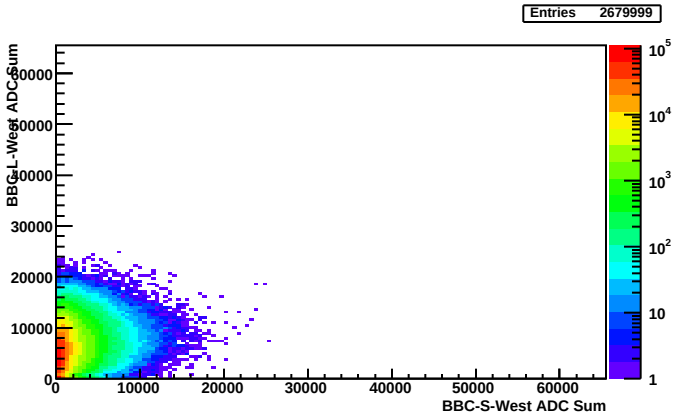

Entries 2679999
TOF Mult
VPD-East ADC Sum

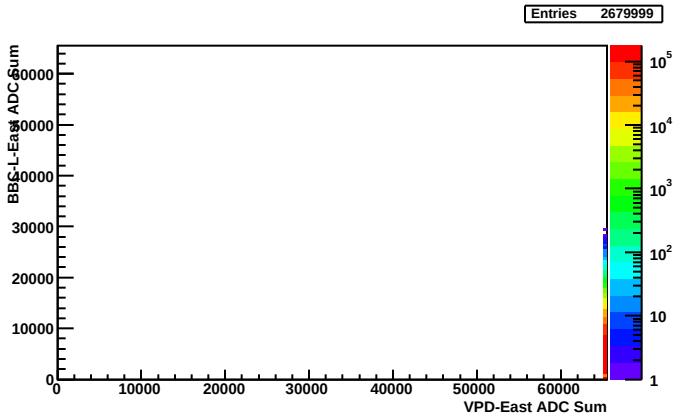

Entries 2679999
TOF Mult
VPD-West ADC Sum

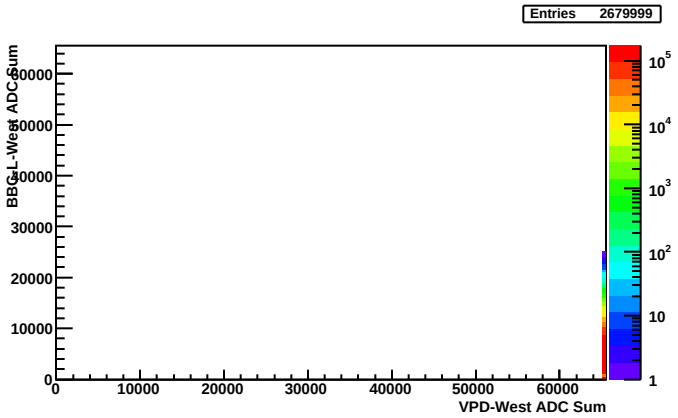

Entries 2679999
TOF Mult
ZDC-East ADC Sum Att

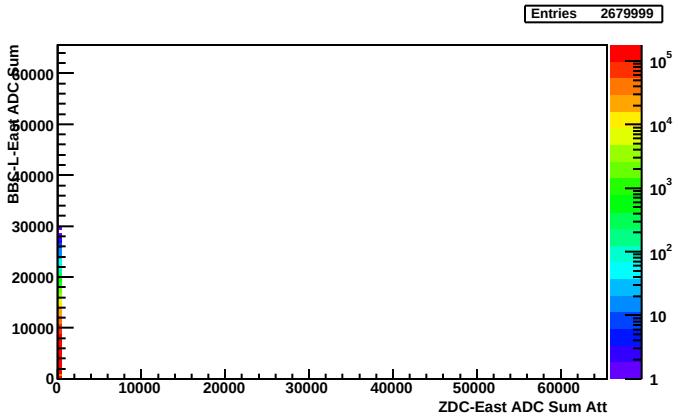

Entries 2679999
TOF Mult
ZDC-West ADC Sum Att

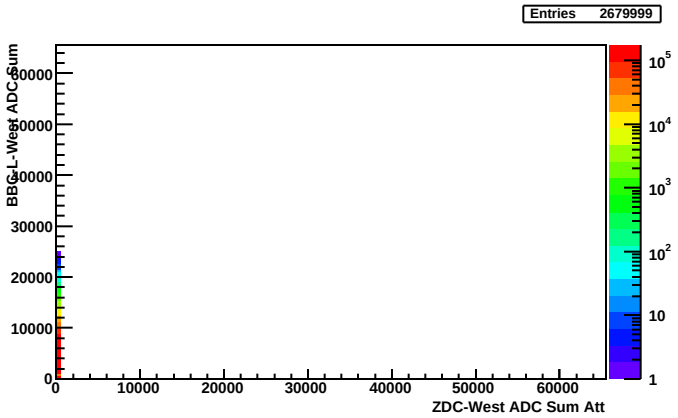

Entries 2679999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

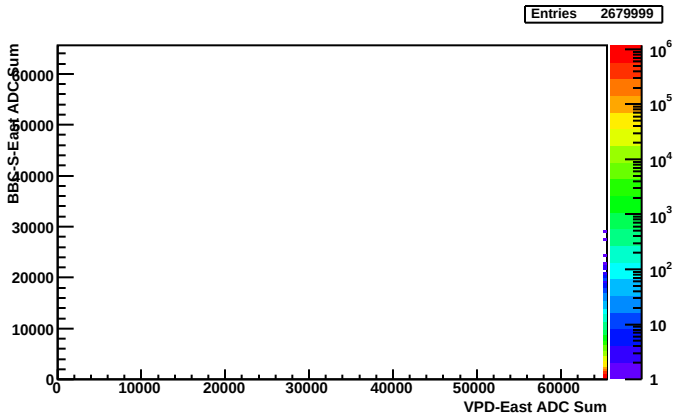

Entries 2679999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

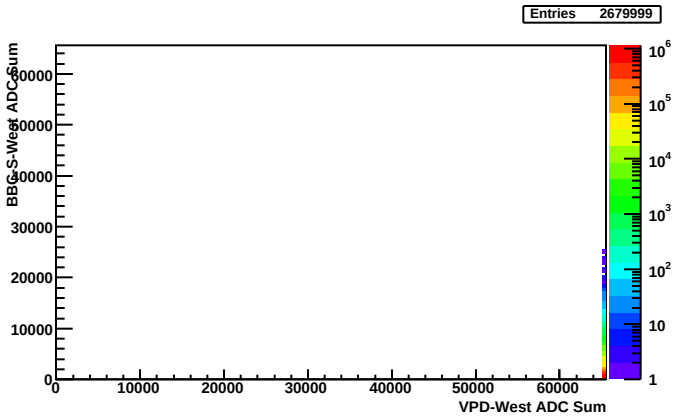

Entries 2679999
BBC-L-East ADC Sum
VPD-East ADC Sum

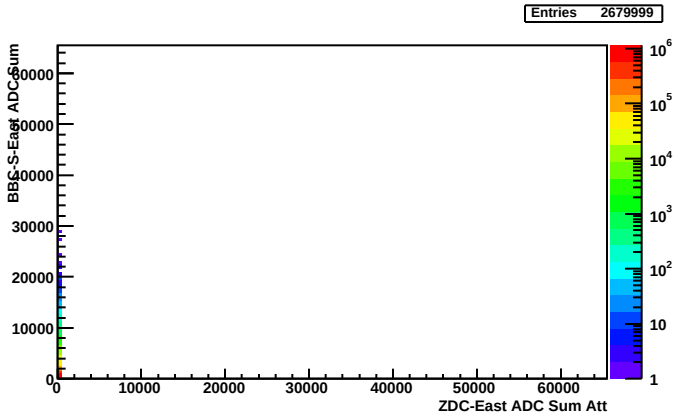

Entries 2679999
BBC-L-West ADC Sum
VPD-West ADC Sum

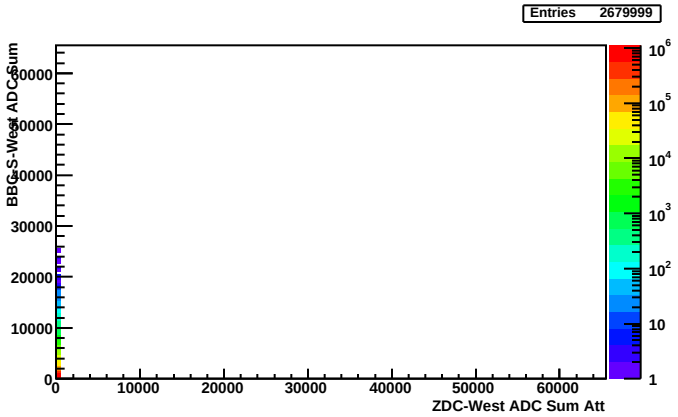

Entries 2679999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

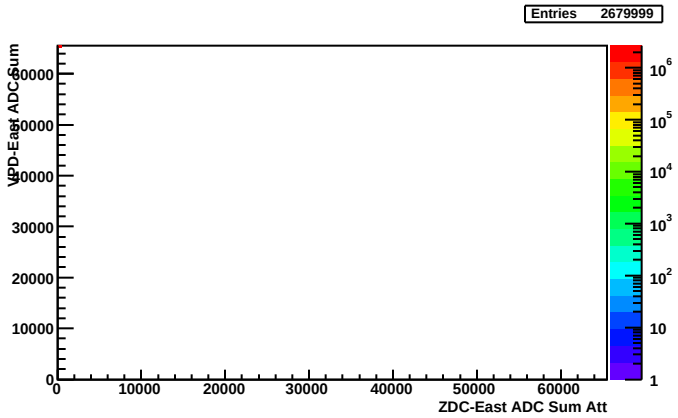

Entries 2679999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

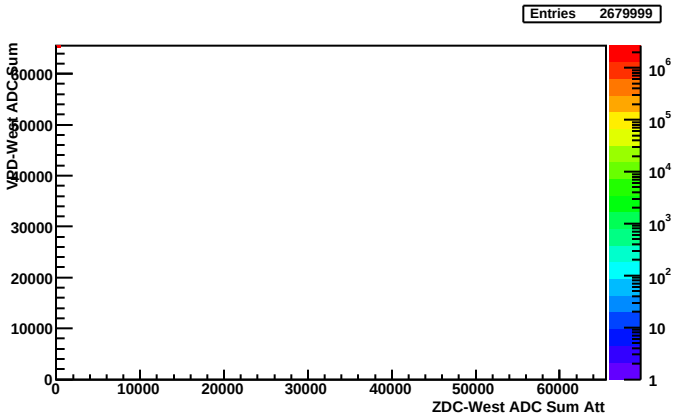

Entries 2679999
BBC-S-East ADC Sum
VPD-East ADC Sum

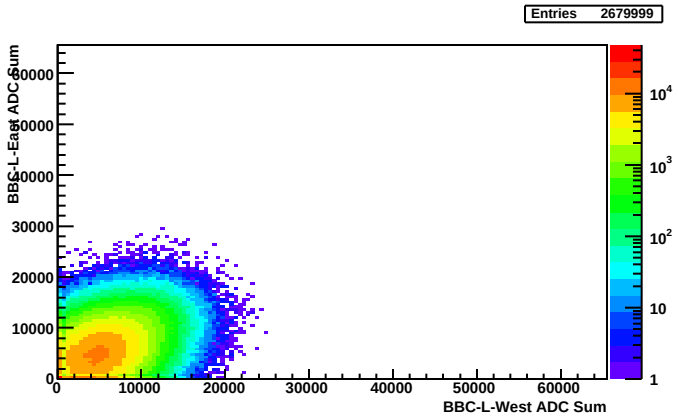

Entries 2679999
BBC-S-West ADC Sum
VPD-West ADC Sum

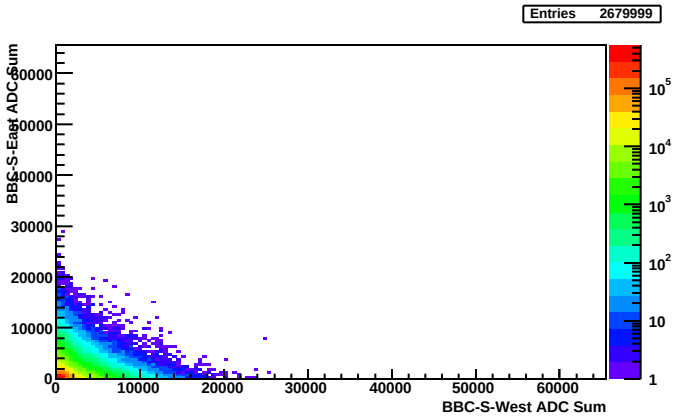

Entries 2679999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

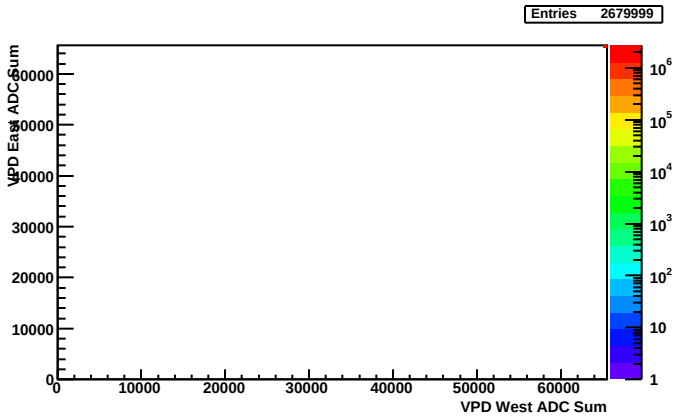

Entries 2679999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

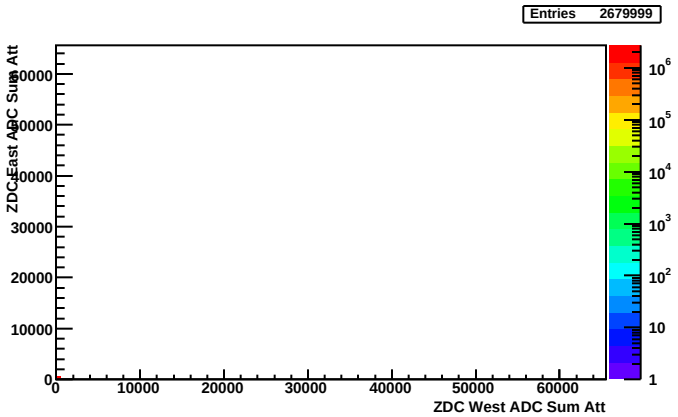

Entries 2679999
VPD-East ADC Sum
ZDC-East ADC Sum Att

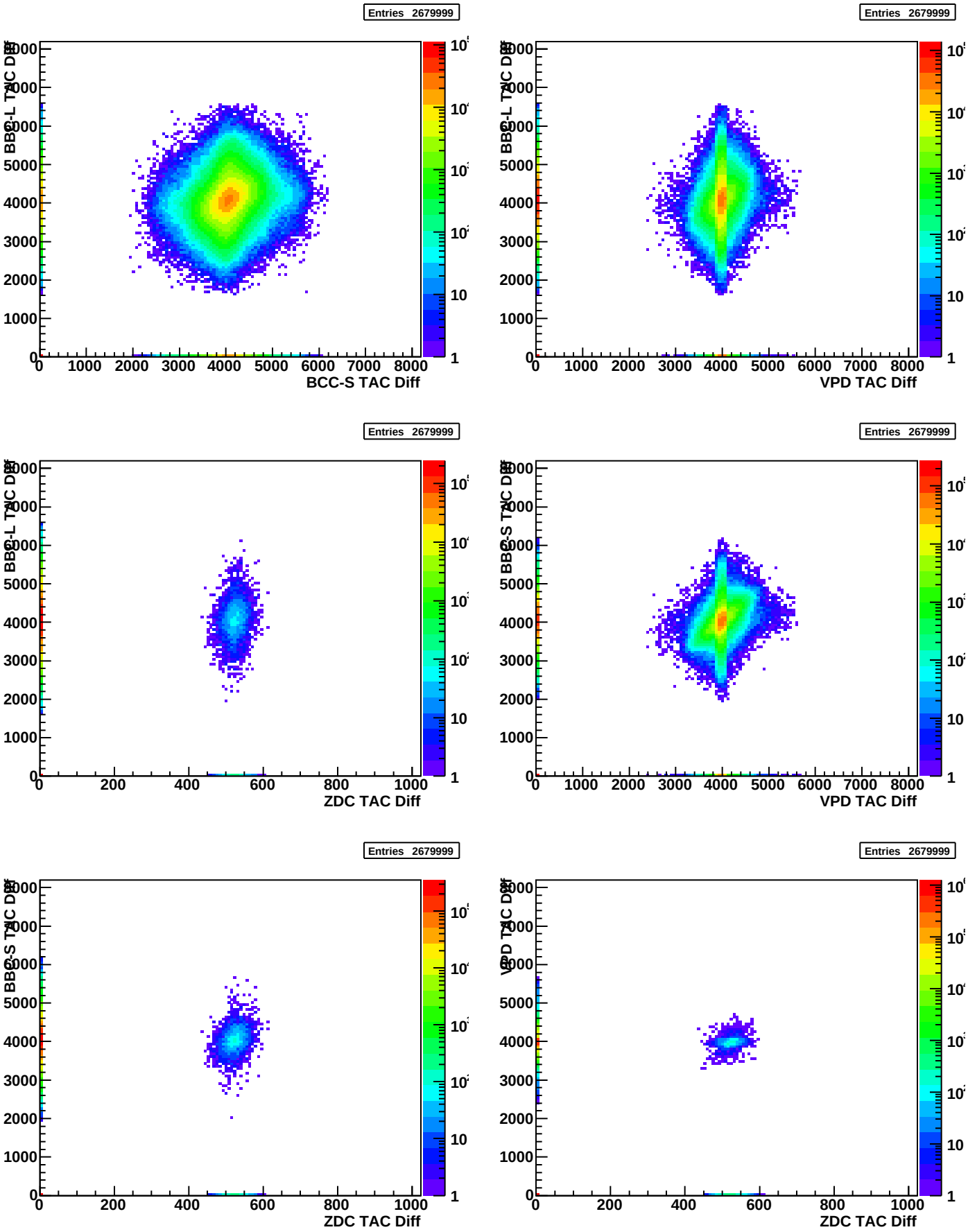

Entries 2679999
VPD-West ADC Sum
ZDC-West ADC Sum Att

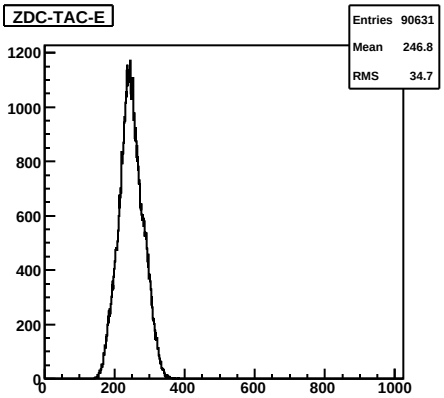

Entries 2679999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

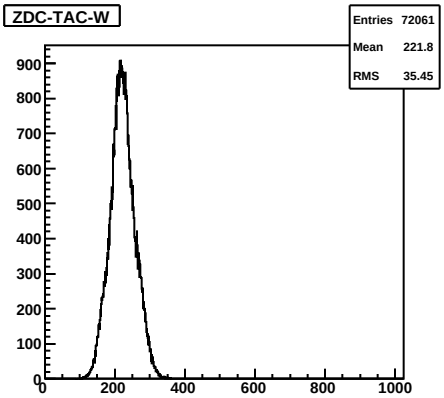

Entries 2679999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

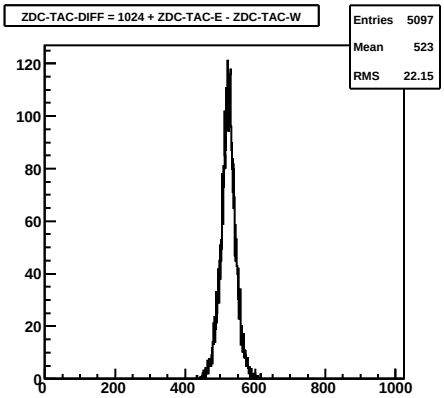

Entries 2679999
VPD East ADC Sum
VPD West ADC Sum

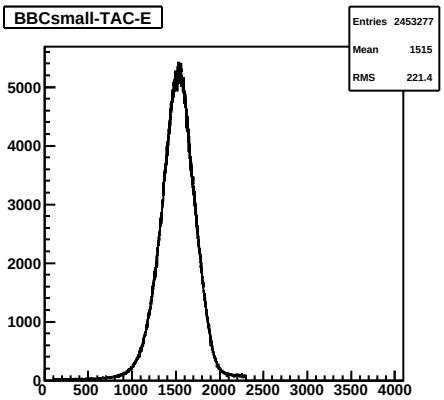

Entries 2679999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

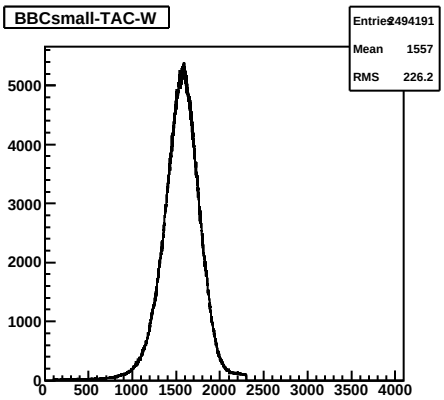

Entries 2679999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2679999
BBC-L TAC Diff
VPD TAC Diff
Entries 2679999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2679999
BBC-S TAC Diff
VPD TAC Diff
Entries 2679999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2679999
VPD TAC Diff
ZDC TAC Diff

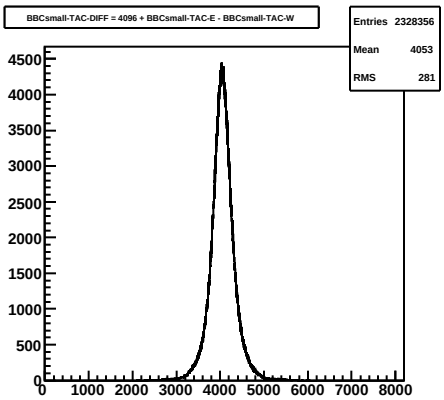

ZDC-TAC-E
Entries 90631
Mean 246.8
RMS 34.7

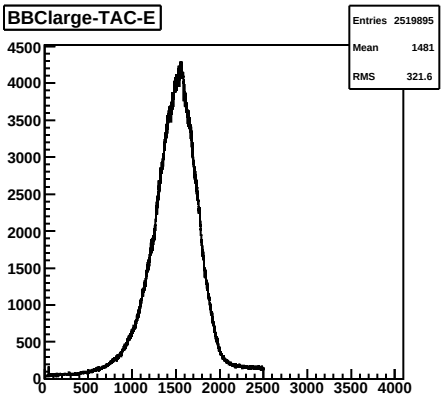

ZDC-TAC-W
Entries 72061
Mean 221.8
RMS 35.45

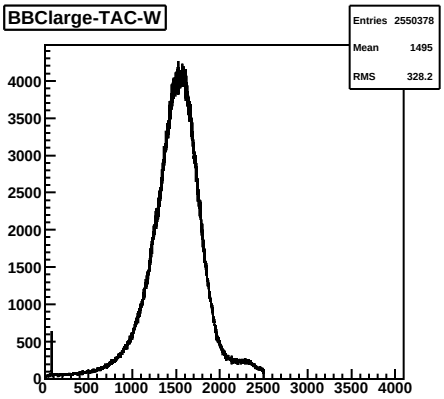

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5097
Mean 523
RMS 22.15

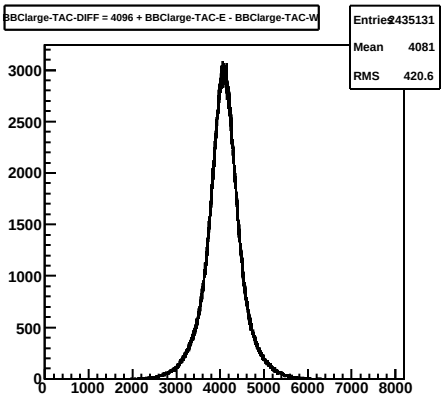

BBCsmall-TAC-E
Entries 2453277
Mean 1515
RMS 221.4

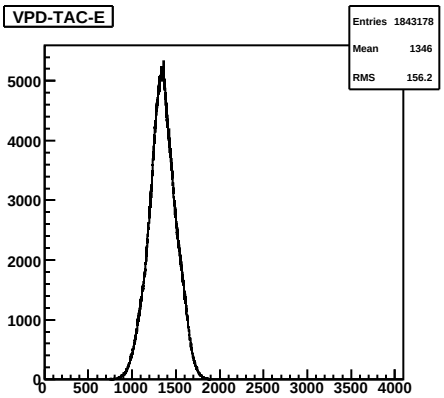

BBCsmall-TAC-W
Entries 2494191
Mean 1557
RMS 226.2

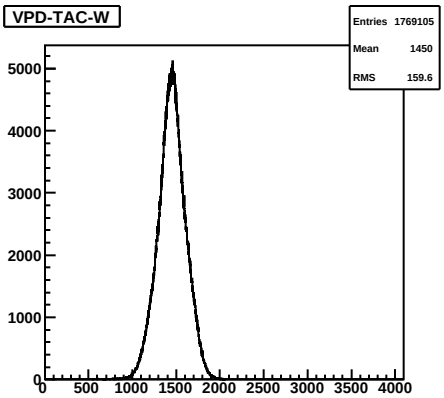

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2328356
Mean 4053
RMS 281

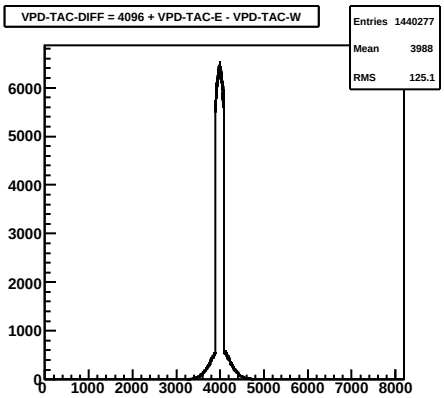

BBClarge-TAC-E
Entries 2519895
Mean 1481
RMS 321.6

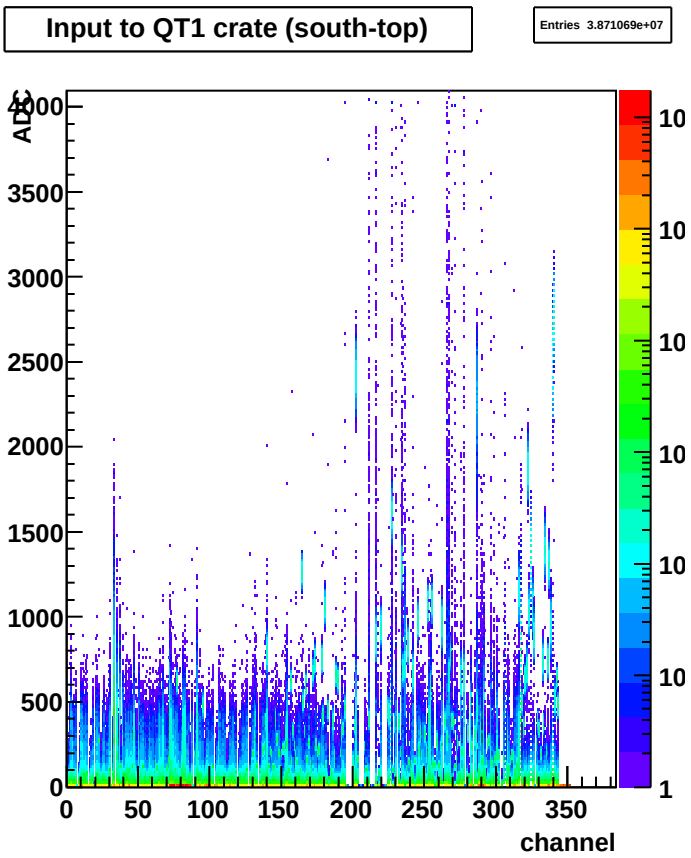

BBClarge-TAC-W
Entries 2550378
Mean 1495
RMS 328.2

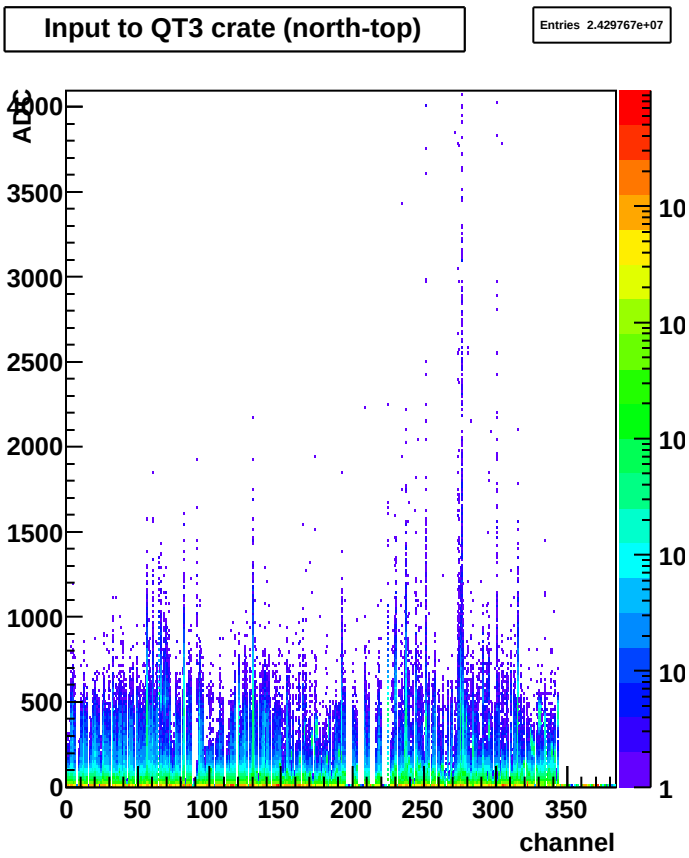

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2435131
Mean 4081
RMS 420.6

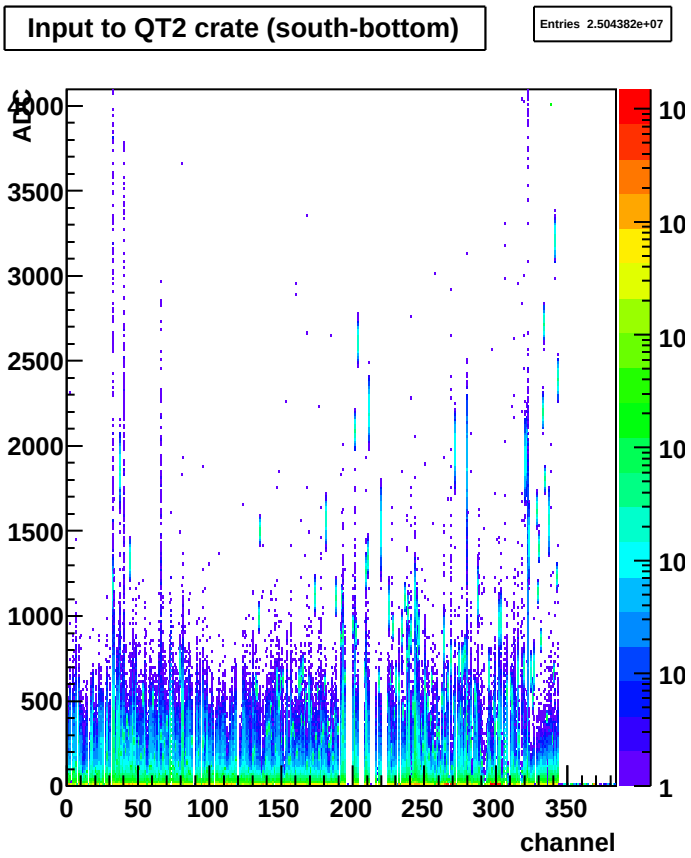

VPD-TAC-E
Entries 1843178
Mean 1346
RMS 156.2

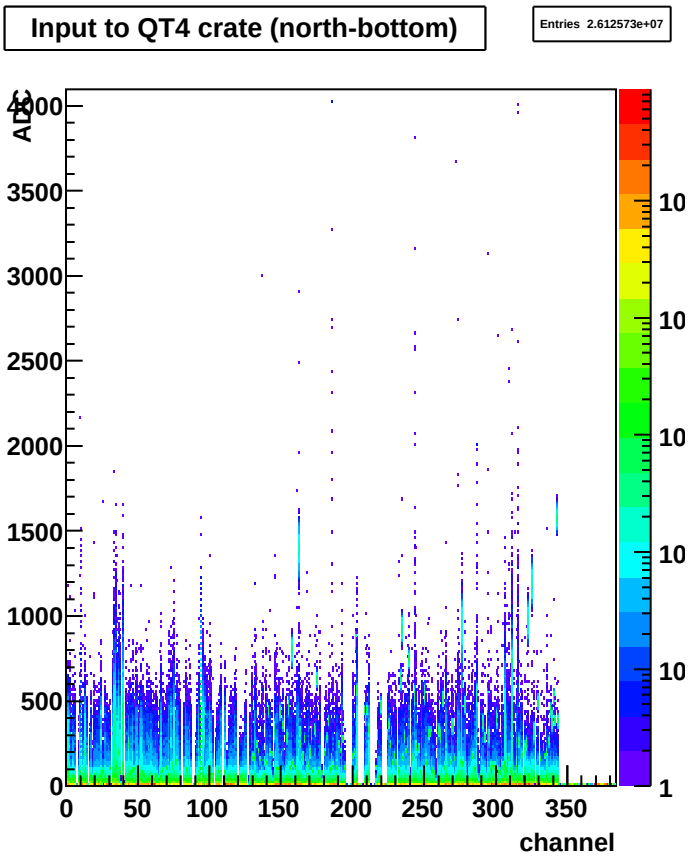

VPD-TAC-W
Entries 1769105
Mean 1450
RMS 159.6

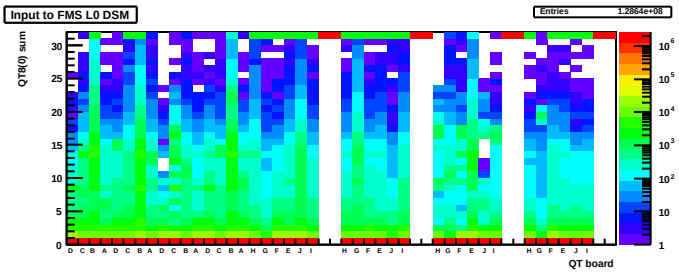

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1440277
Mean 3988
RMS 125.1

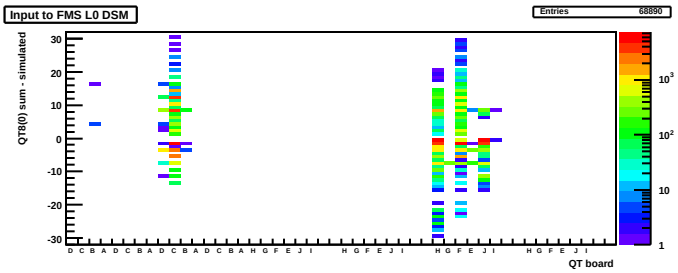

Input to QT1 crate (south-top)
Entries 3.871069e+07
ADC
channel

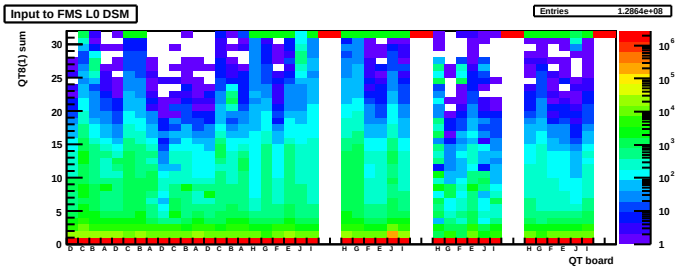

Input to QT3 crate (north-top)
Entries 2.429767e+07
ADC
channel

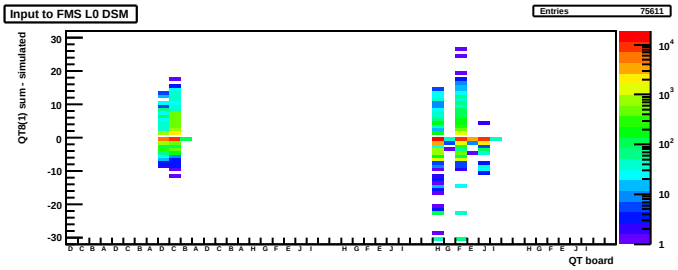

Input to QT2 crate (south-bottom)
Entries 2.504382e+07
ADC
channel

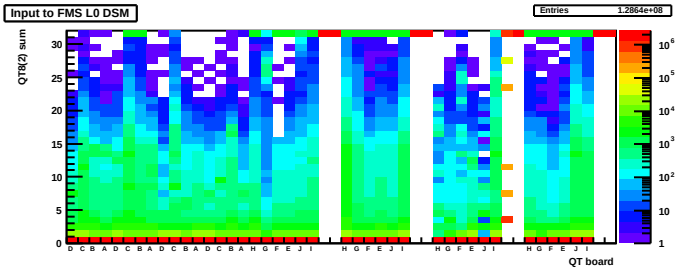

Input to QT4 crate (north-bottom)
Entries 2.612573e+07
ADC
channel

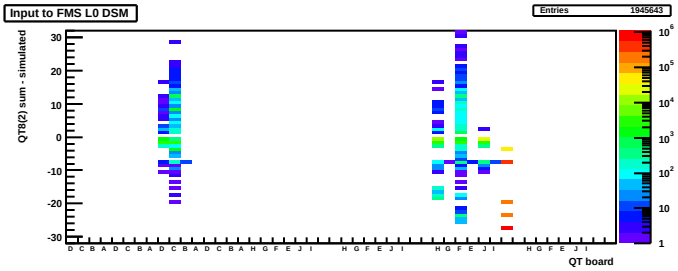

Input to FMS L0 DSM
Entries 1.2864e+08
QT8(0) sum
QT board

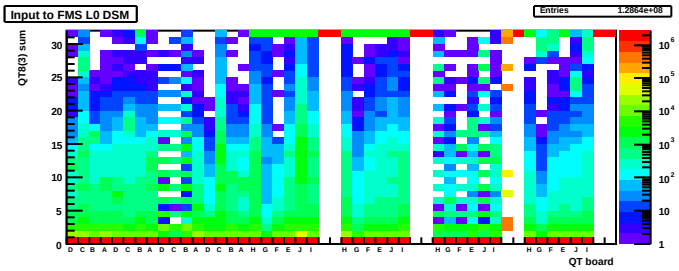

Input to FMS L0 DSM
Entries 68890
QT8(0) sum - simulated
QT board

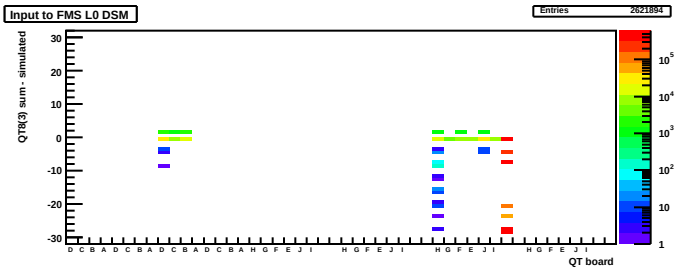

Input to FMS L0 DSM
Entries 1.2864e+08
QT8(1) sum
QT board

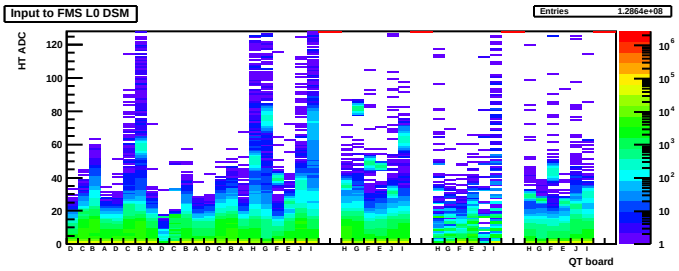

Input to FMS L0 DSM
Entries 75611
QT8(1) sum - simulated
QT board

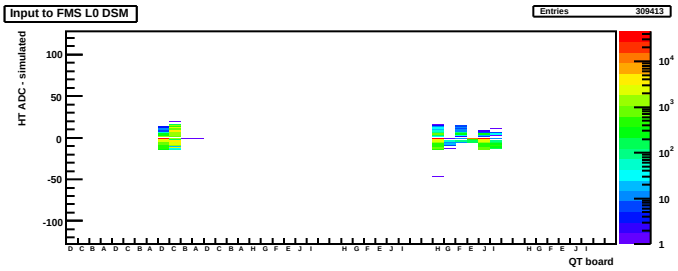

Input to FMS L0 DSM
Entries 1.2864e+08
QT8(2) sum
QT board

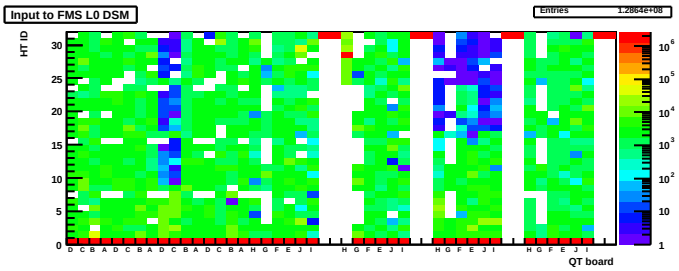

Input to FMS L0 DSM
Entries 1945643
QT8(2) sum - simulated
QT board

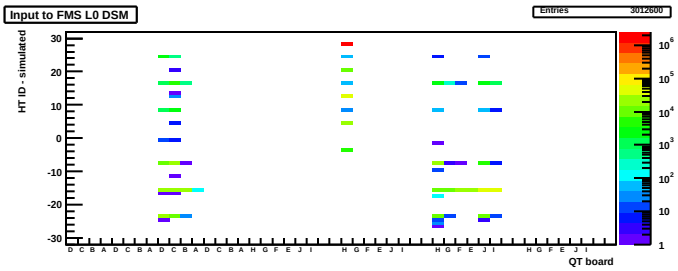

Input to FMS L0 DSM
Entries 1.2864e+08
QT8(3) sum
QT board

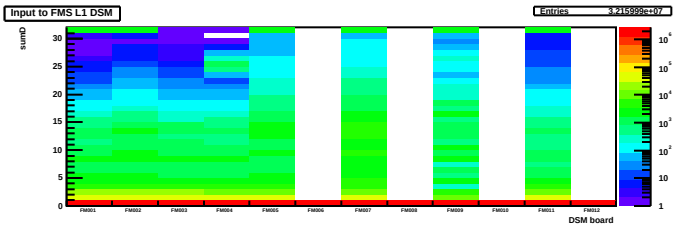

Input to FMS L0 DSM
Entries 2621894
QT8(3) sum - simulated
QT board

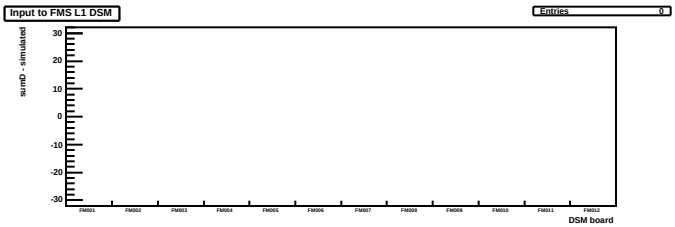

Input to FMS L0 DSM
Entries 1.2864e+08
HT ADC
QT board

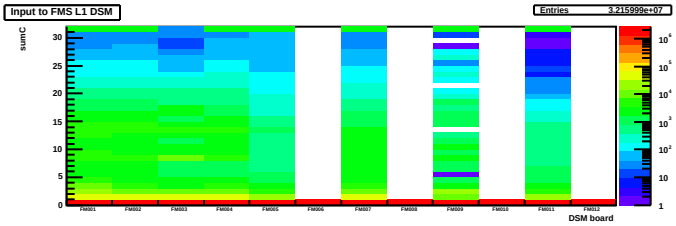

Input to FMS L0 DSM
Entries 309413
HT ADC - simulated
QT board

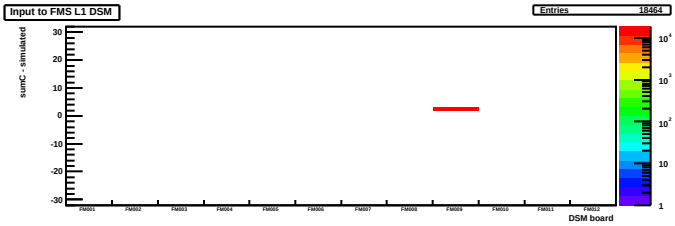

Input to FMS L0 DSM
Entries 1.2864e+08
HT ID
QT board

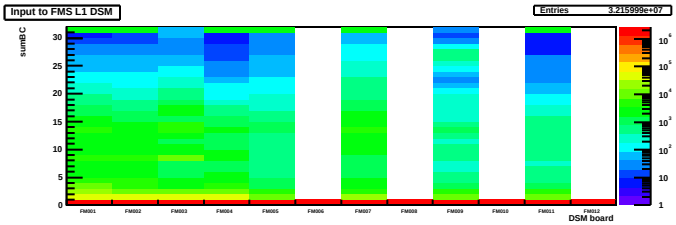

Input to FMS L0 DSM
Entries 3012600
HT ID - simulated
QT board

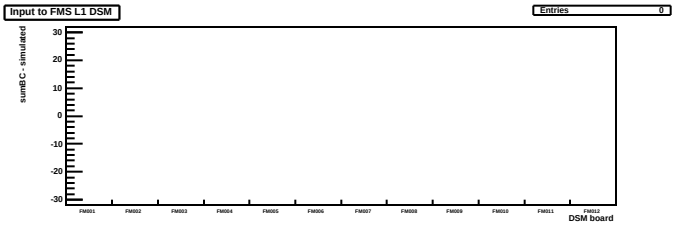

Input to FMS L1 DSM
Entries 3.215999e+07
sumD
DSM board

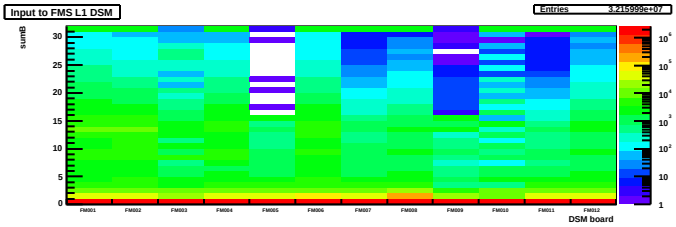

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

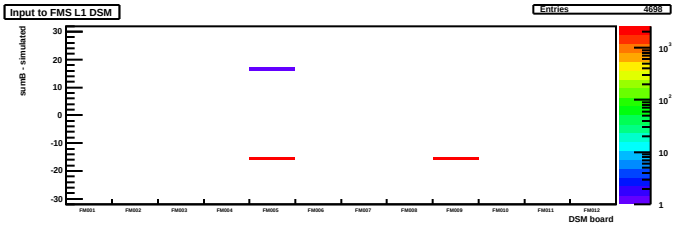

Input to FMS L1 DSM
Entries 3.215999e+07
sumC
DSM board

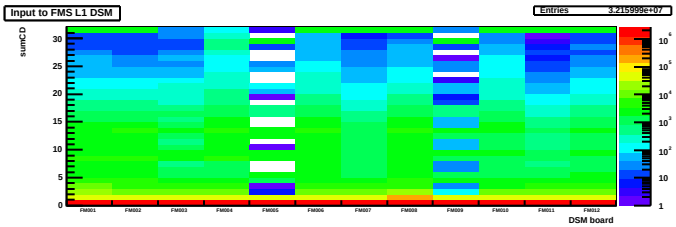

Input to FMS L1 DSM
Entries 18464
sumC - simulated
DSM board

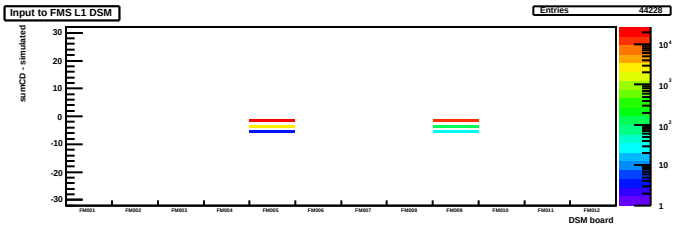

Input to FMS L1 DSM
Entries 3.215999e+07
sumBC
DSM board

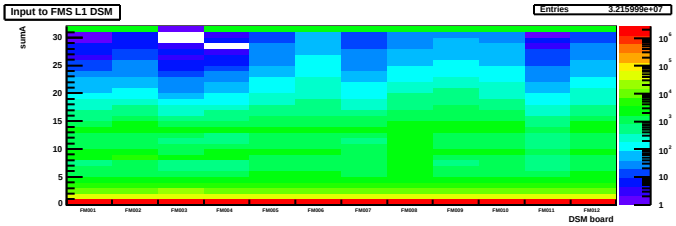

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

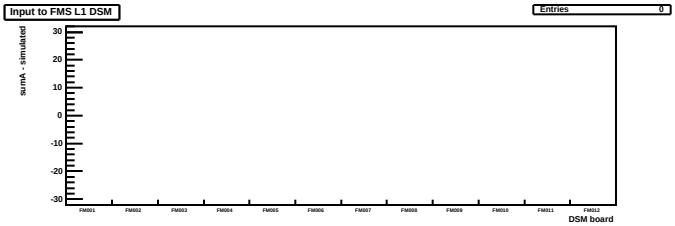

Input to FMS L1 DSM
Entries 3.215999e+07
sumB
DSM board

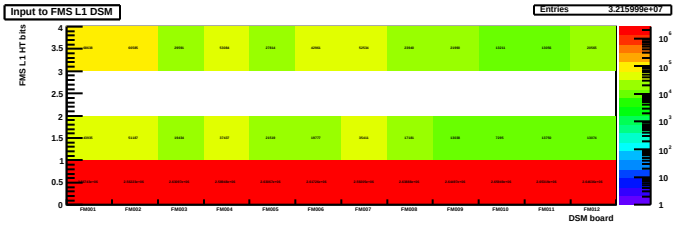

Input to FMS L1 DSM
Entries 4698
sumB - simulated
DSM board

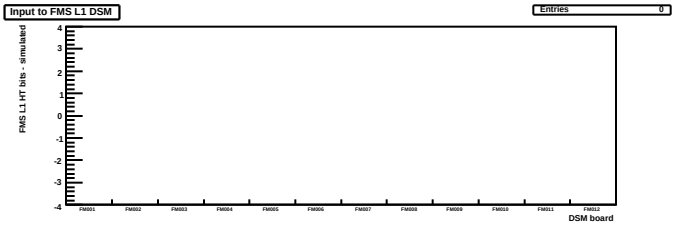

Input to FMS L1 DSM
Entries 3.215999e+07
sumCD
DSM board

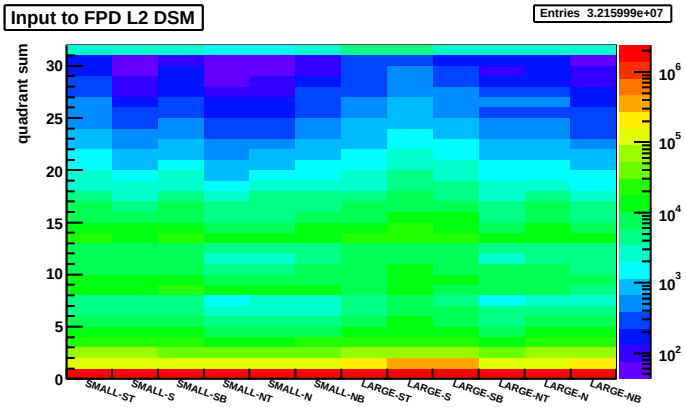

Input to FMS L1 DSM
Entries 44228
sumCD - simulated
DSM board

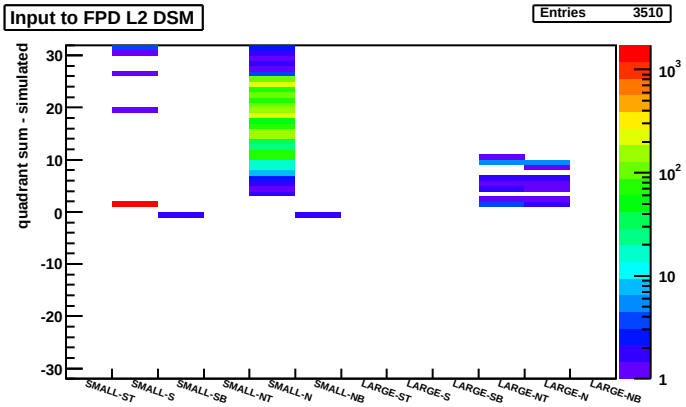

Input to FMS L1 DSM
Entries 3.215999e+07
sumA
DSM board

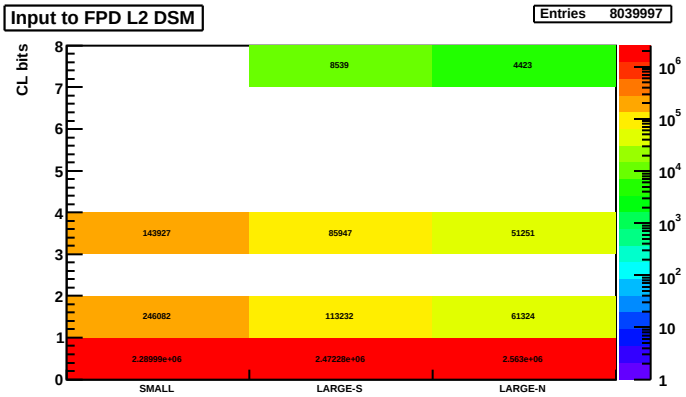

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

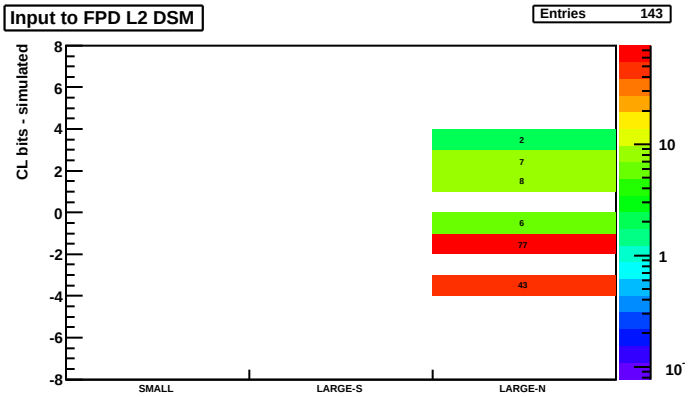

Input to FMS L1 DSM
Entries 3.215999e+07
FMS L1 HT bits
DSM board

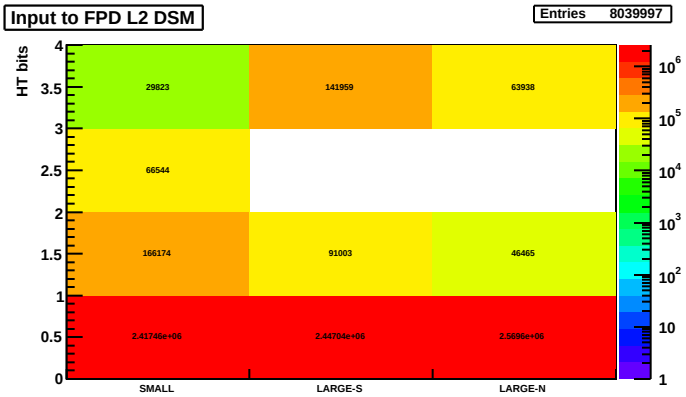

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

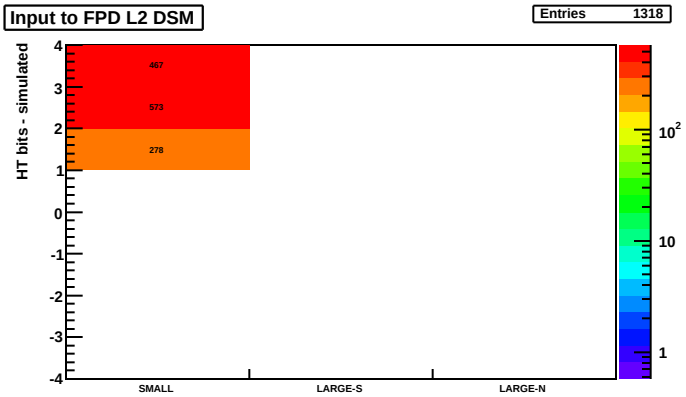

Input to FPD L2 DSM
Entries 3.215999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

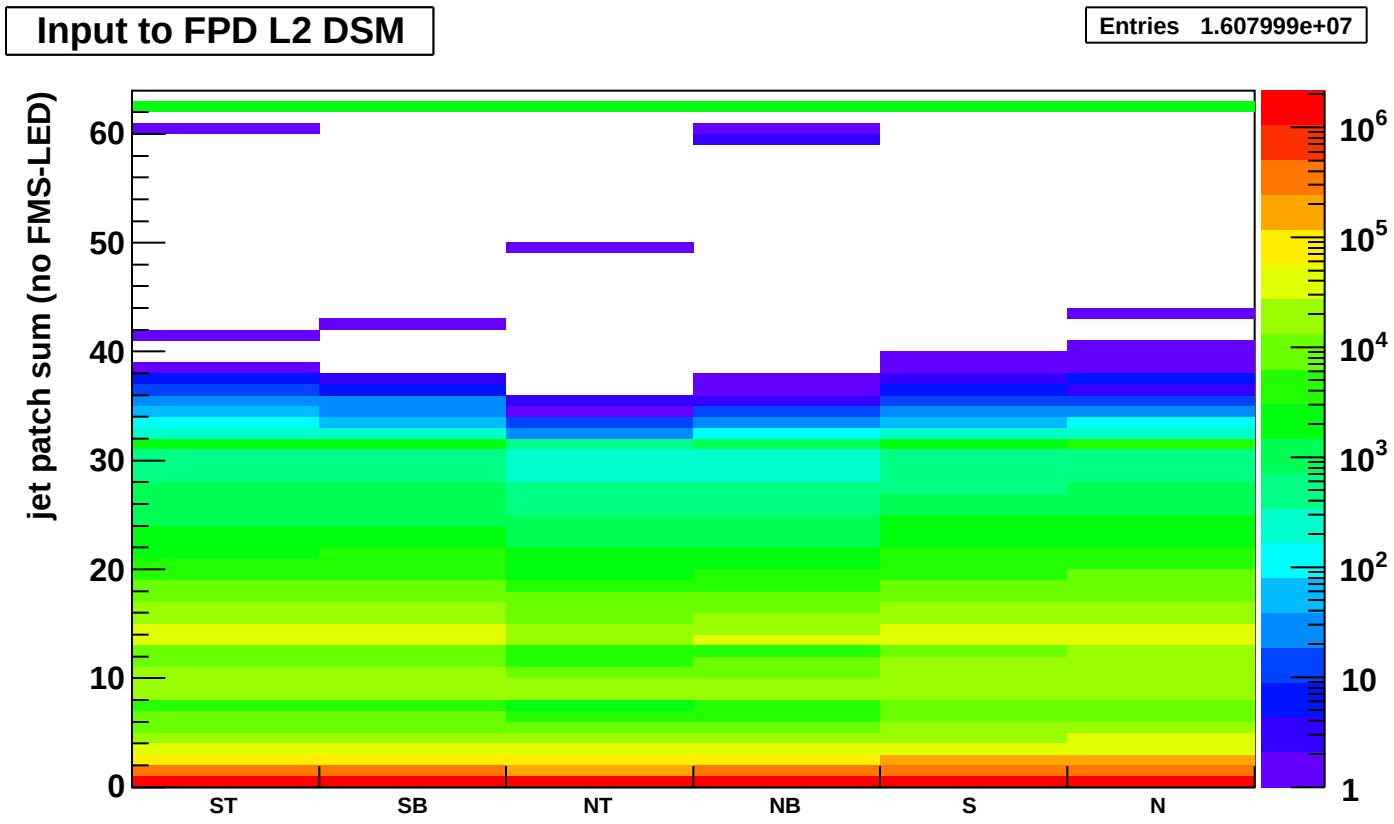

Input to FPD L2 DSM
Entries 3510
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

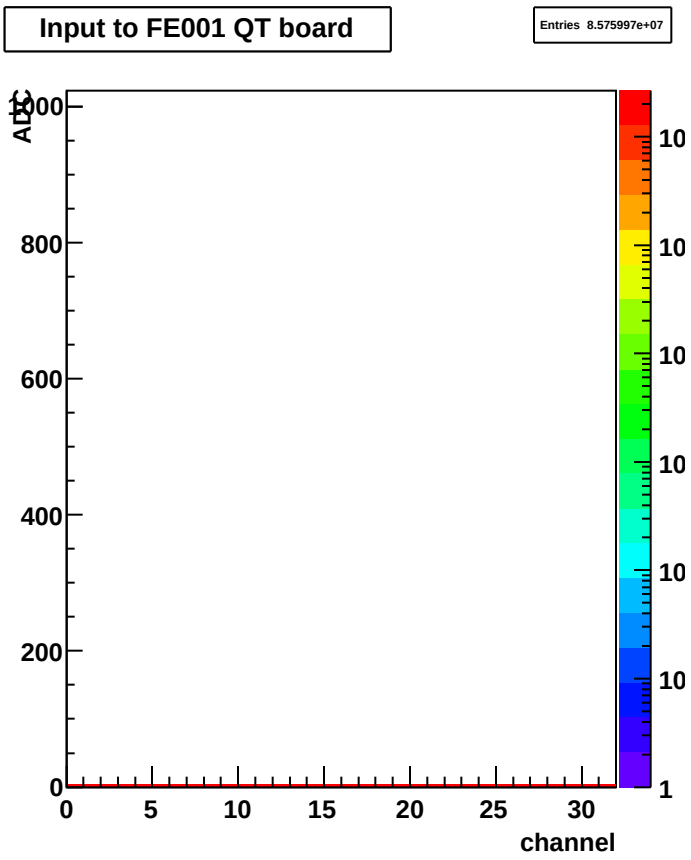

Input to FPD L2 DSM
Entries 8039997
CL bits
8539
4423
143927
85947
51251
246082
113232
61324
2.28999e+06
2.47228e+06
2.563e+06
SMALL
LARGE-S
LARGE-N

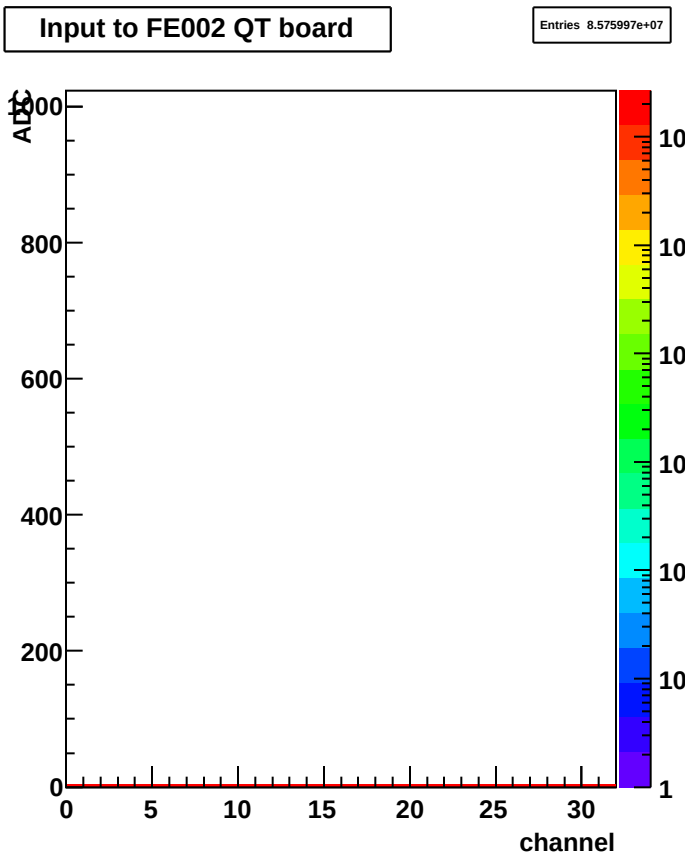

Input to FPD L2 DSM
Entries 143
CL bits - simulated
2
7
8
6
77
43
SMALL
LARGE-S
LARGE-N

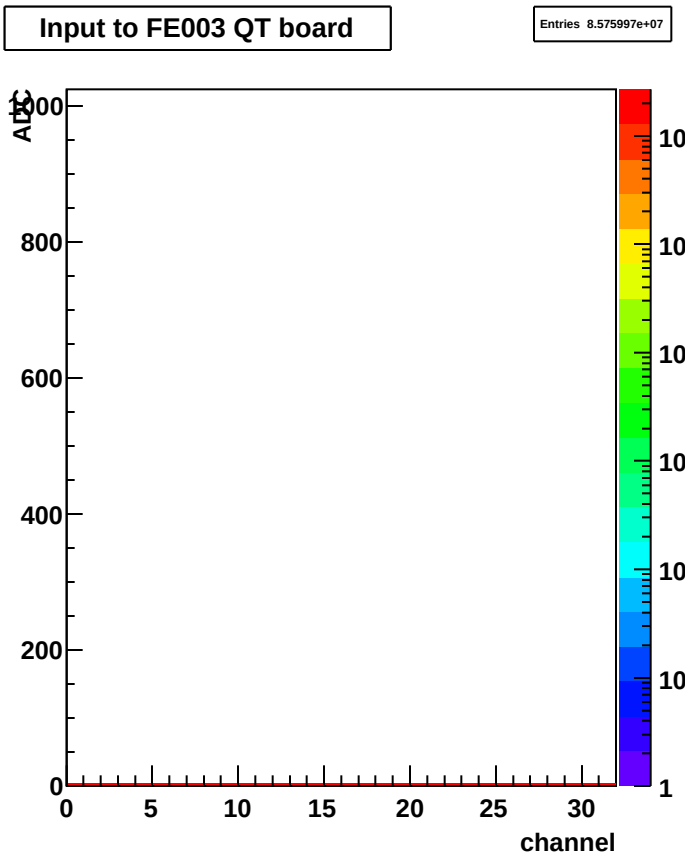

Input to FPD L2 DSM
Entries 8039997
HT bits
29823
141959
63938
66544
166174
91003
46465
2.41746e+06
2.44704e+06
2.5696e+06
SMALL
LARGE-S
LARGE-N

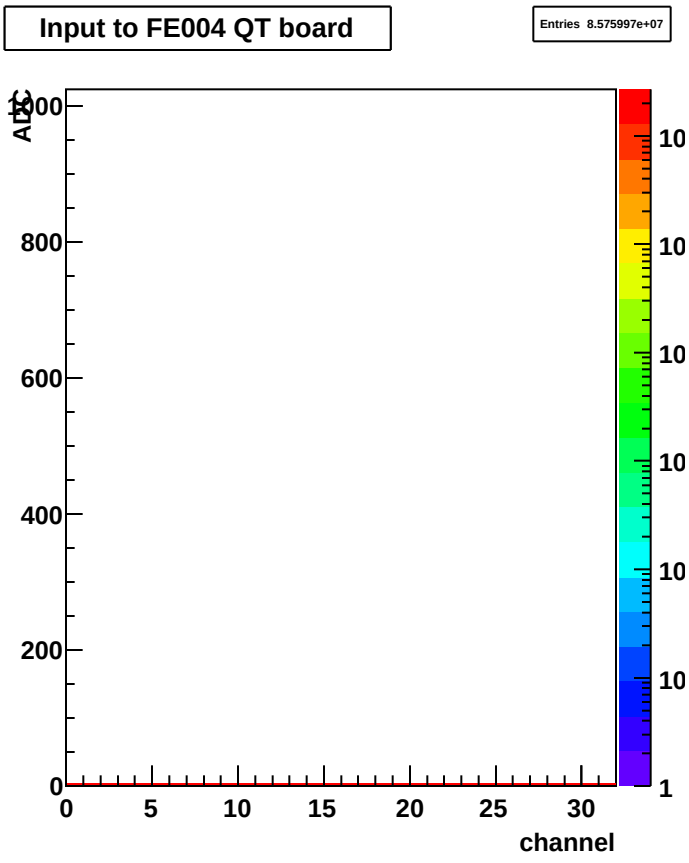

Input to FPD L2 DSM
Entries 1318
HT bits - simulated
467
573
278
SMALL
LARGE-S
LARGE-N

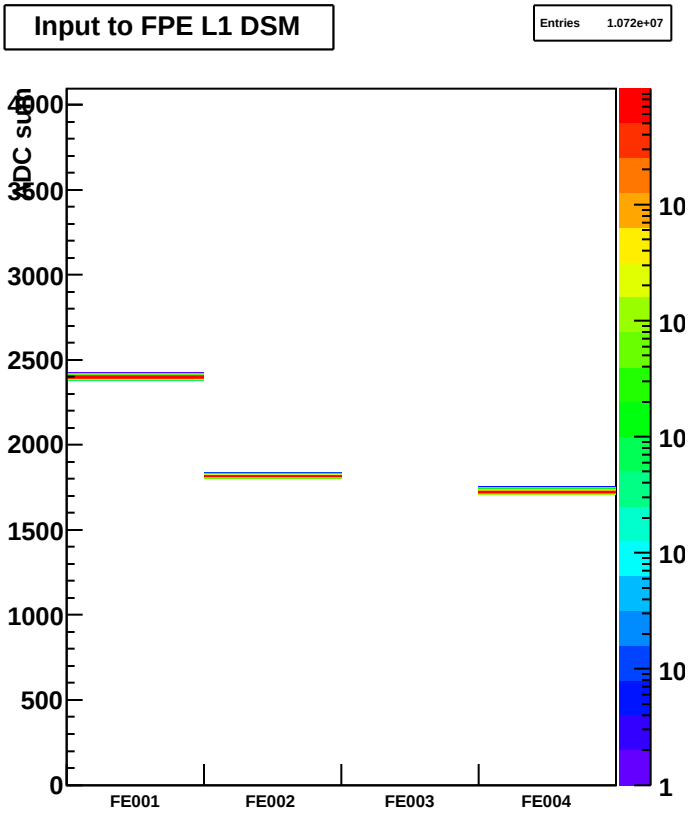

Input to FPD L2 DSM
Entries 1.607999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

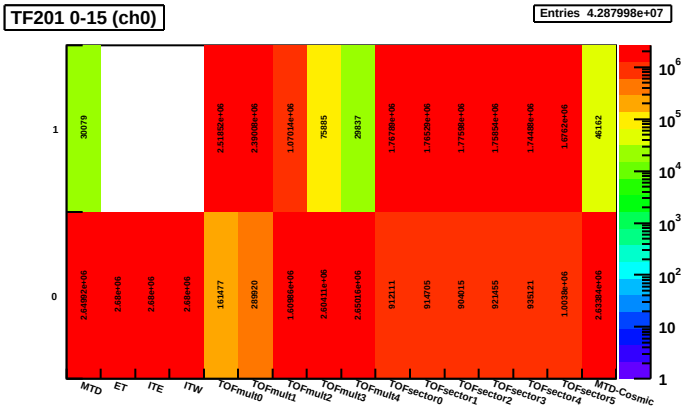

Input to FE001 QT board
Entries 8.575997e+07
ADC
channel

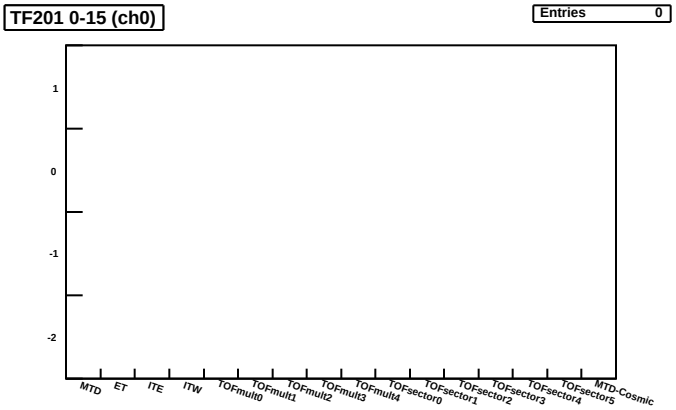

Input to FE002 QT board
Entries 8.575997e+07
ADC
channel

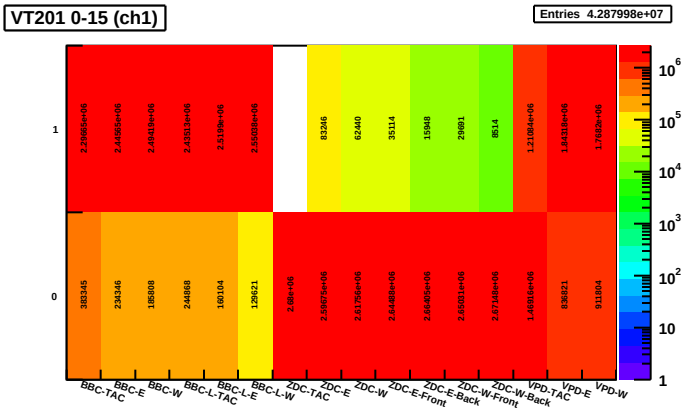

Input to FE003 QT board
Entries 8.575997e+07
ADC
channel

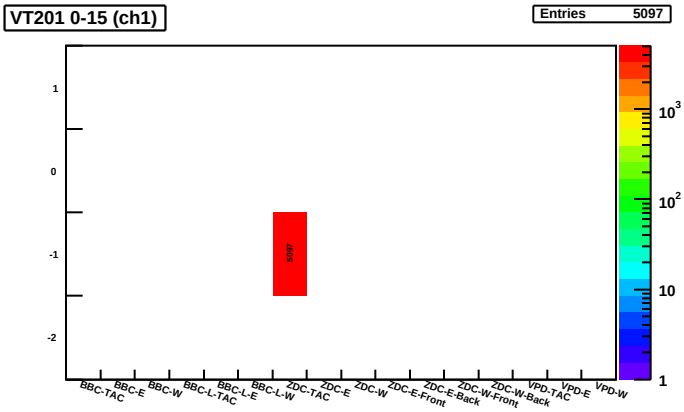

Input to FE004 QT board
Entries 8.575997e+07
ADC
channel

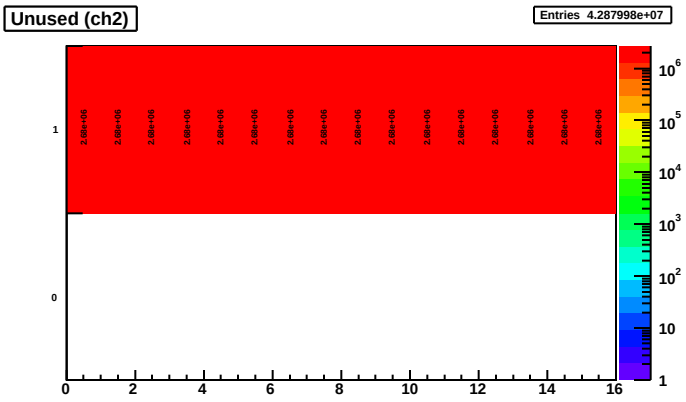

Input to FPE L1 DSM
Entries 1.072e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

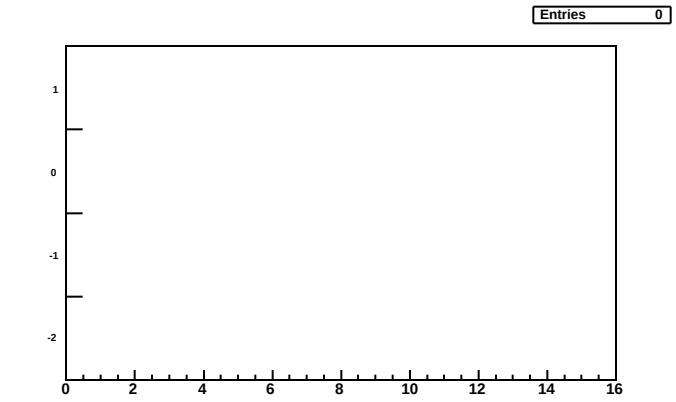

TF201 0-15 (ch0)
Entries 4.287998e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

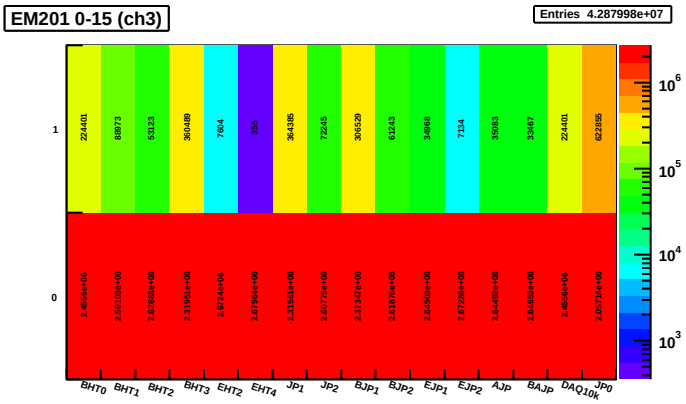

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

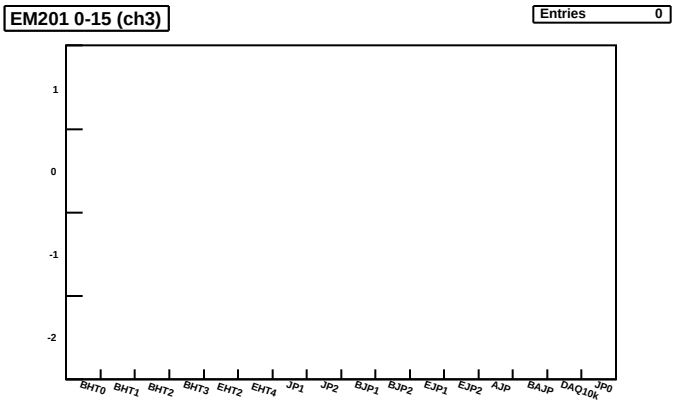

VT201 0-15 (ch1)
Entries 4.287998e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 5097
5097
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

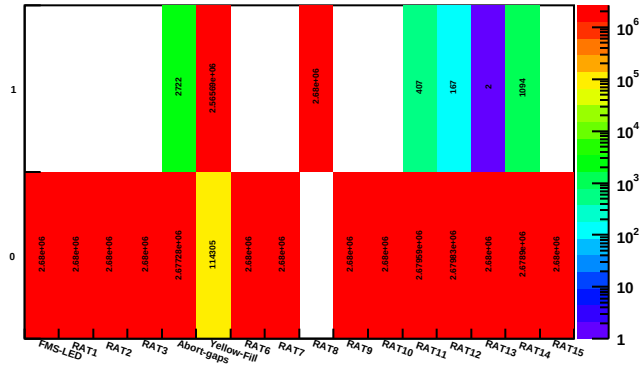

Unused (ch2)
Entries 4.287998e+07

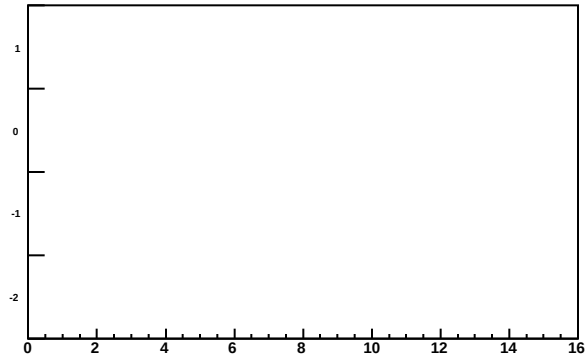

Entries 0


EM201 0-15 (ch3)
Entries 4.287998e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

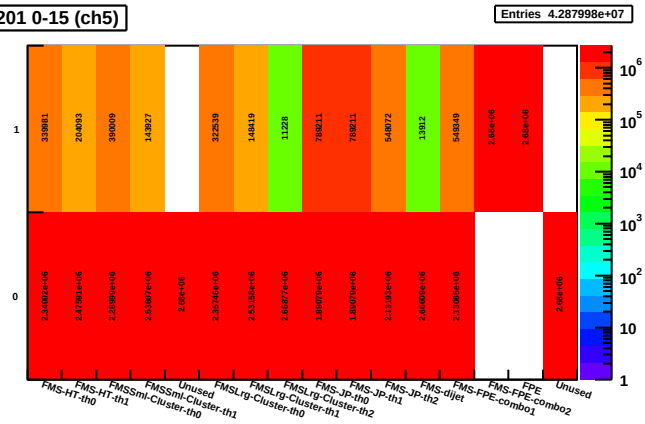

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

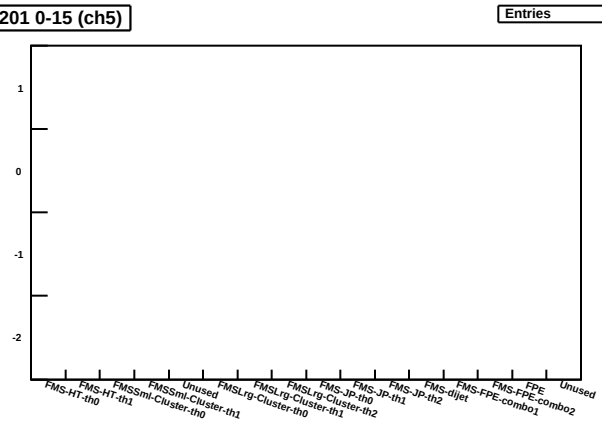




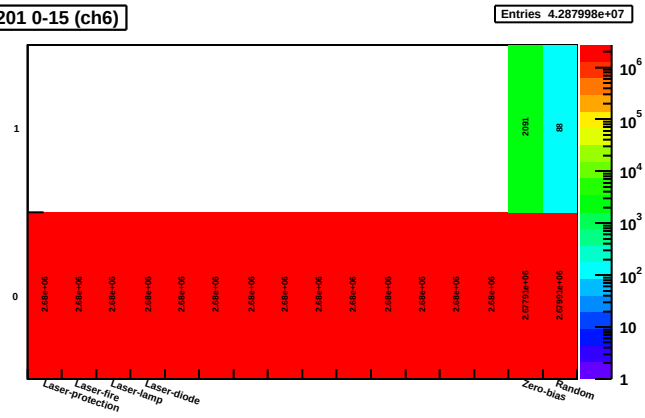
FP201 0-15 (ch5)

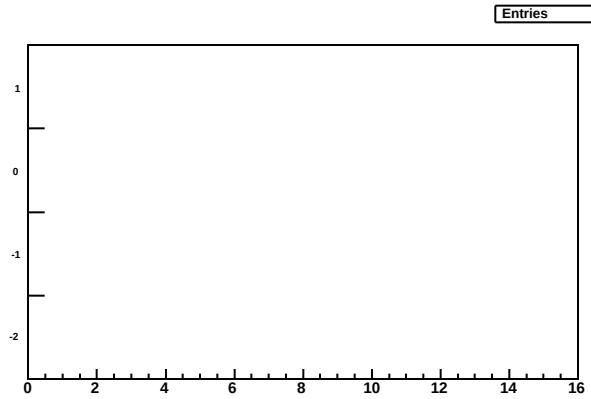
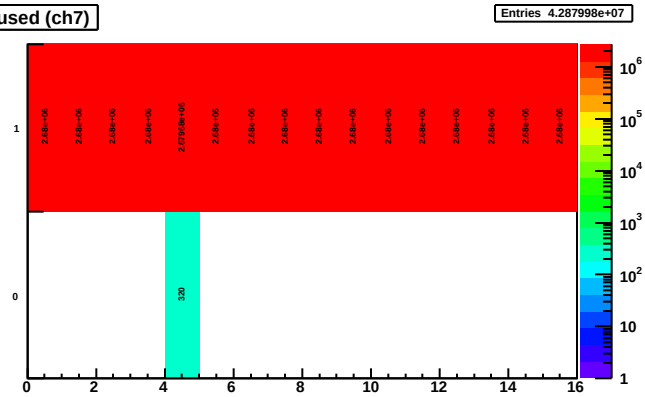
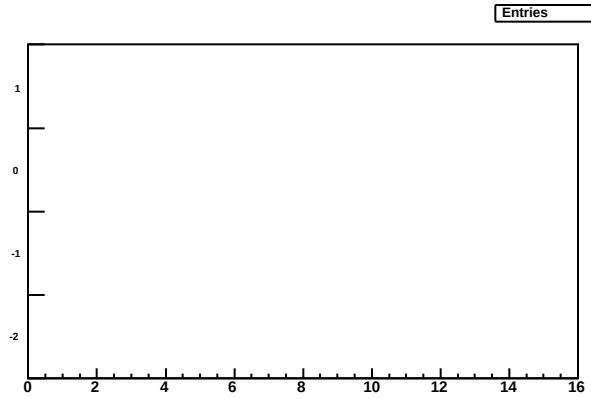


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)