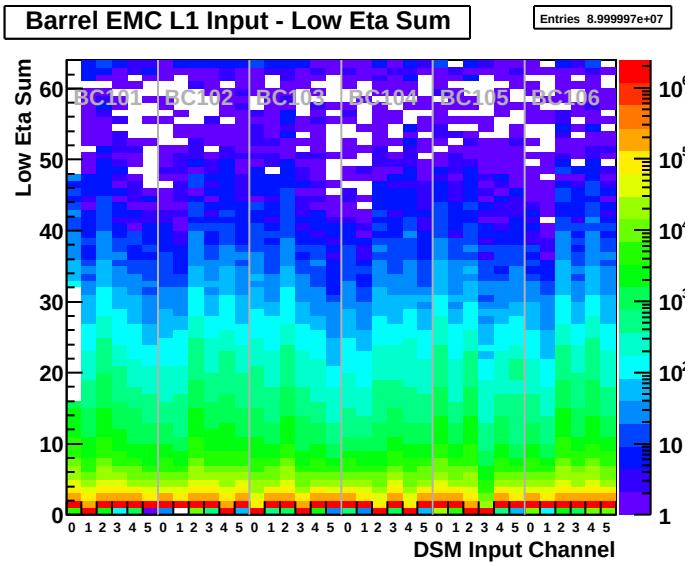
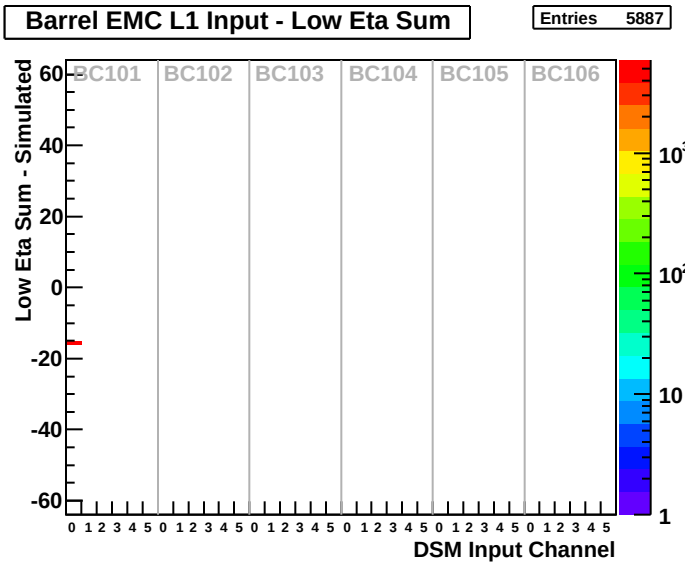
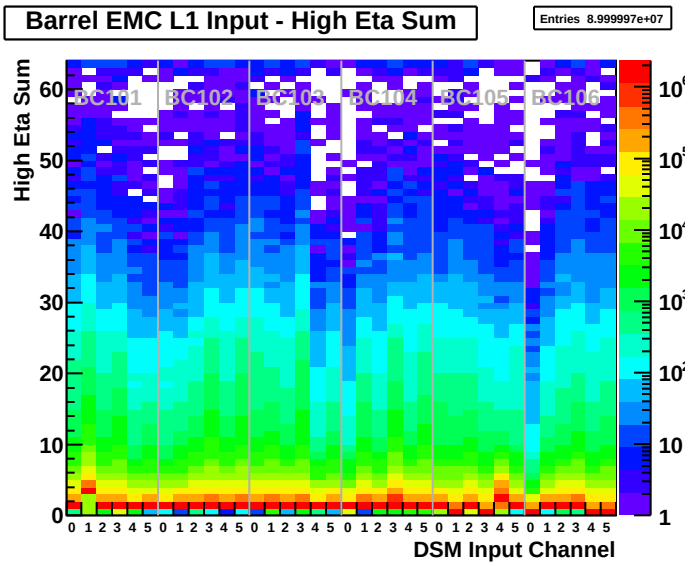
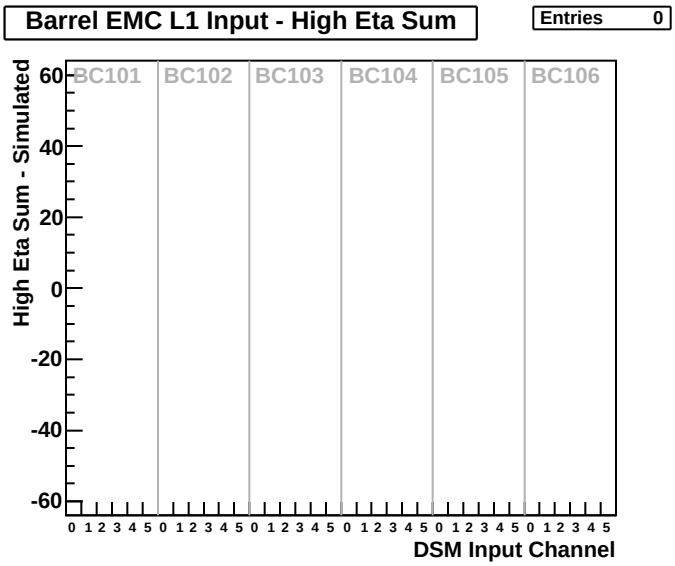
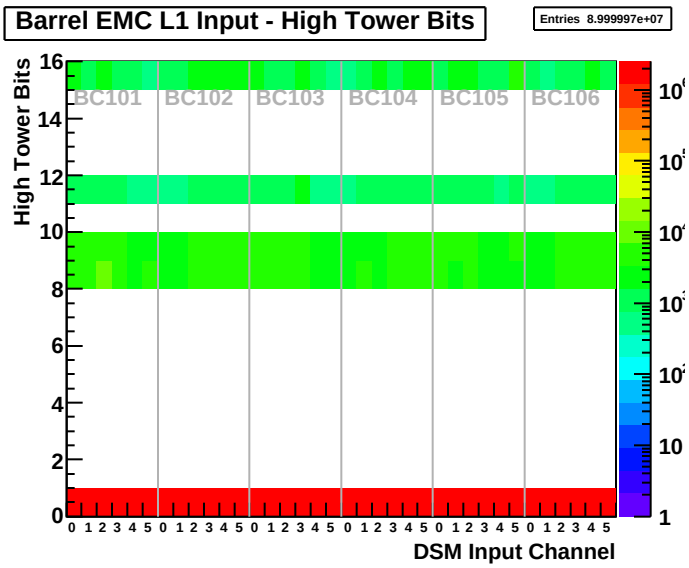
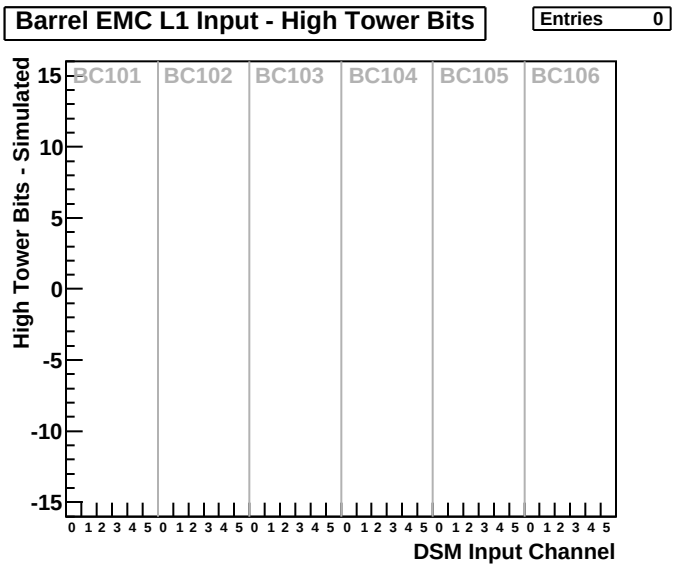

Barrel EMC L1 Input - Low Eta Sum
Entries 8.999997e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

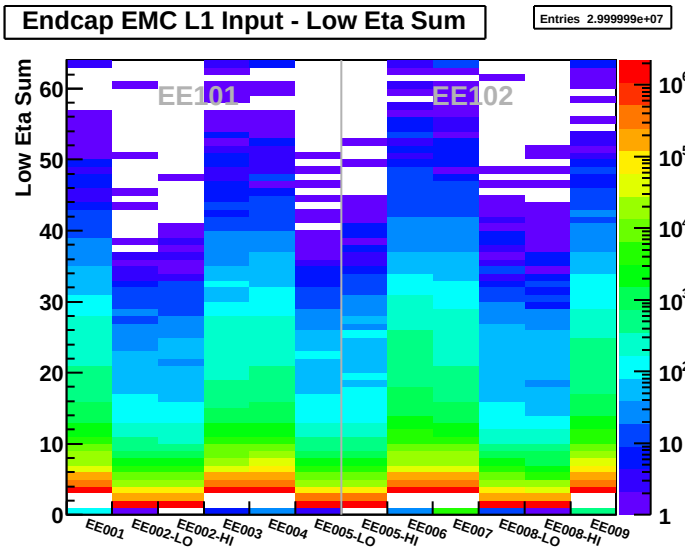

Barrel EMC L1 Input - Low Eta Sum
Entries 5887
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

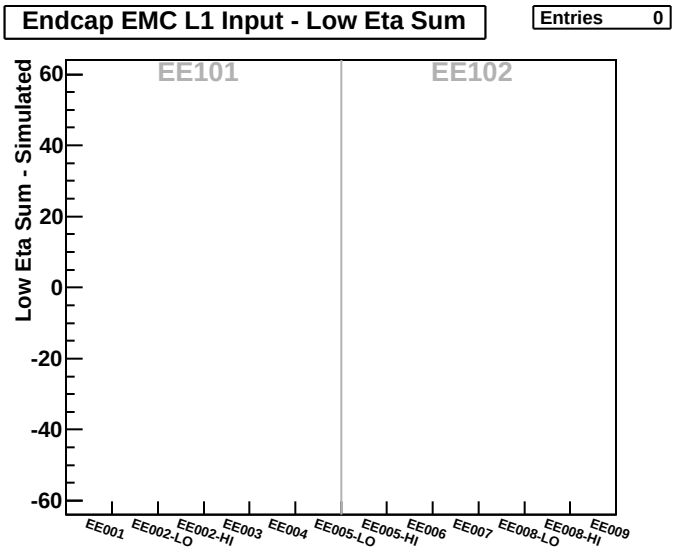

Barrel EMC L1 Input - High Eta Sum
Entries 8.999997e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

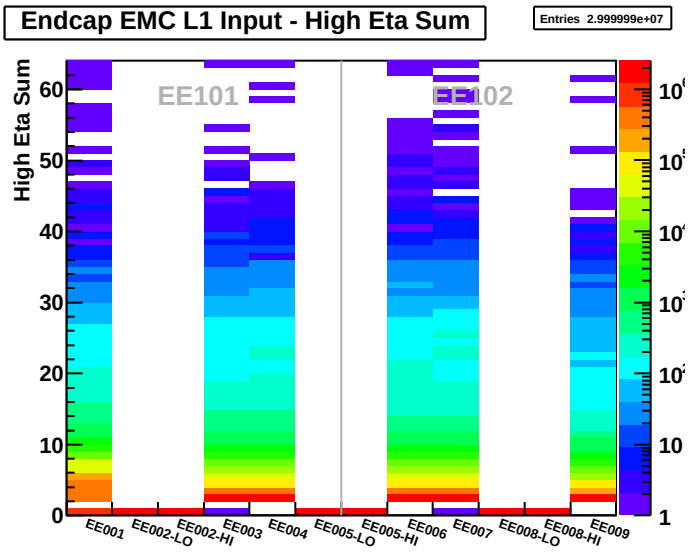

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

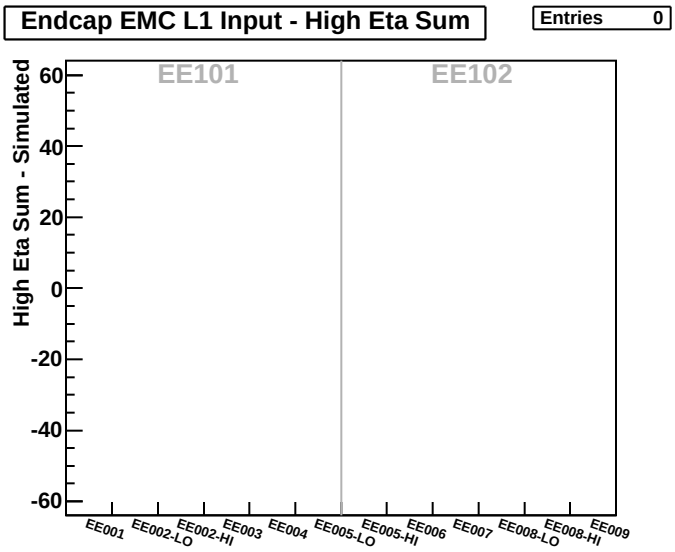

Barrel EMC L1 Input - High Tower Bits
Entries 8.999997e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

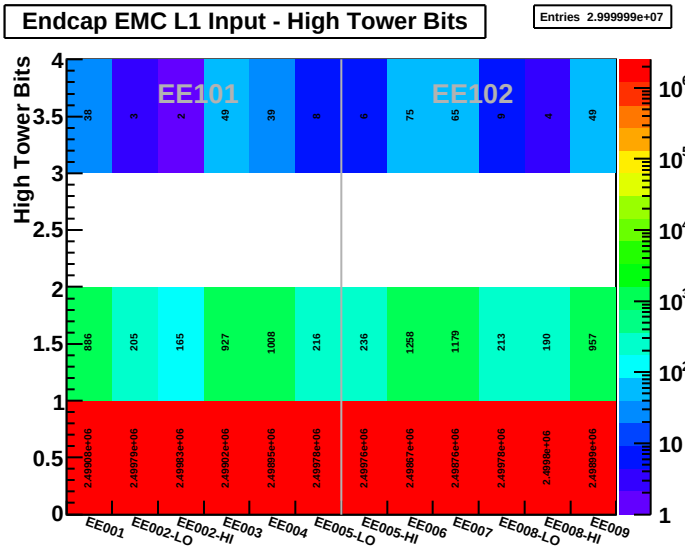

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

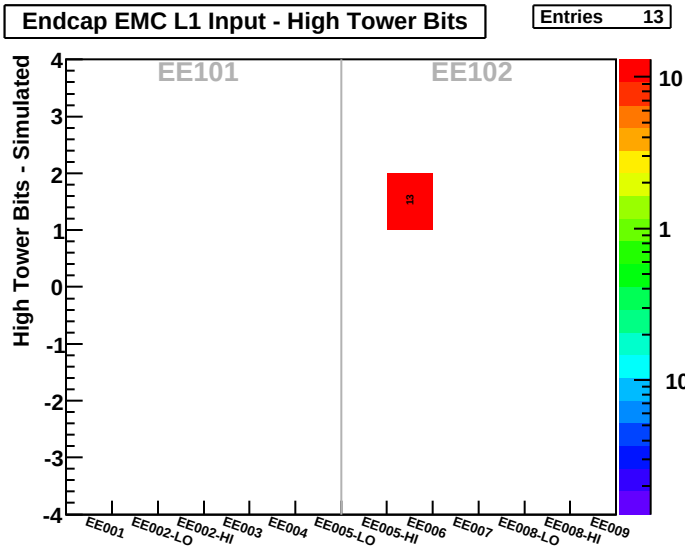

Endcap EMC L1 Input - Low Eta Sum
Entries 2.999999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

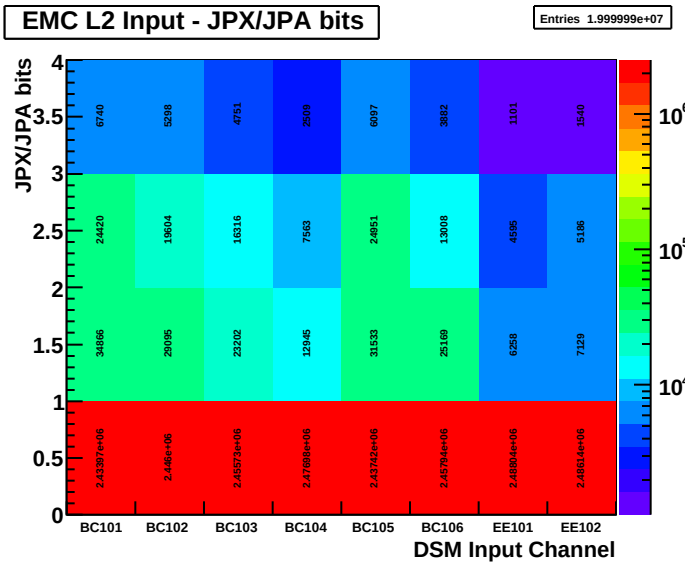

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

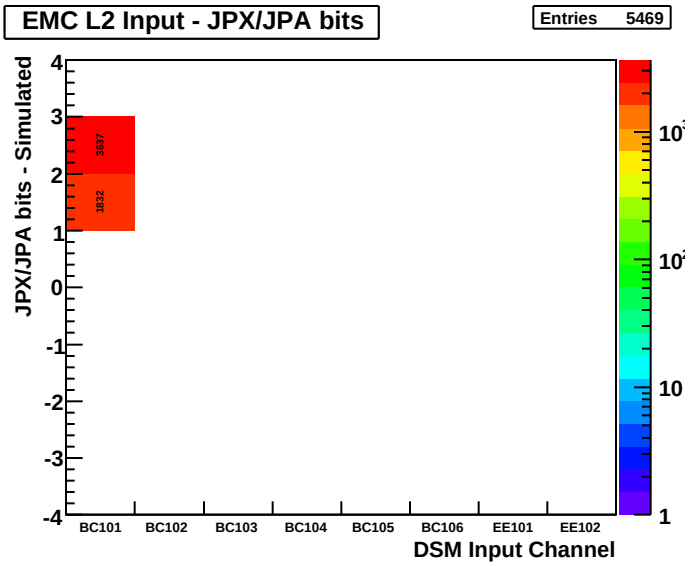

Endcap EMC L1 Input - High Eta Sum
Entries 2.999999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

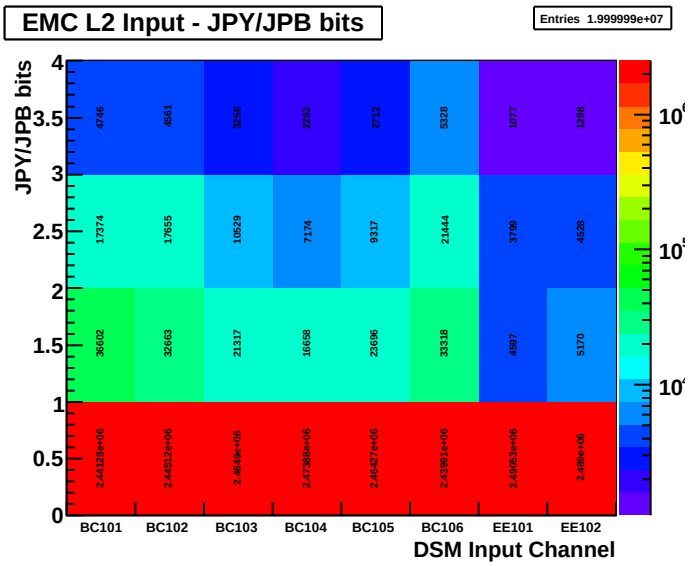

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

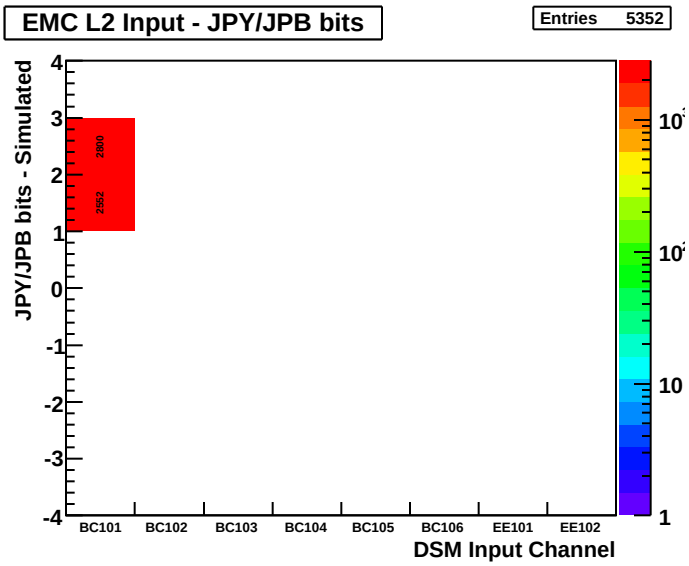

Endcap EMC L1 Input - High Tower Bits
Entries 2.999999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

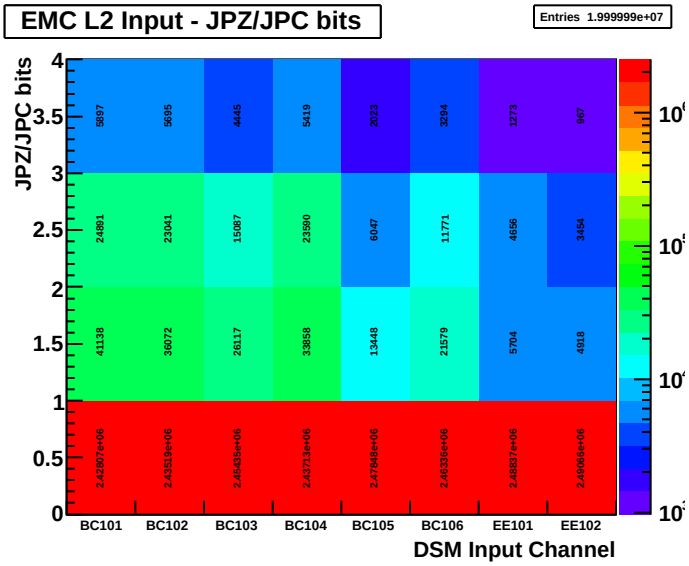

Endcap EMC L1 Input - High Tower Bits
Entries 13
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

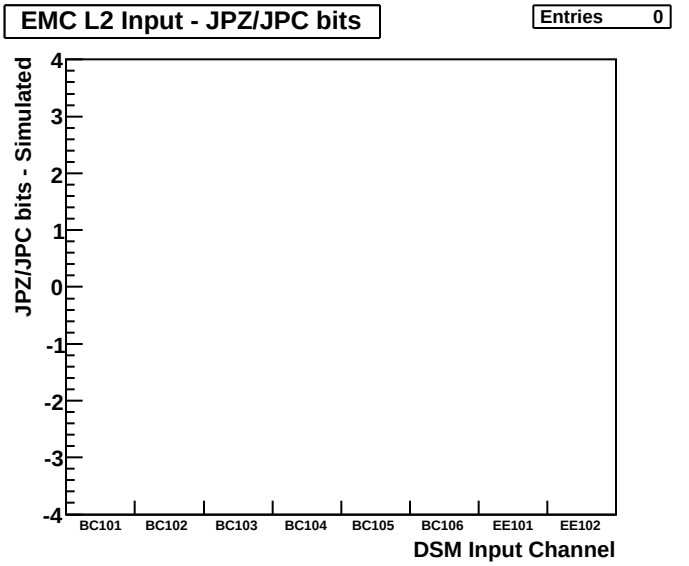

EMC L2 Input - JPX/JPA bits
Entries 1.999999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6740 5298 4751 2509 6097 3882 1101 1540
24420 19604 16316 7563 24951 13008 4595 5186
34866 29095 23202 12945 31533 25169 6258 7129
2.43397e+06 2.446e+06 2.45573e+06 2.47608e+06 2.43742e+06 2.45794e+06 2.48804e+06 2.48614e+06

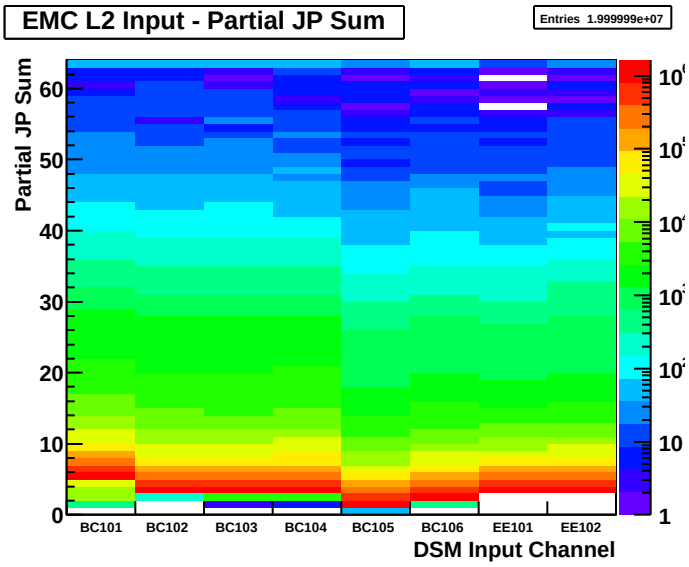

EMC L2 Input - JPX/JPA bits
Entries 5469
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3637
1832

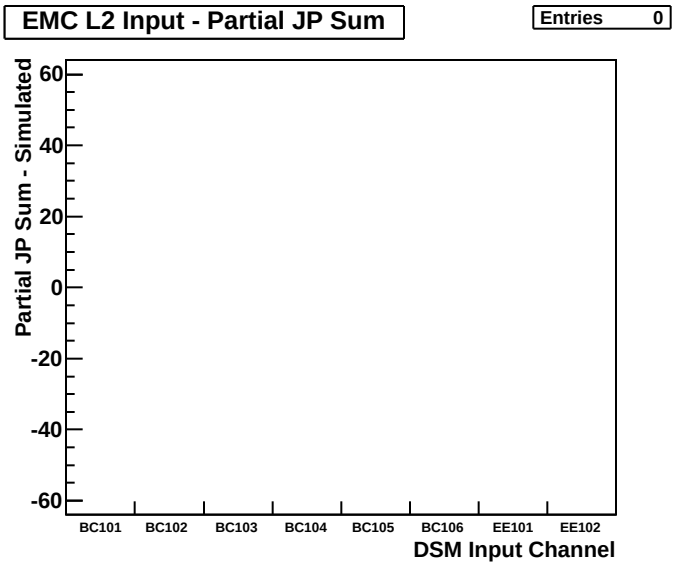

EMC L2 Input - JPY/JPB bits
Entries 1.999999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4746 4561 3256 2202 2712 5328 1077 1298
17374 17655 10529 7174 9317 21444 3799 4528
36602 32663 21317 16658 23696 33318 4997 5170
2.44128e+06 2.44512e+06 2.46490e+06 2.47388e+06 2.46427e+06 2.43991e+06 2.49053e+06 2.489e+06

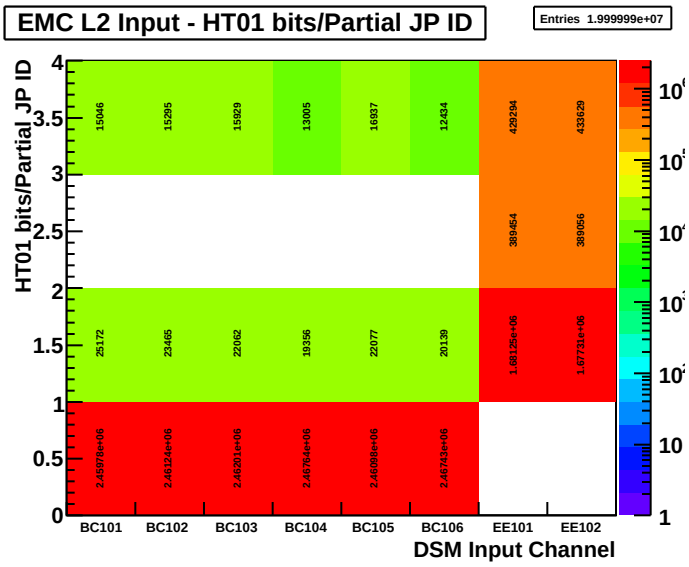

EMC L2 Input - JPY/JPB bits
Entries 5352
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2800
2592

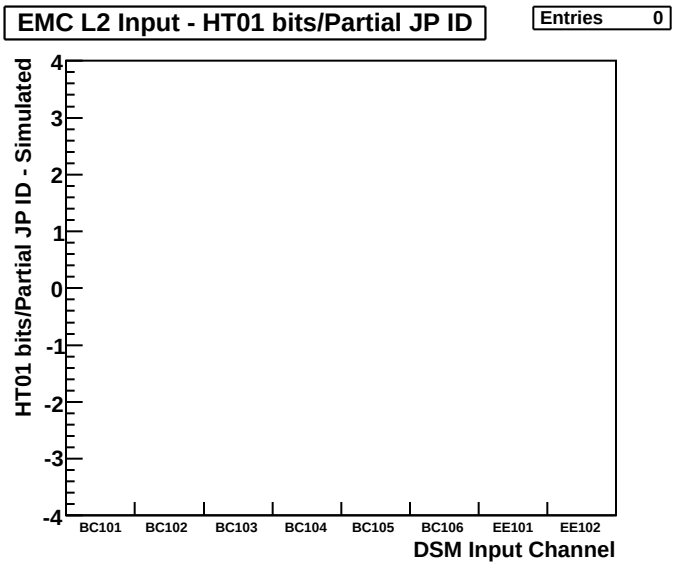

EMC L2 Input - JPZ/JPC bits
Entries 1.999999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5897 5695 4445 5419 2023 3294 1273 967
24891 23041 15087 23590 6047 11771 4656 3454
41138 36072 26117 33858 13448 21579 5704 4918
2.42807e+06 2.43519e+06 2.45435e+06 2.43713e+06 2.47848e+06 2.46336e+06 2.48837e+06 2.49066e+06

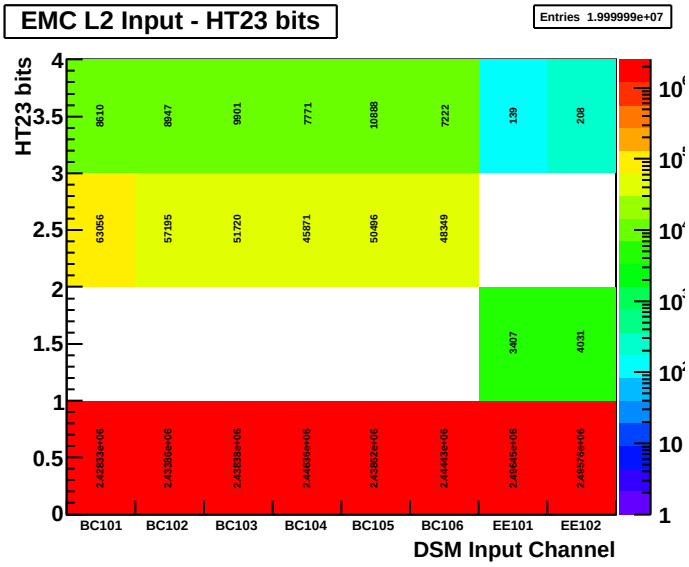

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

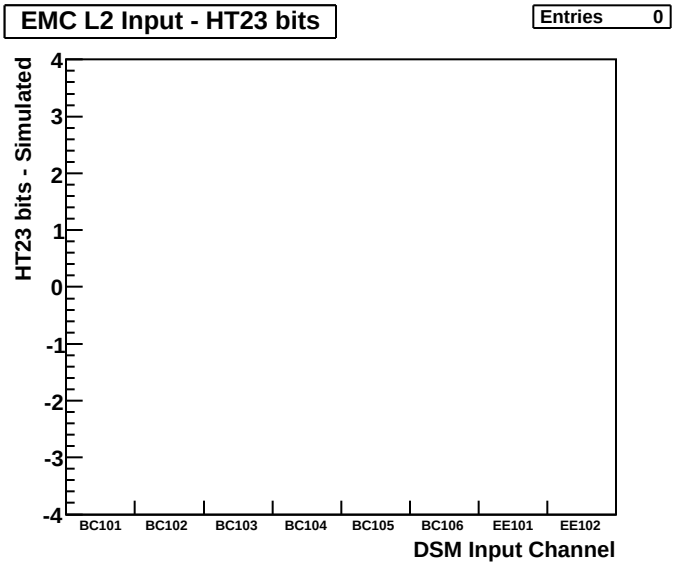

EMC L2 Input - Partial JP Sum
Entries 1.999999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

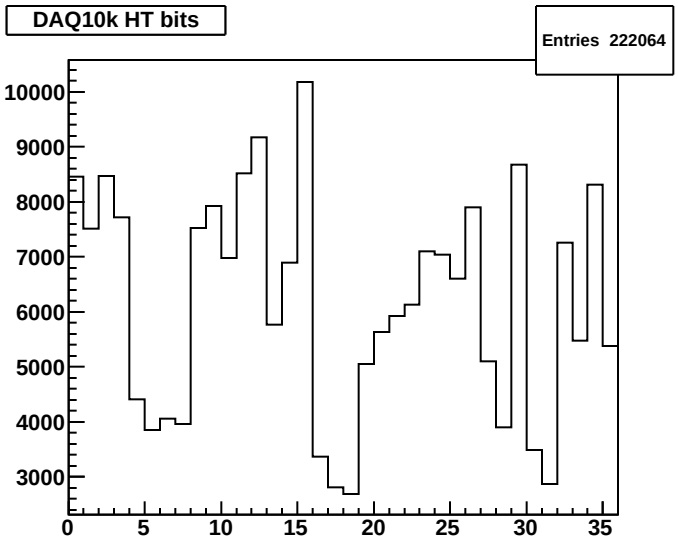

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

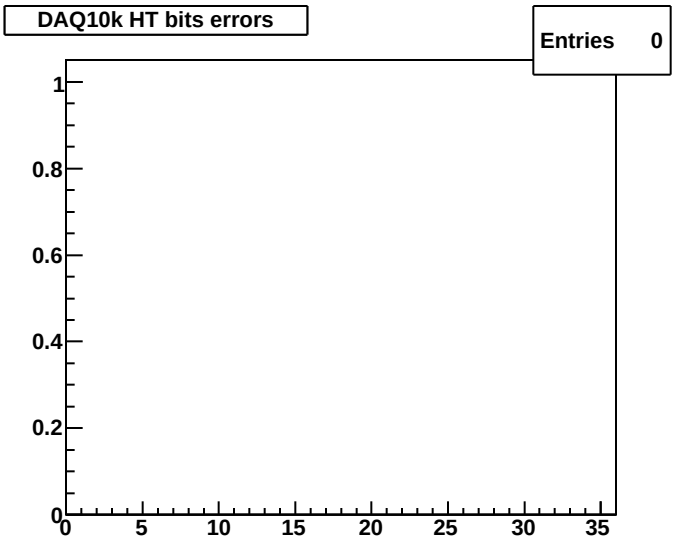

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.999999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

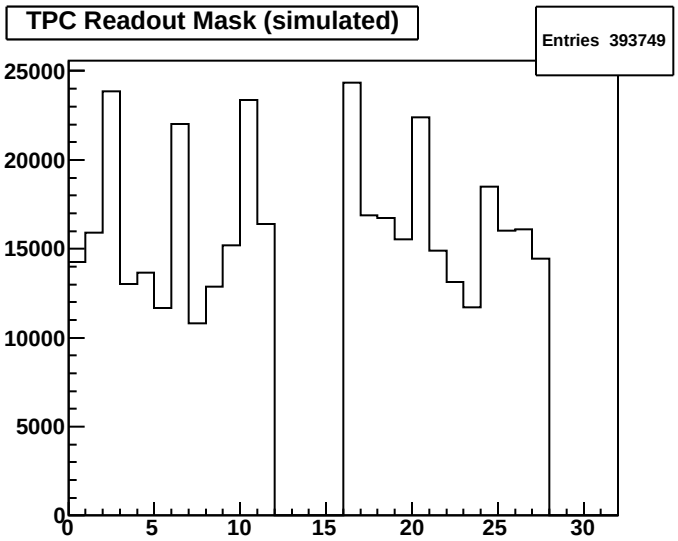

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

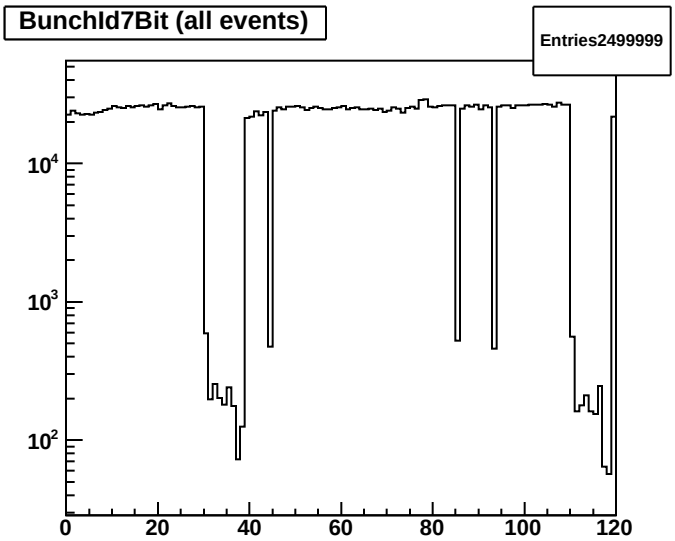

EMC L2 Input - HT23 bits
Entries 1.999999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

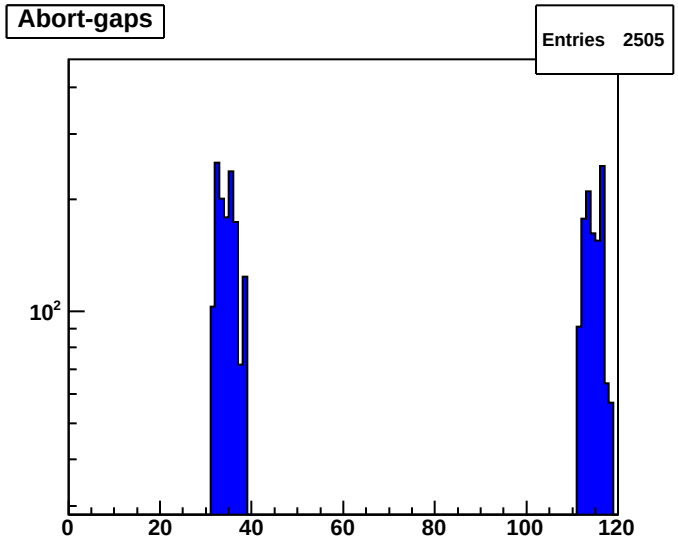

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

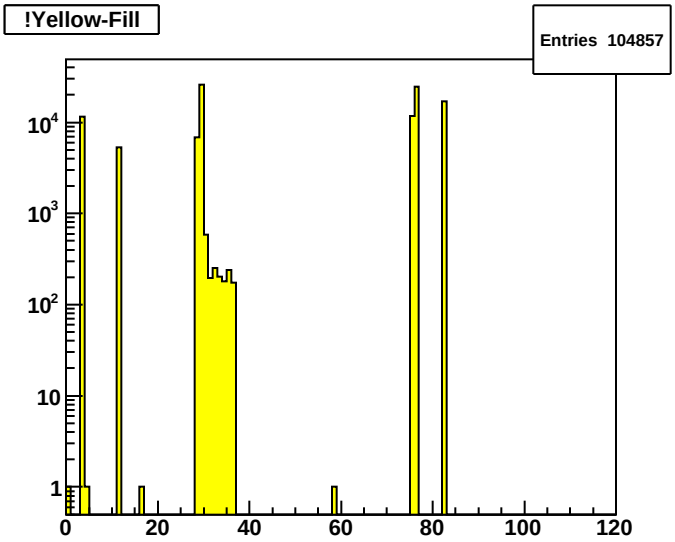

DAQ10k HT bits
Entries 222064

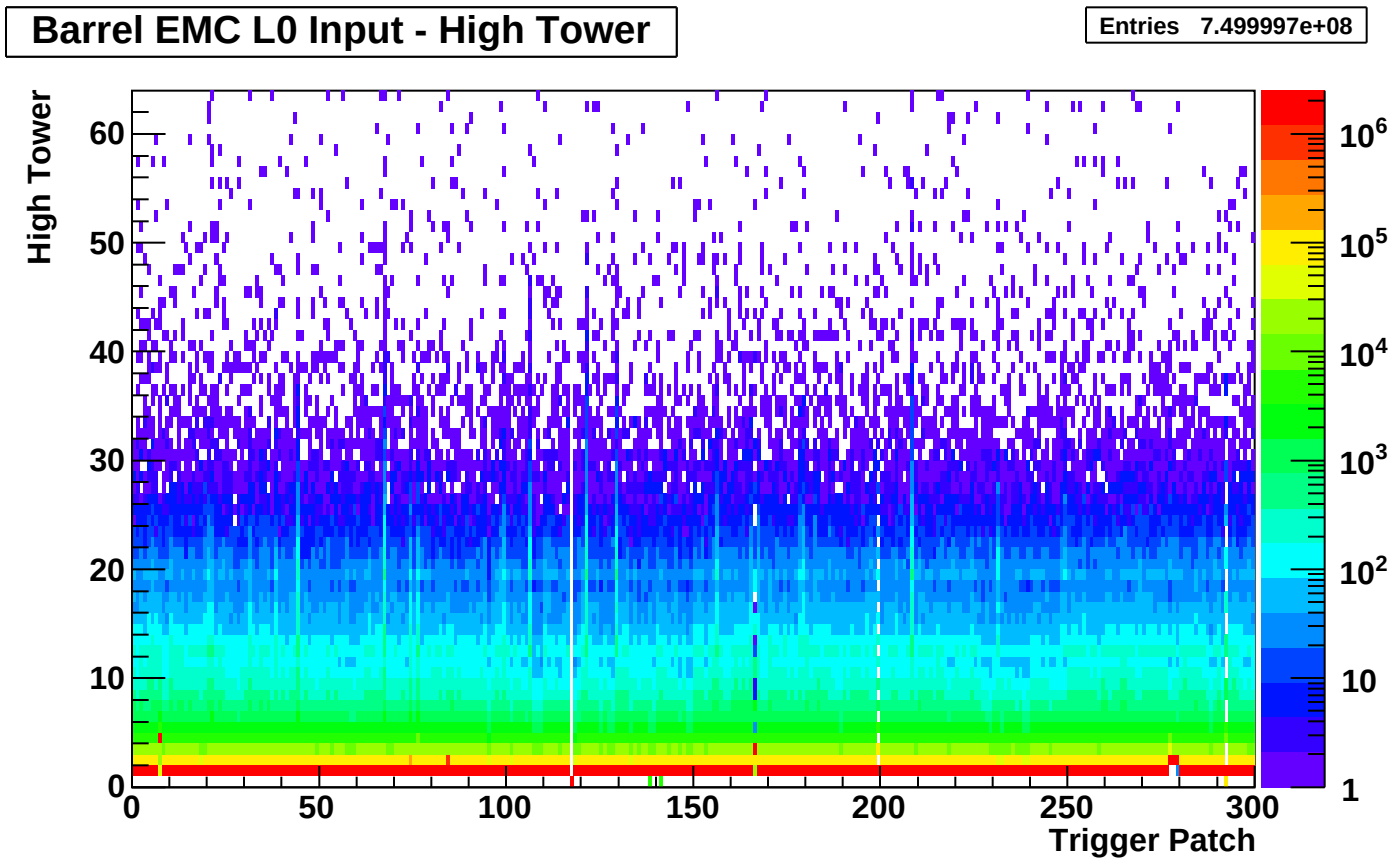

DAQ10k HT bits errors
Entries 0

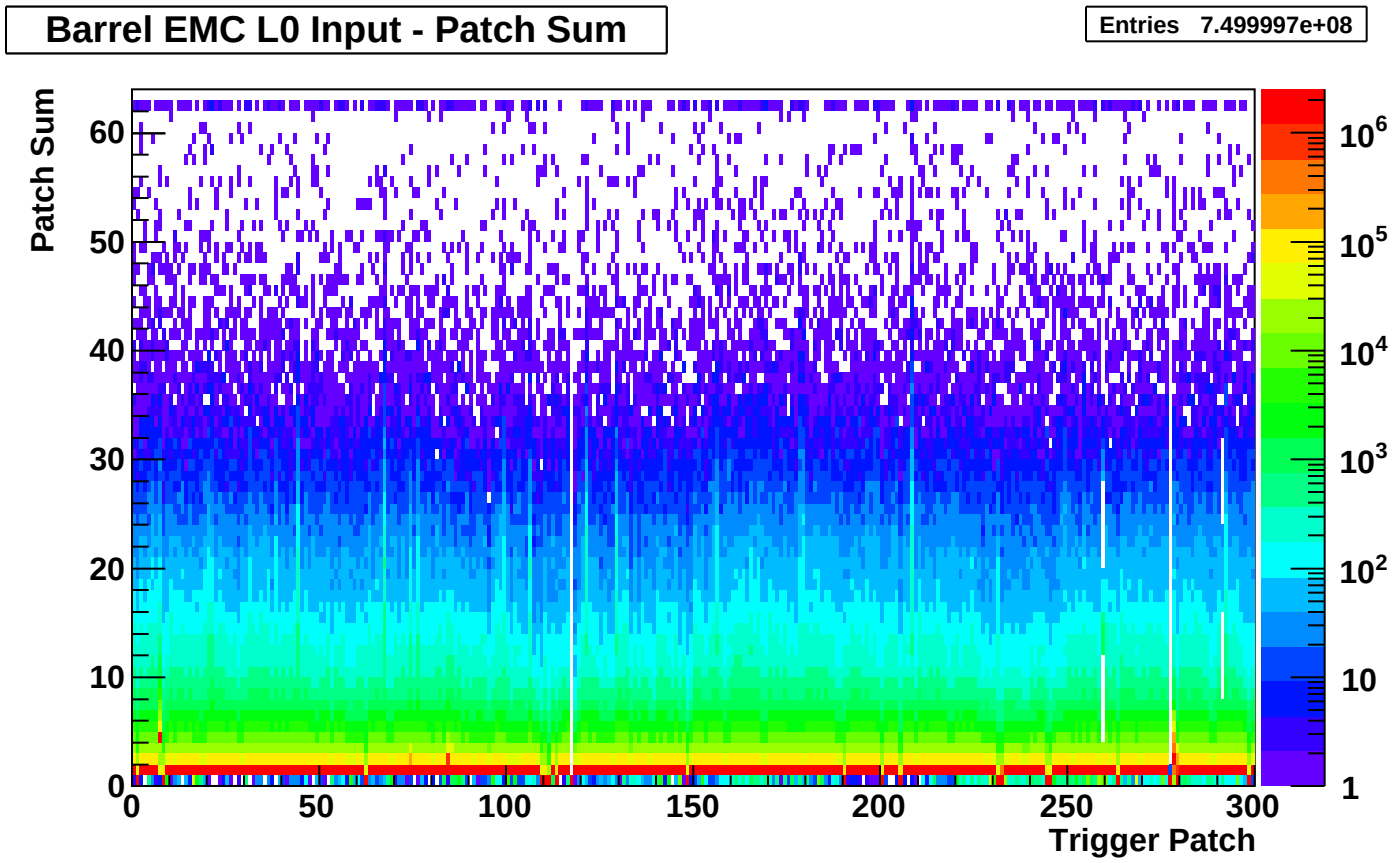

TPC Readout Mask (simulated)
Entries 393749

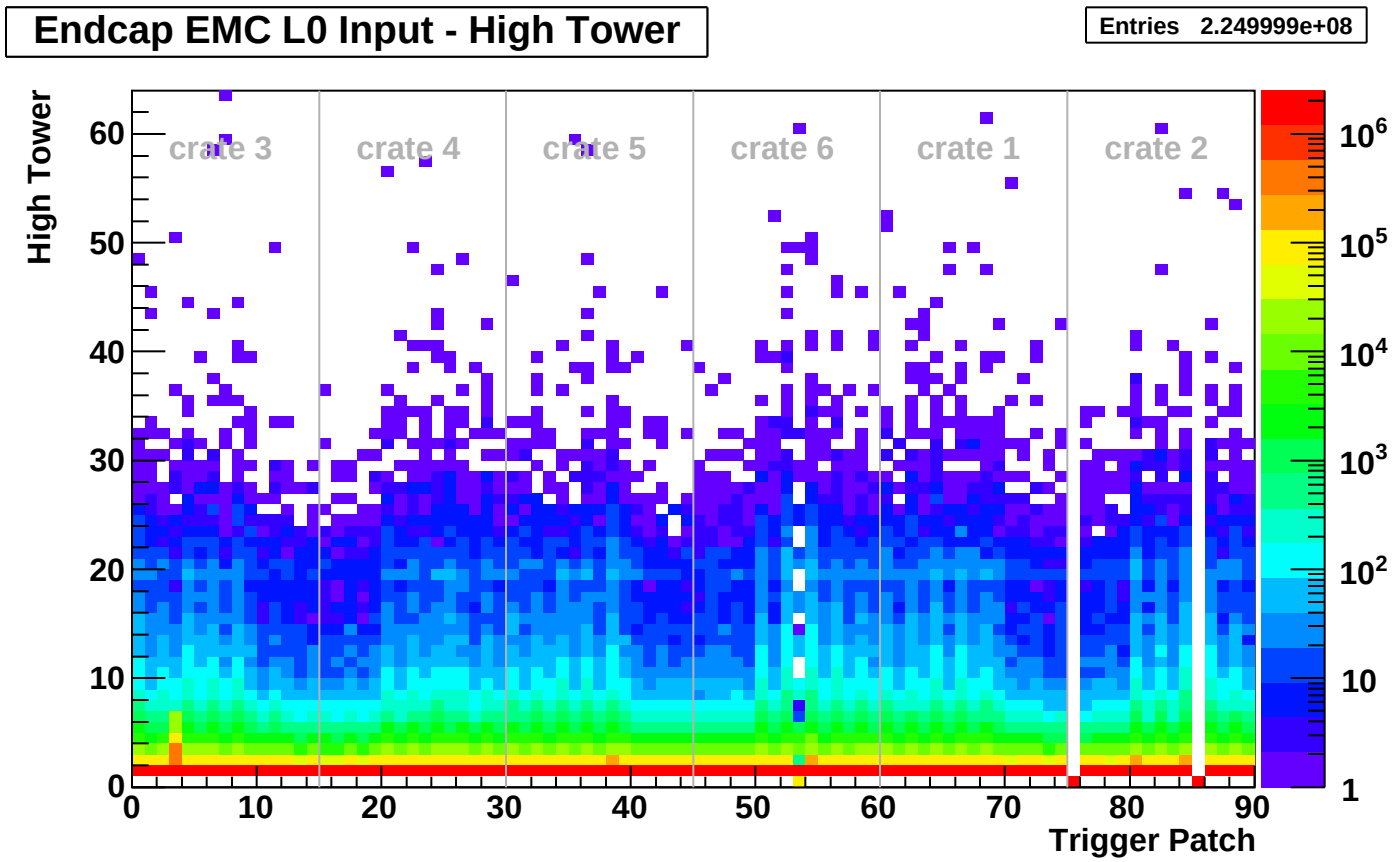

BunchId7Bit (all events)
Entries2499999

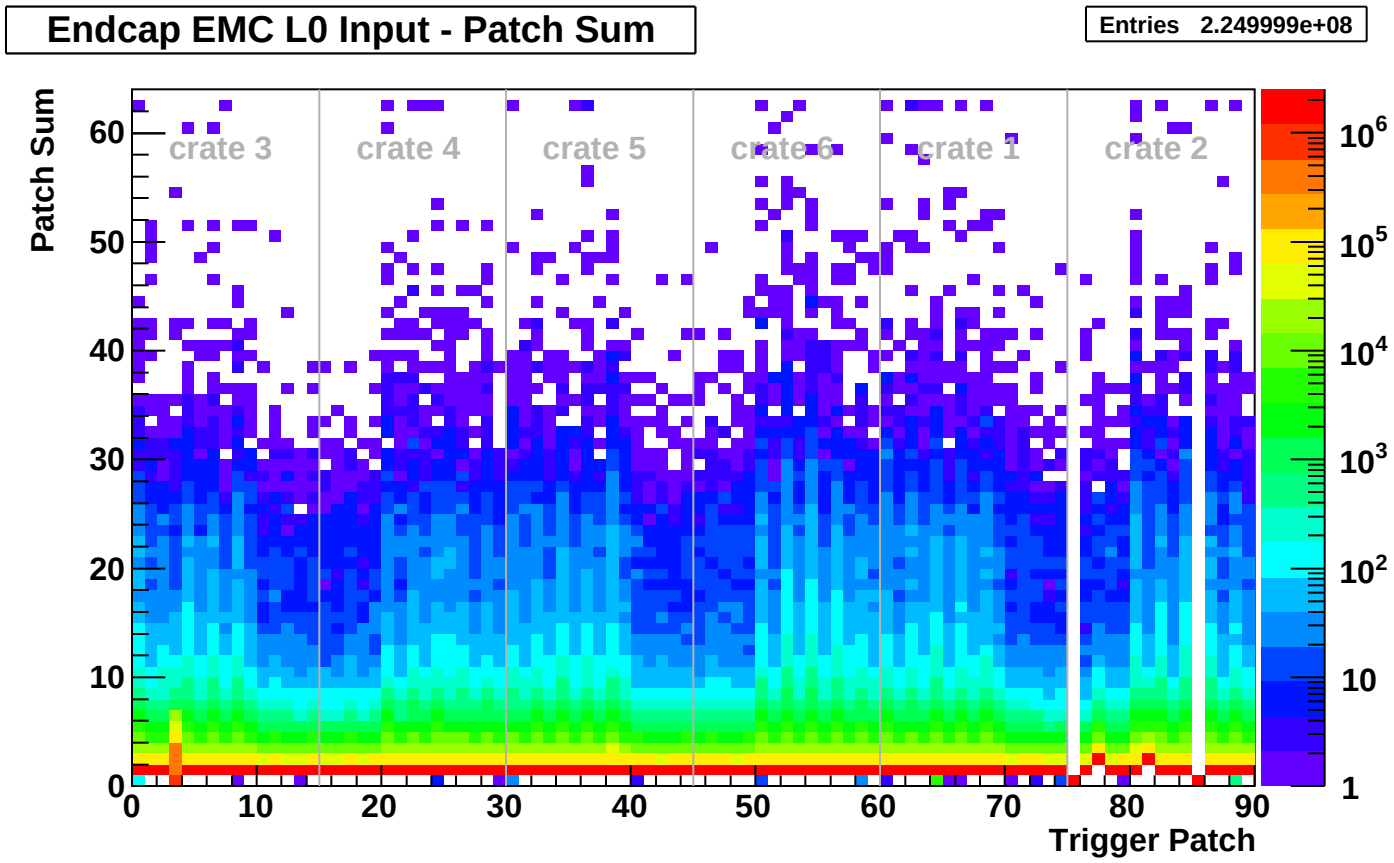

Abort-gaps
Entries 2505

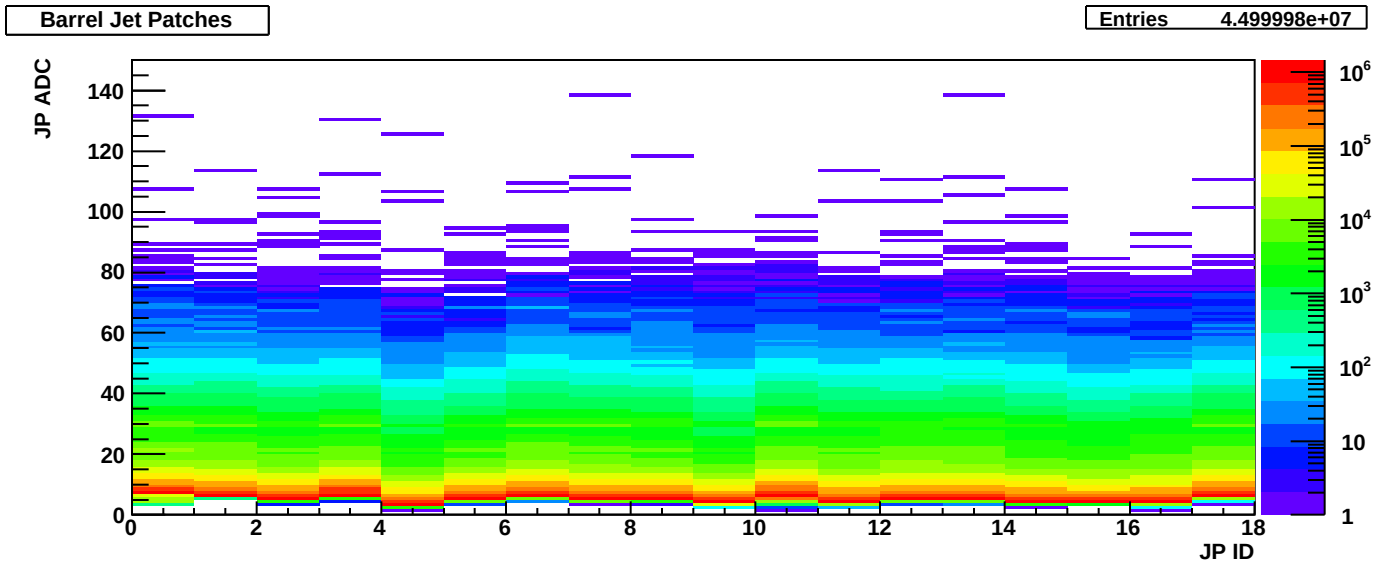

!Yellow-Fill
Entries 104857

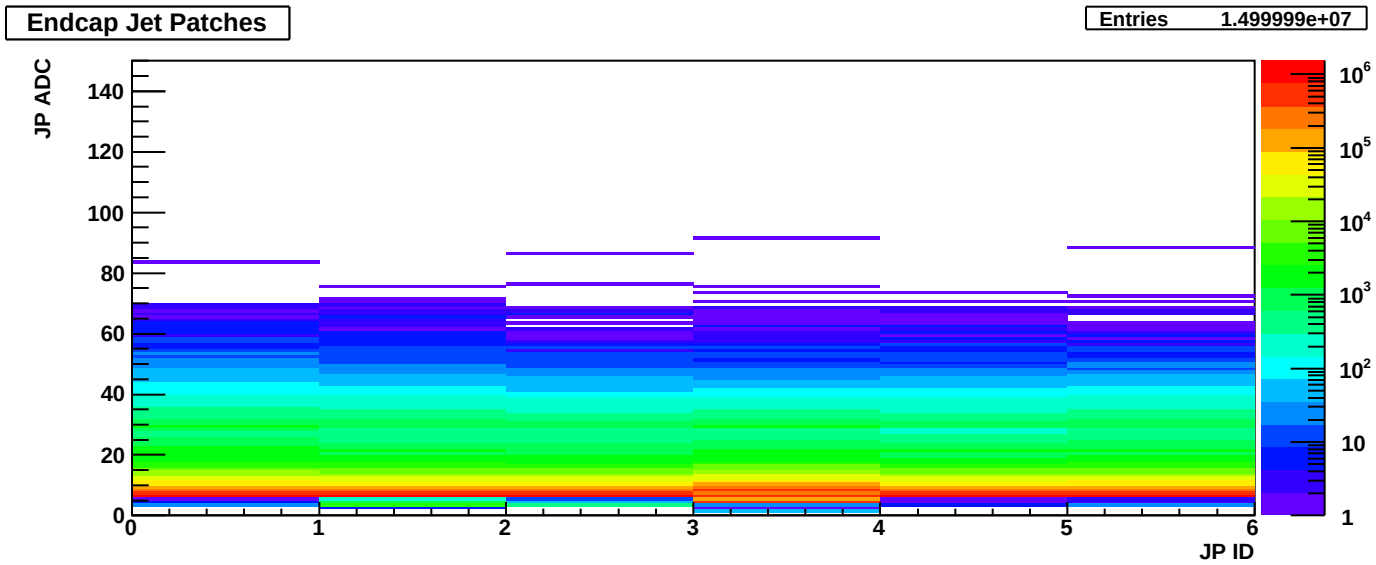

Barrel EMC L0 Input - High Tower
Entries 7.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

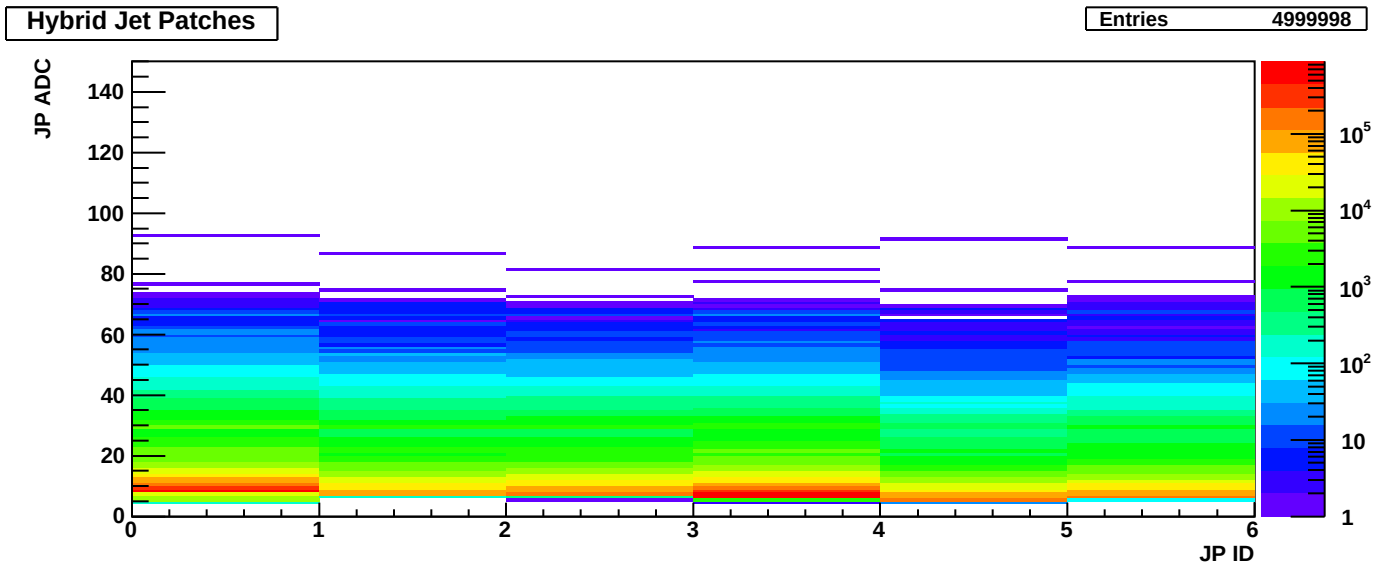

Barrel EMC L0 Input - Patch Sum
Entries 7.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

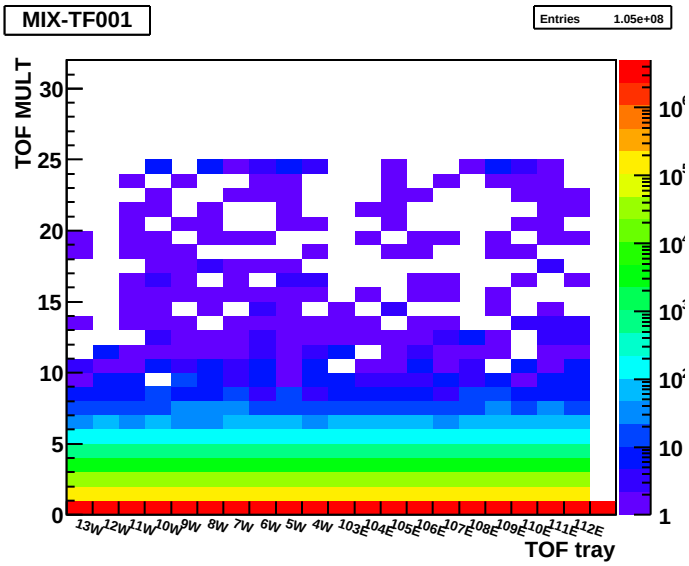

Endcap EMC L0 Input - High Tower
Entries 2.249999e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

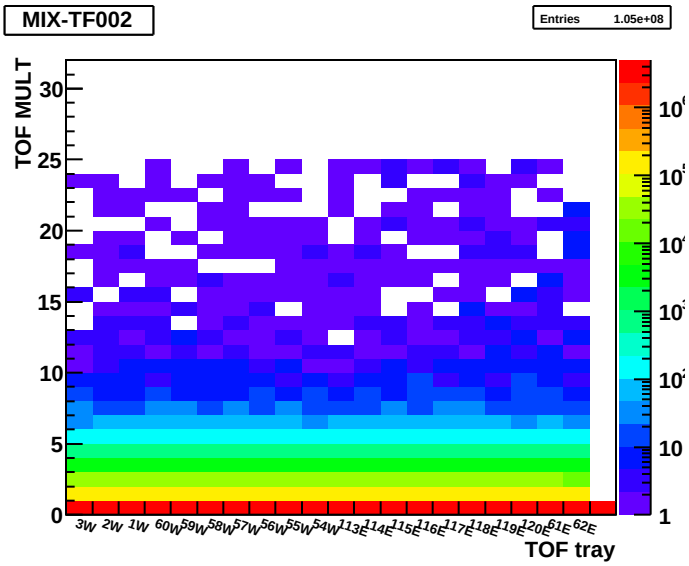

Endcap EMC L0 Input - Patch Sum
Entries 2.249999e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

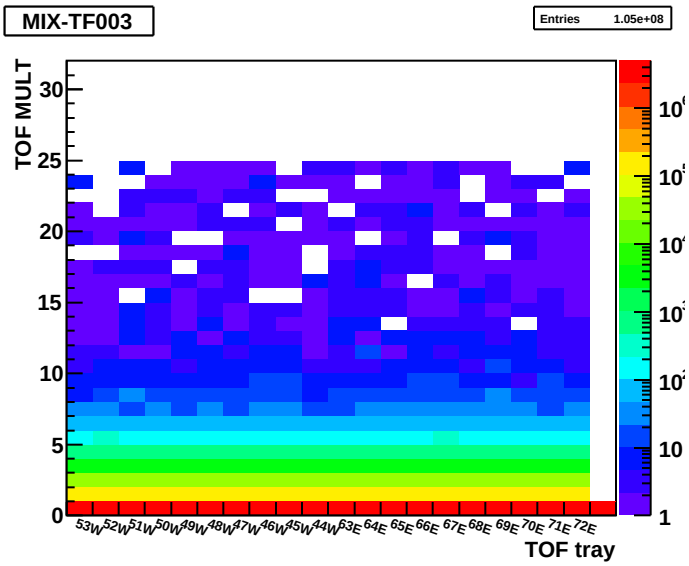

Barrel Jet Patches
Entries 4.499998e+07
JP ADC
JP ID

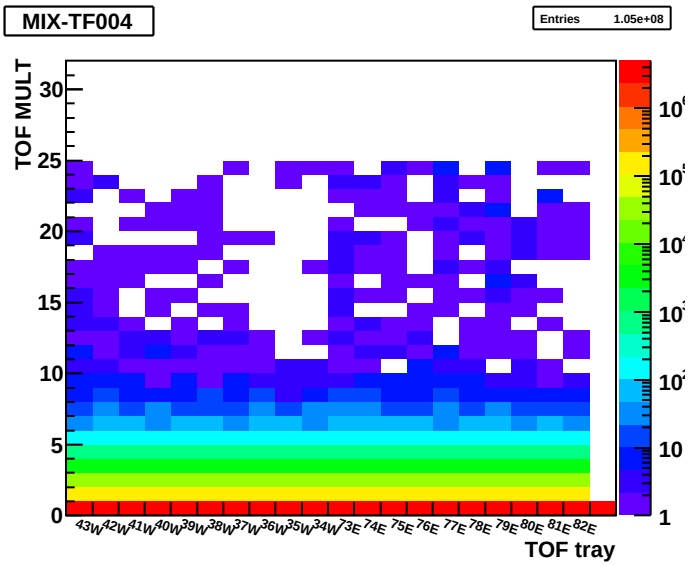

Endcap Jet Patches
Entries 1.499999e+07
JP ADC
JP ID

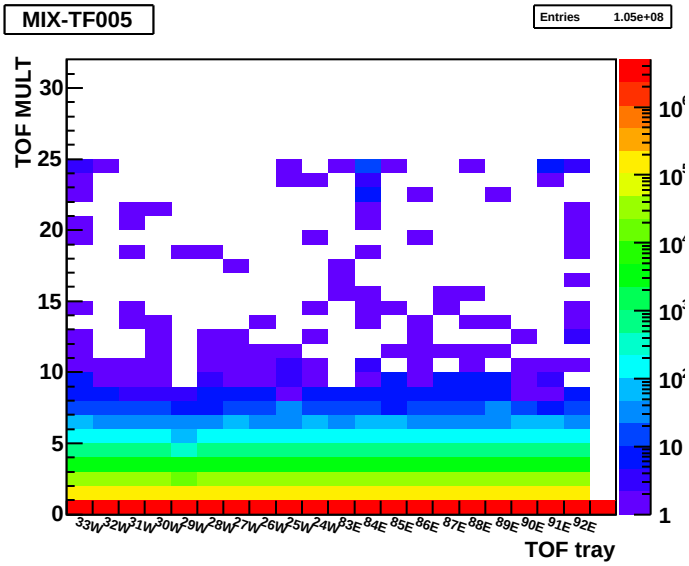

Hybrid Jet Patches
Entries 4999998
JP ADC
JP ID

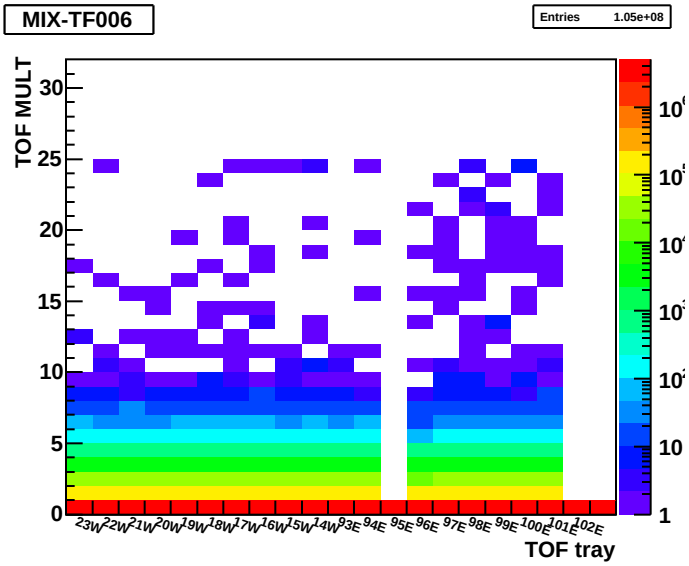

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

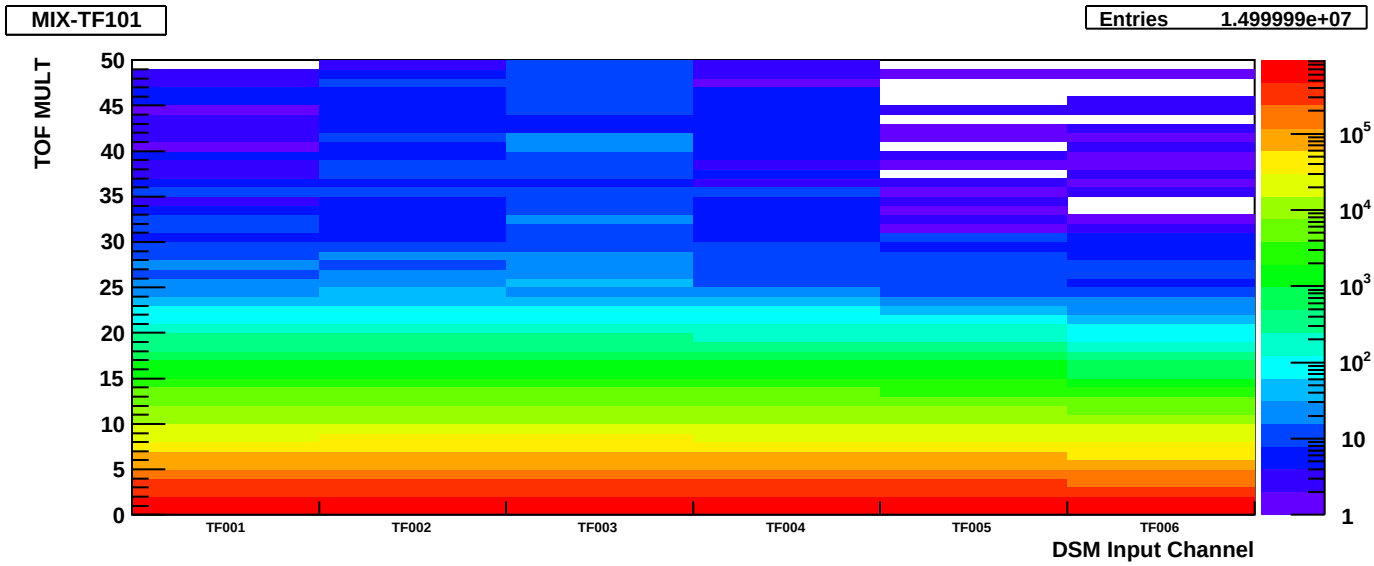

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

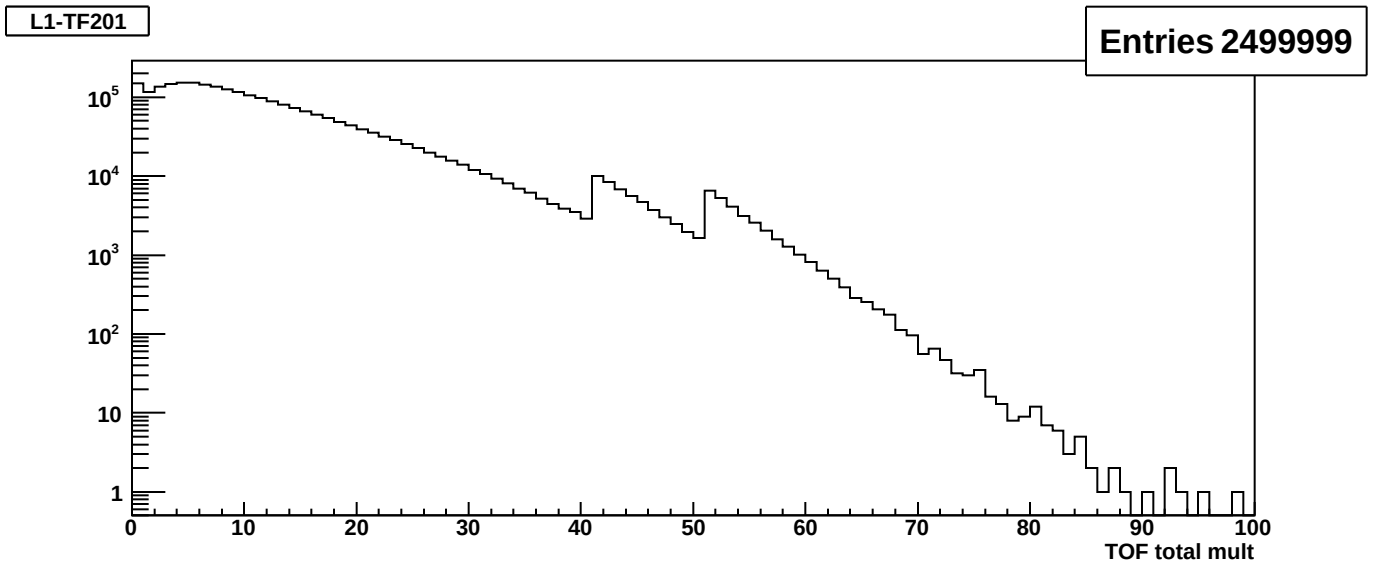

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

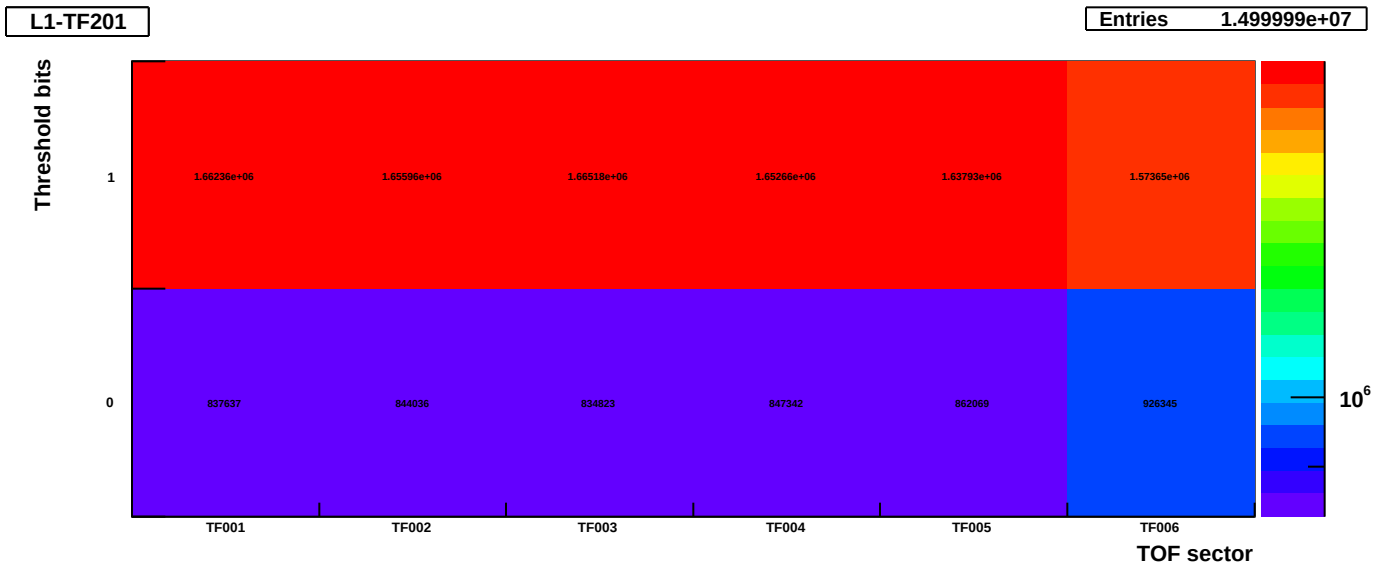

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

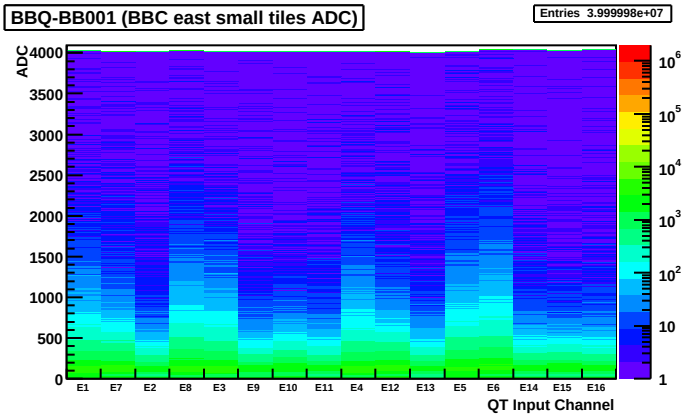

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

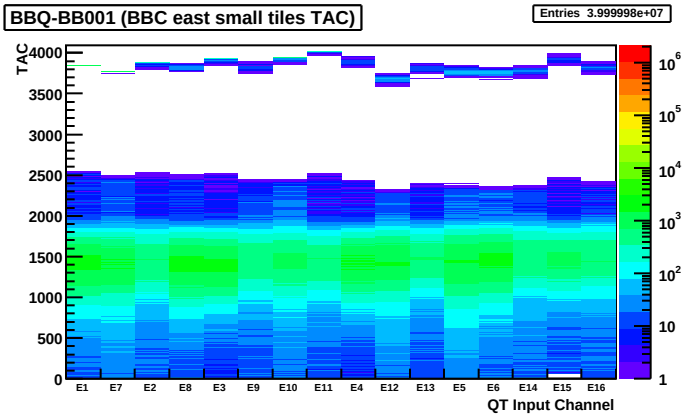

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

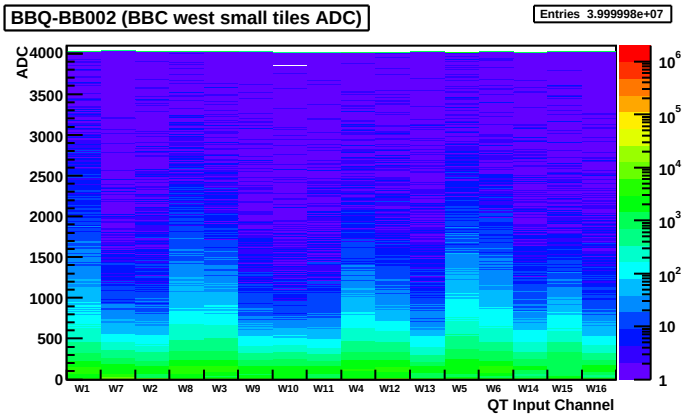

MIX-TF101
Entries 1.499999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

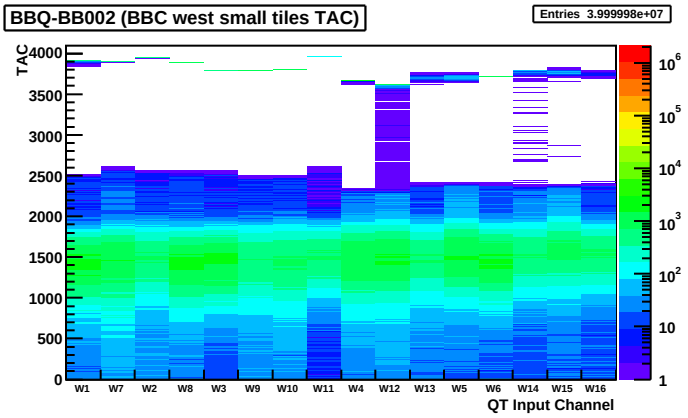

L1-TF201
Entries 2499999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

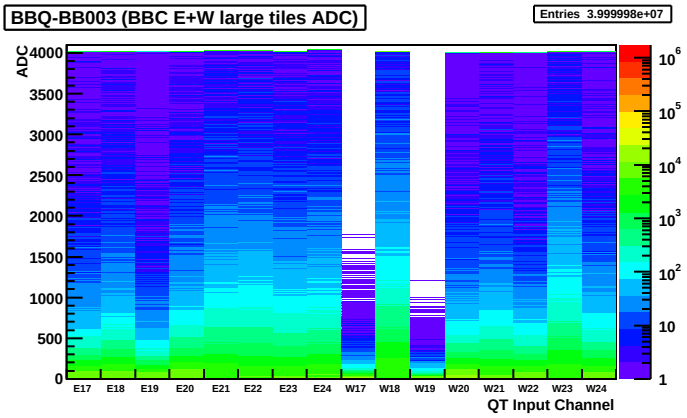

L1-TF201
Entries 1.499999e+07
Threshold bits
1
0
1.66236e+06
1.65596e+06
1.66518e+06
1.65266e+06
1.63793e+06
1.57365e+06
837637
844036
834823
847342
862069
926345
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

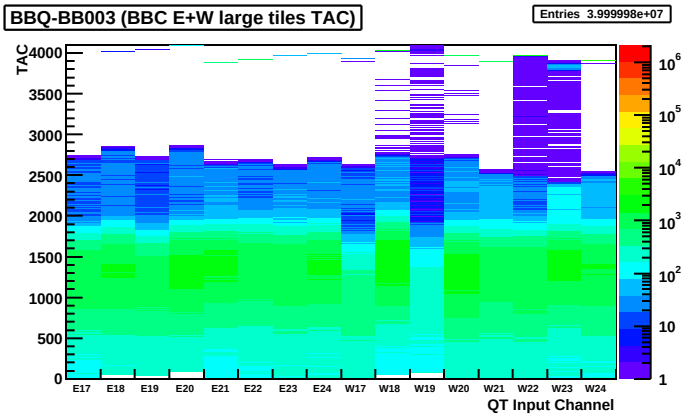

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

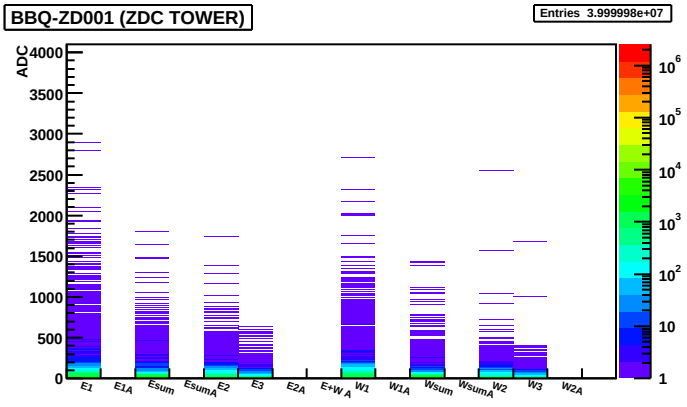

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

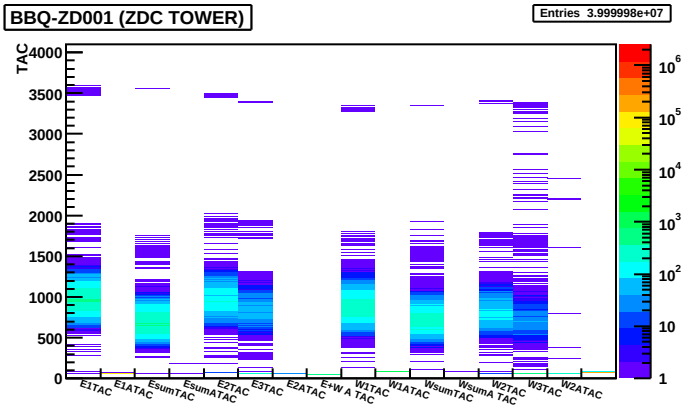

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

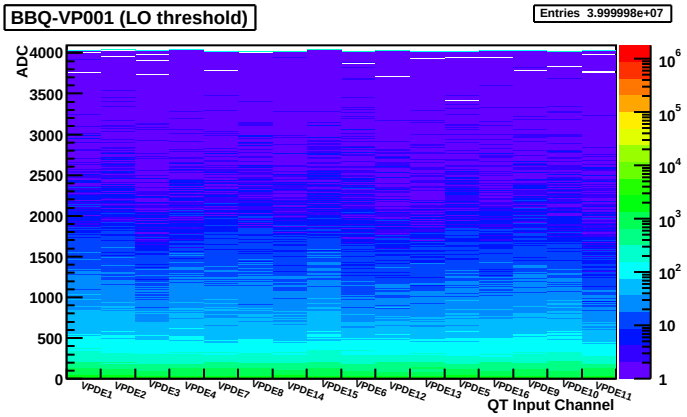

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

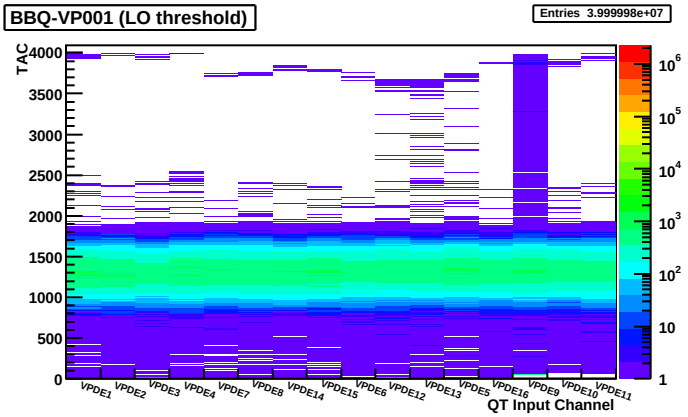

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

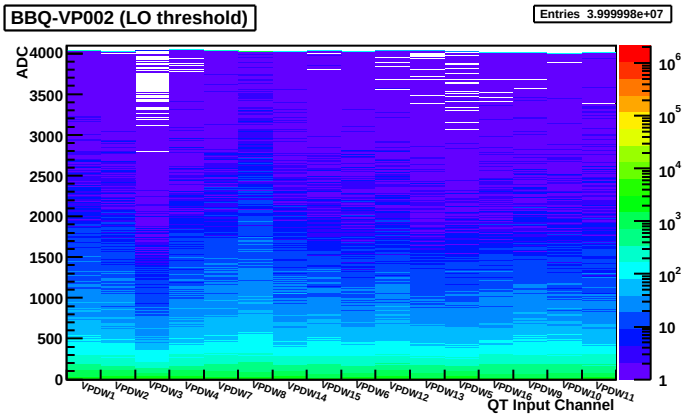

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

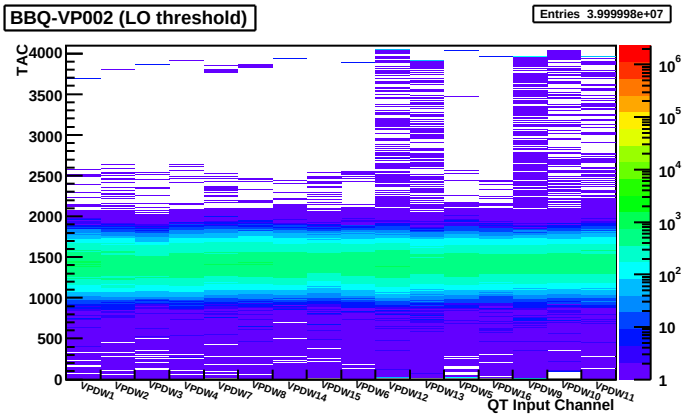

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
ADC

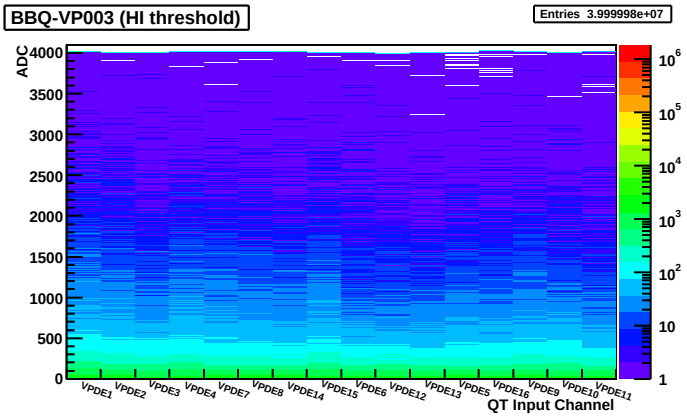

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
TAC

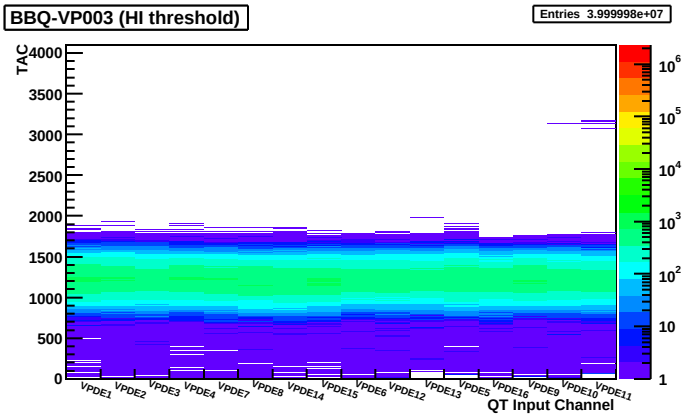

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

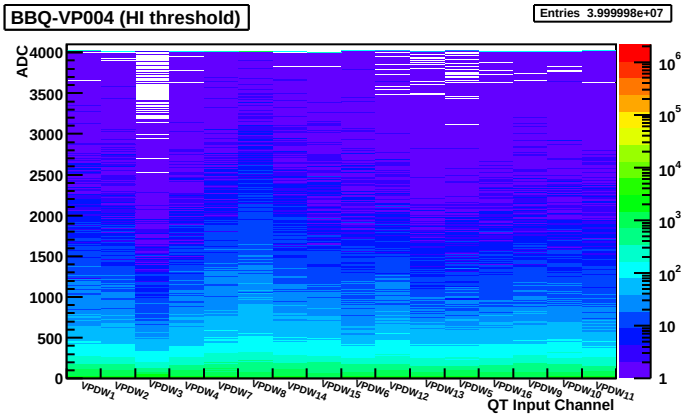

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

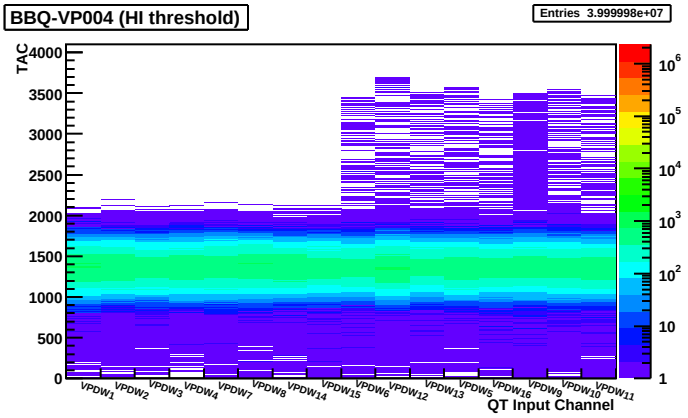

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

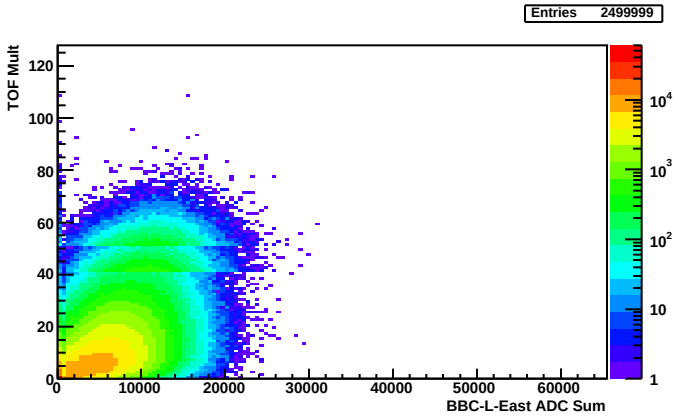

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

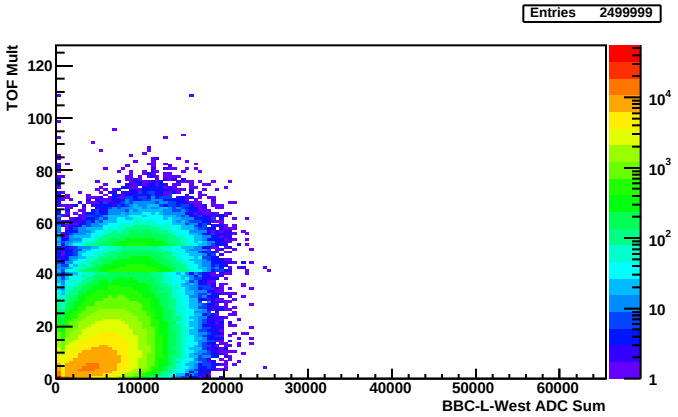

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

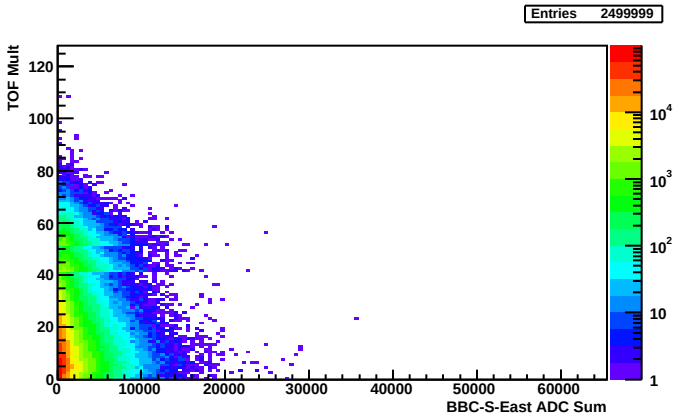

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

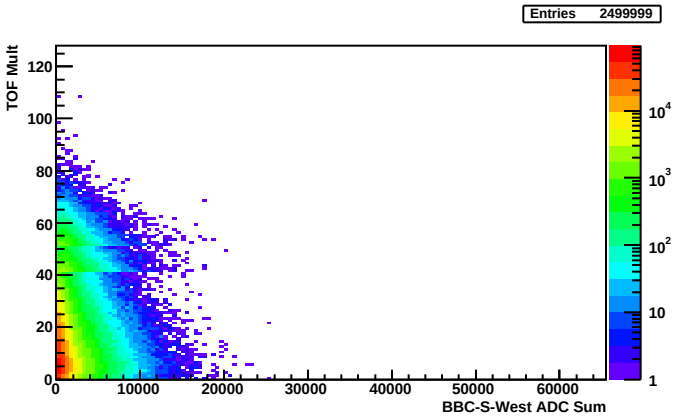

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

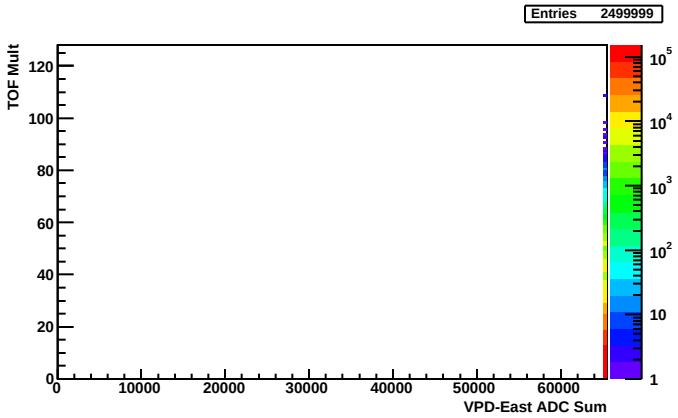

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

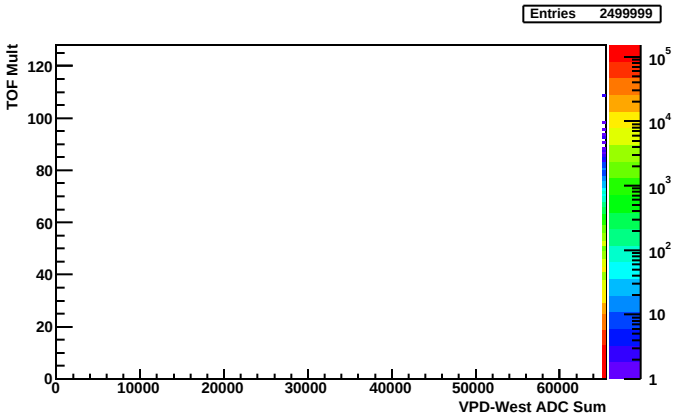

Entries 2499999
TOF Mult
BBC-L-East ADC Sum

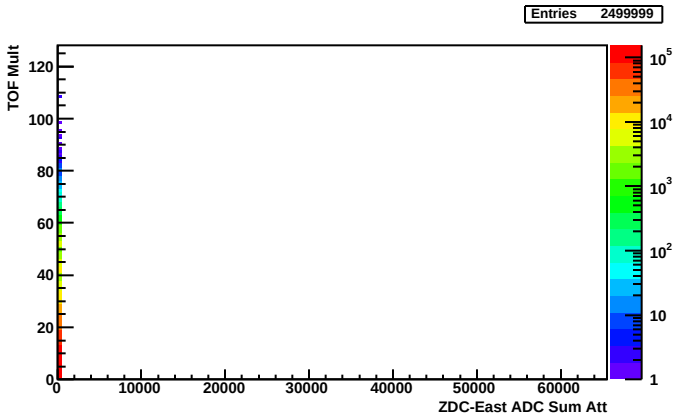

Entries 2499999
TOF Mult
BBC-L-West ADC Sum

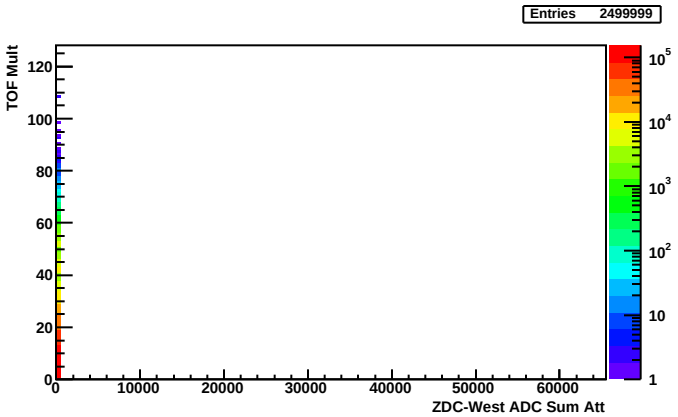

Entries 2499999
TOF Mult
BBC-S-East ADC Sum

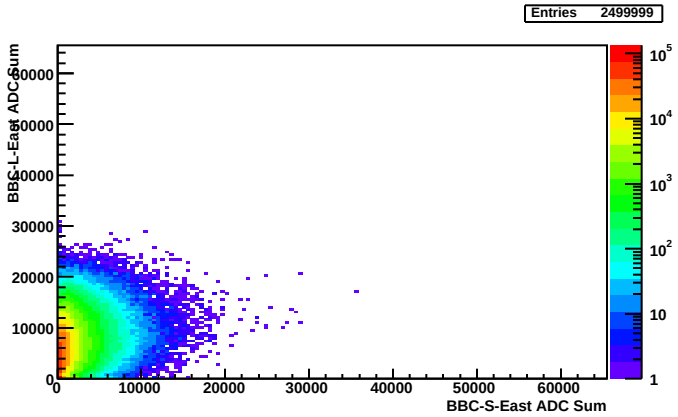

Entries 2499999
TOF Mult
BBC-S-West ADC Sum

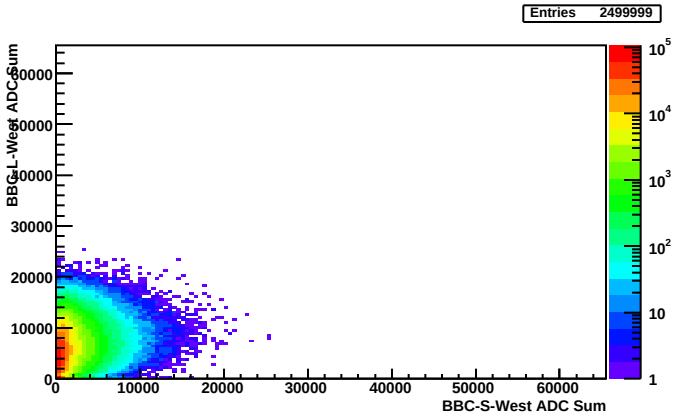

Entries 2499999
TOF Mult
VPD-East ADC Sum

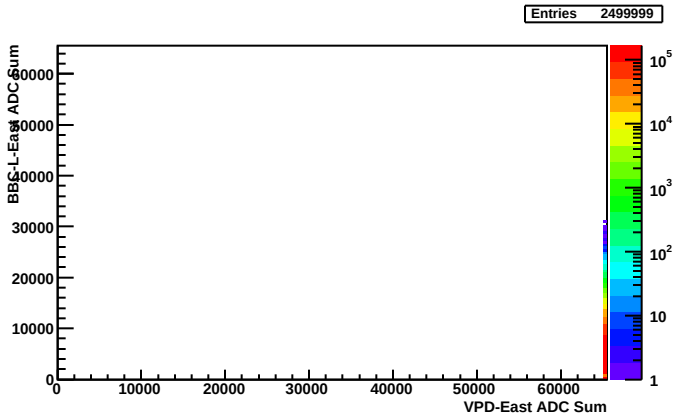

Entries 2499999
TOF Mult
VPD-West ADC Sum

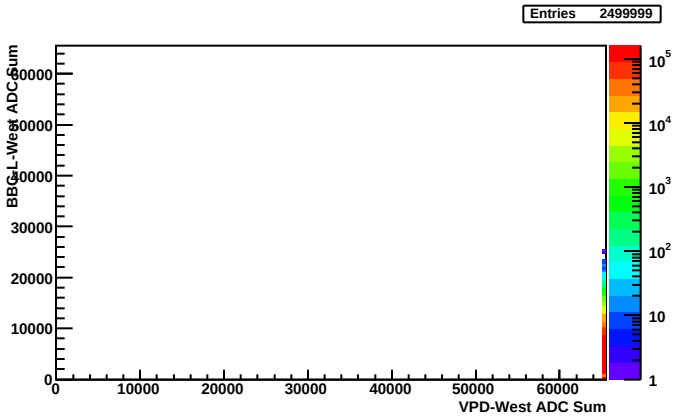

Entries 2499999
TOF Mult
ZDC-East ADC Sum Att

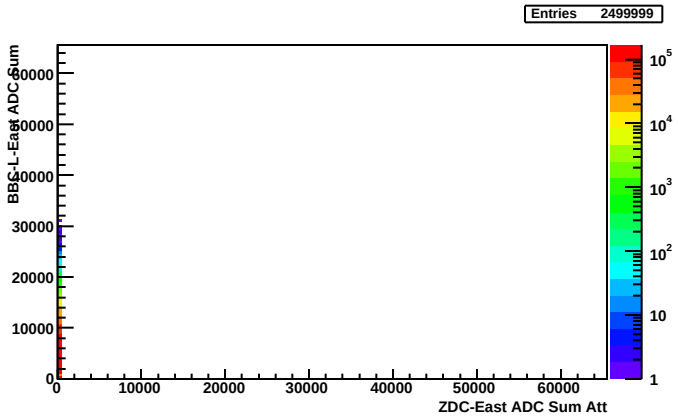

Entries 2499999
TOF Mult
ZDC-West ADC Sum Att

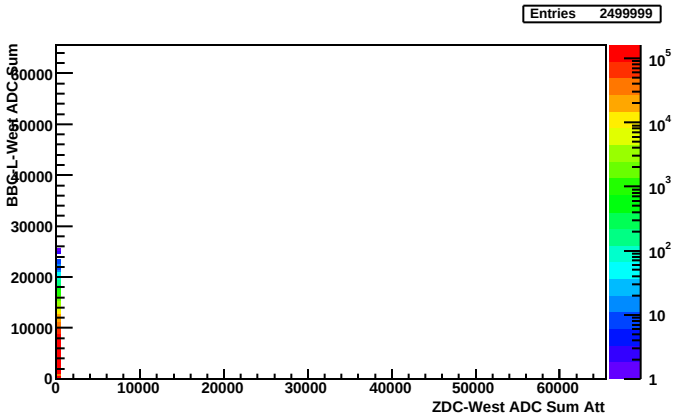

Entries 2499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

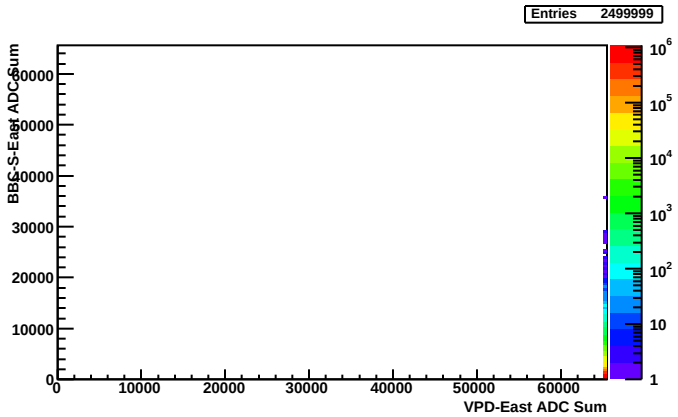

Entries 2499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

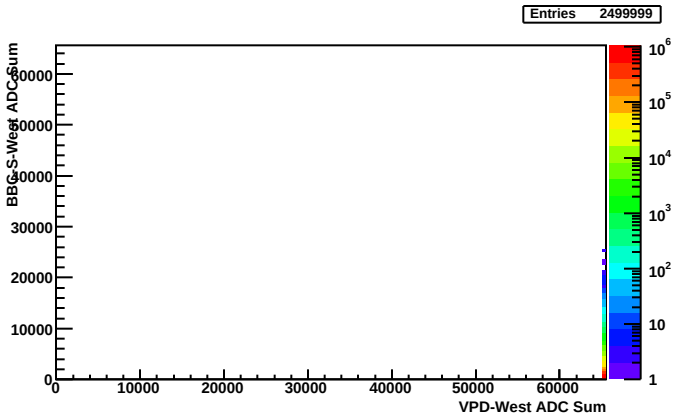

Entries 2499999
BBC-L-East ADC Sum
VPD-East ADC Sum

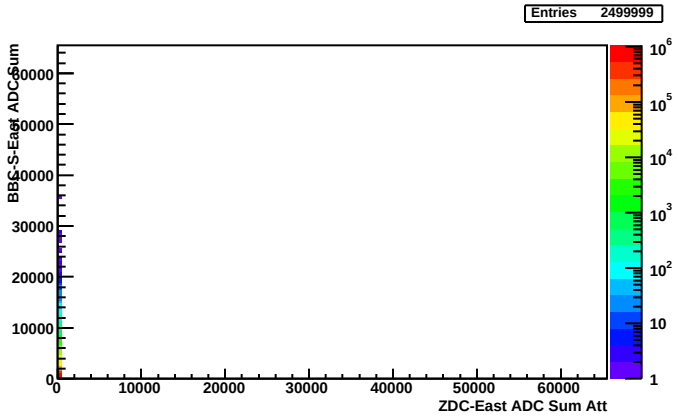

Entries 2499999
BBC-L-West ADC Sum
VPD-West ADC Sum

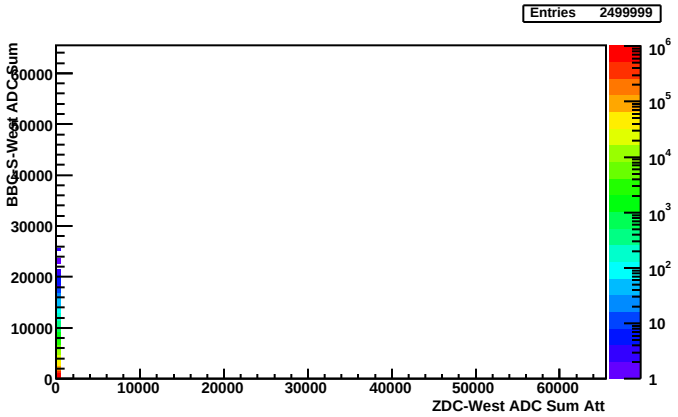

Entries 2499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

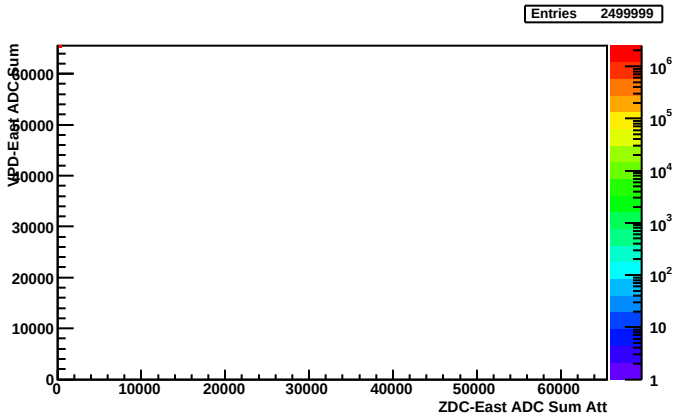

Entries 2499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

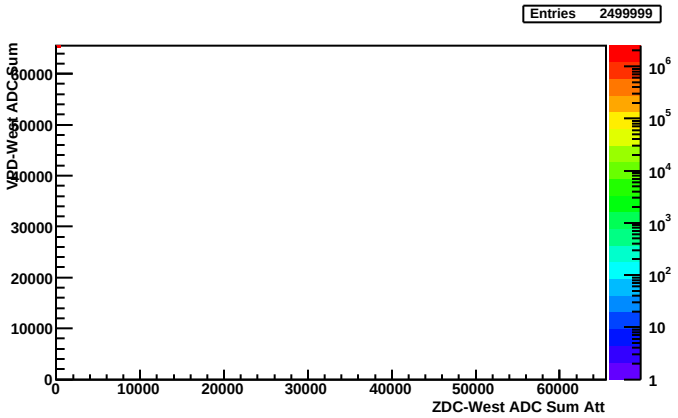

Entries 2499999
BBC-S-East ADC Sum
VPD-East ADC Sum

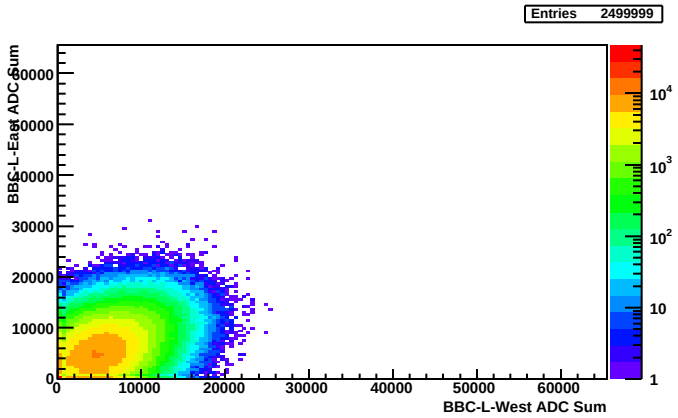

Entries 2499999
BBC-S-West ADC Sum
VPD-West ADC Sum

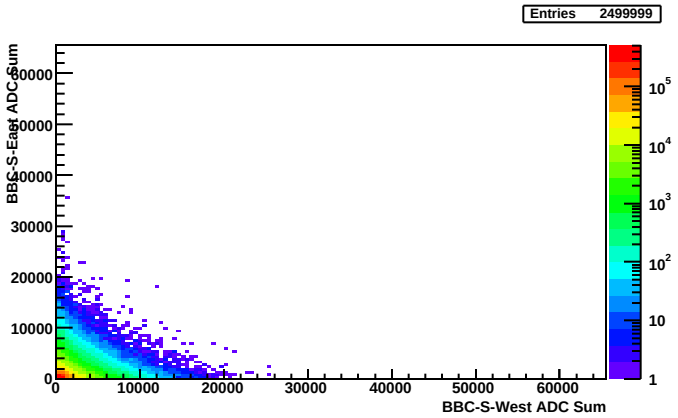

Entries 2499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

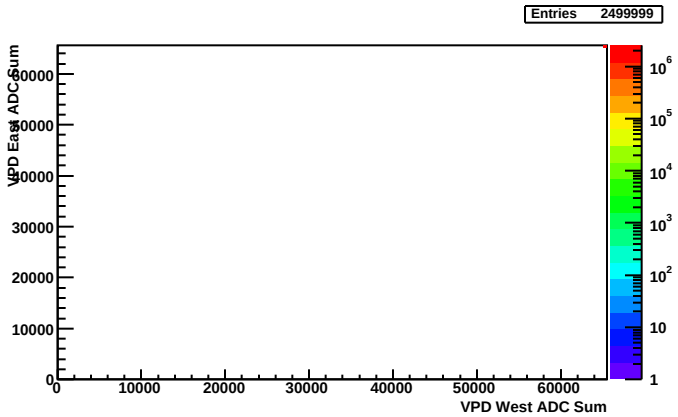

Entries 2499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

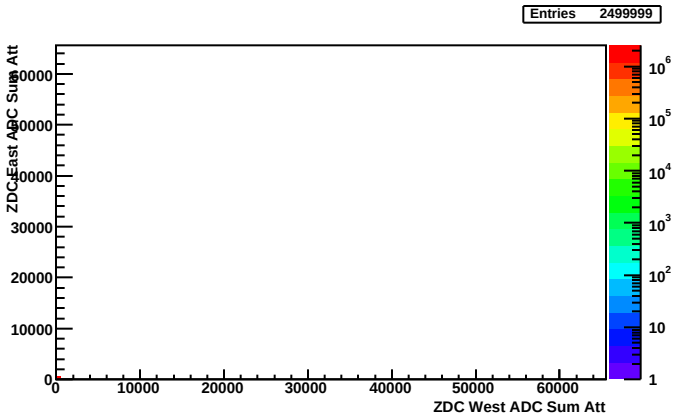

Entries 2499999
VPD-East ADC Sum
ZDC-East ADC Sum Att

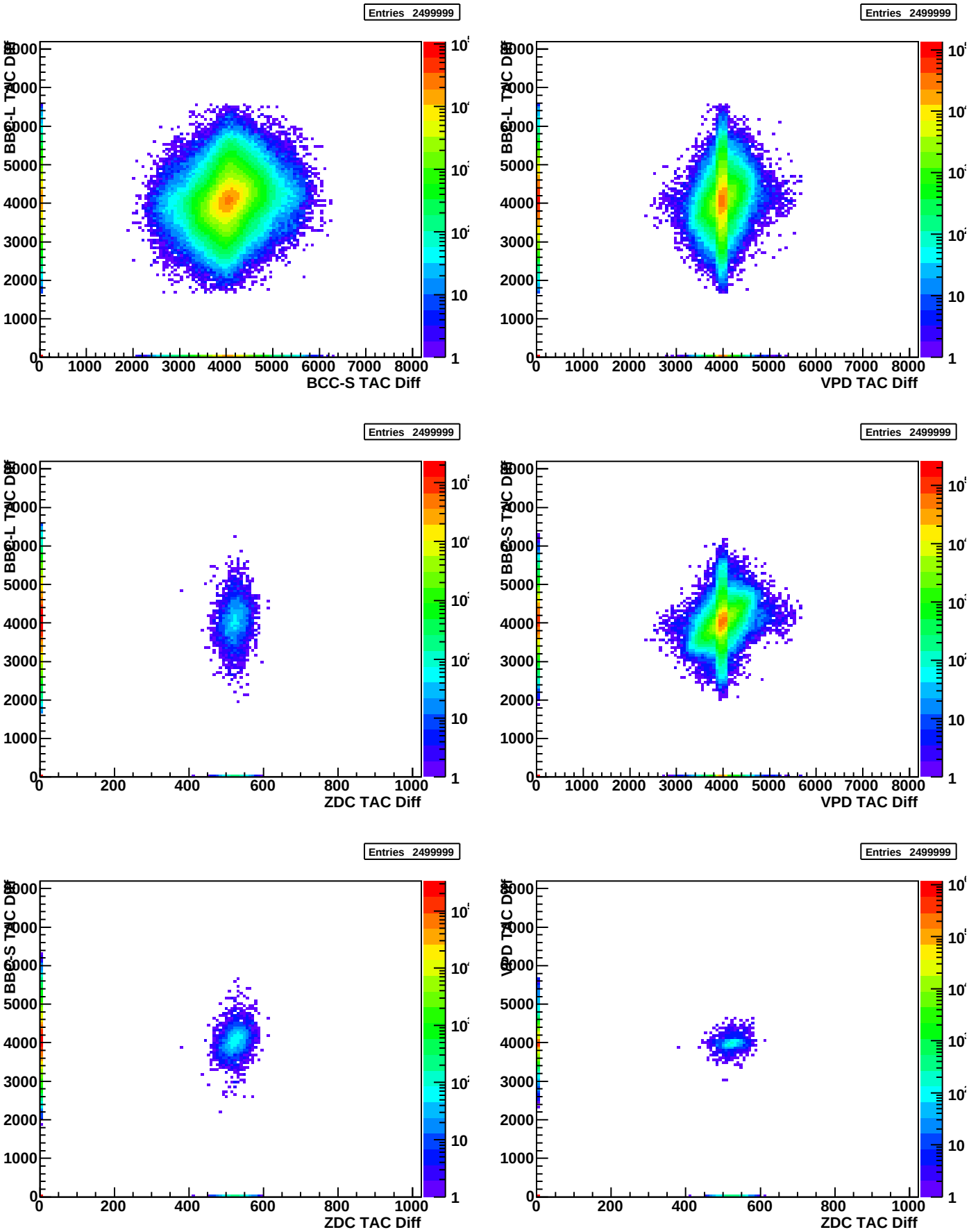

Entries 2499999
VPD-West ADC Sum
ZDC-West ADC Sum Att

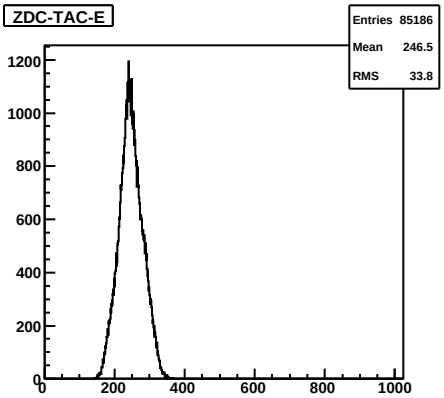

Entries 2499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

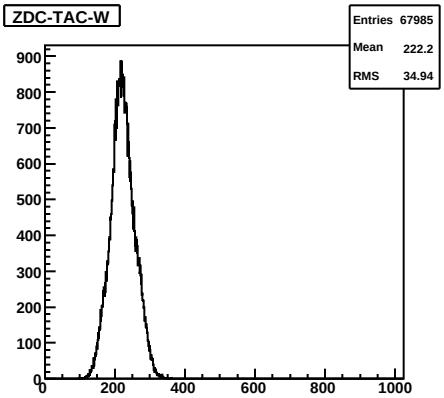

Entries 2499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

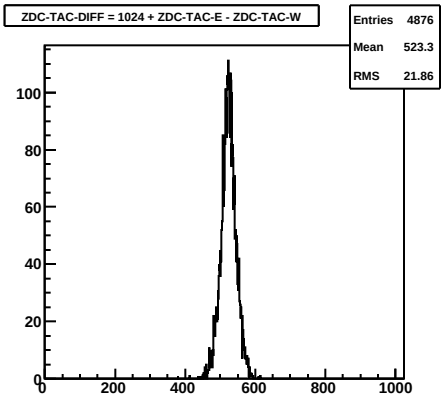

Entries 2499999
VPD East ADC Sum
VPD West ADC Sum

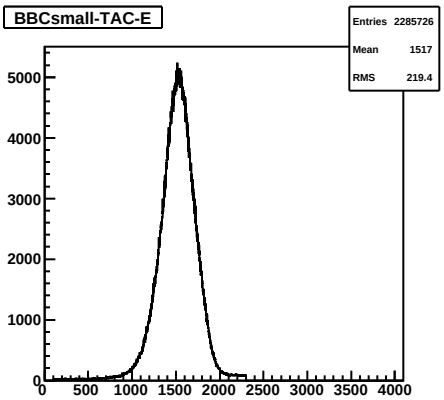

Entries 2499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

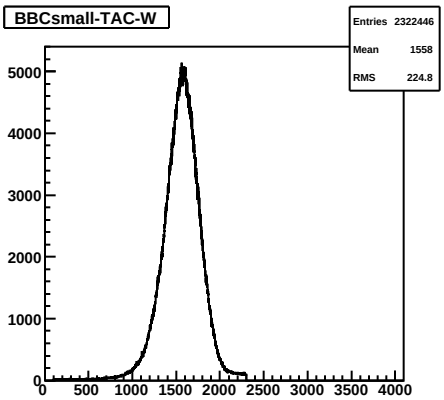

Entries 2499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2499999
BBC-L TAC Diff
VPD TAC Diff
Entries 2499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2499999
BBC-S TAC Diff
VPD TAC Diff
Entries 2499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2499999
VPD TAC Diff
ZDC TAC Diff

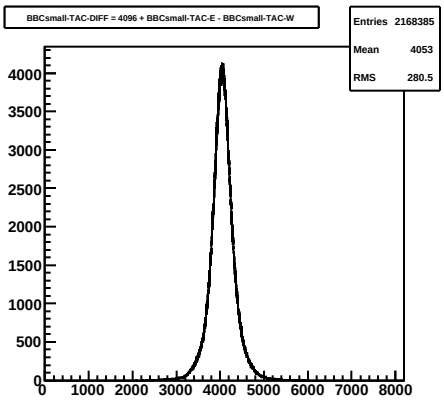

ZDC-TAC-E
Entries 85186
Mean 246.5
RMS 33.8

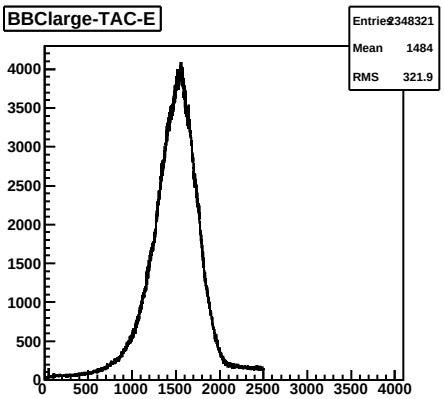

ZDC-TAC-W
Entries 67985
Mean 222.2
RMS 34.94

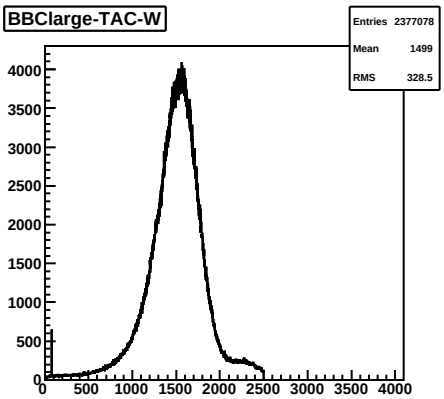

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4876
Mean 523.3
RMS 21.86

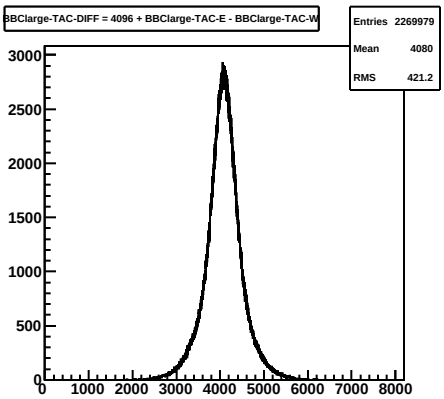

BBCsmall-TAC-E
Entries 2285726
Mean 1517
RMS 219.4

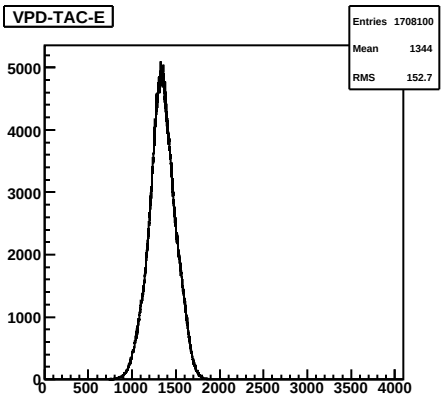

BBCsmall-TAC-W
Entries 2322446
Mean 1558
RMS 224.8

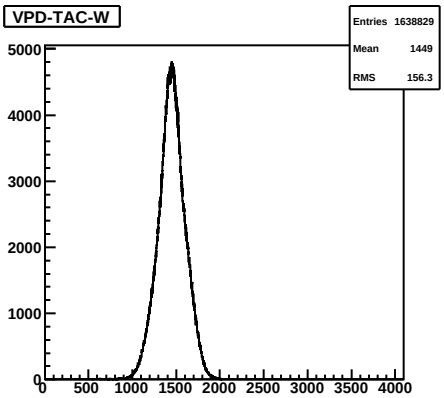

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2168385
Mean 4053
RMS 280.5

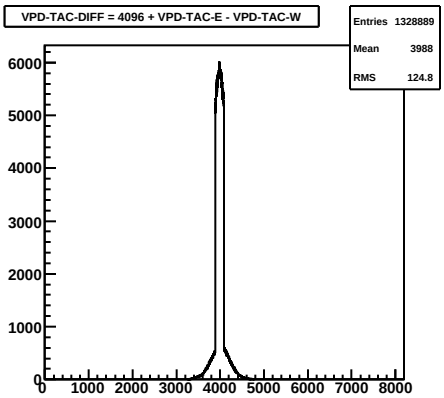

BBClarge-TAC-E
Entries 2348321
Mean 1484
RMS 321.9

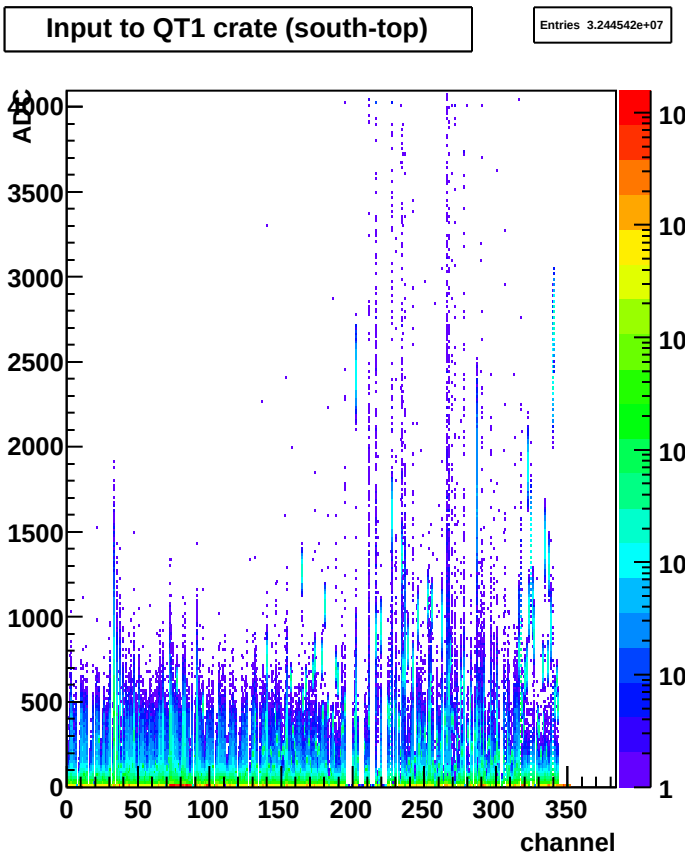

BBClarge-TAC-W
Entries 2377078
Mean 1499
RMS 328.5

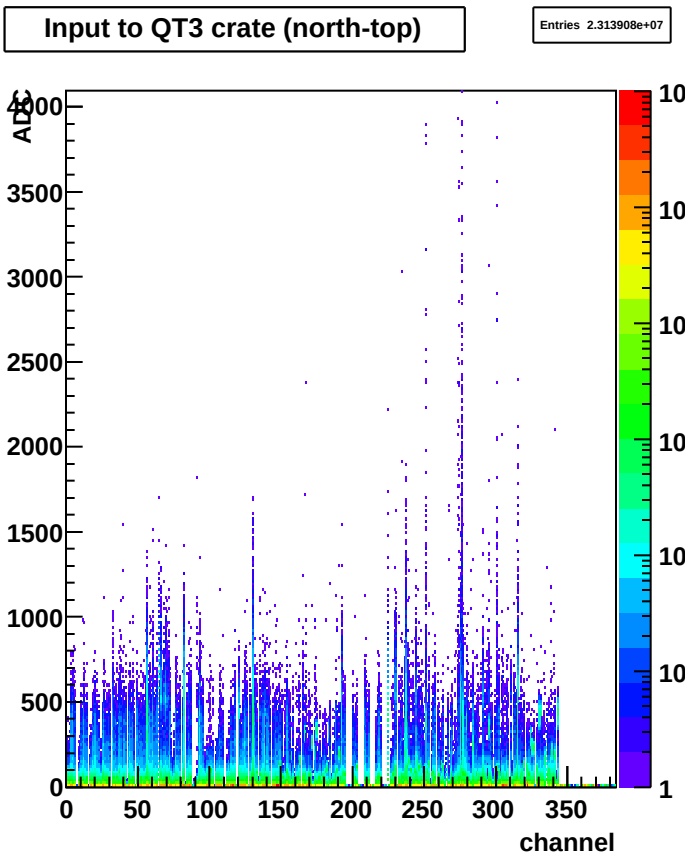

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2269979
Mean 4080
RMS 421.2

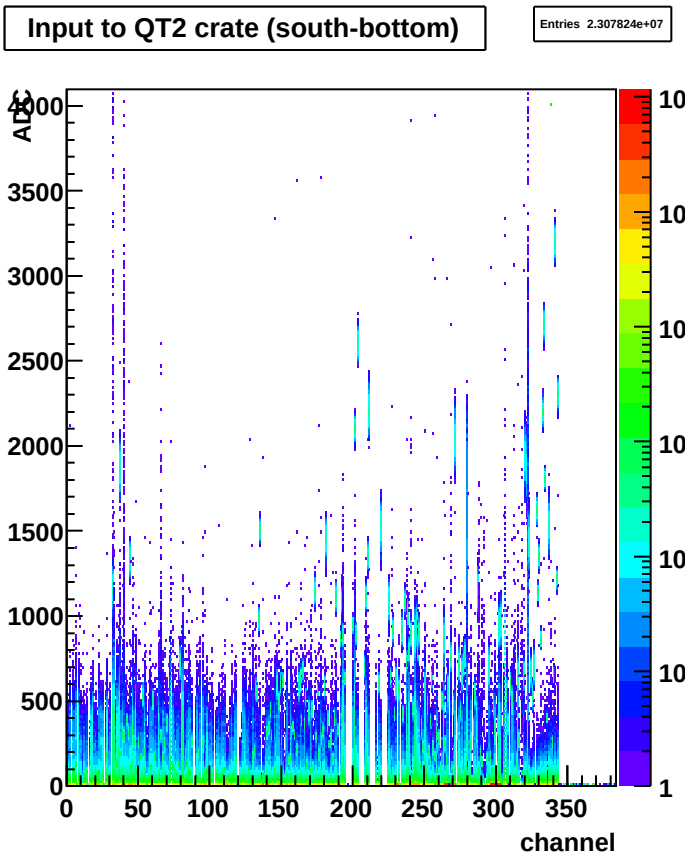

VPD-TAC-E
Entries 1708100
Mean 1344
RMS 152.7

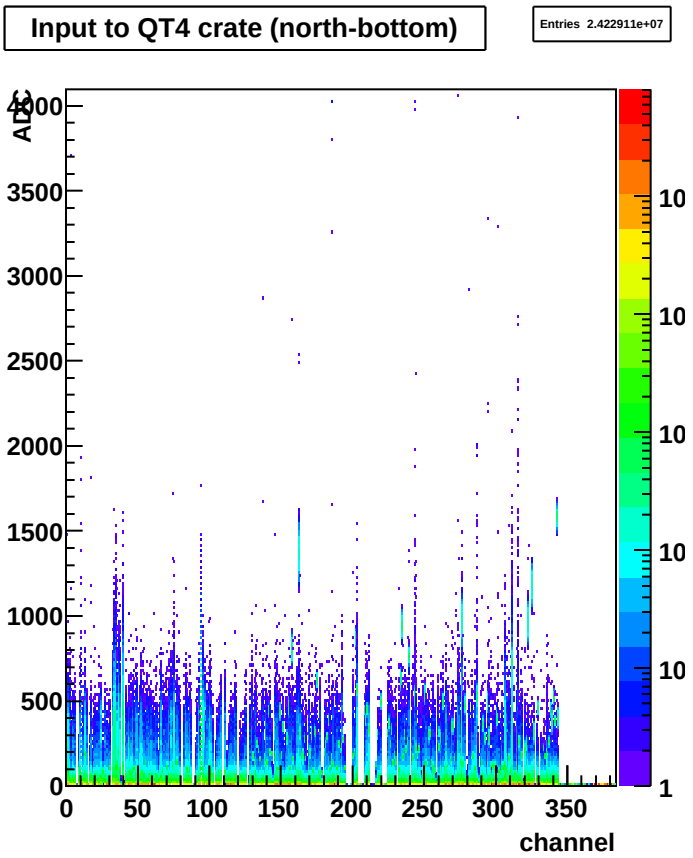

VPD-TAC-W
Entries 1638829
Mean 1449
RMS 156.3

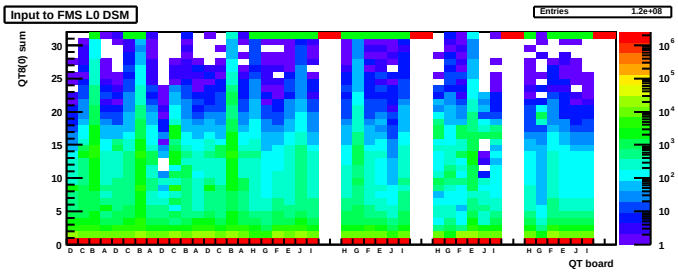

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1328889
Mean 3988
RMS 124.8

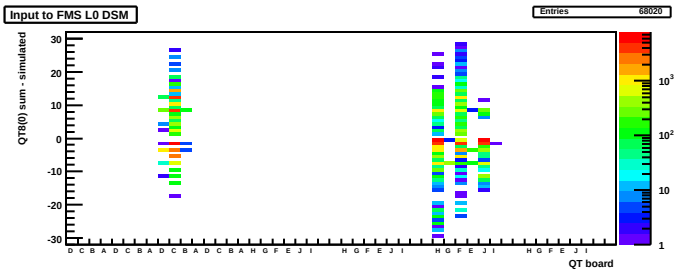

Input to QT1 crate (south-top)
Entries 3.244542e+07
ADC
channel

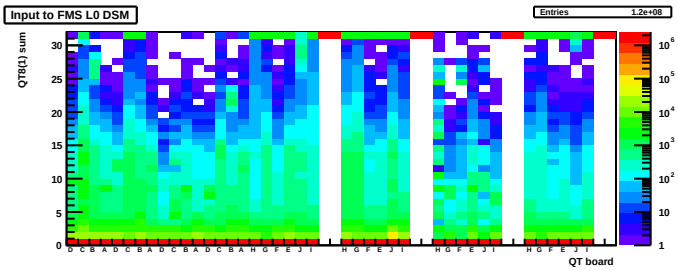

Input to QT3 crate (north-top)
Entries 2.313908e+07
ADC
channel

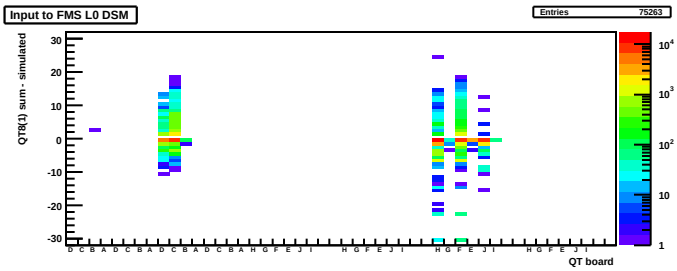

Input to QT2 crate (south-bottom)
Entries 2.307824e+07
ADC
channel

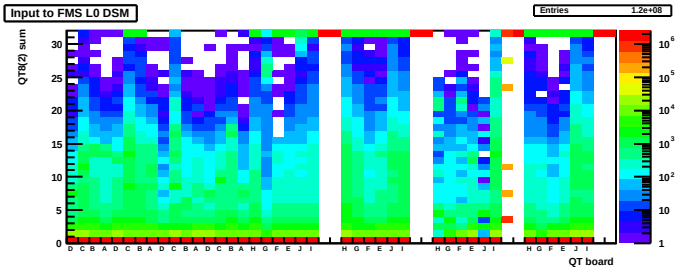

Input to QT4 crate (north-bottom)
Entries 2.422911e+07
ADC
channel

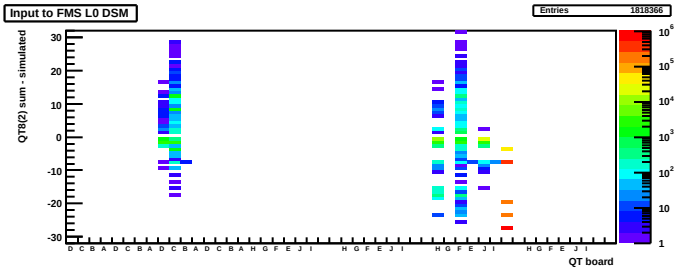

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

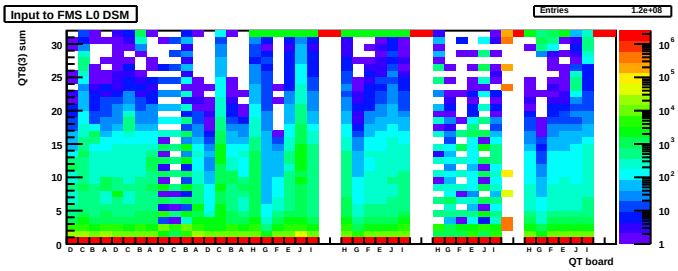

Input to FMS L0 DSM
Entries 68020
QT8(0) sum - simulated
QT board

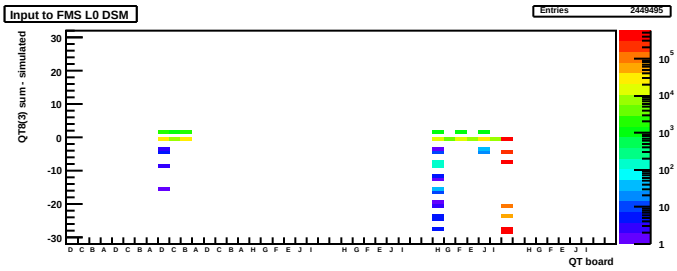

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

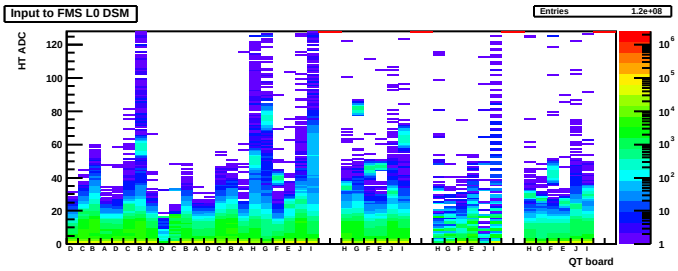

Input to FMS L0 DSM
Entries 75263
QT8(1) sum - simulated
QT board

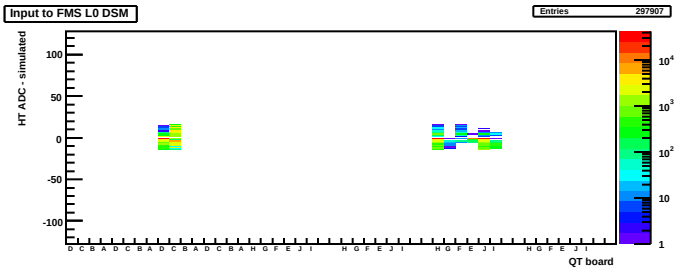

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

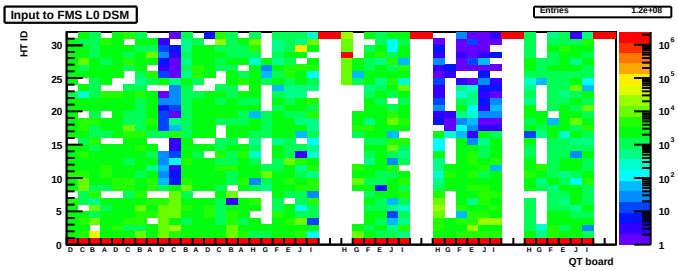

Input to FMS L0 DSM
Entries 1818366
QT8(2) sum - simulated
QT board

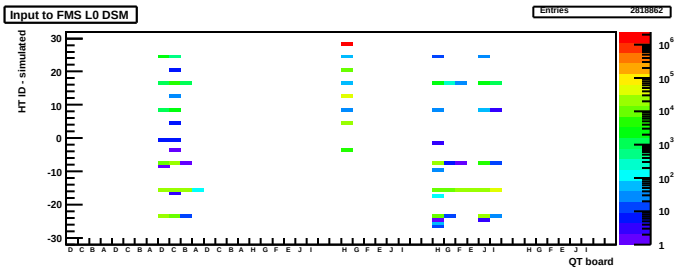

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

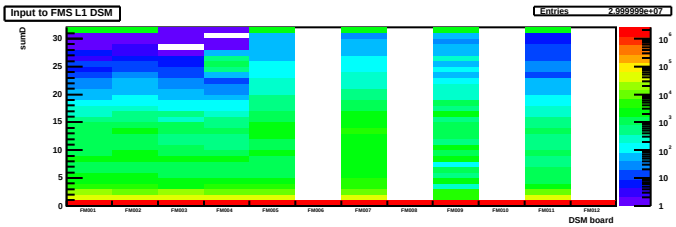

Input to FMS L0 DSM
Entries 2449495
QT8(3) sum - simulated
QT board

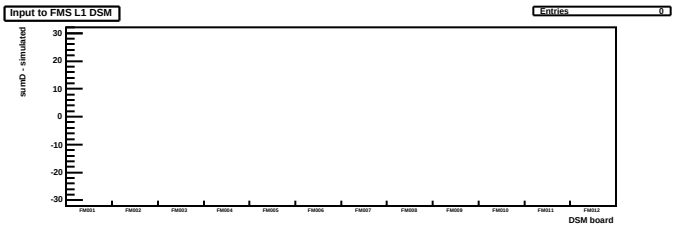

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

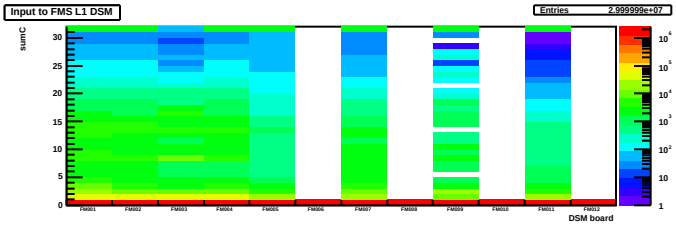

Input to FMS L0 DSM
Entries 297907
HT ADC - simulated
QT board

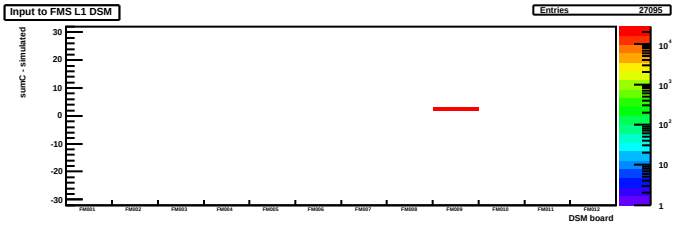

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

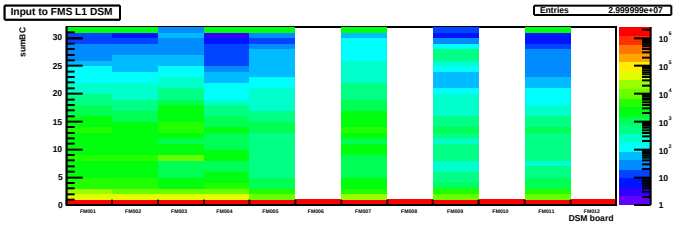

Input to FMS L0 DSM
Entries 2818862
HT ID - simulated
QT board

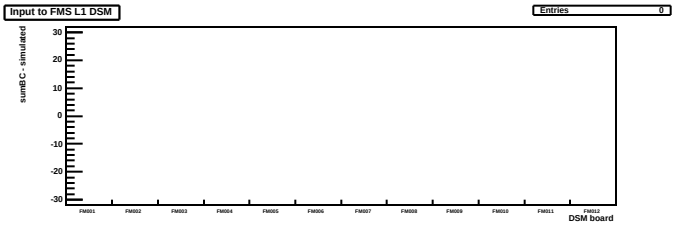

Input to FMS L1 DSM
Entries 2.999999e+07
sumD
DSM board

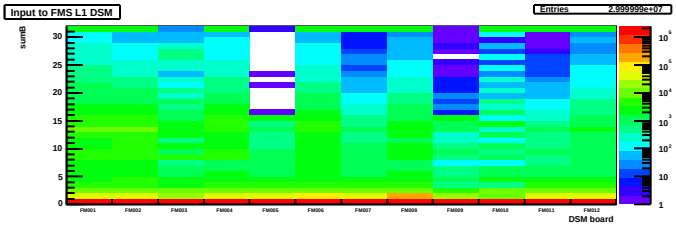

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

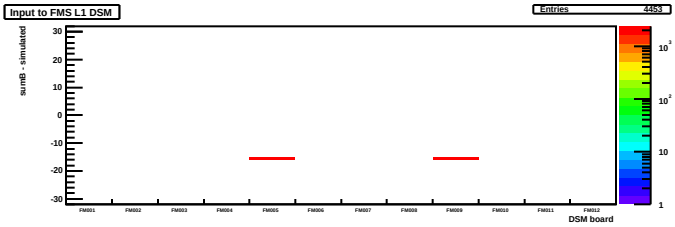

Input to FMS L1 DSM
Entries 2.999999e+07
sumC
DSM board

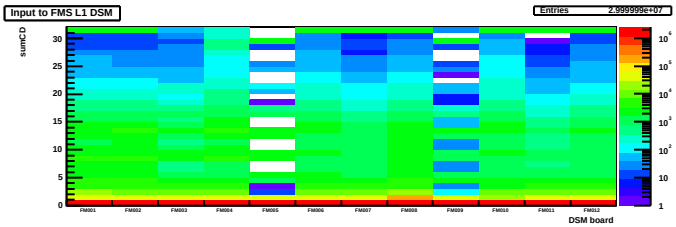

Input to FMS L1 DSM
Entries 27095
sumC - simulated
DSM board

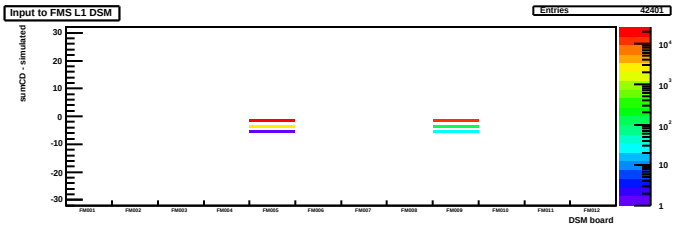

Input to FMS L1 DSM
Entries 2.999999e+07
sumBC
DSM board

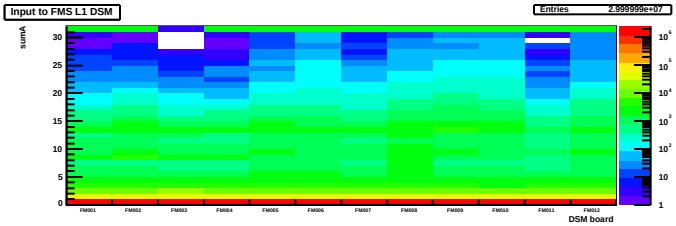

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

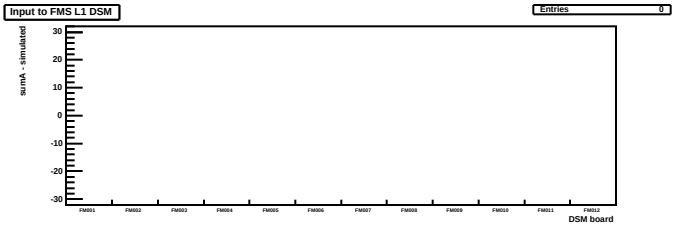

Input to FMS L1 DSM
Entries 2.999999e+07
sumB
DSM board

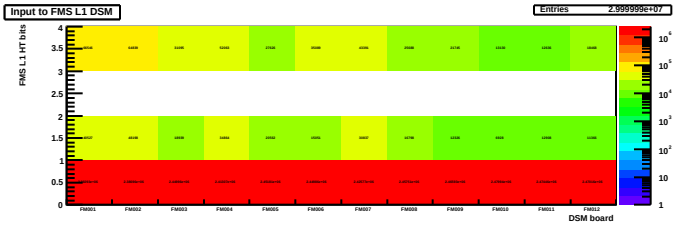

Input to FMS L1 DSM
Entries 4453
sumB - simulated
DSM board

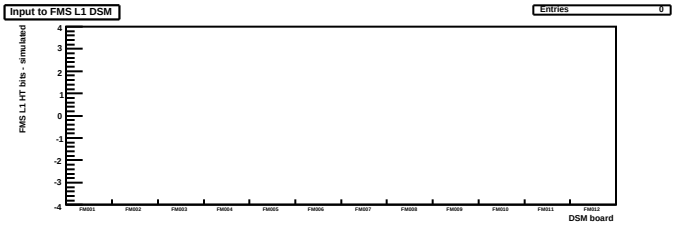

Input to FMS L1 DSM
Entries 2.999999e+07
sumCD
DSM board

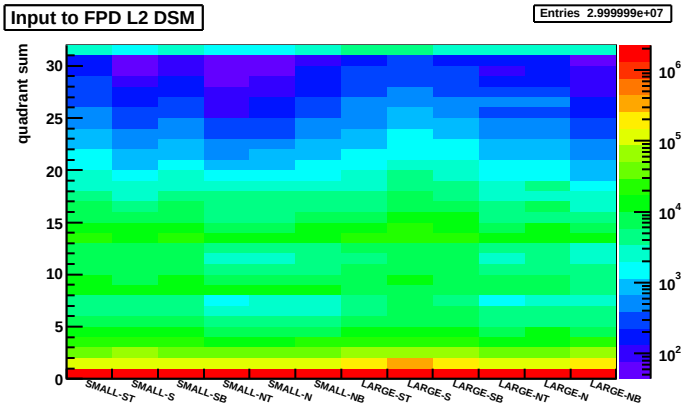

Input to FMS L1 DSM
Entries 42401
sumCD - simulated
DSM board

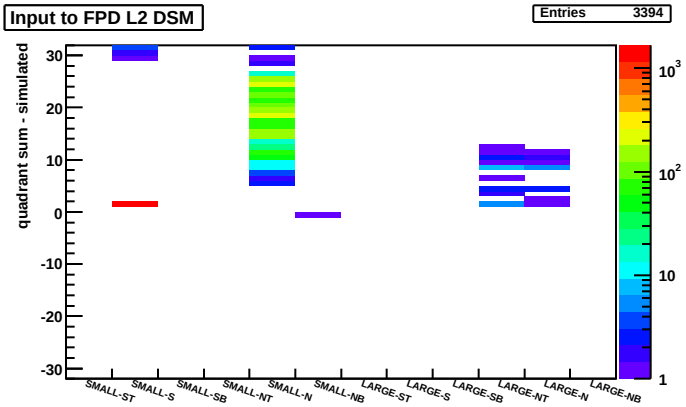

Input to FMS L1 DSM
Entries 2.999999e+07
sumA
DSM board

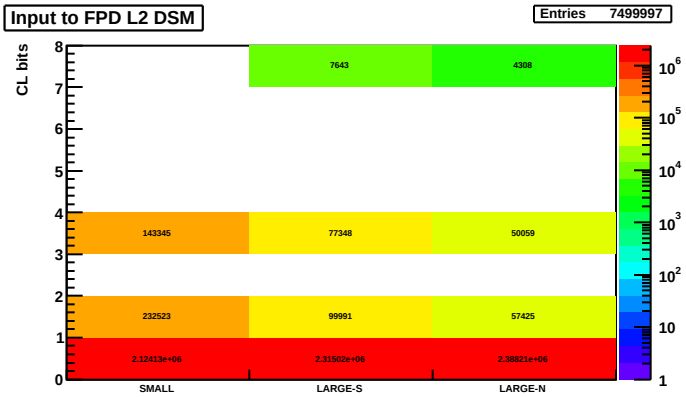

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

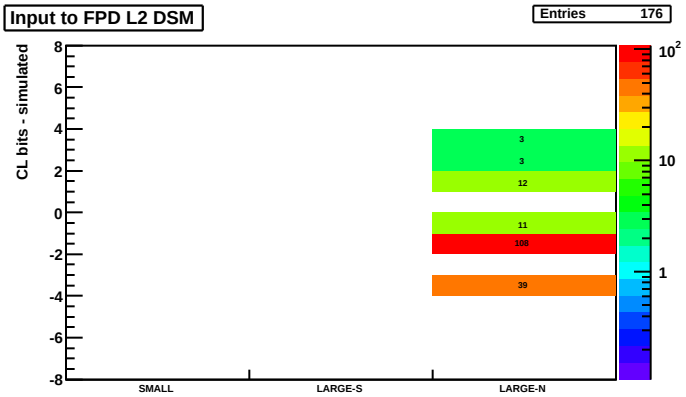

Input to FMS L1 DSM
Entries 2.999999e+07
FMS L1 HT bits
DSM board

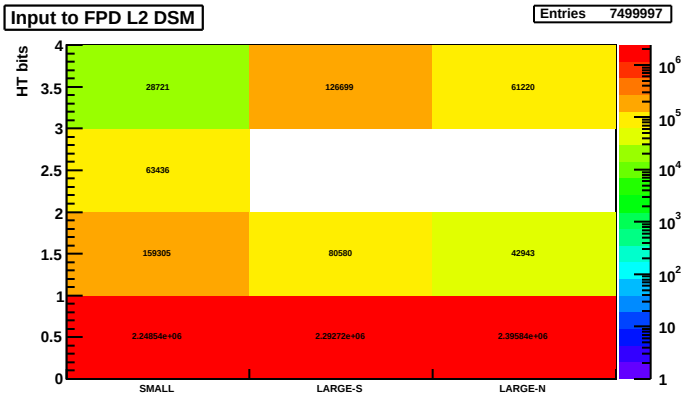

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

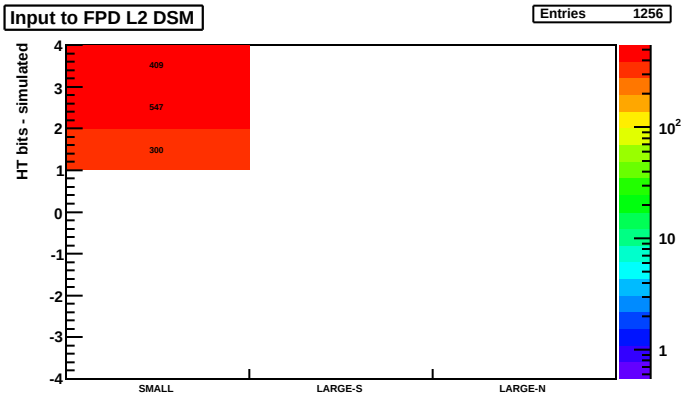

Input to FPD L2 DSM
Entries 2.999999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

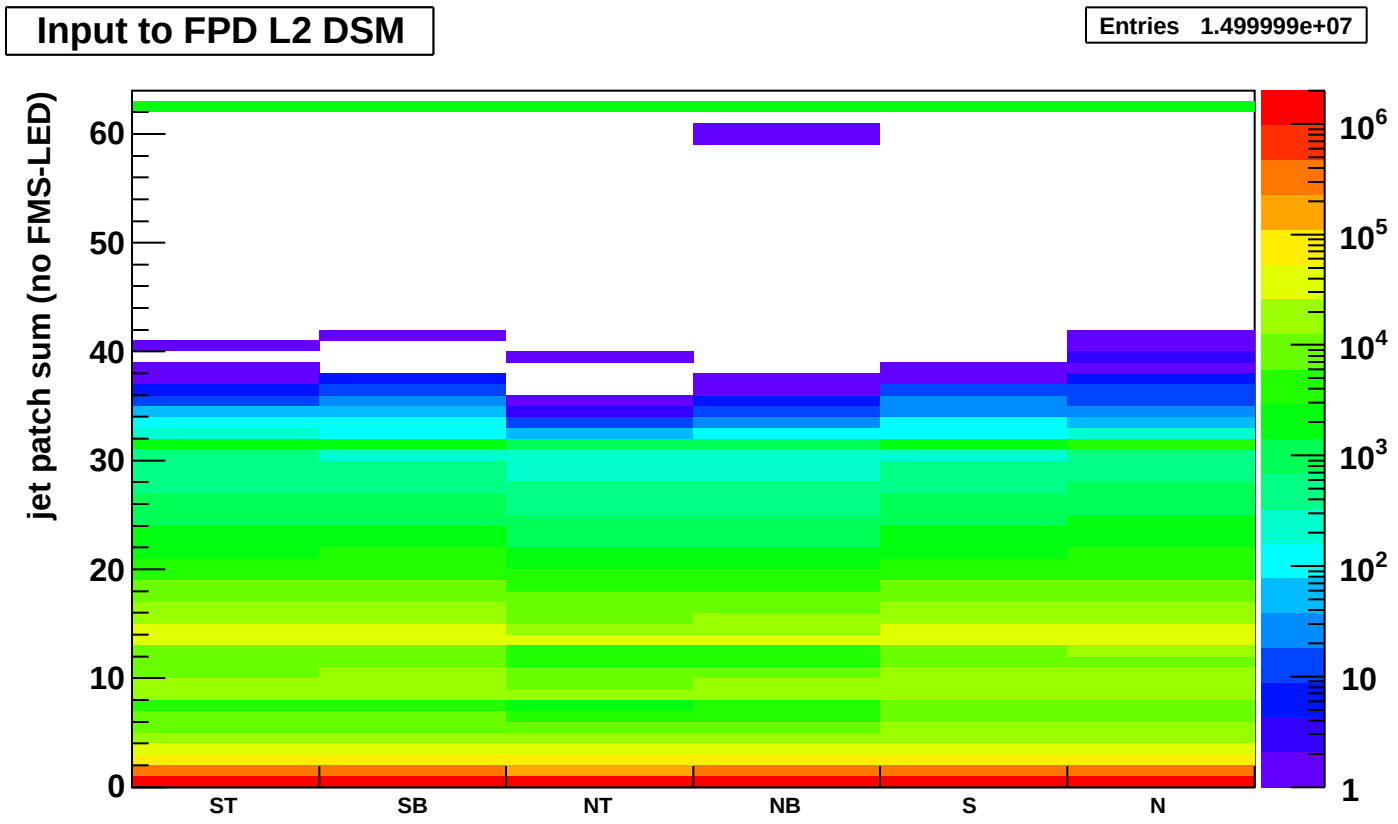

Input to FPD L2 DSM
Entries 3394
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

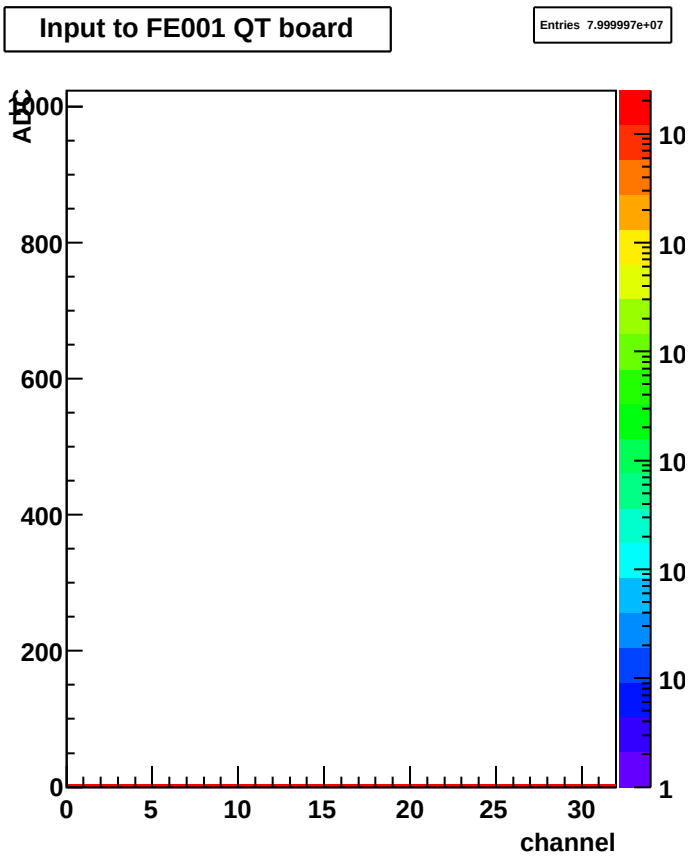

Input to FPD L2 DSM
Entries 7499997
CL bits
7643
4308
143345
77348
50059
232523
99991
57425
2.12413e+06
2.31502e+06
2.38821e+06
SMALL
LARGE-S
LARGE-N

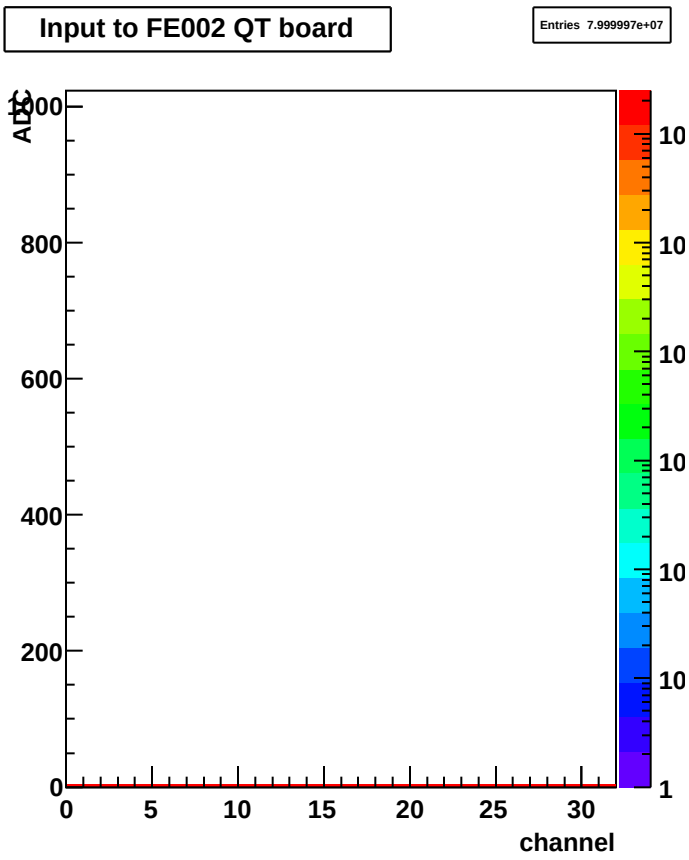

Input to FPD L2 DSM
Entries 176
CL bits - simulated
3
3
12
11
108
39
SMALL
LARGE-S
LARGE-N

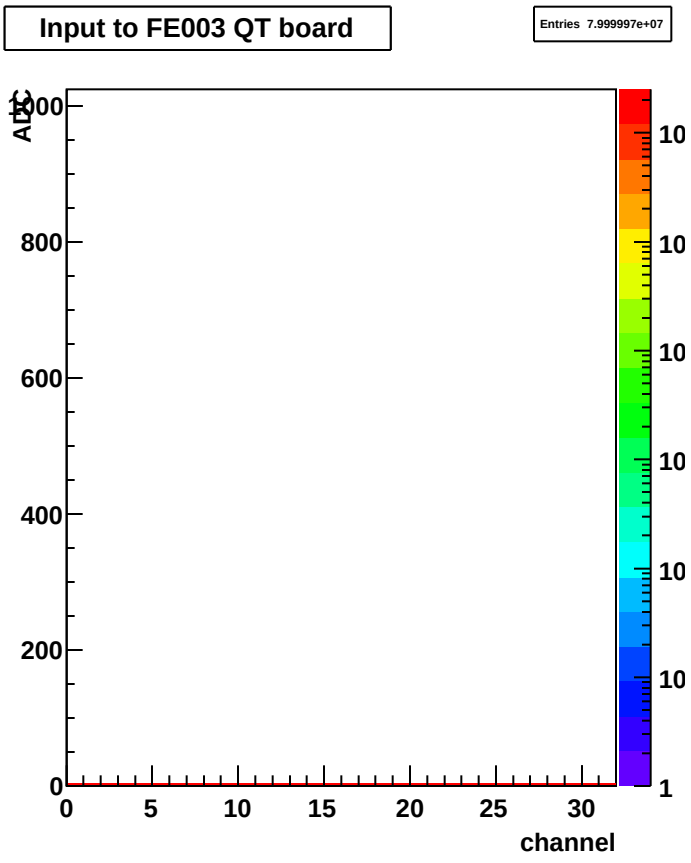

Input to FPD L2 DSM
Entries 7499997
HT bits
28721
126699
61220
63436
159305
80580
42943
2.24854e+06
2.29272e+06
2.39584e+06
SMALL
LARGE-S
LARGE-N

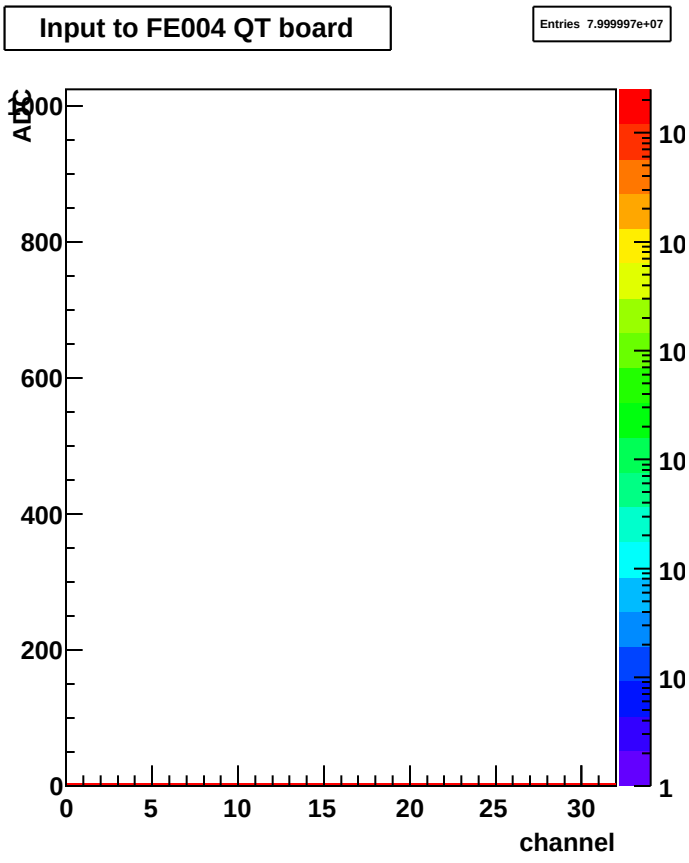

Input to FPD L2 DSM
Entries 1256
HT bits - simulated
409
547
300
SMALL
LARGE-S
LARGE-N

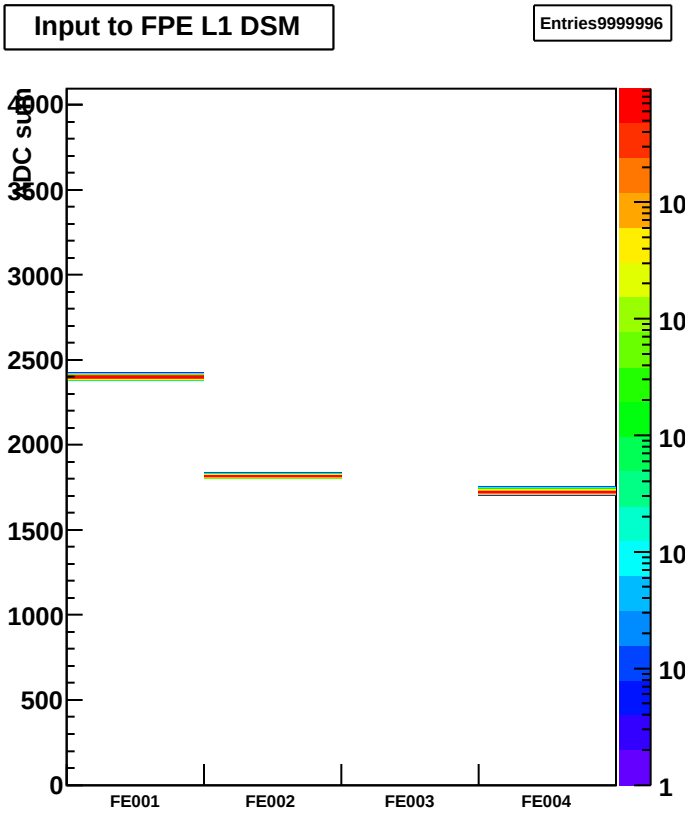

Input to FPD L2 DSM
Entries 1.499999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.999997e+07
ADC
channel

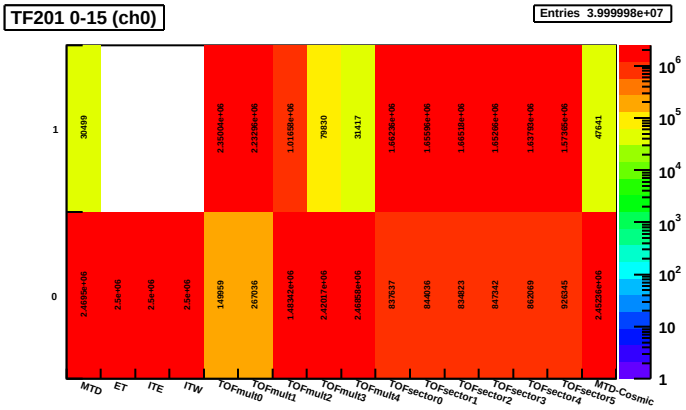

Input to FE002 QT board
Entries 7.999997e+07
ADC
channel


Input to FE003 QT board
Entries 7.999997e+07
ADC
channel

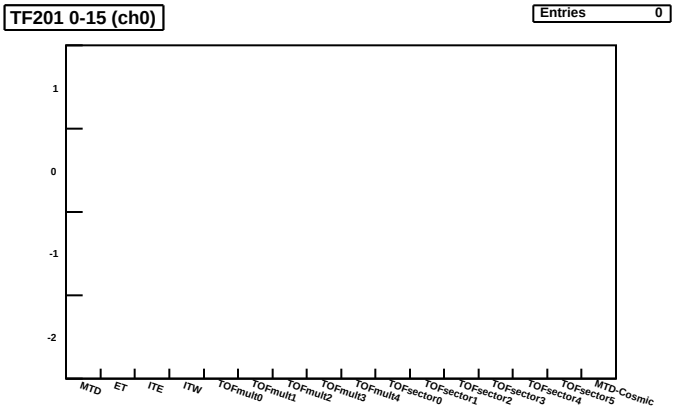

Input to FE004 QT board
Entries 7.999997e+07
ADC
channel


Input to FPE L1 DSM
Entries9999996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

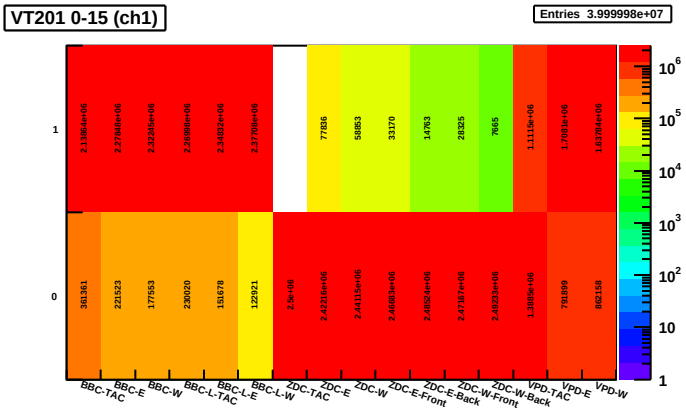
TF201 0-15 (ch0)



TF201 0-15 (ch0)



VT201 0-15 (ch1)



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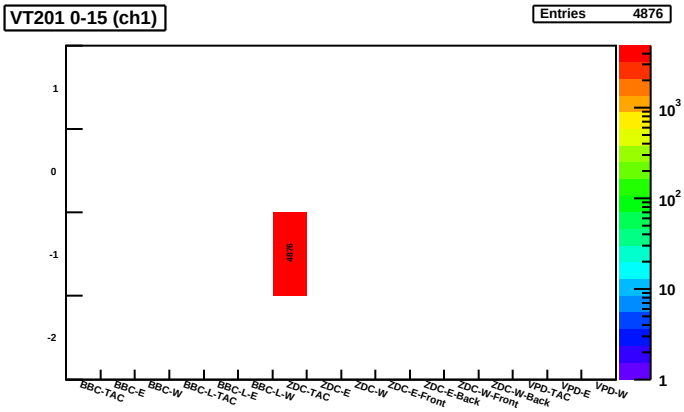

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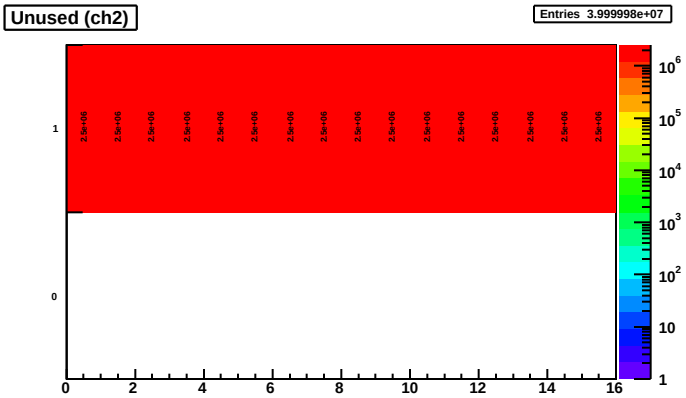

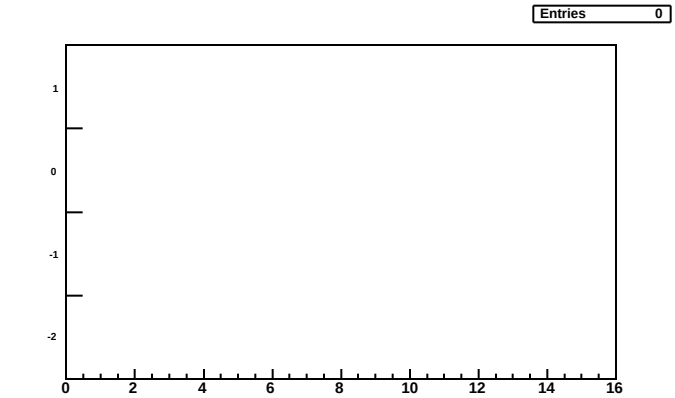

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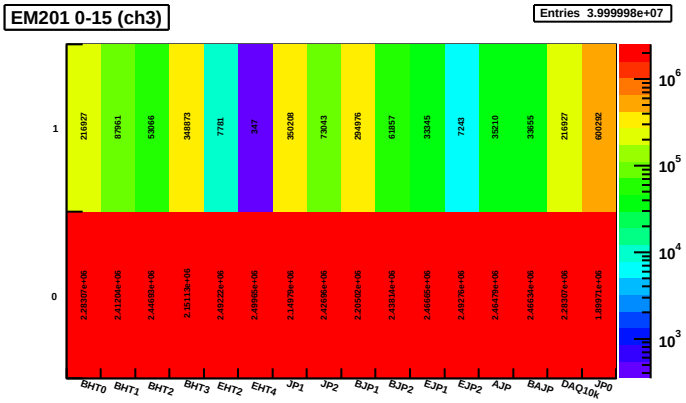

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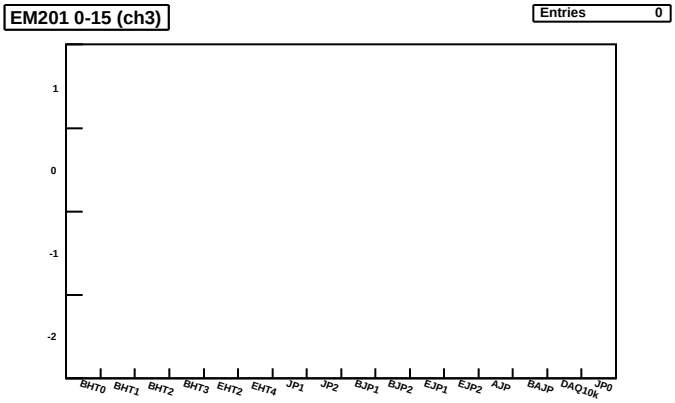
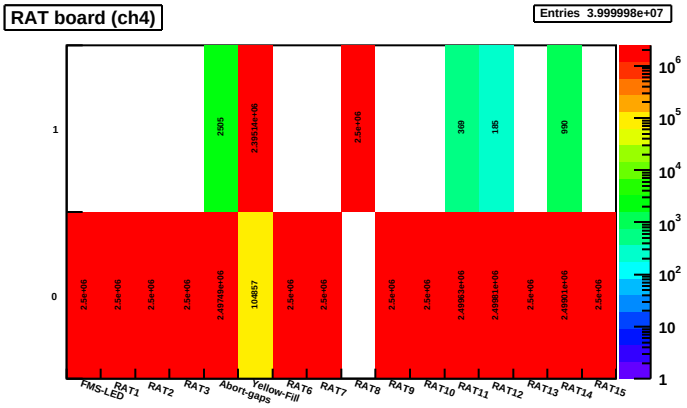
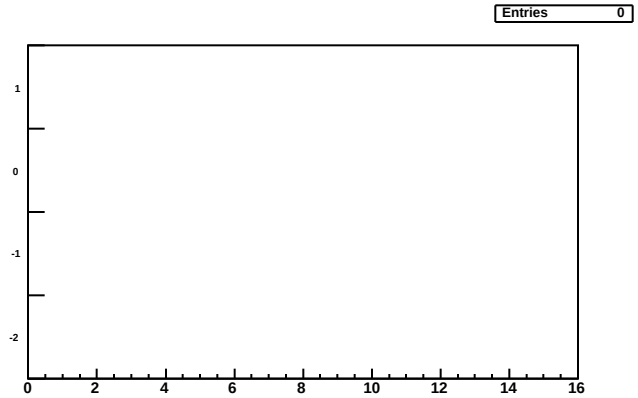
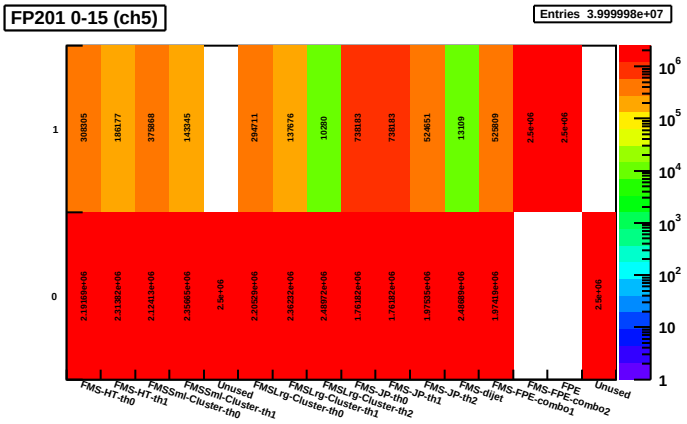


RAT board (ch4)





FP201 0-15 (ch5)



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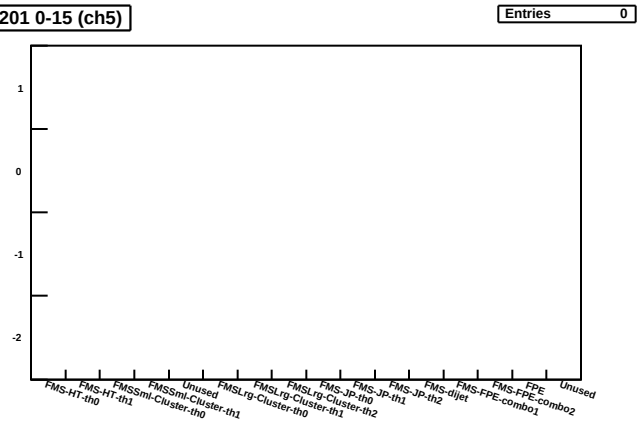
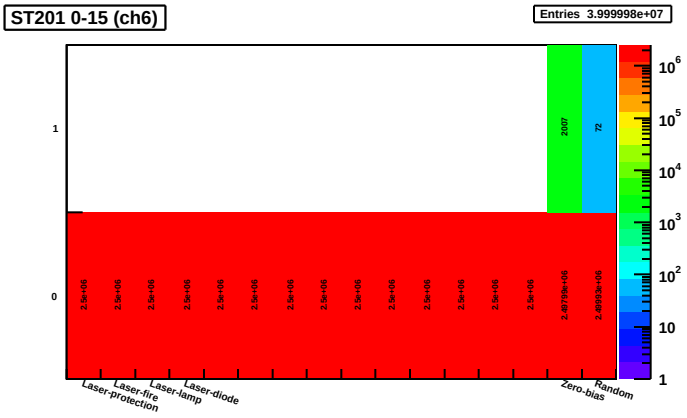
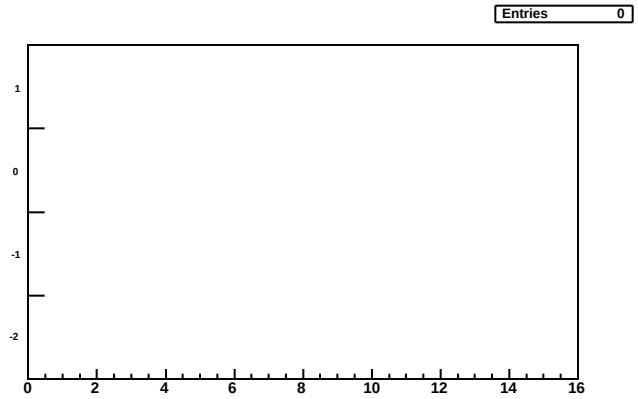
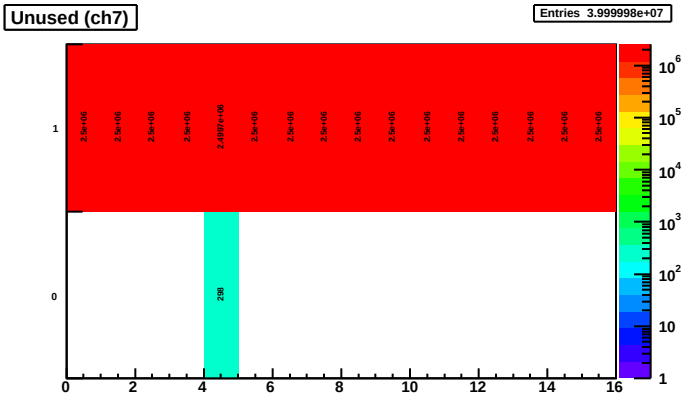
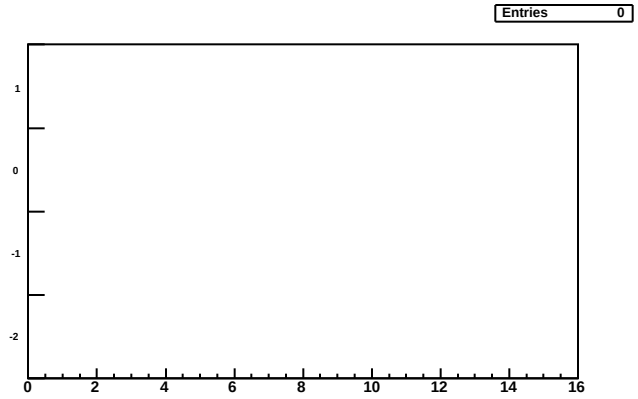


ST201 0-15 (ch6)





Unused (ch7)