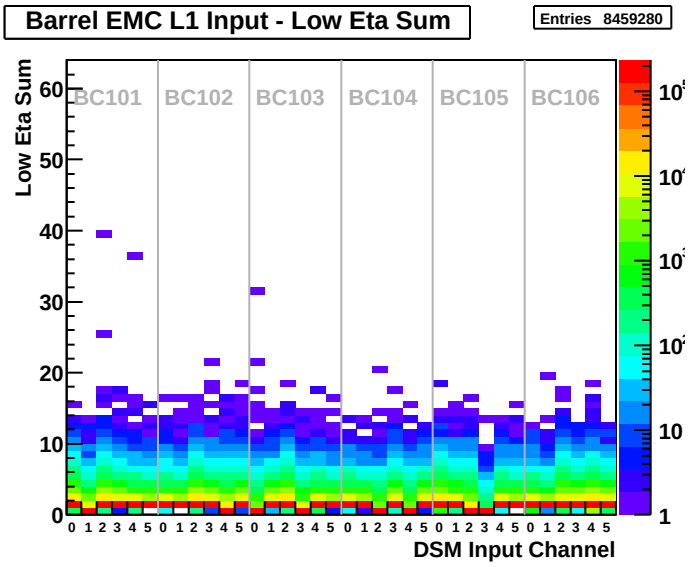
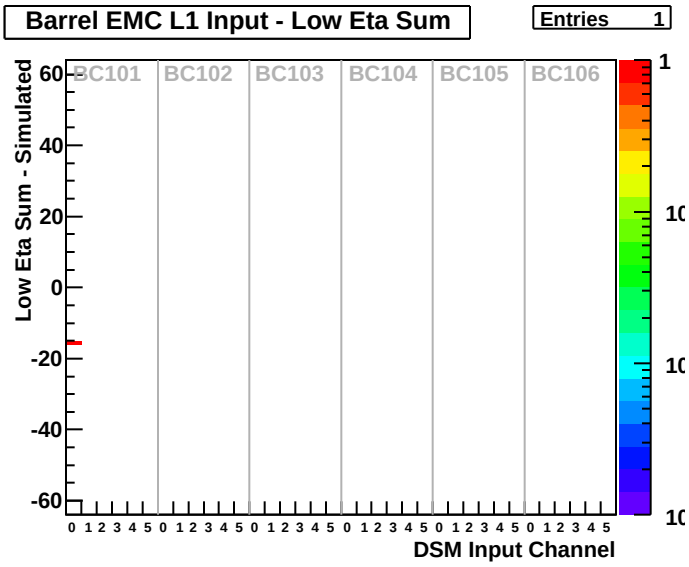
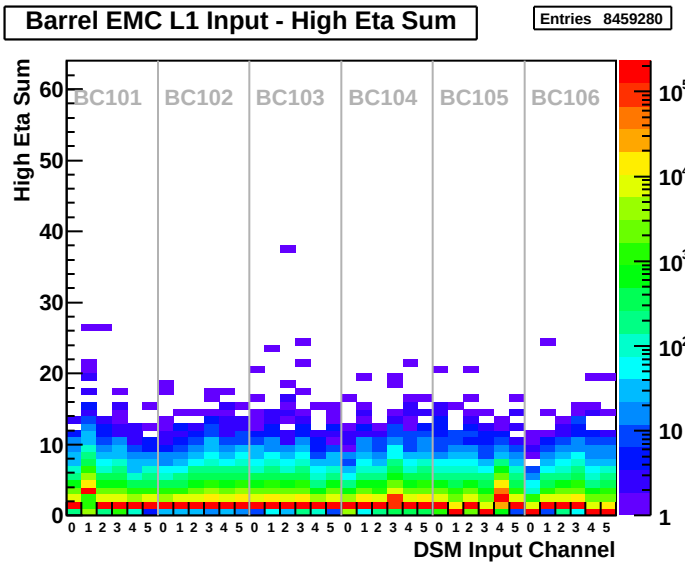
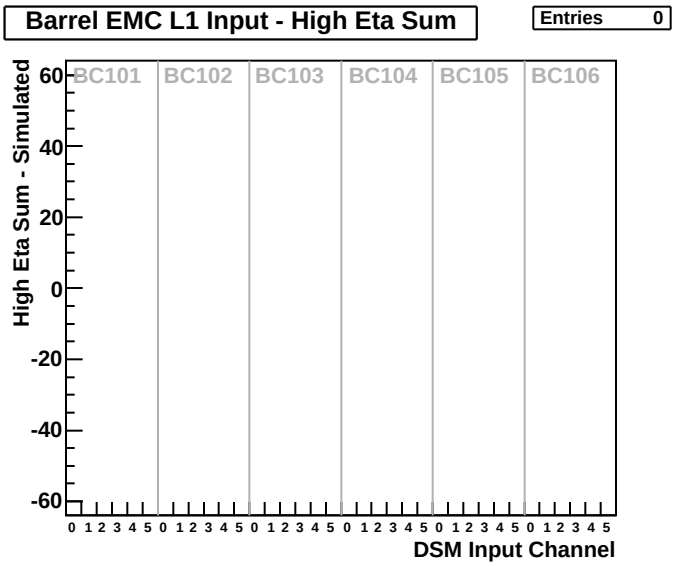
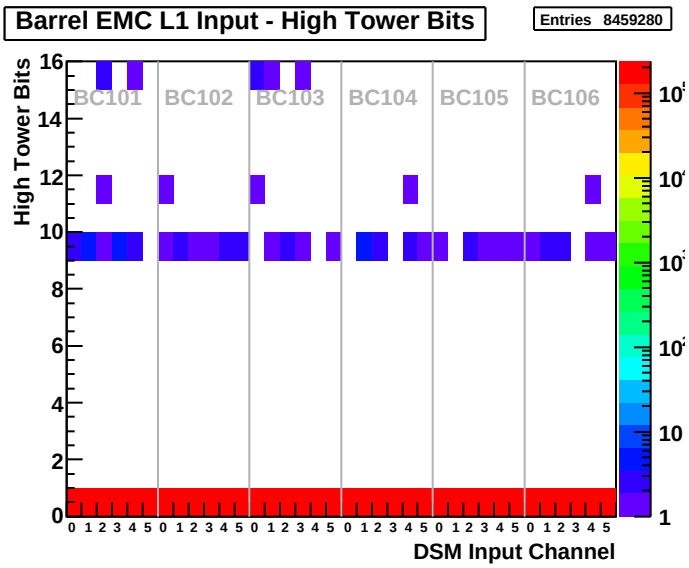
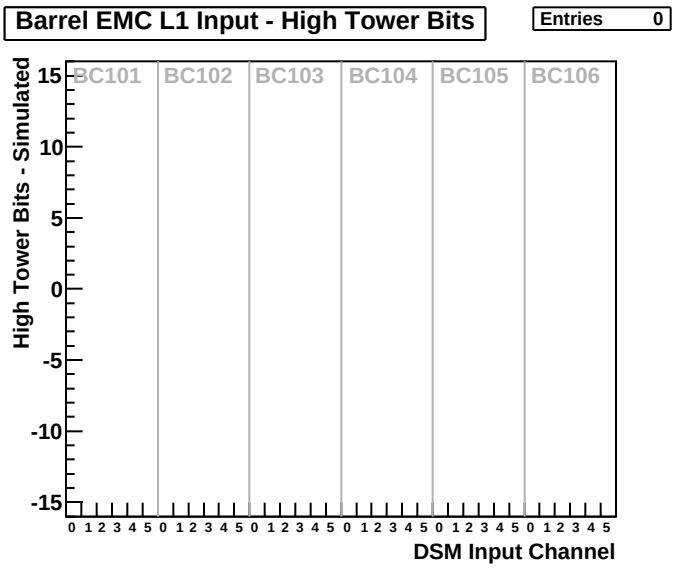

Barrel EMC L1 Input - Low Eta Sum
Entries 8459280
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

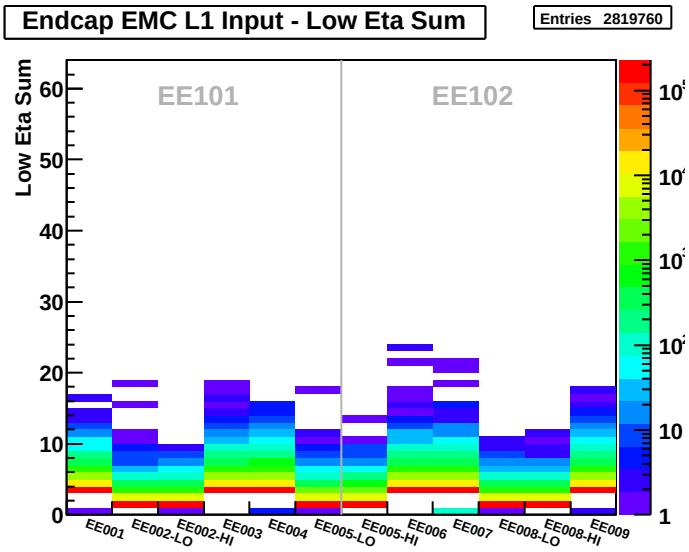

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

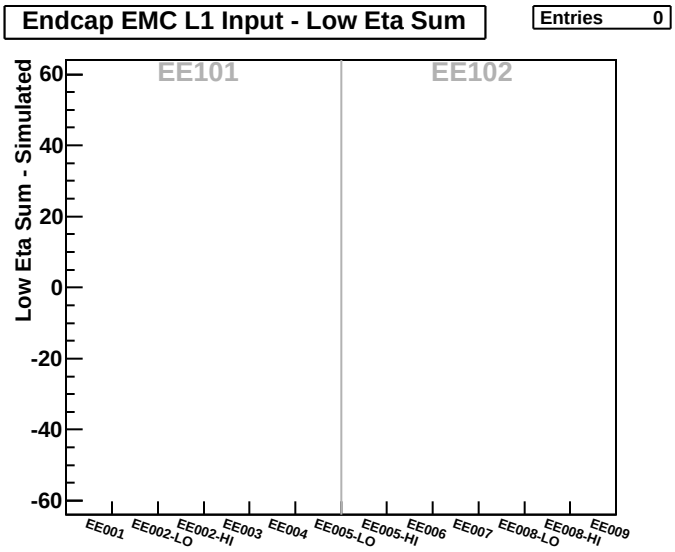

Barrel EMC L1 Input - High Eta Sum
Entries 8459280
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

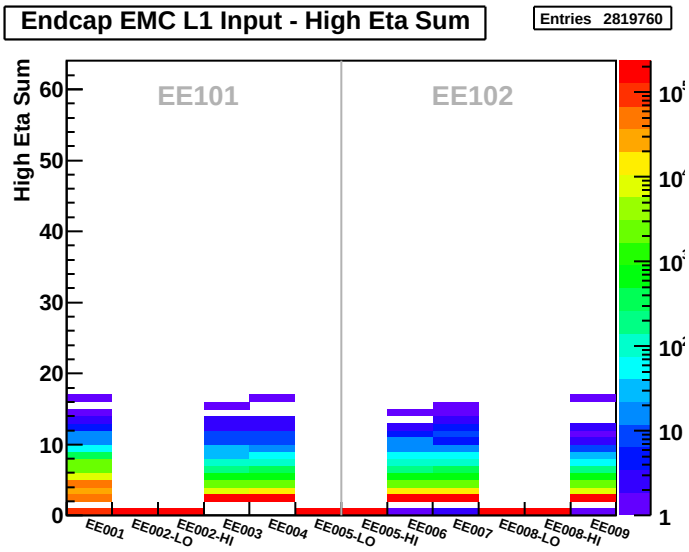

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

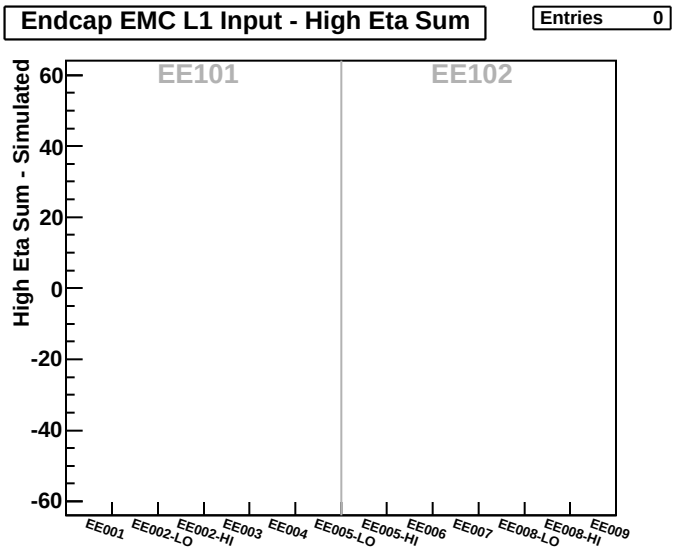

Barrel EMC L1 Input - High Tower Bits
Entries 8459280
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

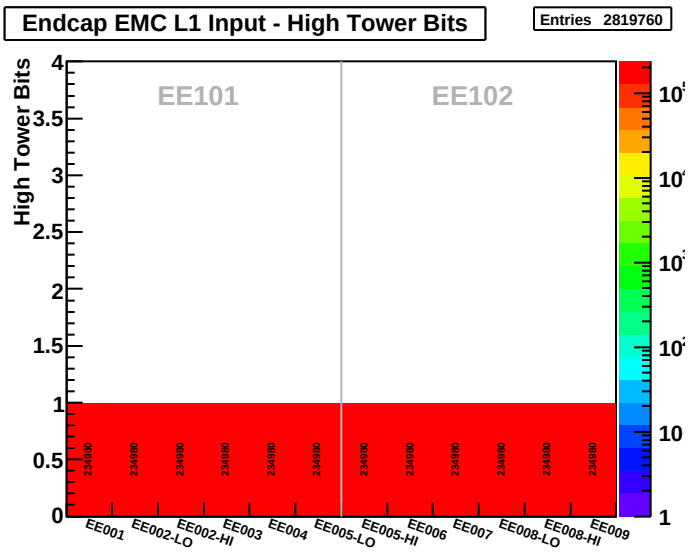

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

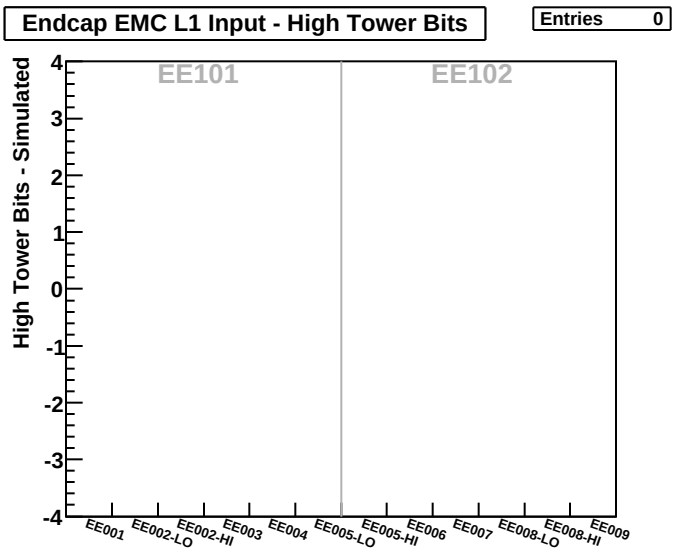

Endcap EMC L1 Input - Low Eta Sum
Entries 2819760
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

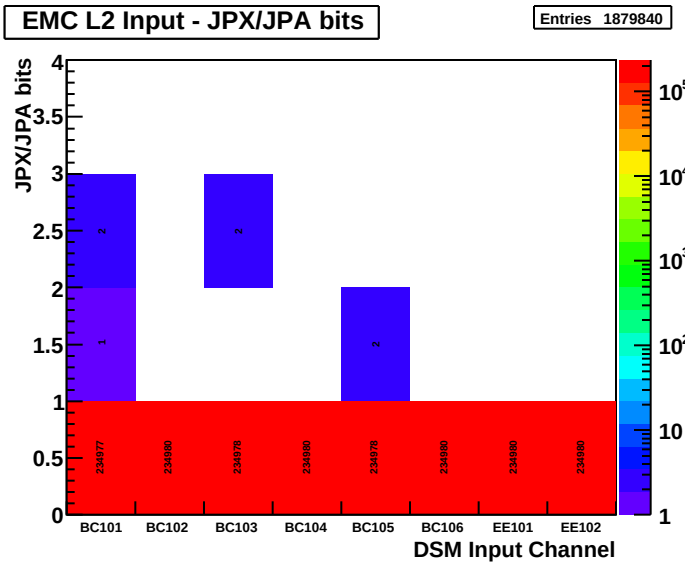

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

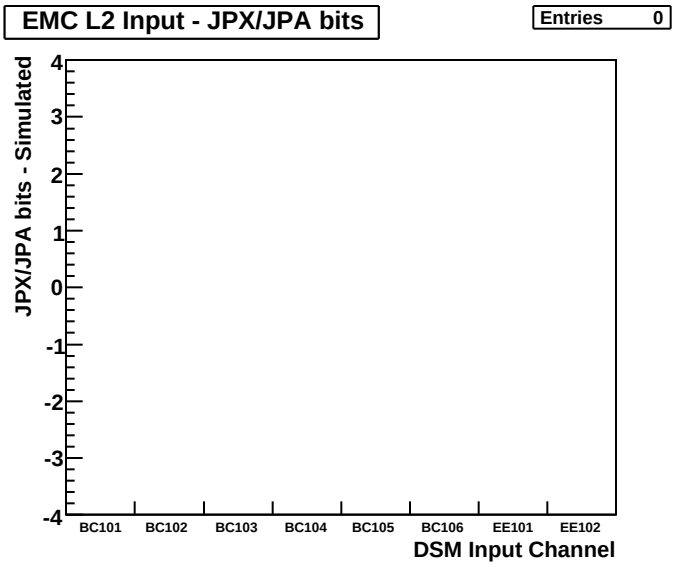

Endcap EMC L1 Input - High Eta Sum
Entries 2819760
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

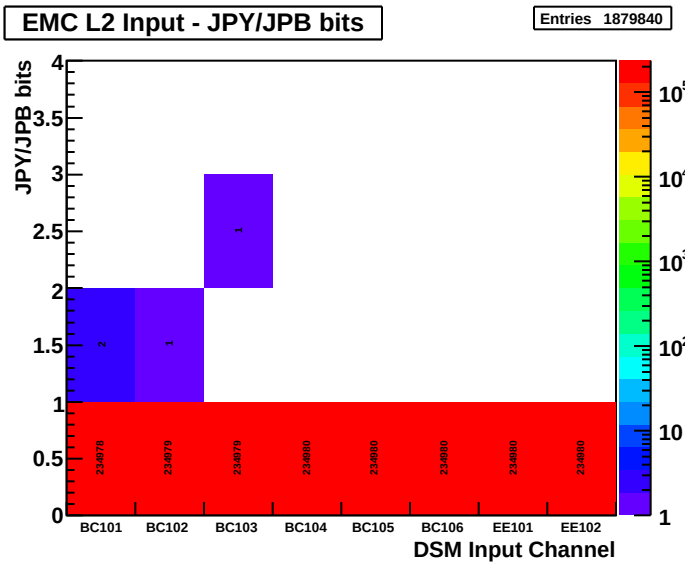

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

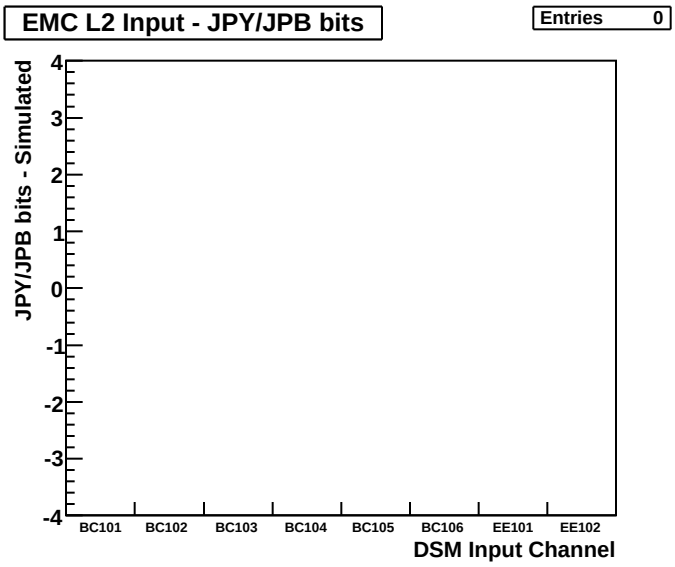

Endcap EMC L1 Input - High Tower Bits
Entries 2819760
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

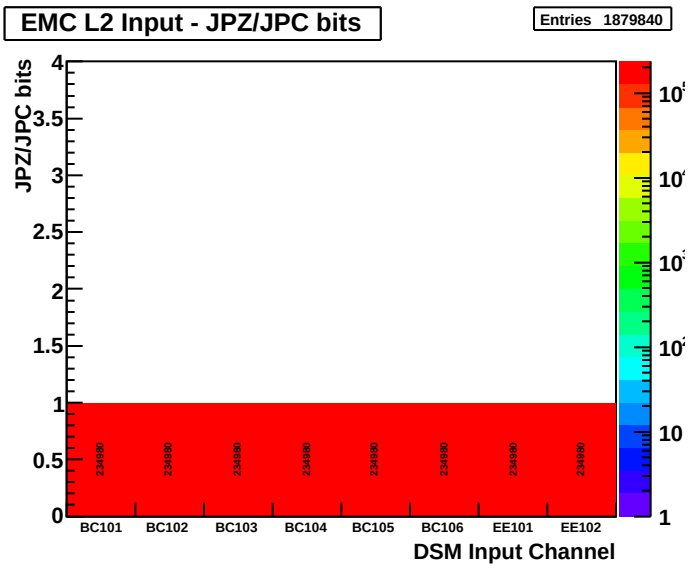

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

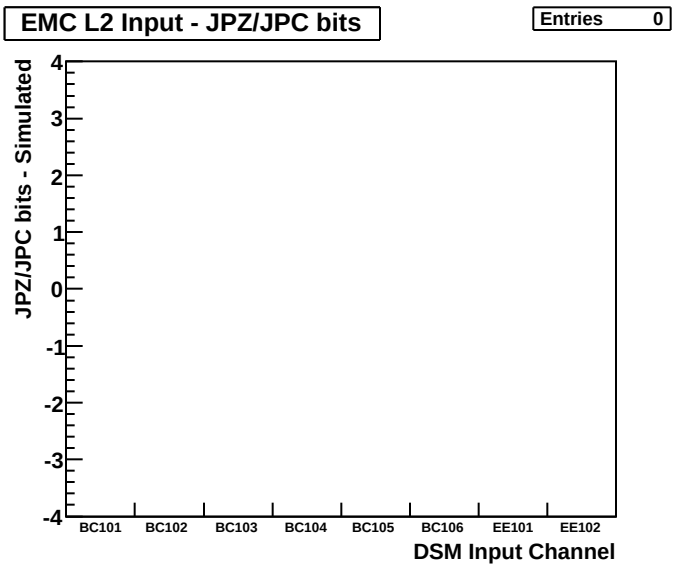

EMC L2 Input - JPX/JPA bits
Entries 1879840
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

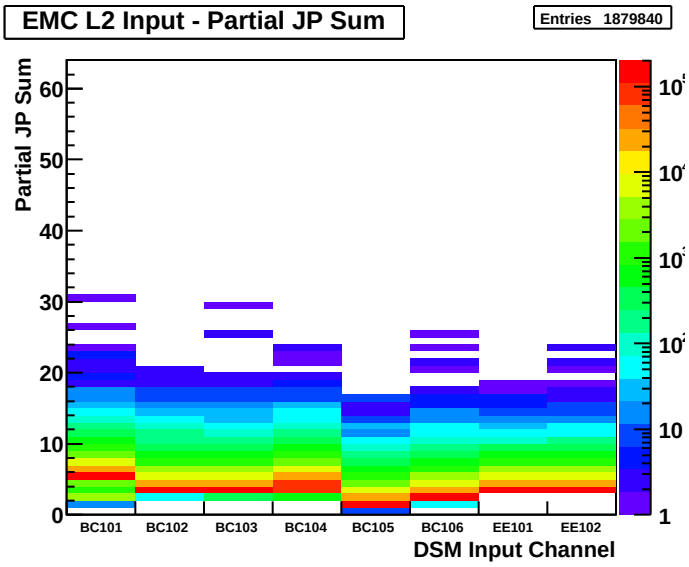

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

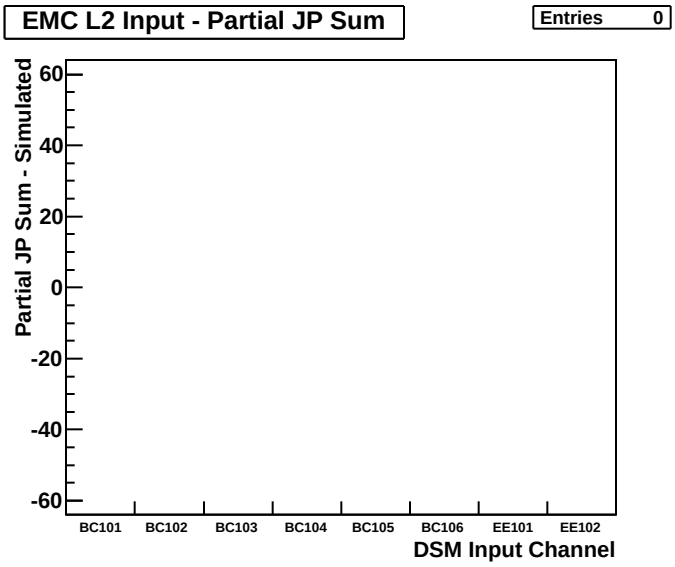

EMC L2 Input - JPY/JPB bits
Entries 1879840
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

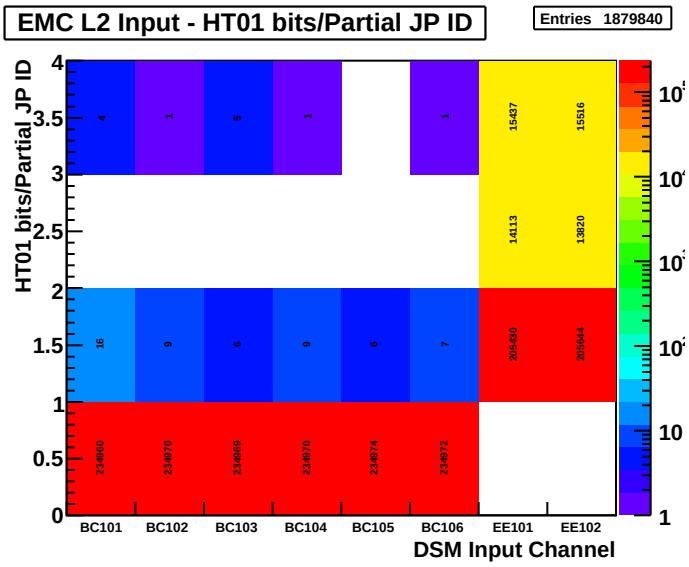

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

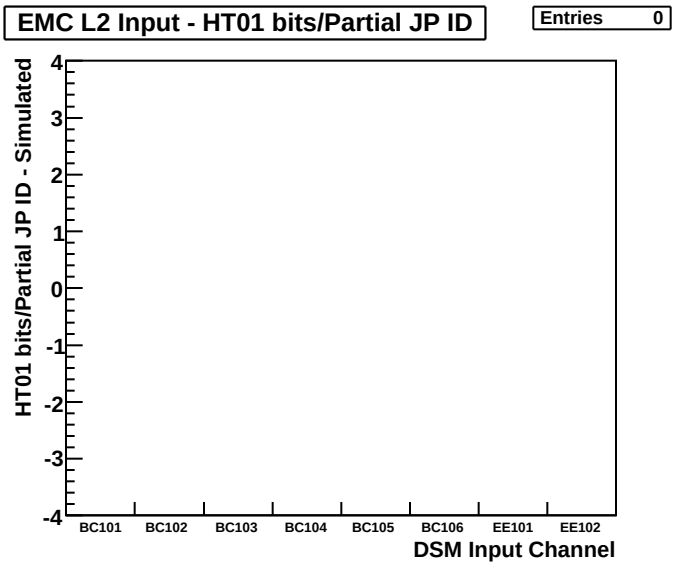

EMC L2 Input - JPZ/JPC bits
Entries 1879840
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

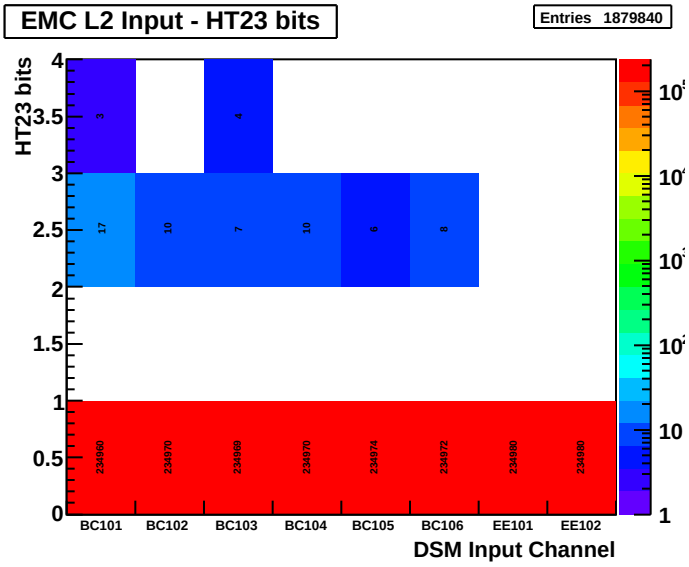

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

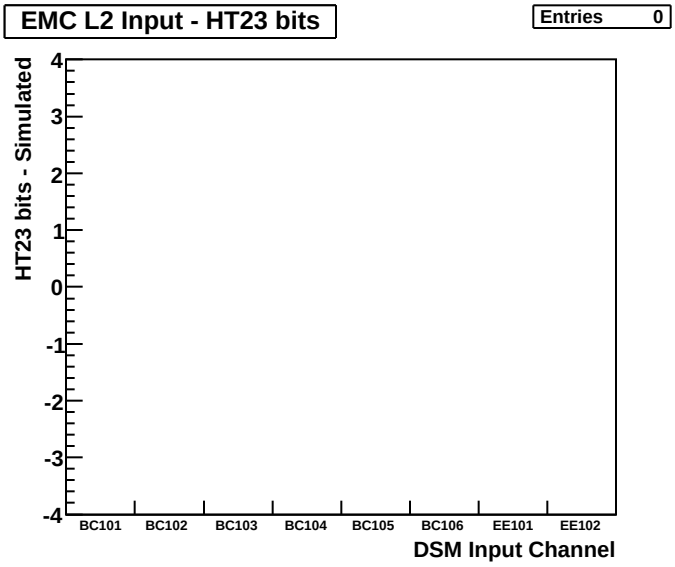

EMC L2 Input - Partial JP Sum
Entries 1879840
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

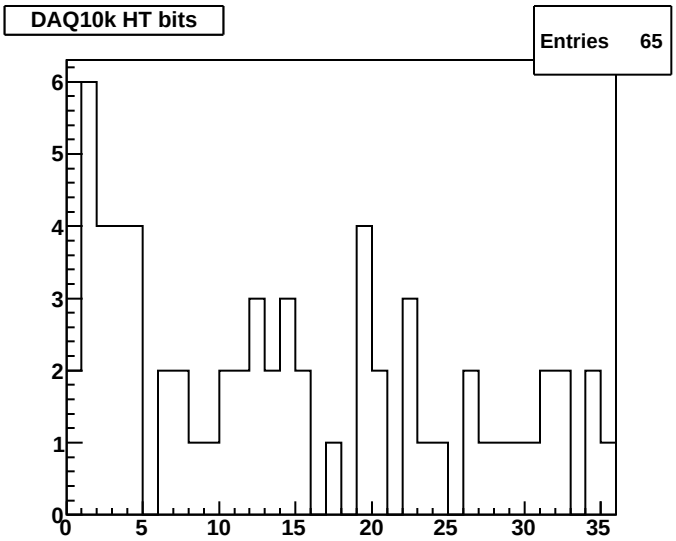

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

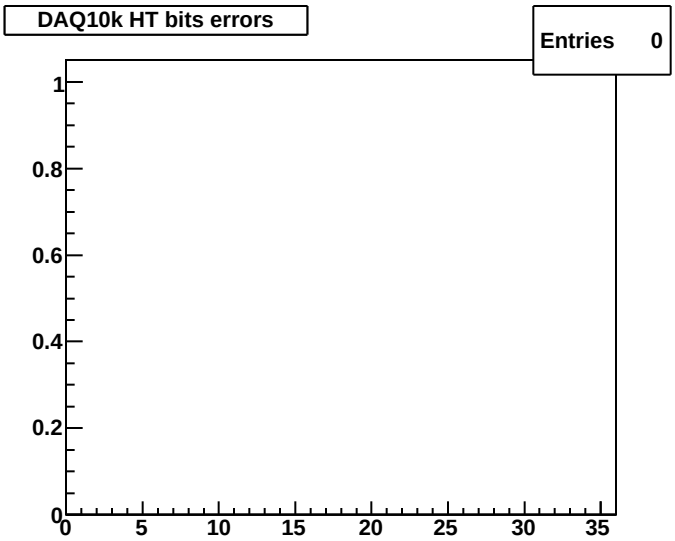

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1879840
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

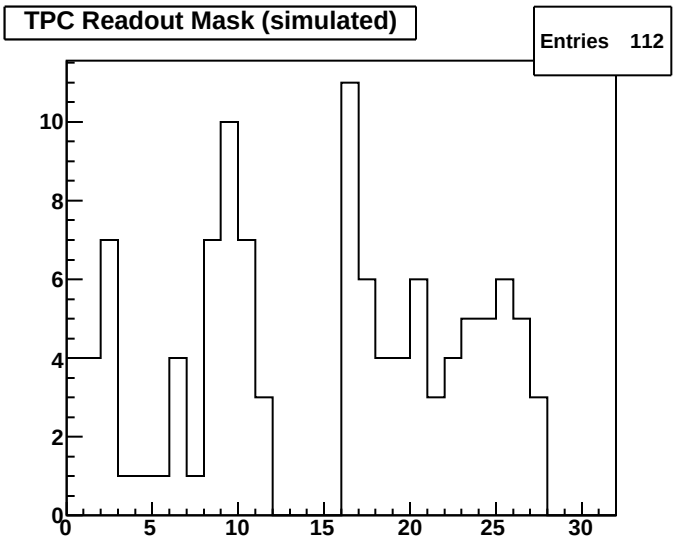

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

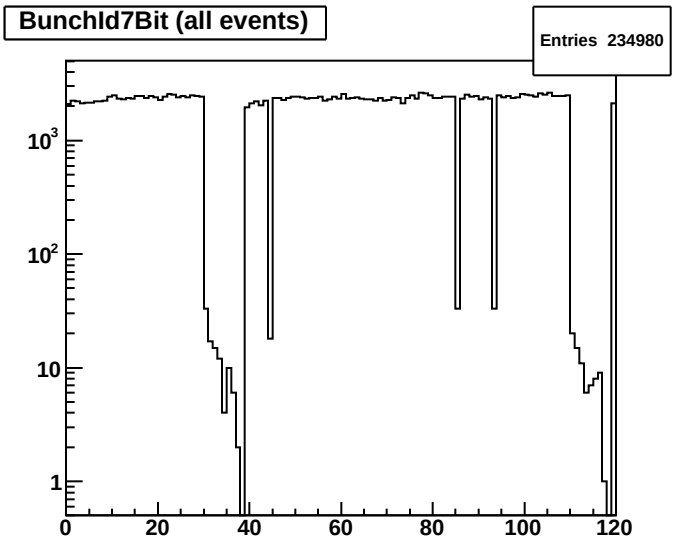

EMC L2 Input - HT23 bits
Entries 1879840
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

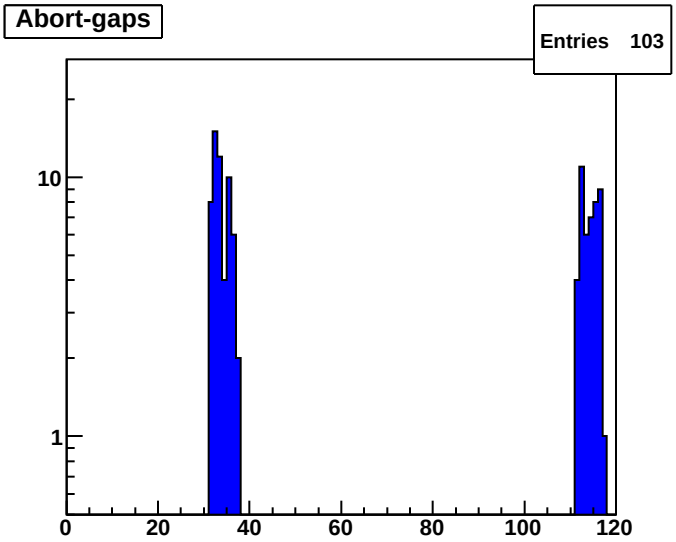

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

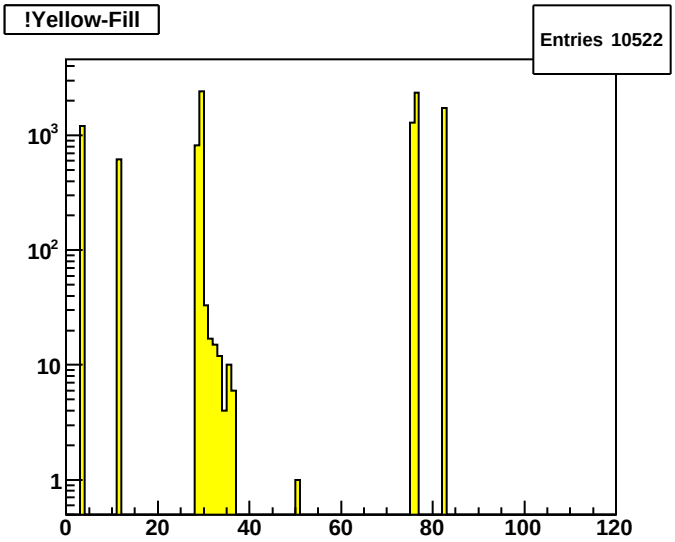

DAQ10k HT bits
Entries 65

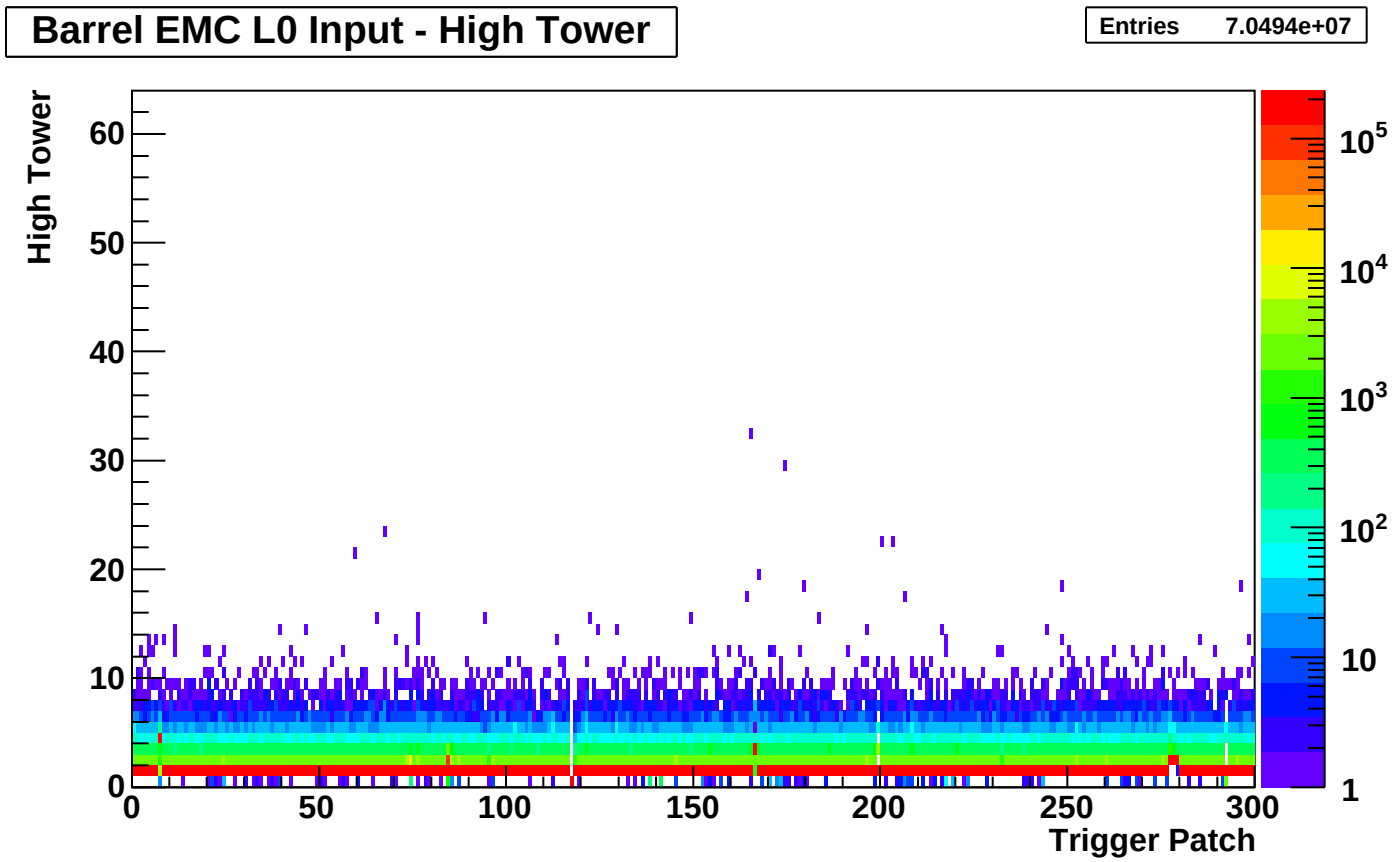

DAQ10k HT bits errors
Entries 0

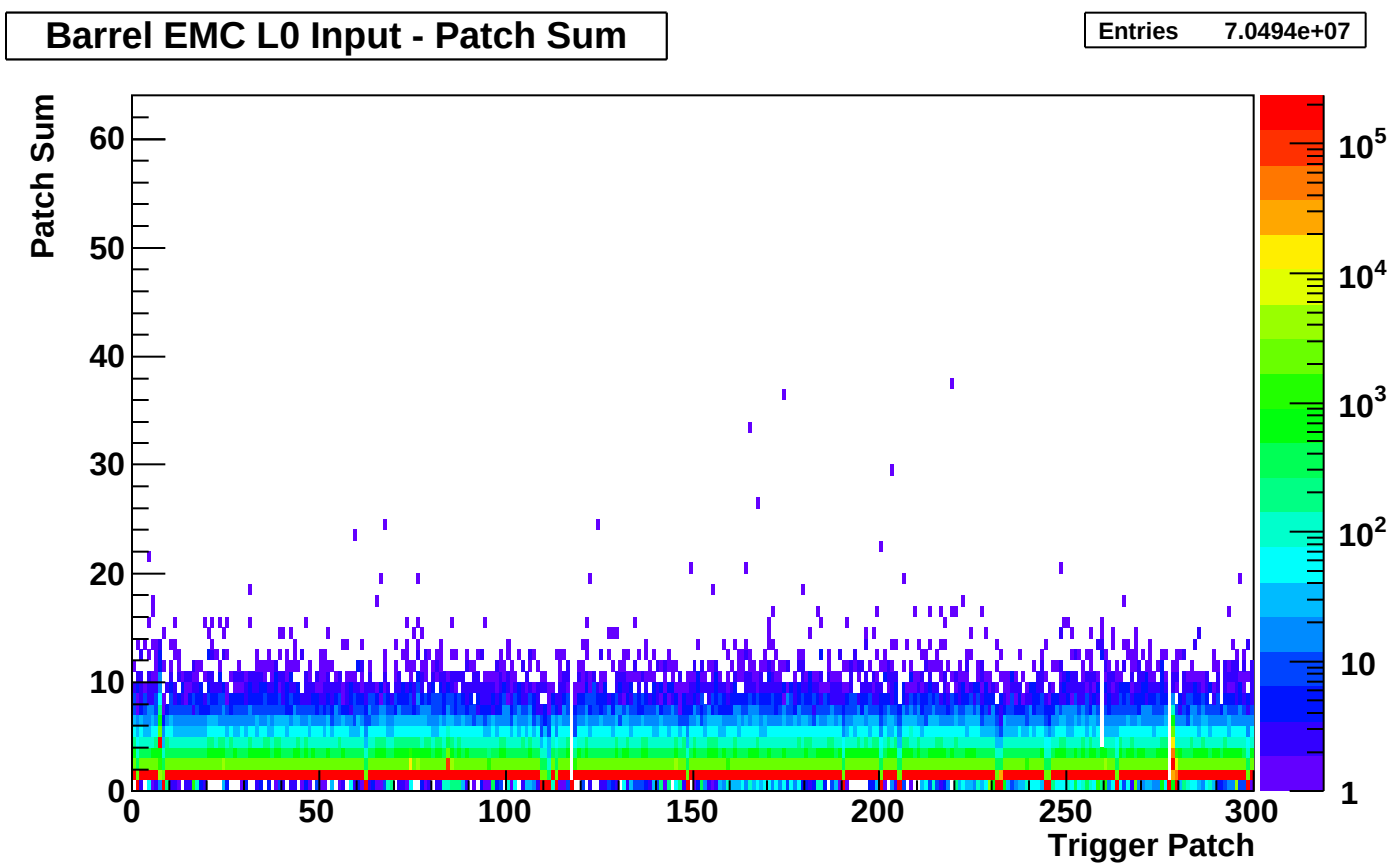

TPC Readout Mask (simulated)
Entries 112

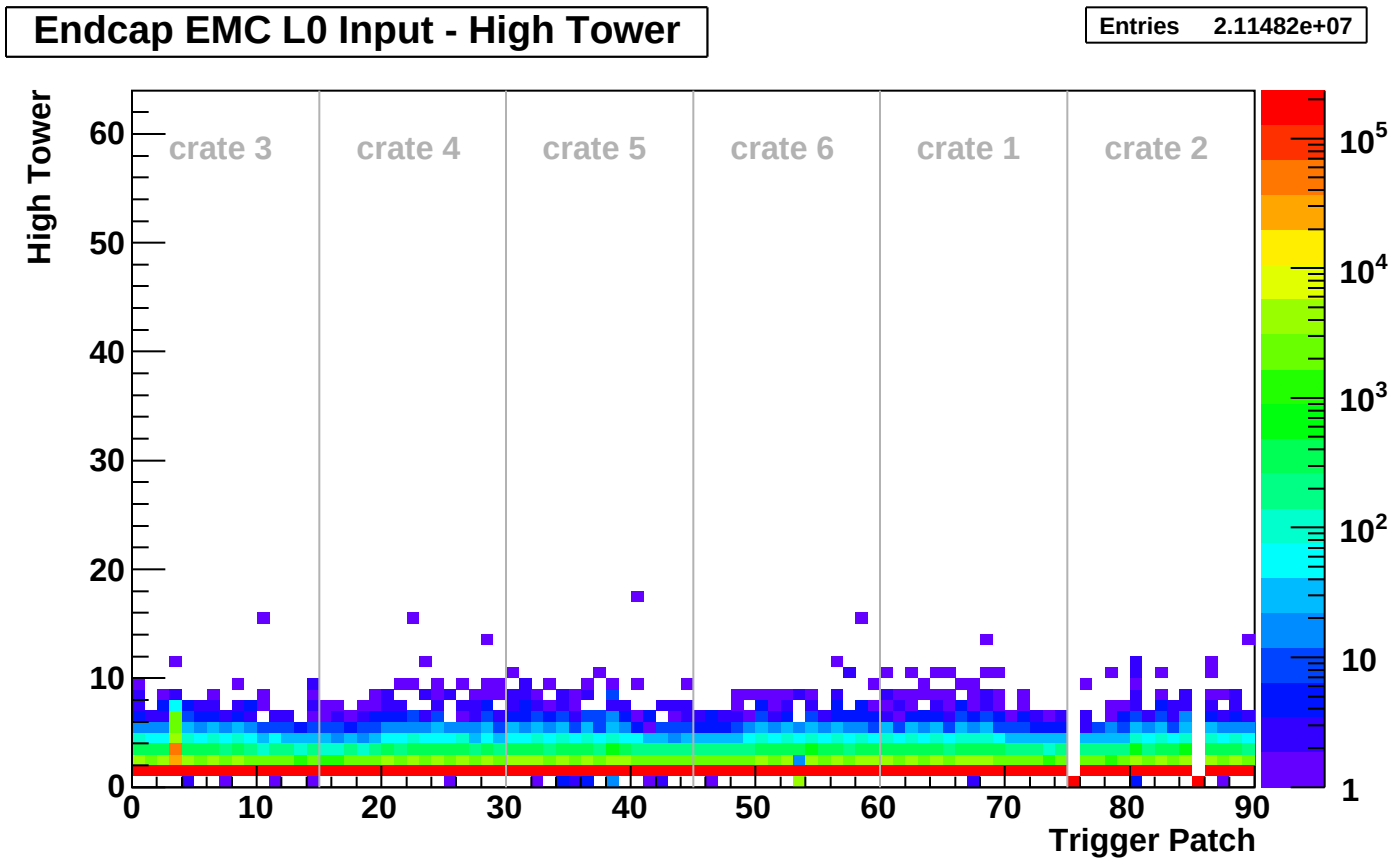

BunchId7Bit (all events)
Entries 234980

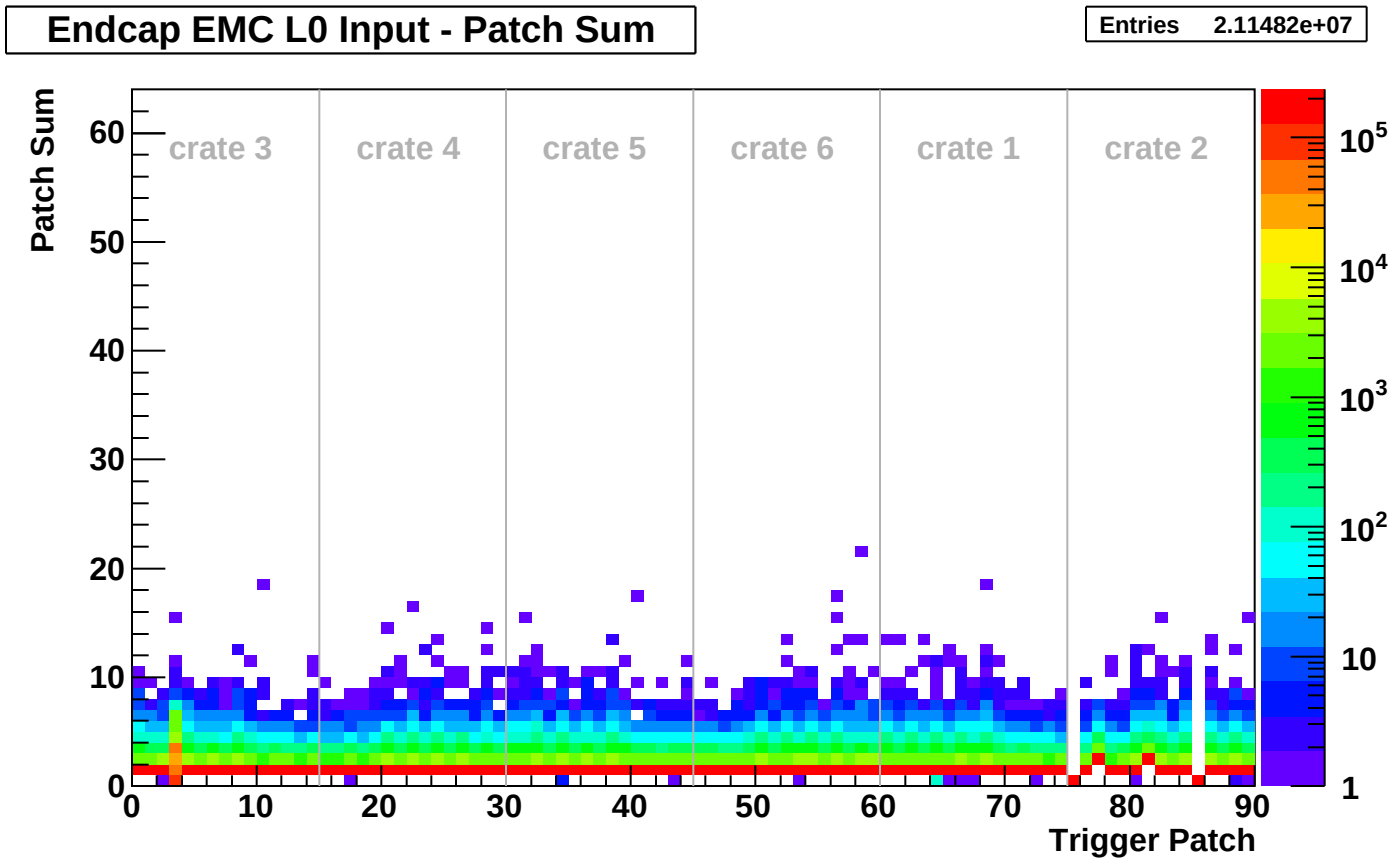

Abort-gaps
Entries 103

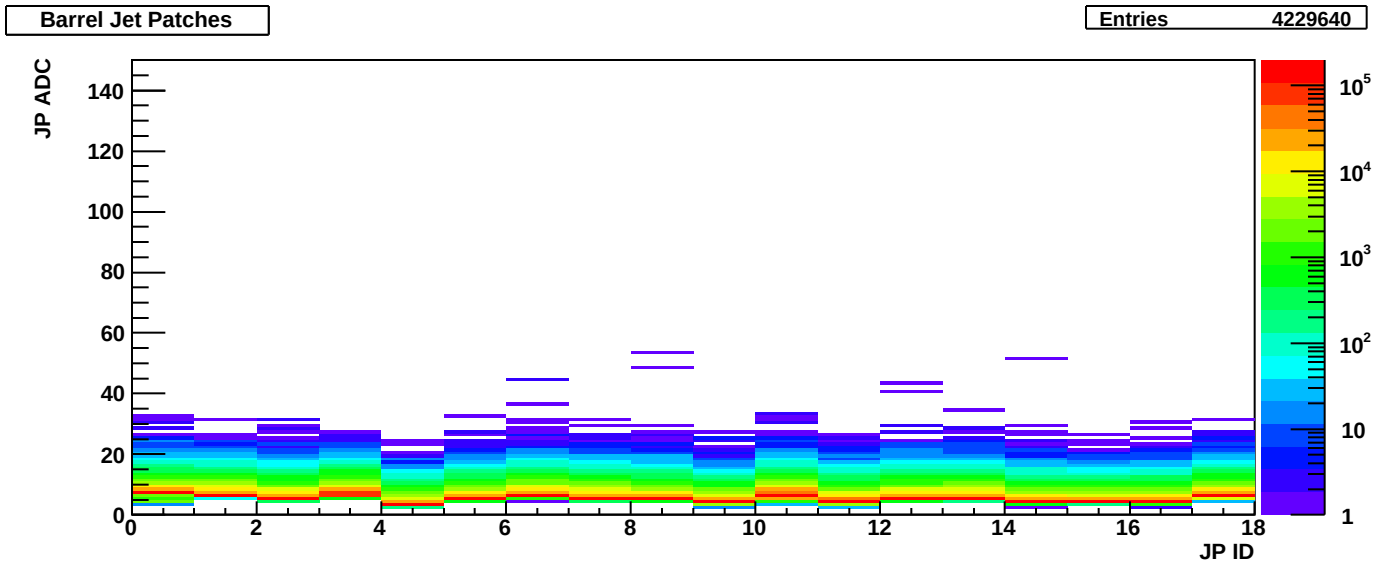

!Yellow-Fill
Entries 10522

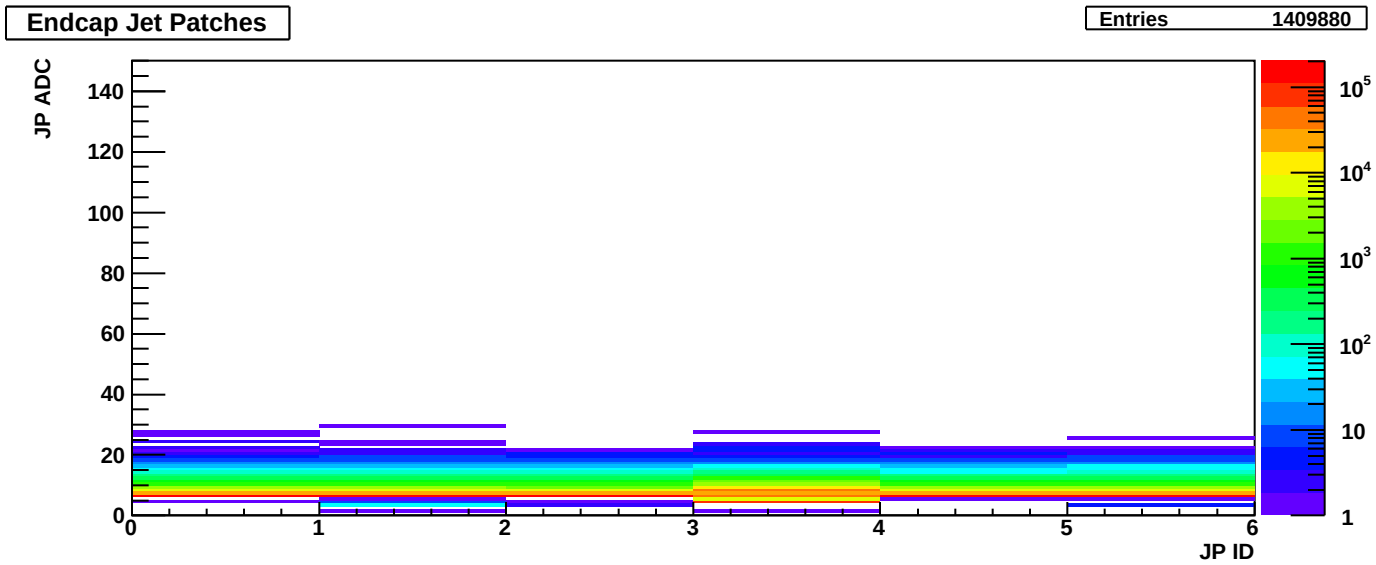

Barrel EMC L0 Input - High Tower
Entries 7.0494e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

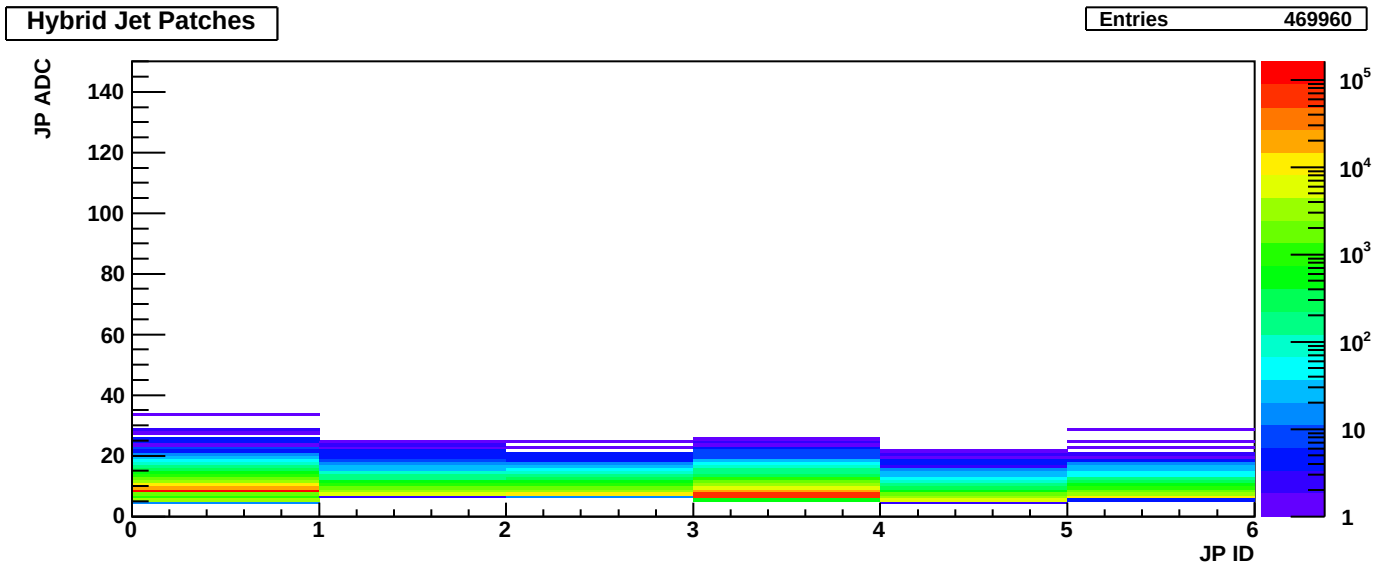

Barrel EMC L0 Input - Patch Sum
Entries 7.0494e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

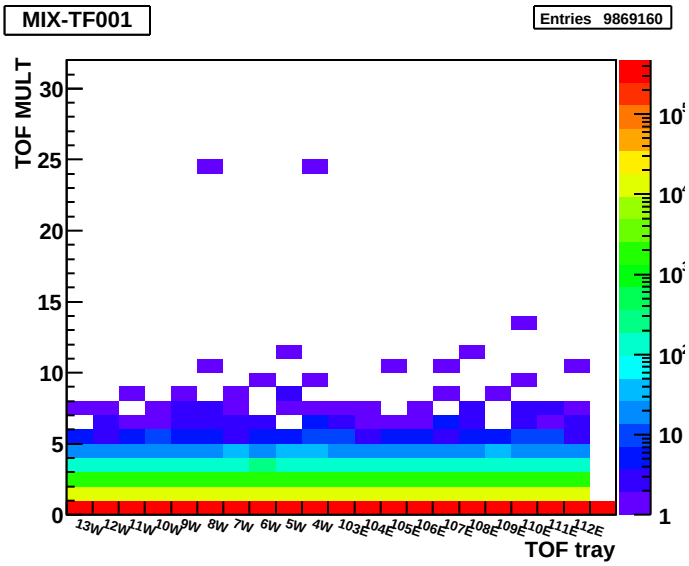

Endcap EMC L0 Input - High Tower
Entries 2.11482e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

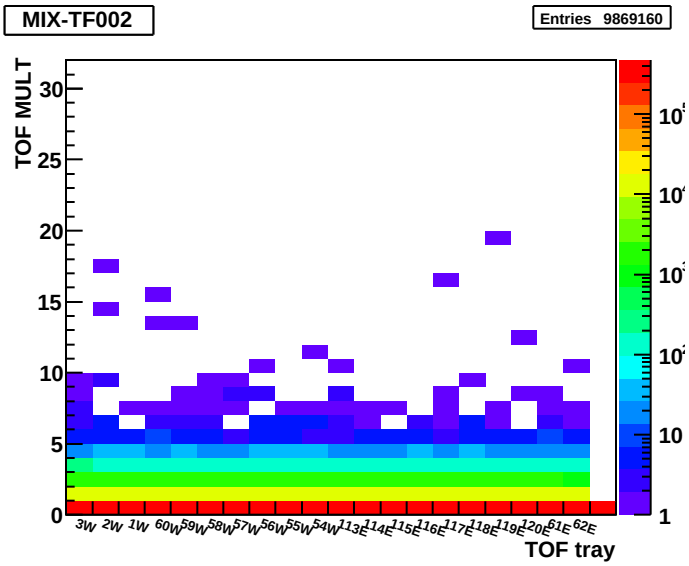

Endcap EMC L0 Input - Patch Sum
Entries 2.11482e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

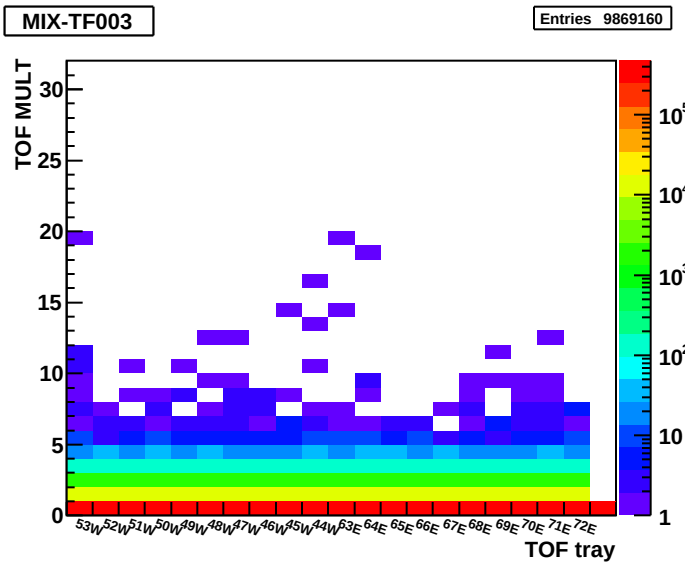

Barrel Jet Patches
Entries 4229640
JP ADC
JP ID

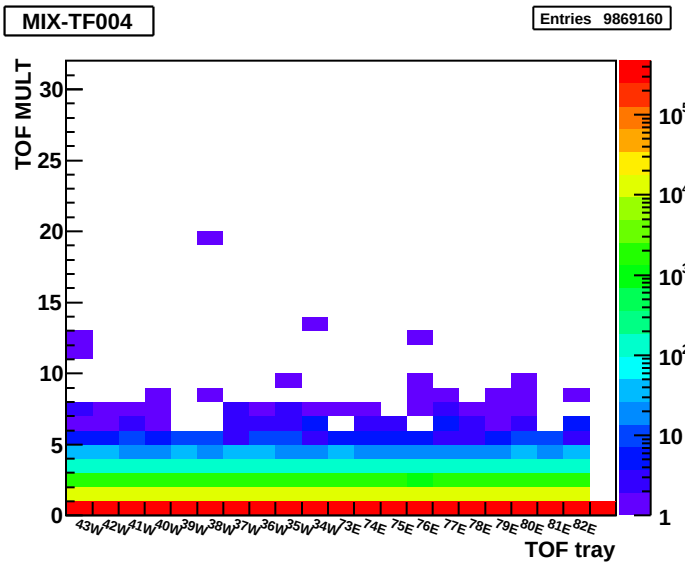

Endcap Jet Patches
Entries 1409880
JP ADC
JP ID

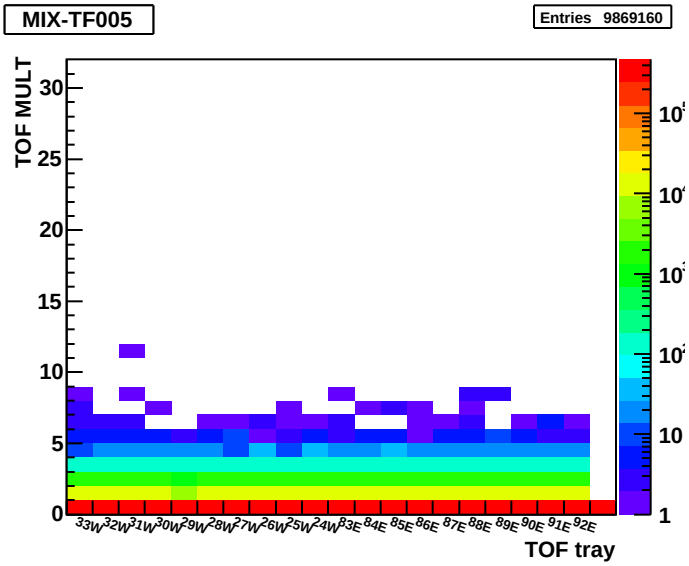

Hybrid Jet Patches
Entries 469960
JP ADC
JP ID

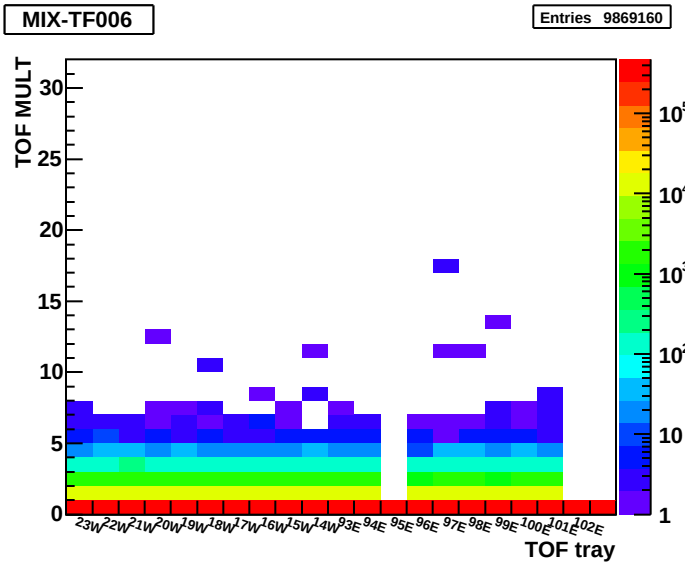

MIX-TF001
Entries 9869160
TOF MULT
TOF tray

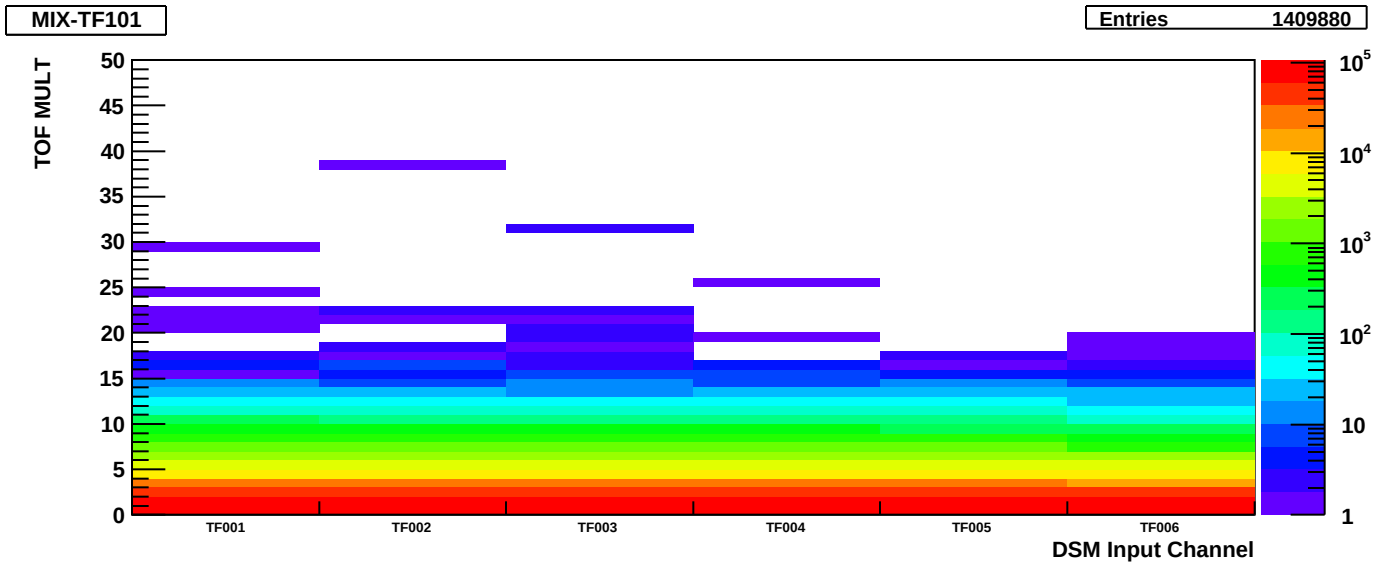

MIX-TF002
Entries 9869160
TOF MULT
TOF tray

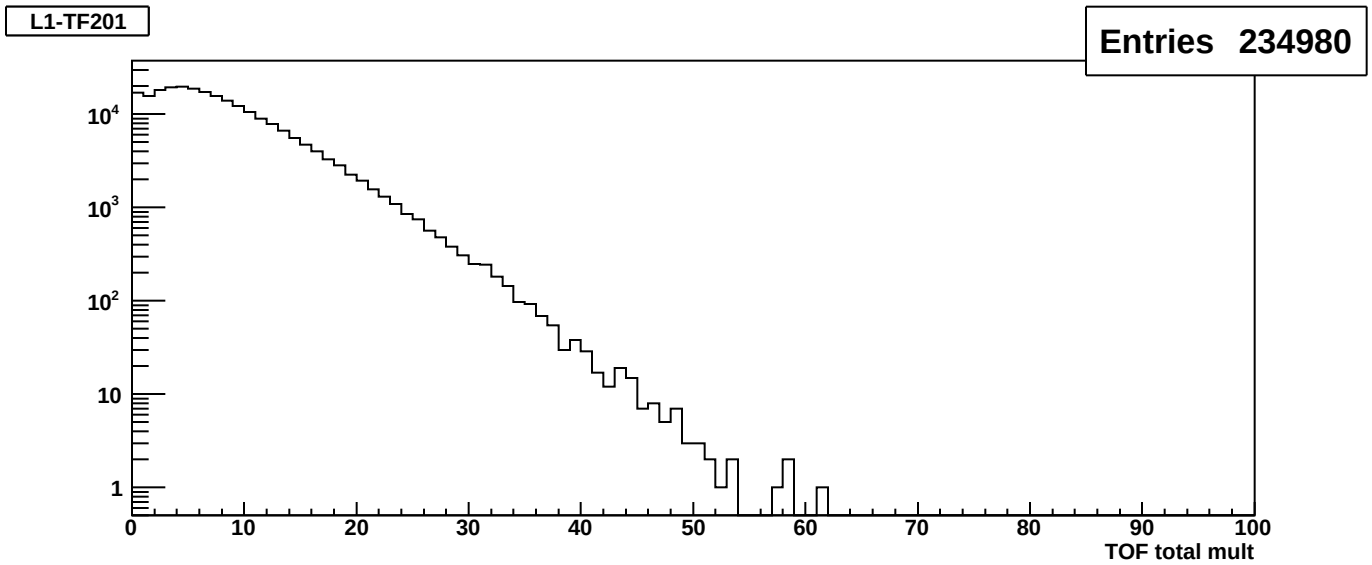

MIX-TF003
Entries 9869160
TOF MULT
TOF tray

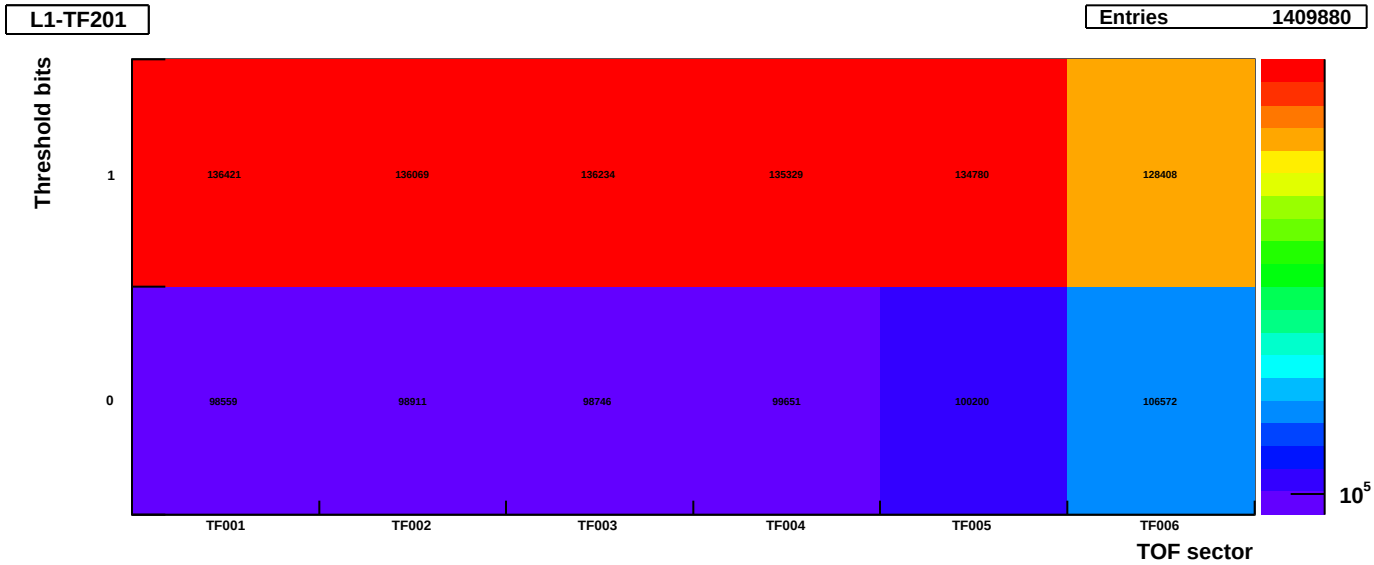

MIX-TF004
Entries 9869160
TOF MULT
TOF tray

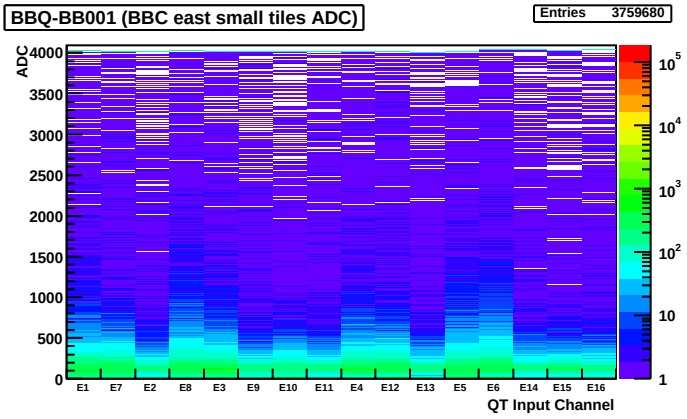

MIX-TF005
Entries 9869160
TOF MULT
TOF tray

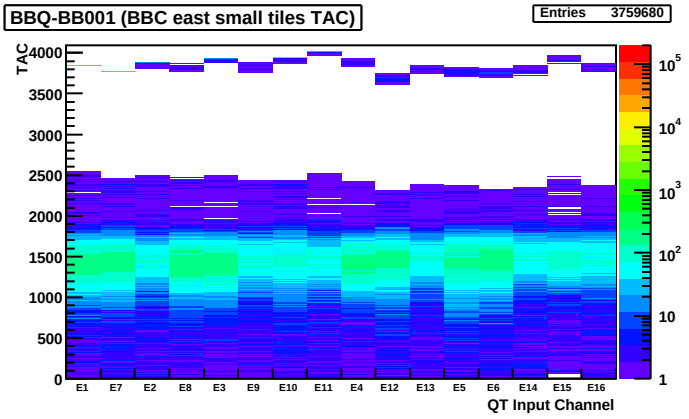

MIX-TF006
Entries 9869160
TOF MULT
TOF tray

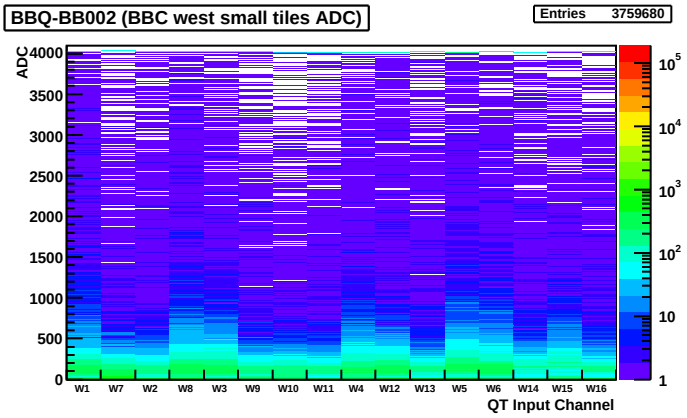

MIX-TF101
Entries 1409880
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

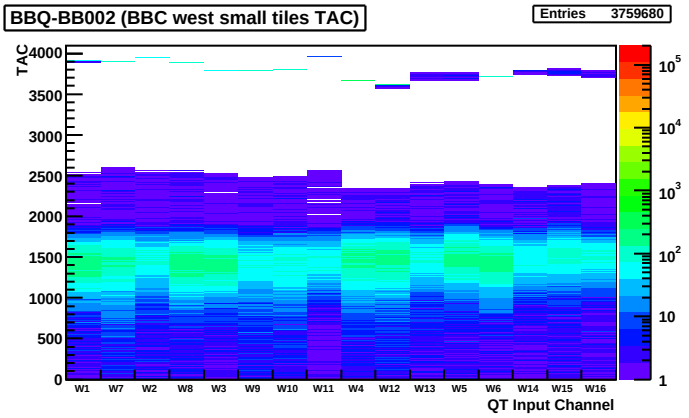

L1-TF201
Entries 234980
TOF total mult

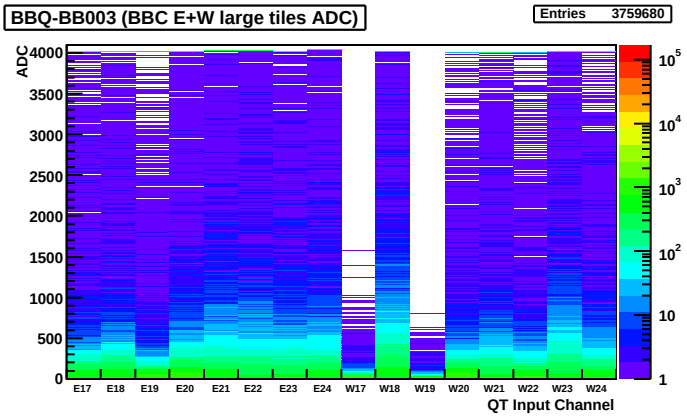

L1-TF201
Entries 1409880
Threshold bits
136421
136069
136234
135329
134780
128408
98559
98911
98746
99651
100200
106572
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

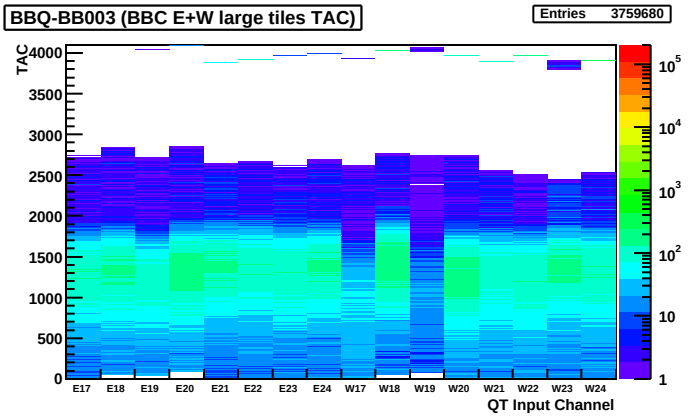

BBQ-BB001 (BBC east small tiles ADC)
Entries 3759680
ADC
QT Input Channel

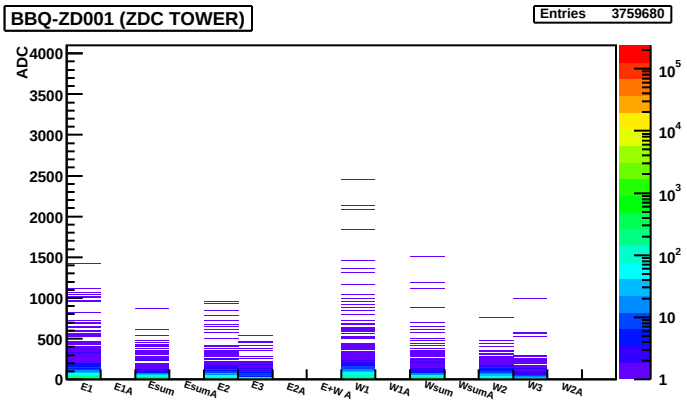

BBQ-BB001 (BBC east small tiles TAC)
Entries 3759680
TAC
QT Input Channel

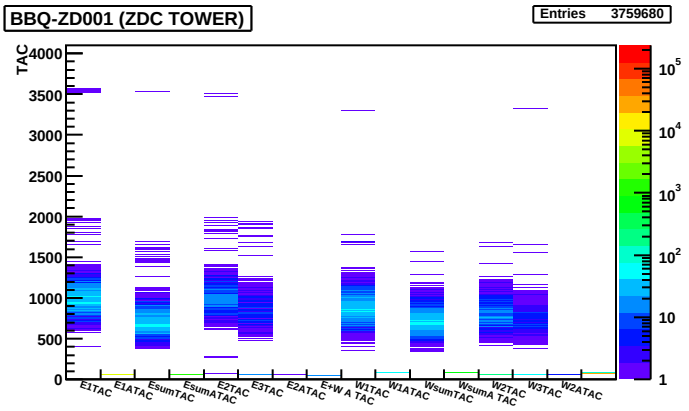

BBQ-BB002 (BBC west small tiles ADC)
Entries 3759680
ADC
QT Input Channel

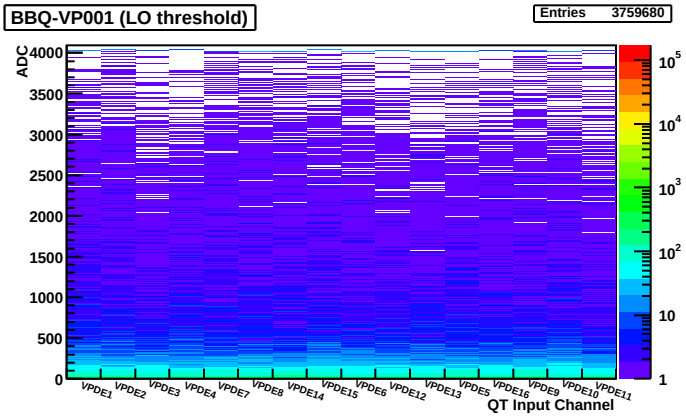

BBQ-BB002 (BBC west small tiles TAC)
Entries 3759680
TAC
QT Input Channel

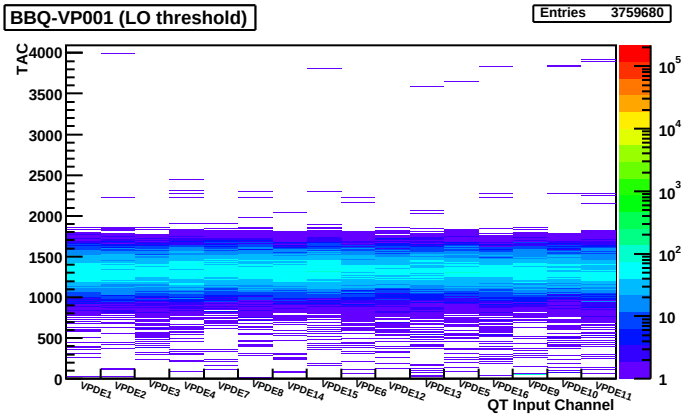

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3759680
ADC
QT Input Channel

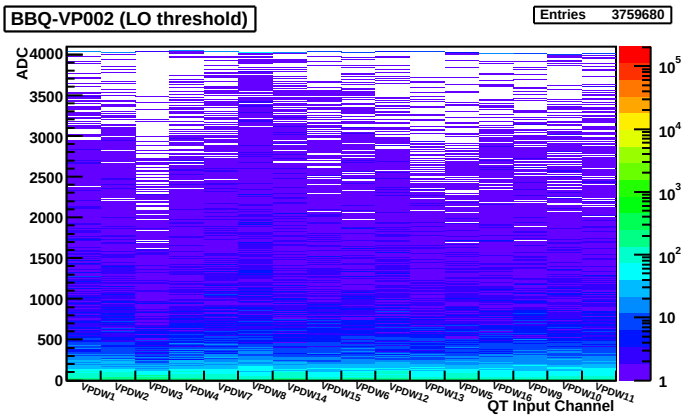

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3759680
TAC
QT Input Channel

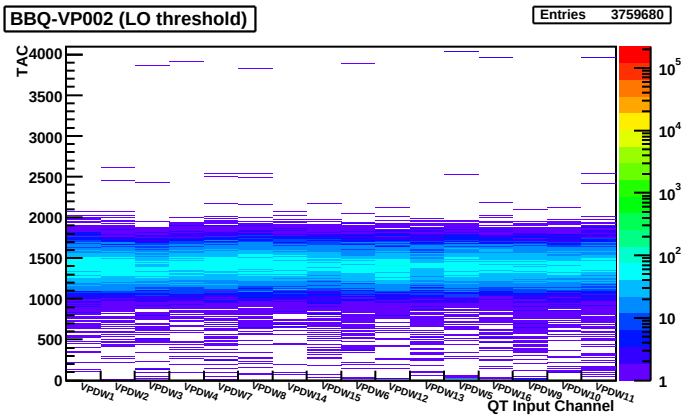

BBQ-ZD001 (ZDC TOWER)
Entries 3759680
ADC

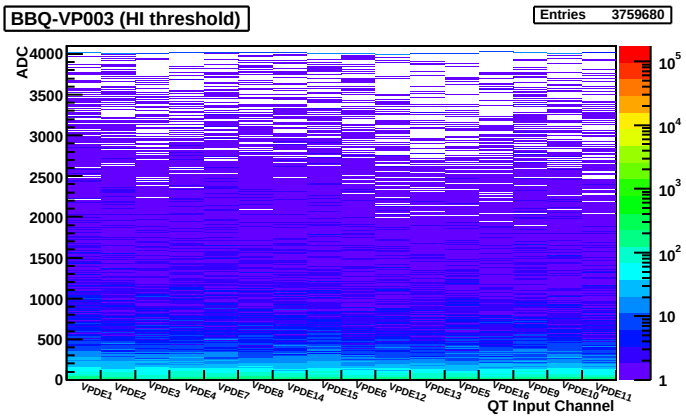

BBQ-ZD001 (ZDC TOWER)
Entries 3759680
TAC

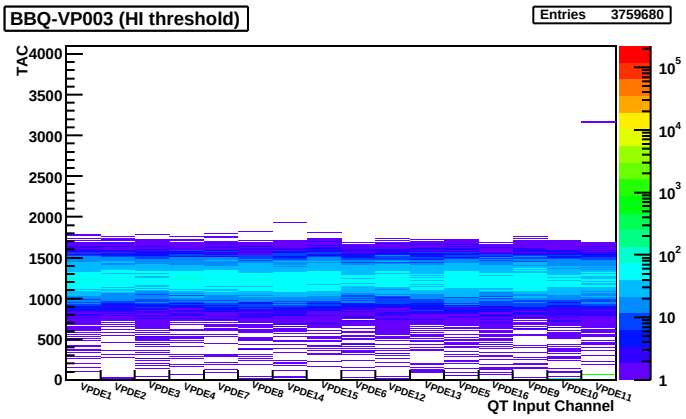

BBQ-VP001 (LO threshold)
Entries 3759680
ADC
QT Input Channel

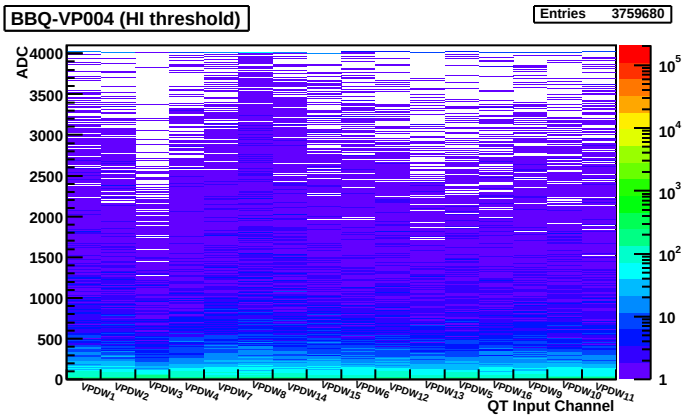

BBQ-VP001 (LO threshold)
Entries 3759680
TAC
QT Input Channel

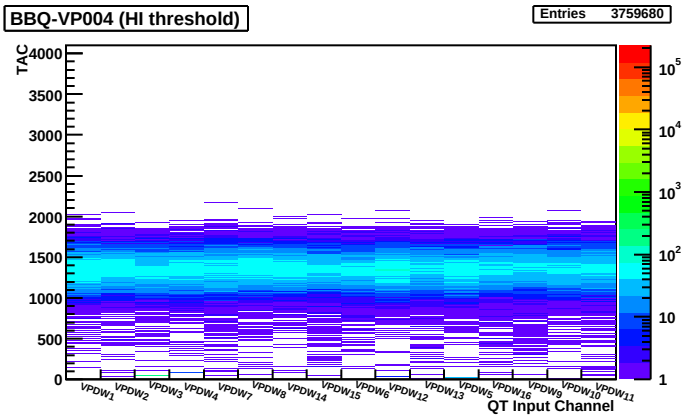

BBQ-VP002 (LO threshold)
Entries 3759680
ADC
QT Input Channel

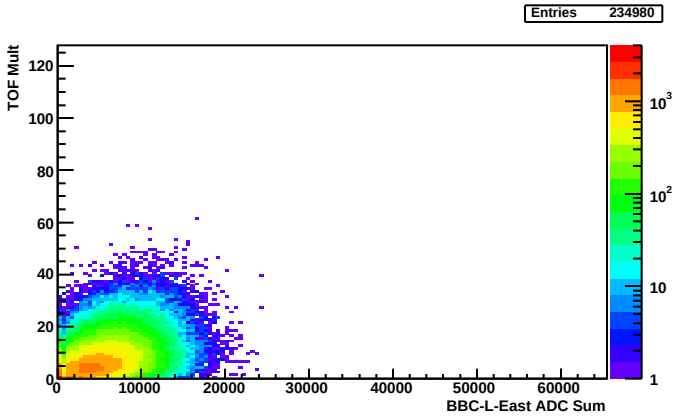

BBQ-VP002 (LO threshold)
Entries 3759680
TAC
QT Input Channel

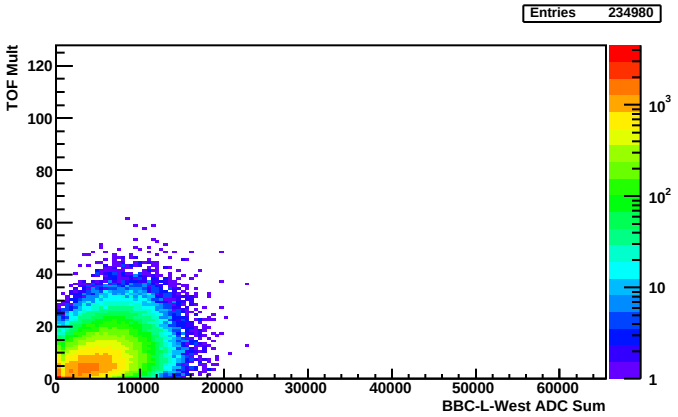

BBQ-VP003 (HI threshold)
Entries 3759680
ADC
QT Input Channel

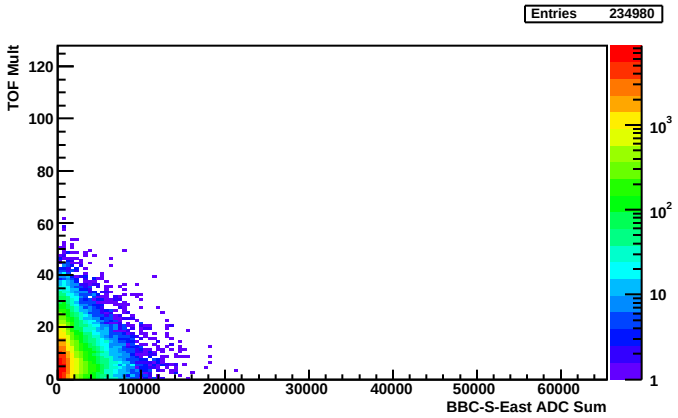

BBQ-VP003 (HI threshold)
Entries 3759680
TAC
QT Input Channel

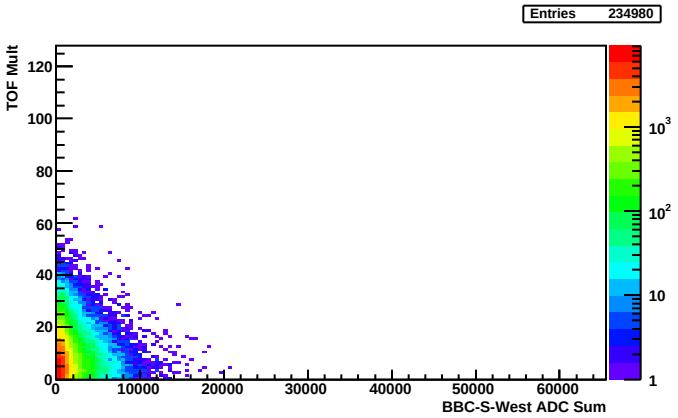

BBQ-VP004 (HI threshold)
Entries 3759680
ADC
QT Input Channel

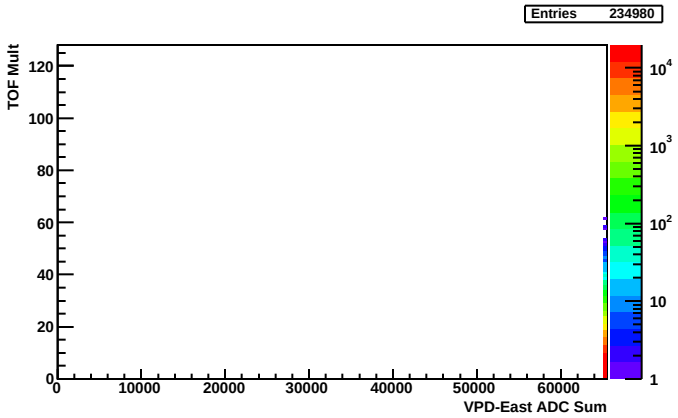

BBQ-VP004 (HI threshold)
Entries 3759680
TAC
QT Input Channel

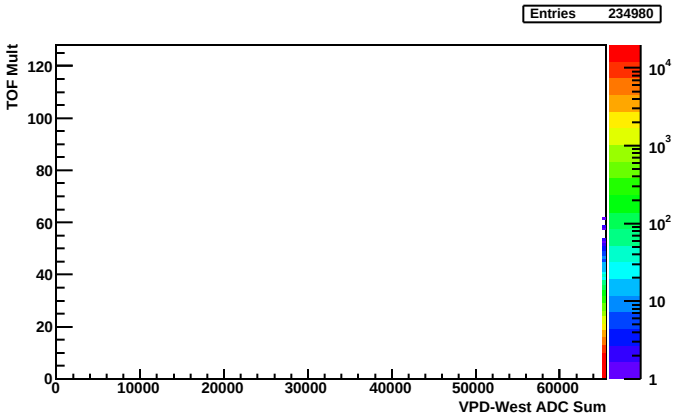

Entries 234980
TOF Mult
BBC-L-East ADC Sum

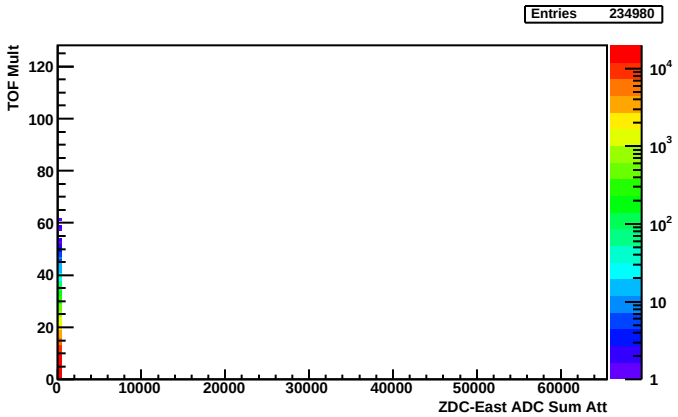

Entries 234980
TOF Mult
BBC-L-West ADC Sum

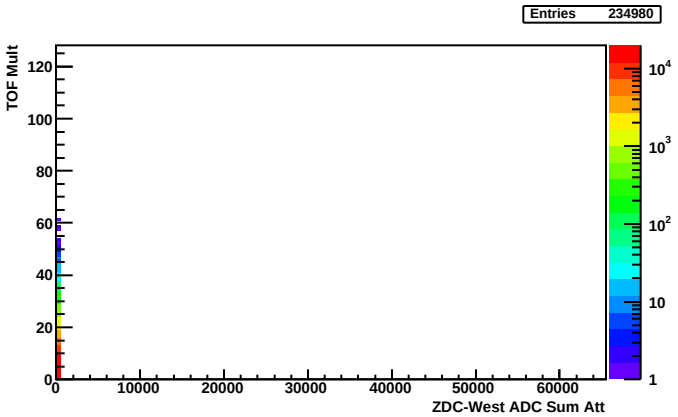

Entries 234980
TOF Mult
BBC-S-East ADC Sum

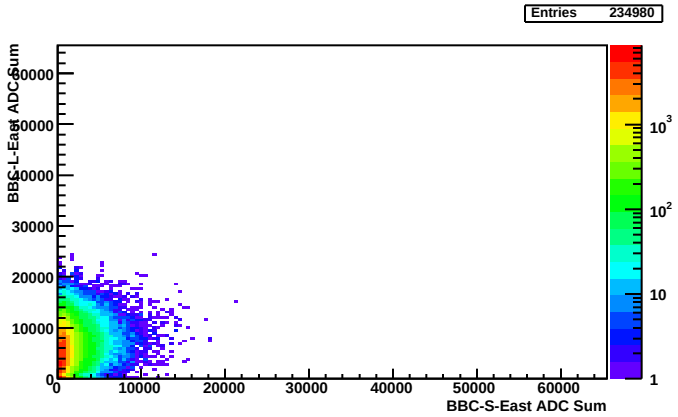

Entries 234980
TOF Mult
BBC-S-West ADC Sum

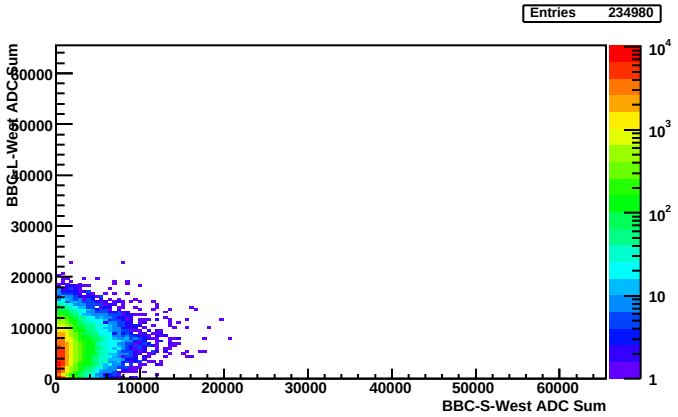

Entries 234980
TOF Mult
VPD-East ADC Sum

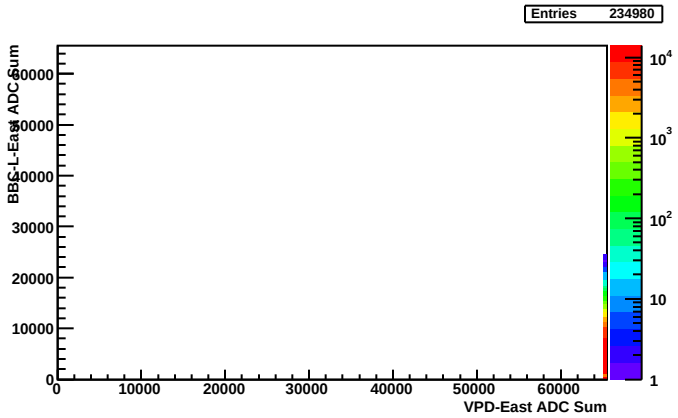

Entries 234980
TOF Mult
VPD-West ADC Sum

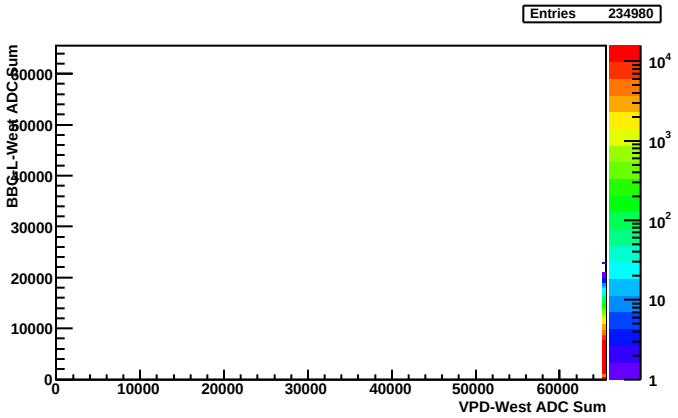

Entries 234980
TOF Mult
ZDC-East ADC Sum Att

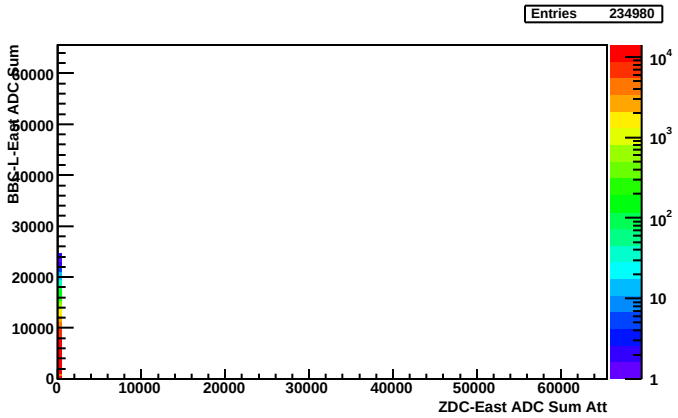

Entries 234980
TOF Mult
ZDC-West ADC Sum Att

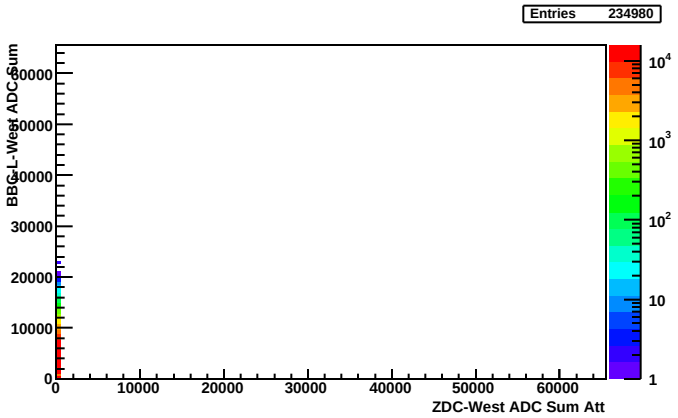

Entries 234980
BBC-L-East ADC Sum
BBC-S-East ADC Sum

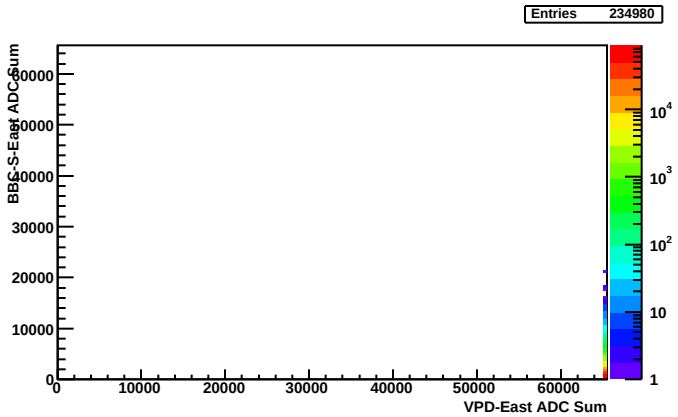

Entries 234980
BBC-L-West ADC Sum
BBC-S-West ADC Sum

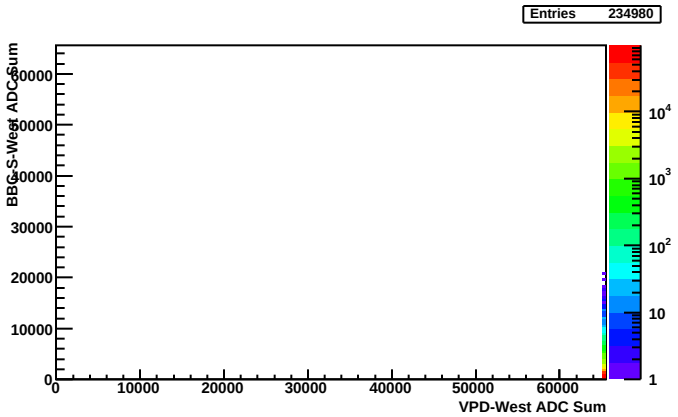

Entries 234980
BBC-L-East ADC Sum
VPD-East ADC Sum

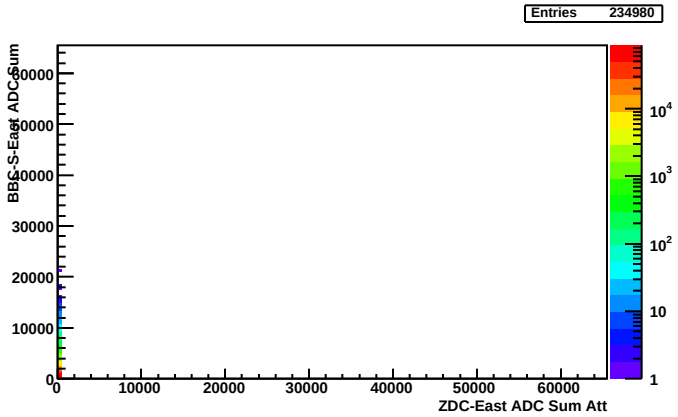

Entries 234980
BBC-L-West ADC Sum
VPD-West ADC Sum

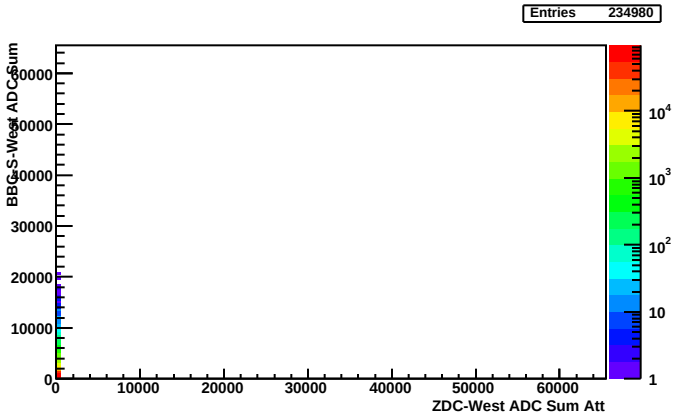

Entries 234980
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

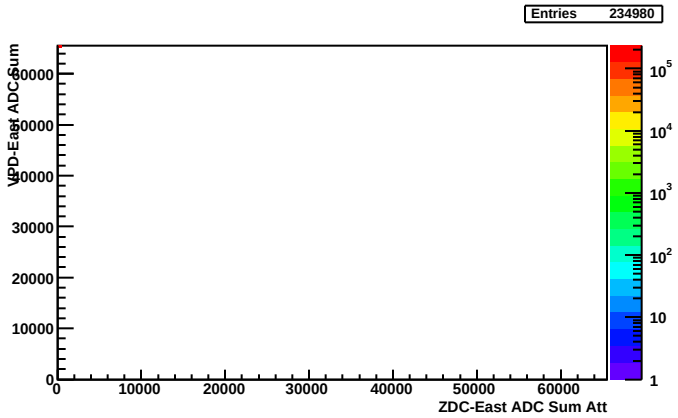

Entries 234980
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

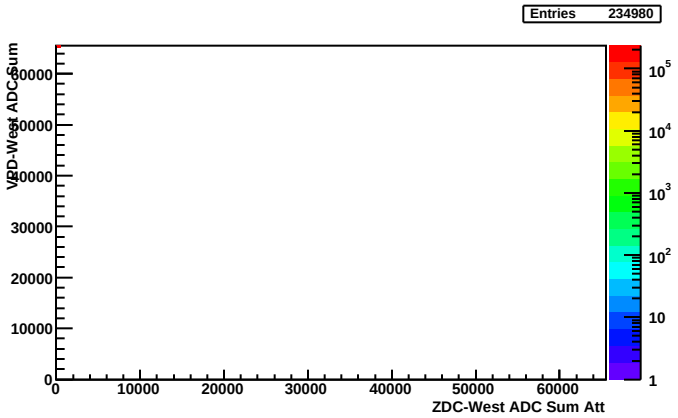

Entries 234980
BBC-S-East ADC Sum
VPD-East ADC Sum

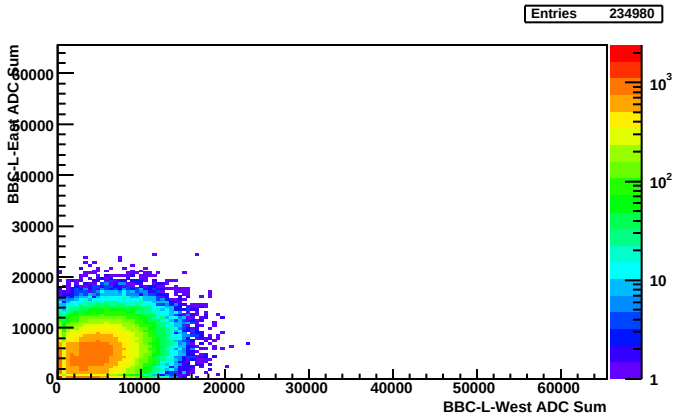

Entries 234980
BBC-S-West ADC Sum
VPD-West ADC Sum

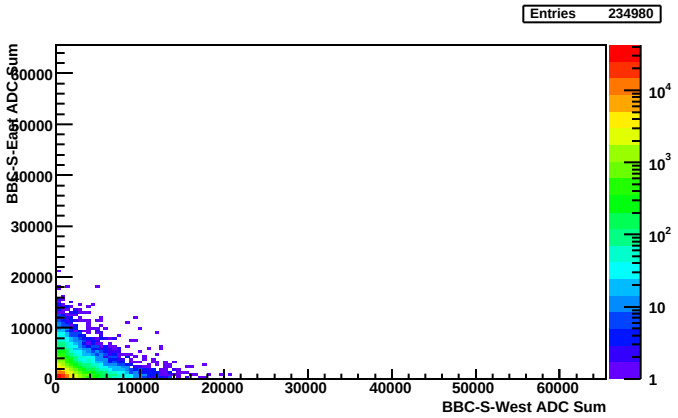

Entries 234980
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

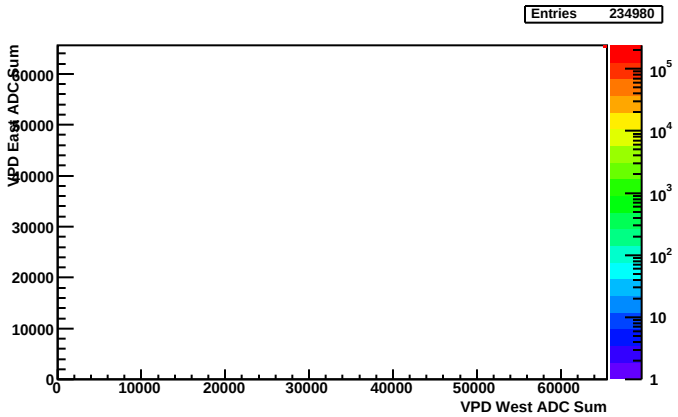

Entries 234980
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

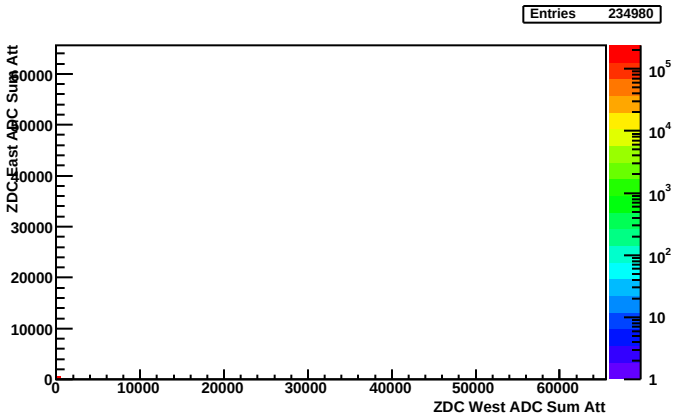

Entries 234980
VPD-East ADC Sum
ZDC-East ADC Sum Att

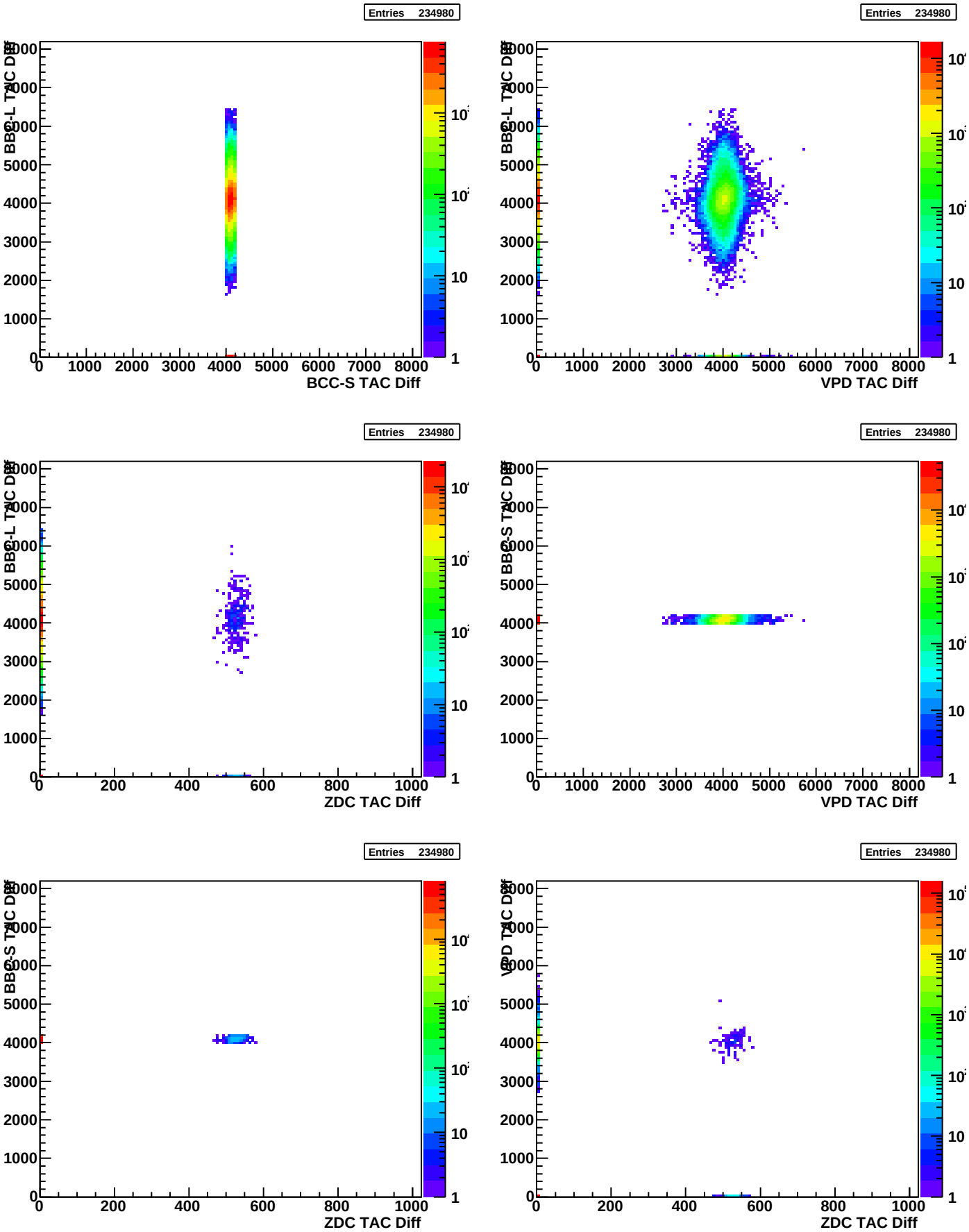

Entries 234980
VPD-West ADC Sum
ZDC-West ADC Sum Att

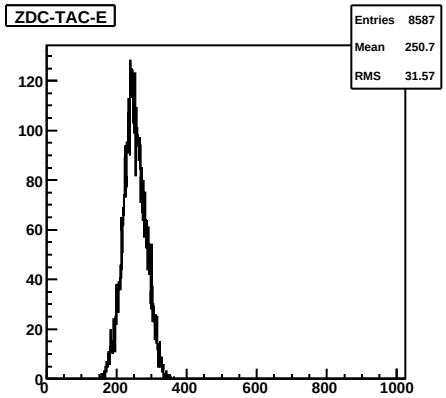

Entries 234980
BBC-L-East ADC Sum
BBC-L-West ADC Sum

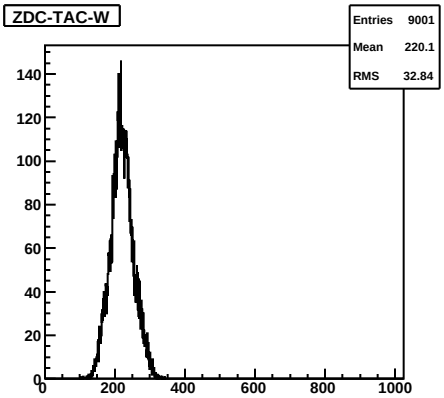

Entries 234980
BBC-S-East ADC Sum
BBC-S-West ADC Sum

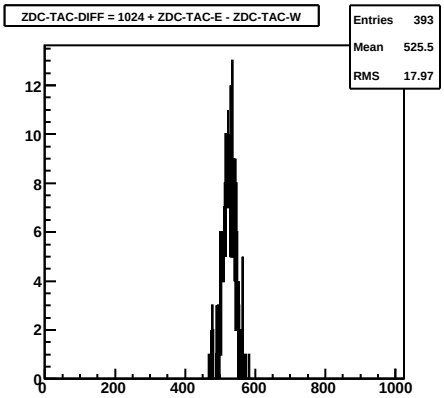

Entries 234980
VPD East ADC Sum
VPD West ADC Sum

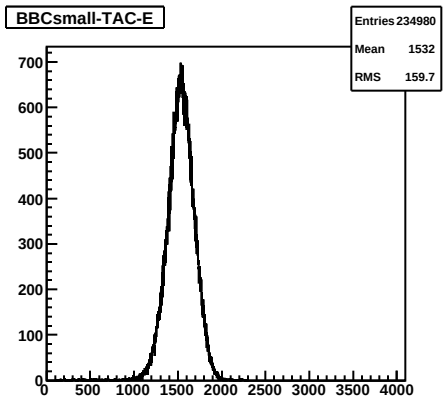

Entries 234980
ZDC East ADC Sum Att
ZDC West ADC Sum Att

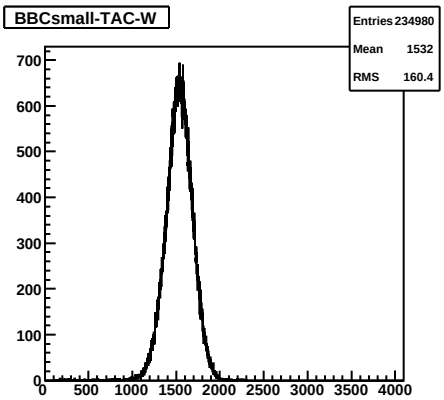

Entries 234980
BBC-L TAC Diff
BCC-S TAC Diff
Entries 234980
BBC-L TAC Diff
VPD TAC Diff
Entries 234980
BBC-L TAC Diff
ZDC TAC Diff
Entries 234980
BBC-S TAC Diff
VPD TAC Diff
Entries 234980
BBC-S TAC Diff
ZDC TAC Diff
Entries 234980
VPD TAC Diff
ZDC TAC Diff

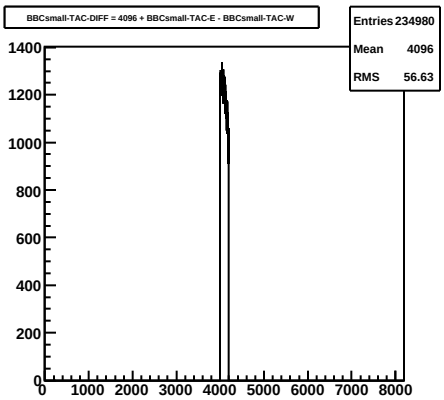

ZDC-TAC-E
Entries 8587
Mean 250.7
RMS 31.57

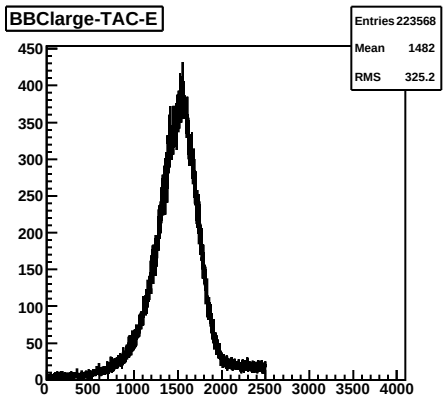

ZDC-TAC-W
Entries 9001
Mean 220.1
RMS 32.84

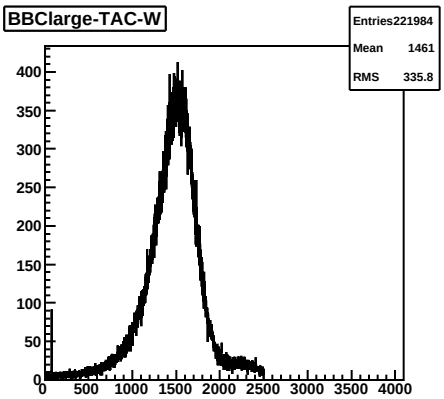

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 393
Mean 525.5
RMS 17.97

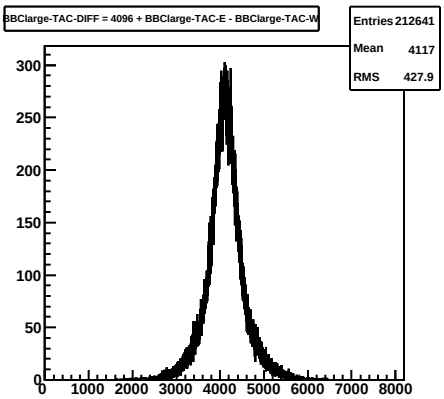

BBCsmall-TAC-E
Entries 234980
Mean 1532
RMS 159.7

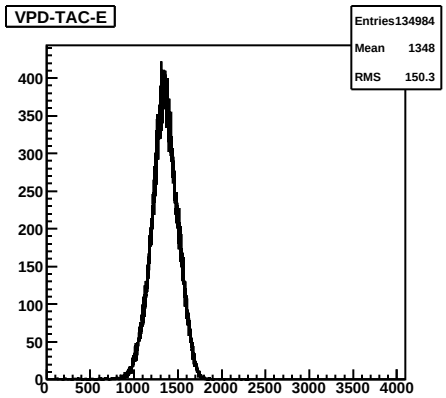

BBCsmall-TAC-W
Entries 234980
Mean 1532
RMS 160.4

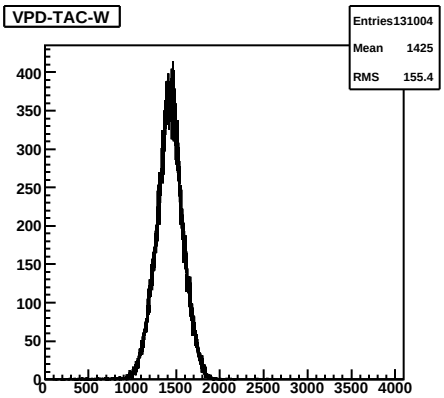

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 234980
Mean 4096
RMS 56.63

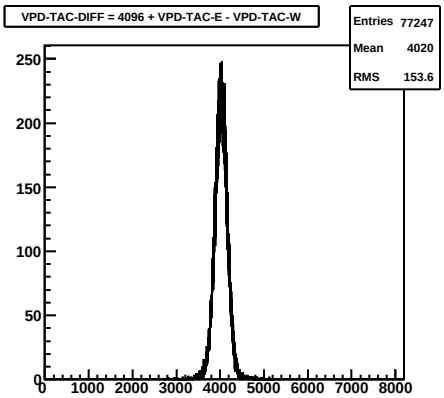

BBClarge-TAC-E
Entries 223568
Mean 1482
RMS 325.2

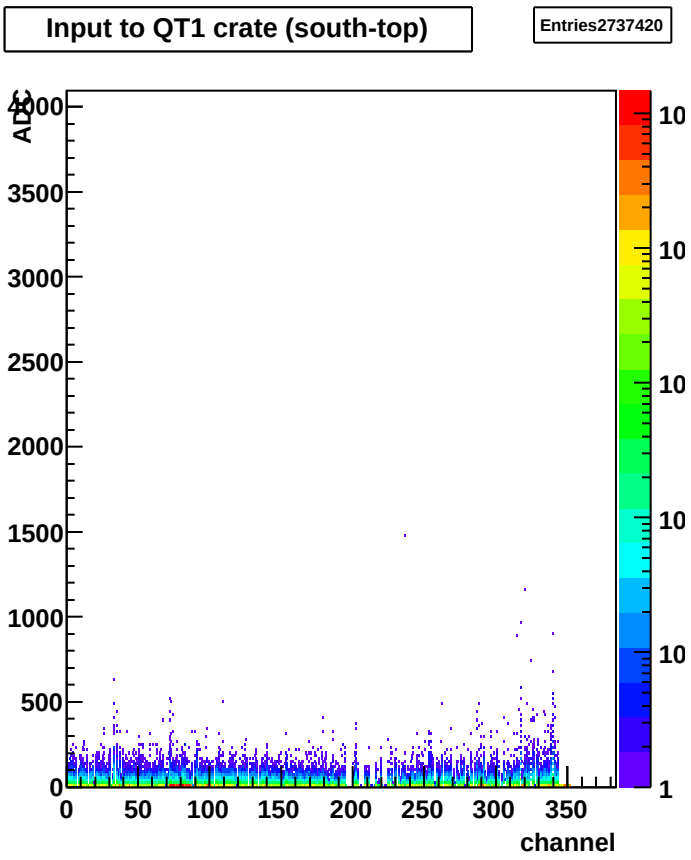

BBClarge-TAC-W
Entries 221984
Mean 1461
RMS 335.8

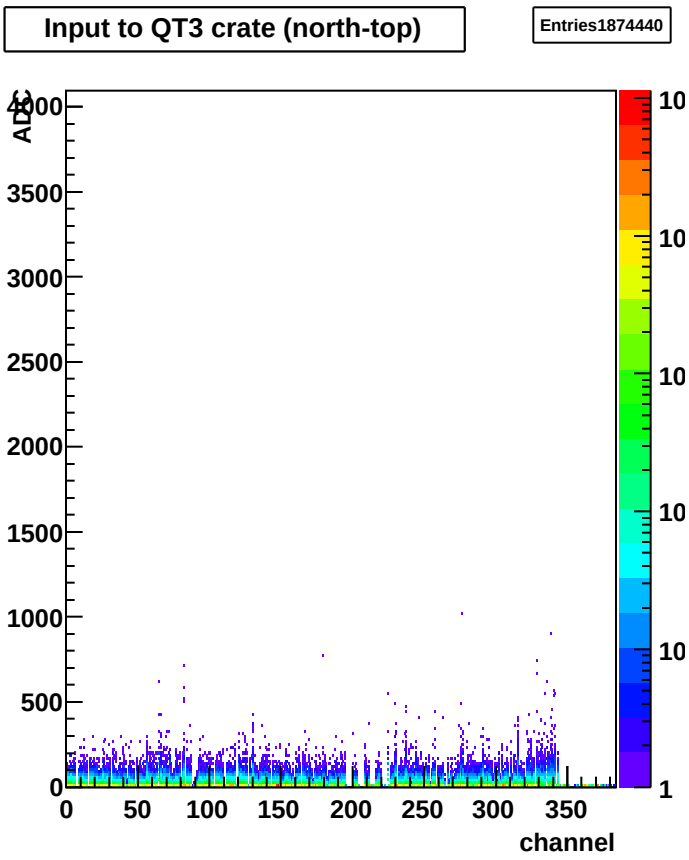

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 212641
Mean 4117
RMS 427.9

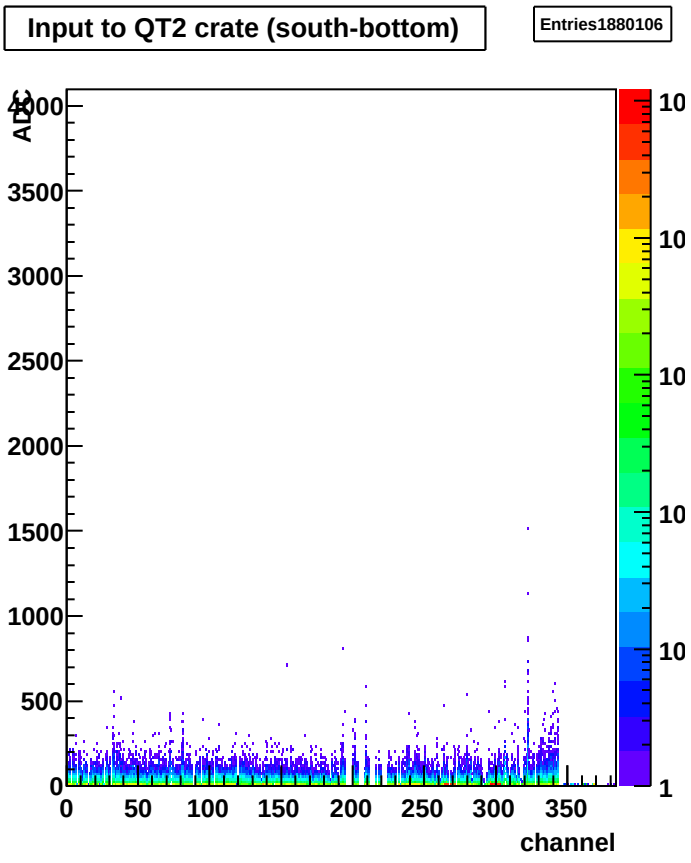

VPD-TAC-E
Entries 134984
Mean 1348
RMS 150.3

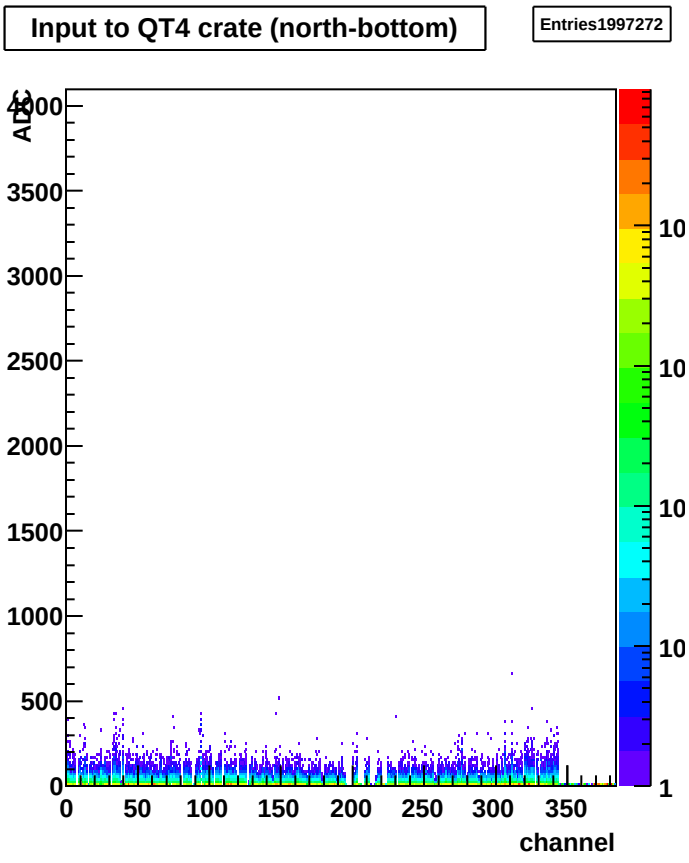

VPD-TAC-W
Entries 131004
Mean 1425
RMS 155.4

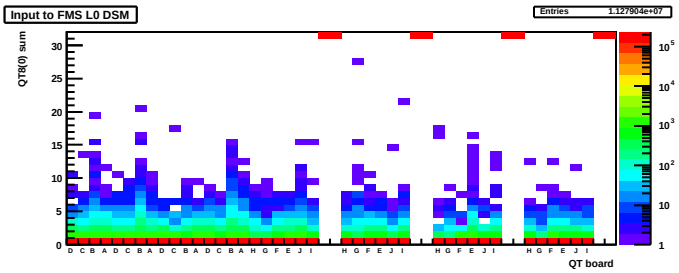

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 77247
Mean 4020
RMS 153.6

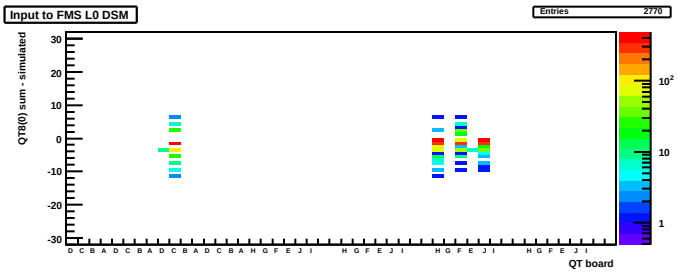

Input to QT1 crate (south-top)
Entries2737420
ADC
channel

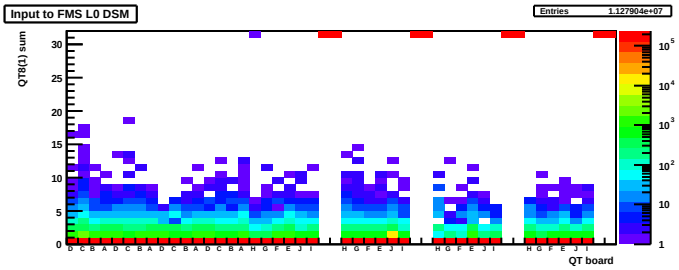

Input to QT3 crate (north-top)
Entries1874440
ADC
channel

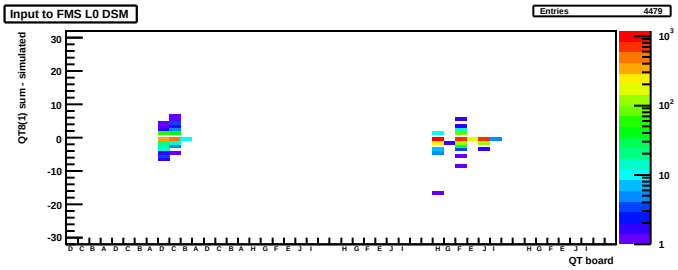

Input to QT2 crate (south-bottom)
Entries1880106
ADC
channel

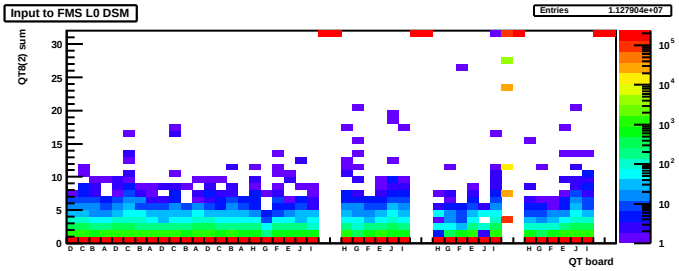

Input to QT4 crate (north-bottom)
Entries1997272
ADC
channel

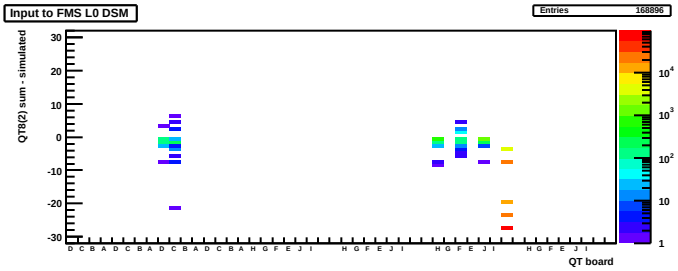

Input to FMS L0 DSM
Entries 1.127904e+07
QT8(0) sum
QT board

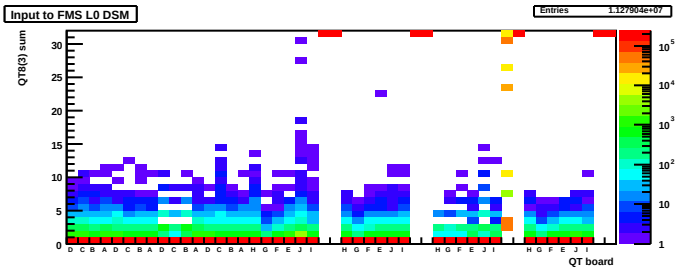

Input to FMS L0 DSM
Entries 2770
QT8(0) sum - simulated
QT board

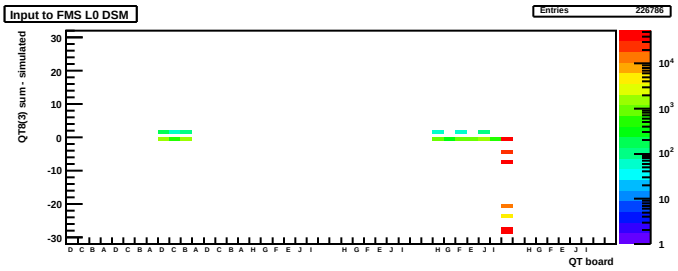

Input to FMS L0 DSM
Entries 1.127904e+07
QT8(1) sum
QT board

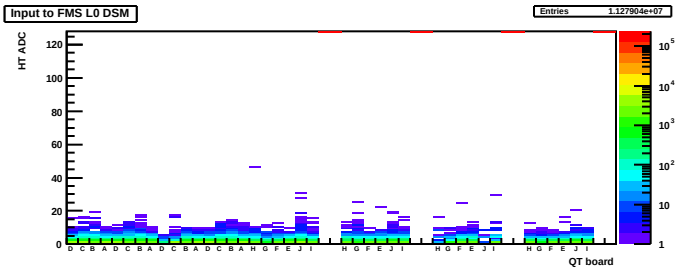

Input to FMS L0 DSM
Entries 4479
QT8(1) sum - simulated
QT board

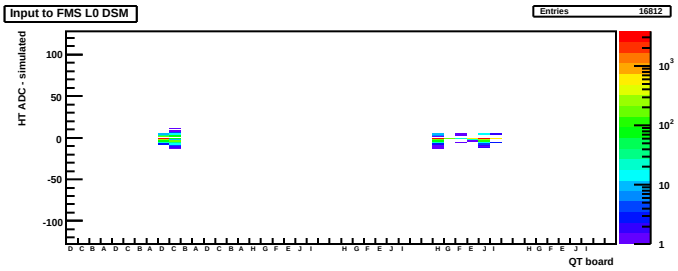

Input to FMS L0 DSM
Entries 1.127904e+07
QT8(2) sum
QT board

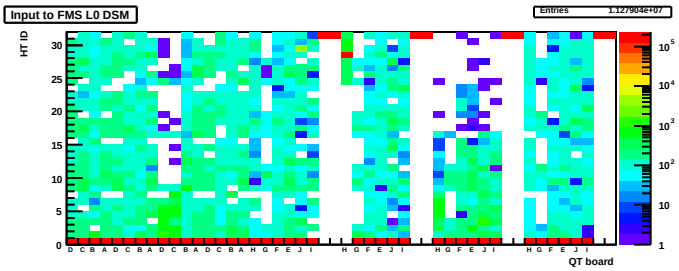

Input to FMS L0 DSM
Entries 168896
QT8(2) sum - simulated
QT board

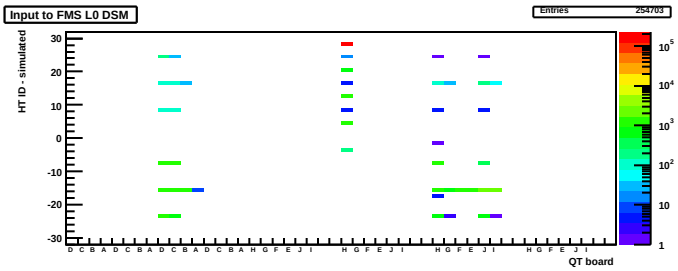

Input to FMS L0 DSM
Entries 1.127904e+07
QT8(3) sum
QT board

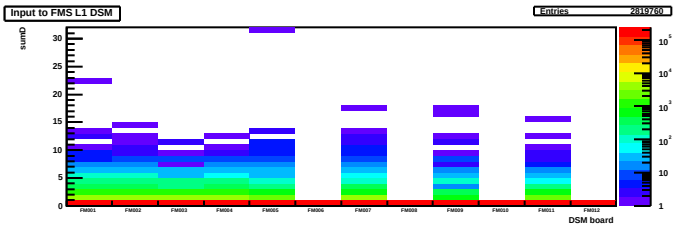

Input to FMS L0 DSM
Entries 226786
QT8(3) sum - simulated
QT board

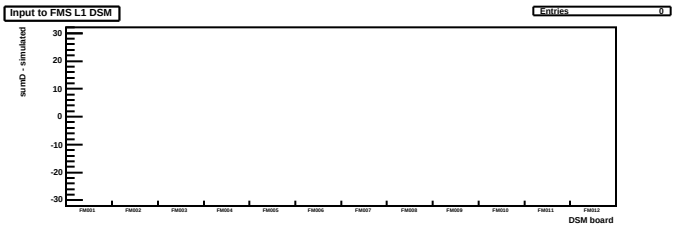

Input to FMS L0 DSM
Entries 1.127904e+07
HT ADC
QT board

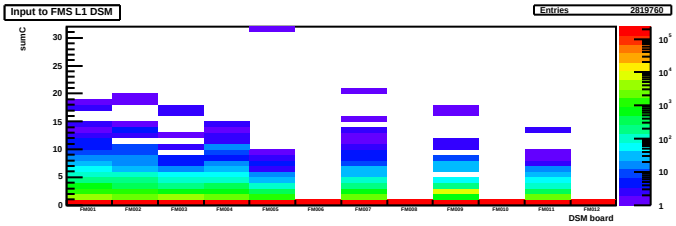

Input to FMS L0 DSM
Entries 16812
HT ADC - simulated
QT board

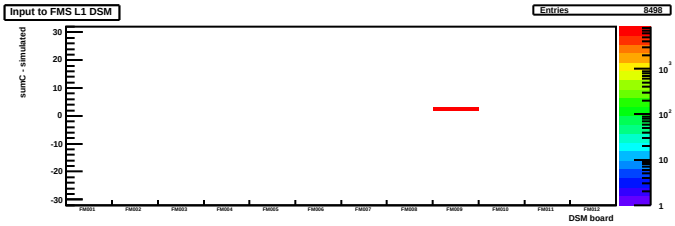

Input to FMS L0 DSM
Entries 1.127904e+07
HT ID
QT board

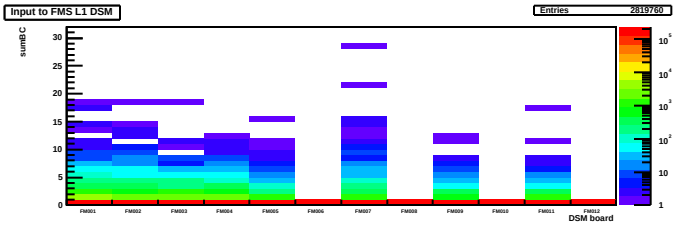

Input to FMS L0 DSM
Entries 254703
HT ID - simulated
QT board

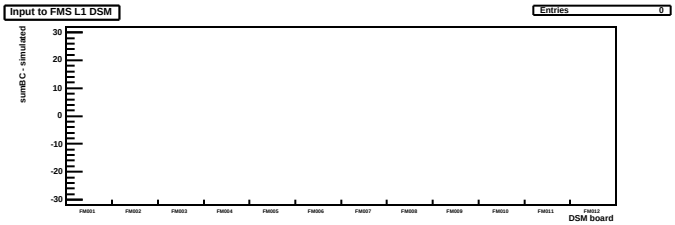

Input to FMS L1 DSM
Entries 2819760
sumD
DSM board

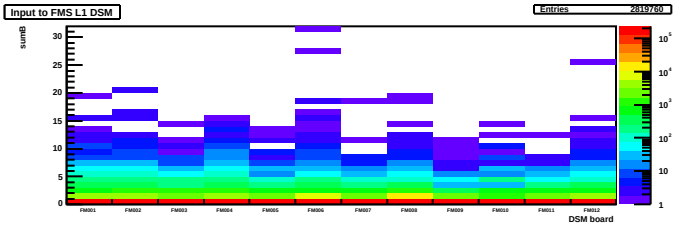

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

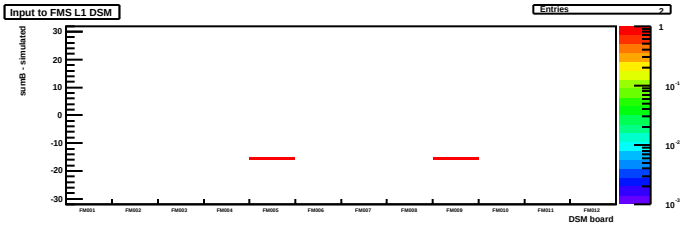

Input to FMS L1 DSM
Entries 2819760
sumC
DSM board

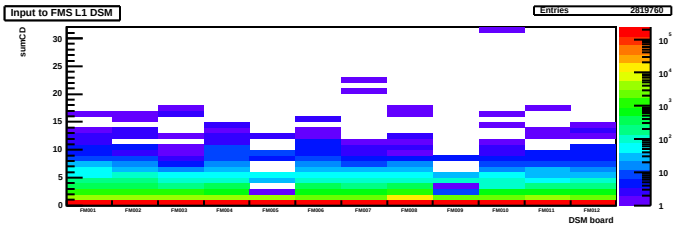

Input to FMS L1 DSM
Entries 8498
sumC - simulated
DSM board

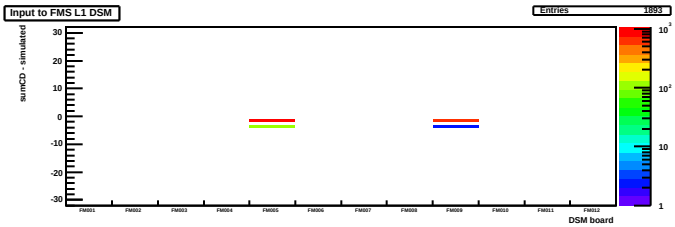

Input to FMS L1 DSM
Entries 2819760
sumBC
DSM board

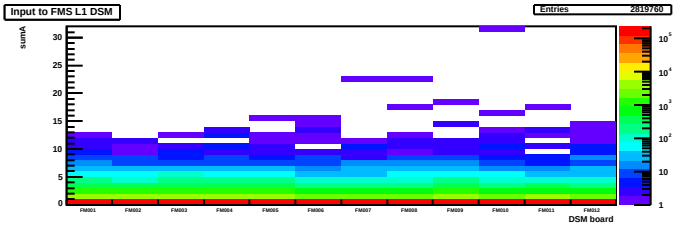

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

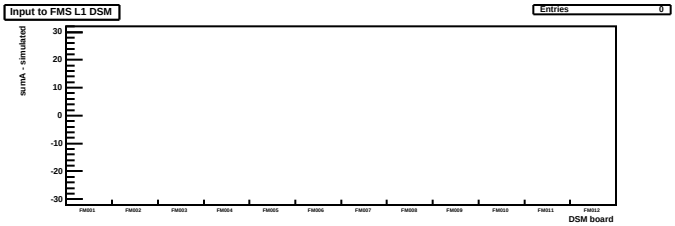

Input to FMS L1 DSM
Entries 2819760
sumB
DSM board

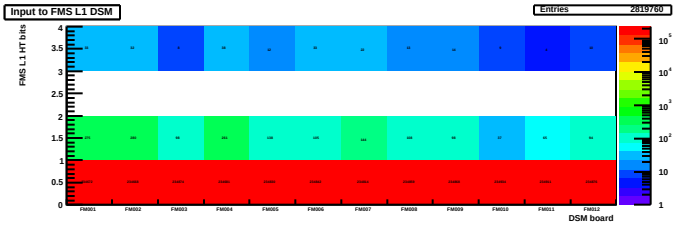

Input to FMS L1 DSM
Entries 2
sumB - simulated
DSM board

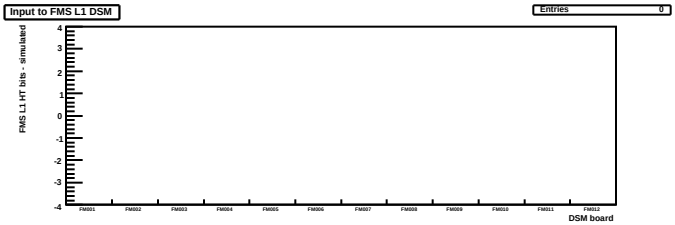

Input to FMS L1 DSM
Entries 2819760
sumCD
DSM board

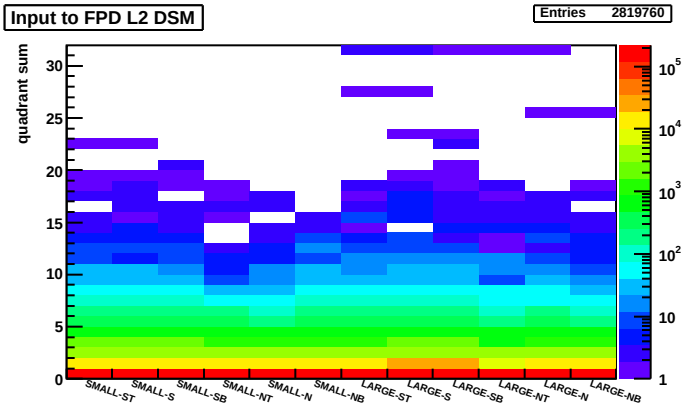

Input to FMS L1 DSM
Entries 1893
sumCD - simulated
DSM board

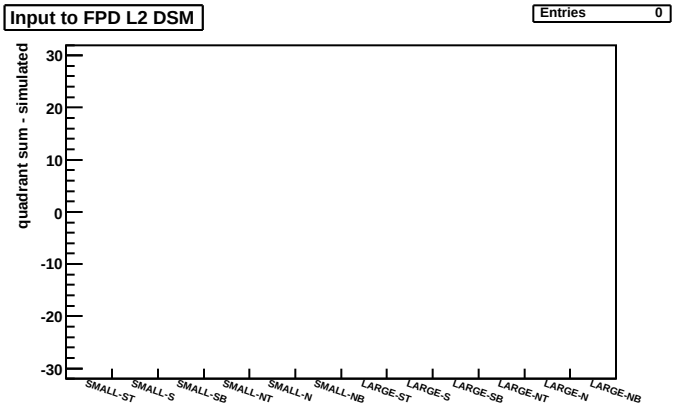

Input to FMS L1 DSM
Entries 2819760
sumA
DSM board

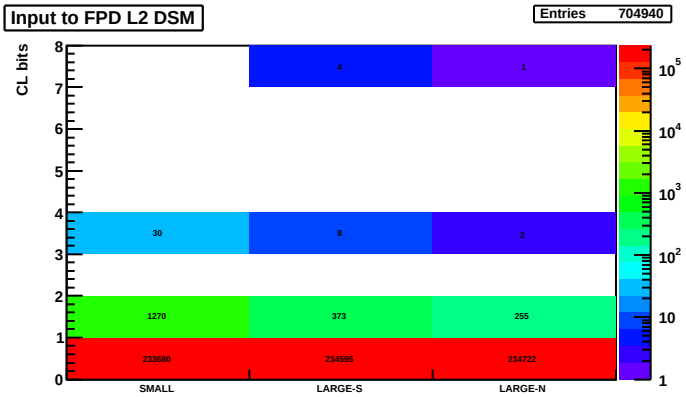

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

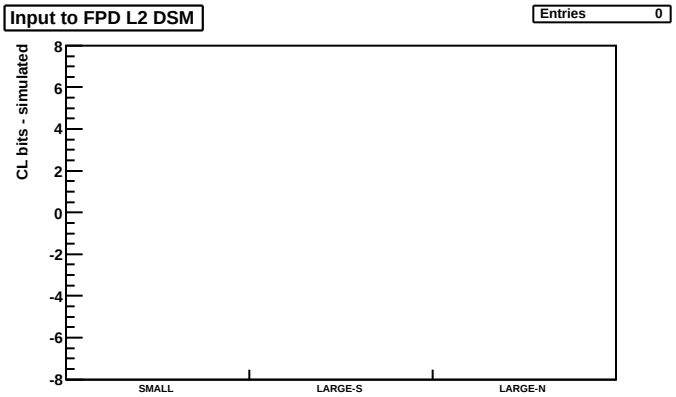

Input to FMS L1 DSM
Entries 2819760
FMS L1 HT bits
DSM board

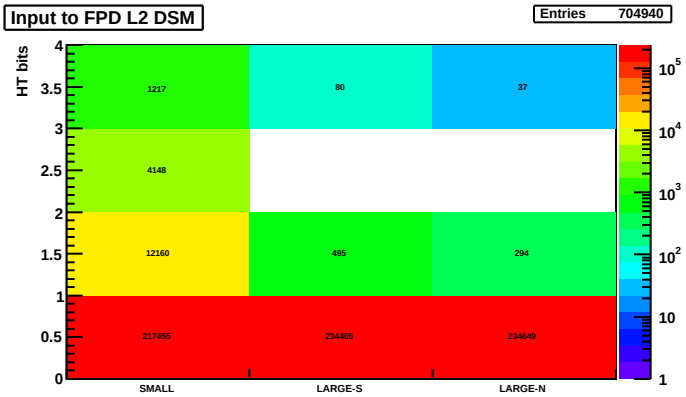

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

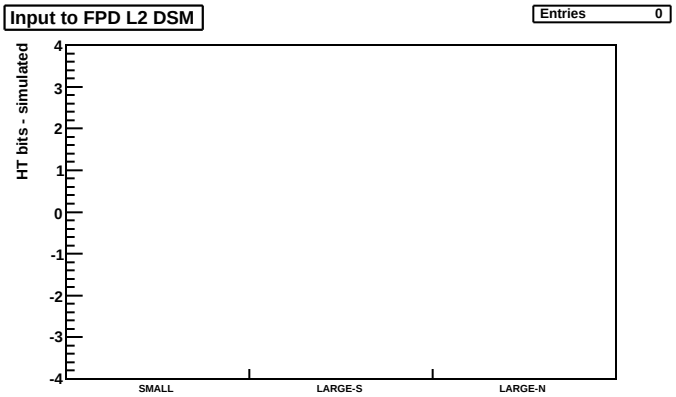

Input to FPD L2 DSM
Entries 2819760
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

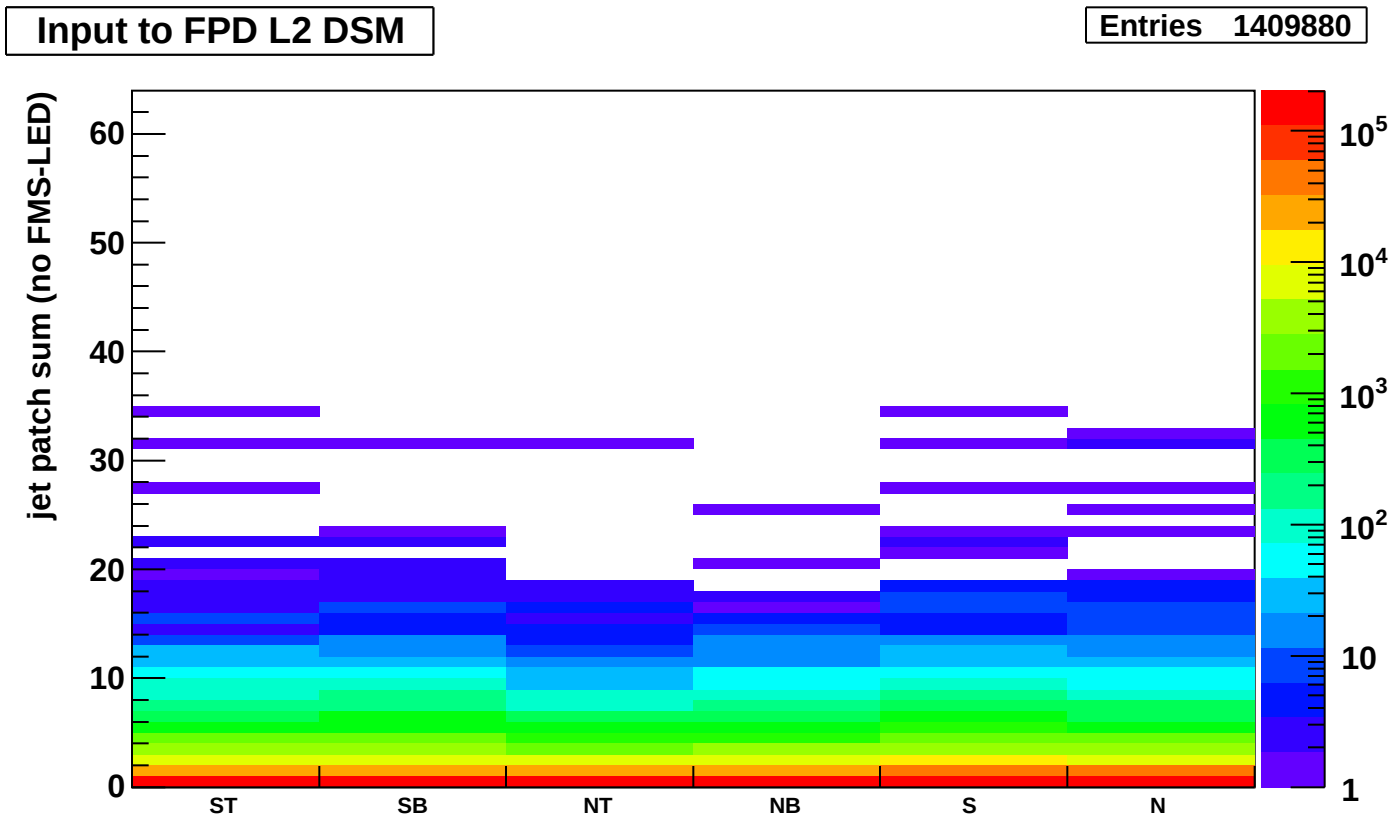

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

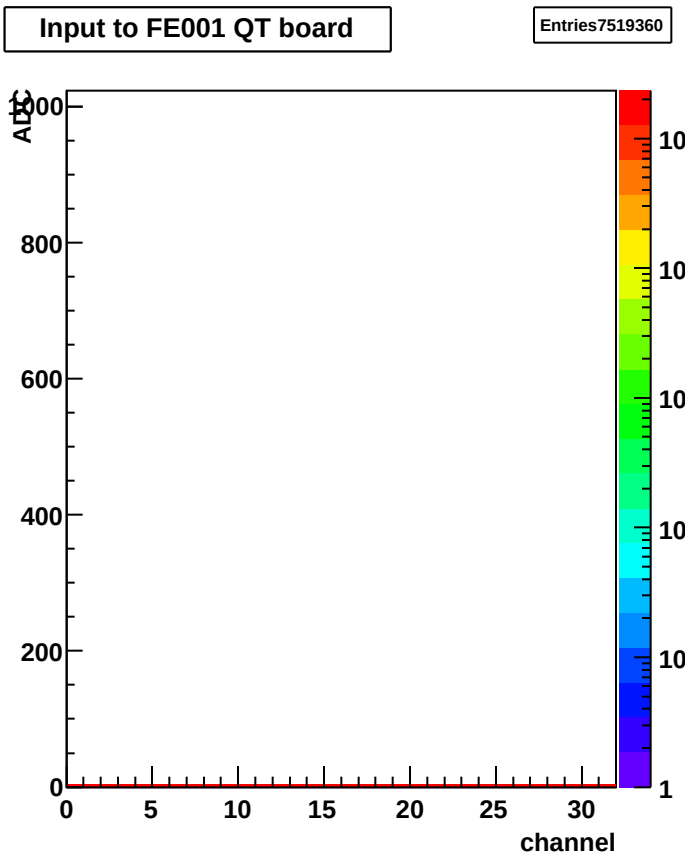

Input to FPD L2 DSM
Entries 704940
CL bits
4
1
30
8
2
1270
373
255
233680
234595
234722
SMALL
LARGE-S
LARGE-N

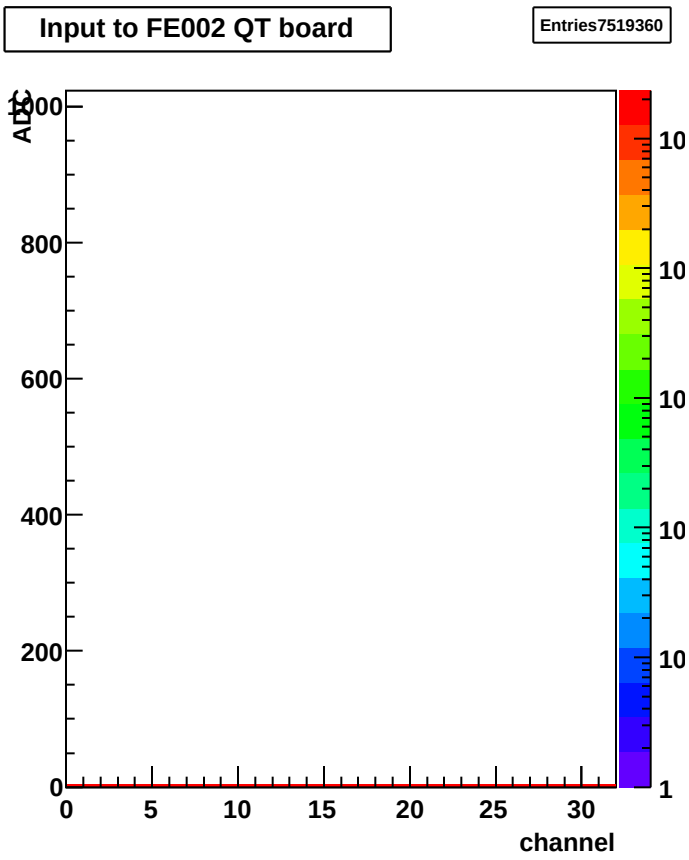

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

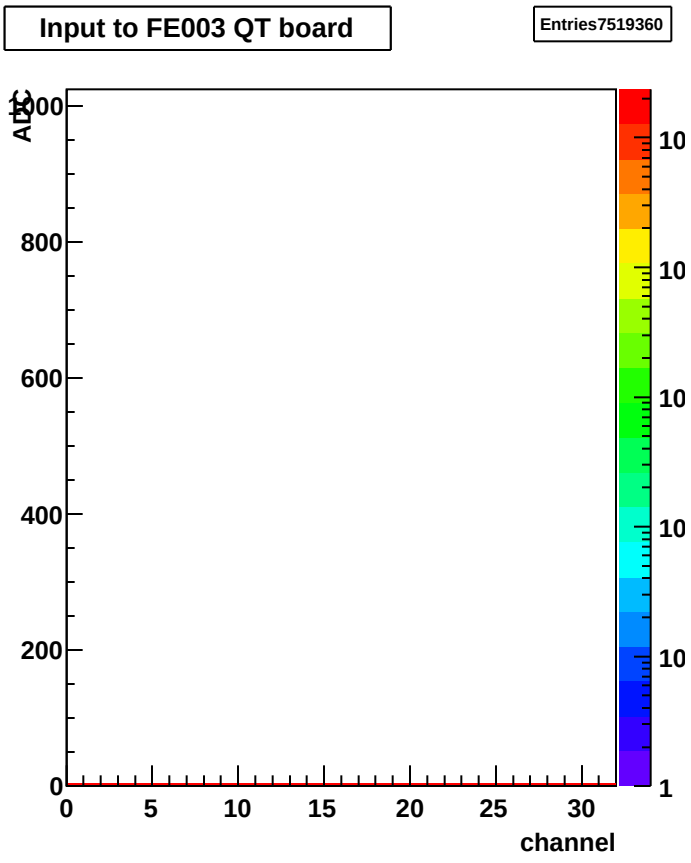

Input to FPD L2 DSM
Entries 704940
HT bits
1217
80
37
4148
12160
495
294
217455
234405
234649
SMALL
LARGE-S
LARGE-N

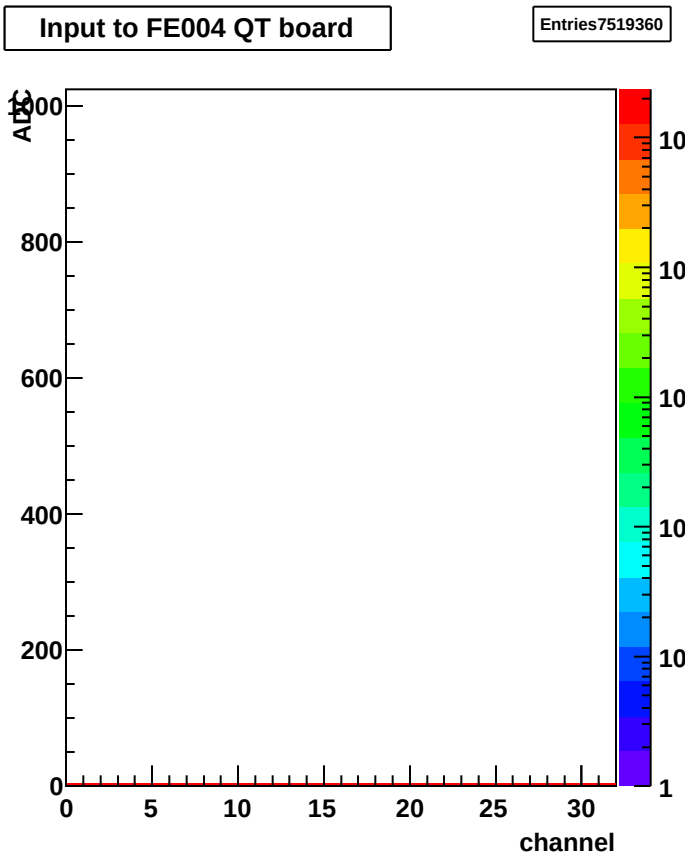

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

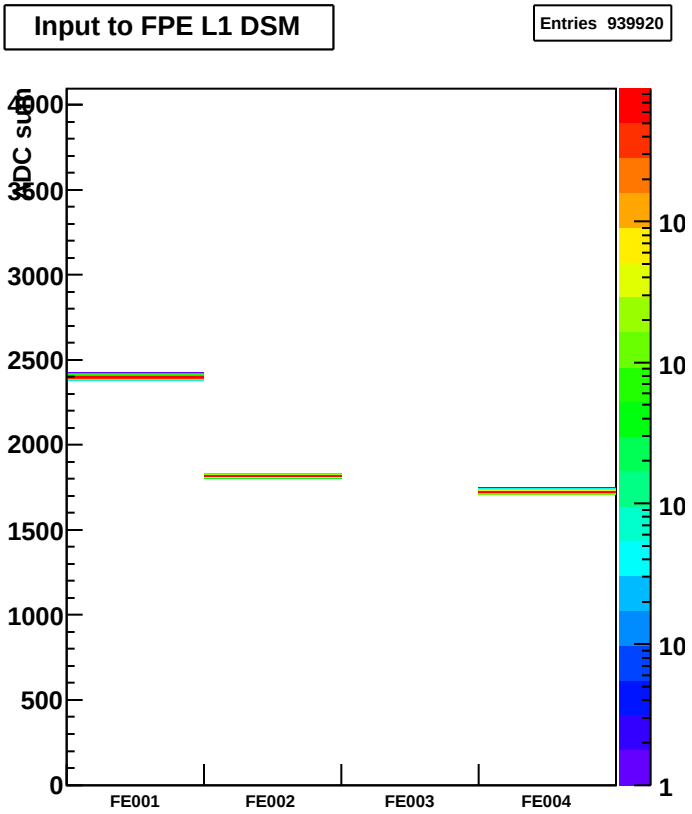

Input to FPD L2 DSM
Entries 1409880
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

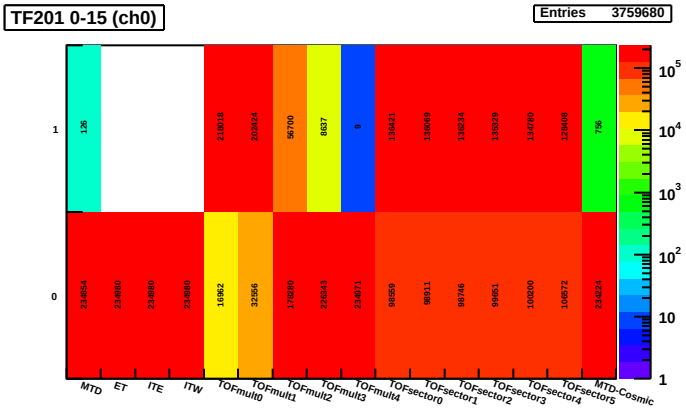

Input to FE001 QT board
Entries7519360
ADC
channel

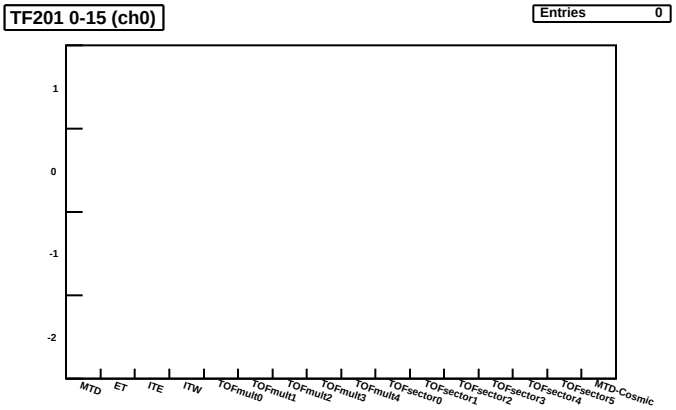

Input to FE002 QT board
Entries7519360
ADC
channel

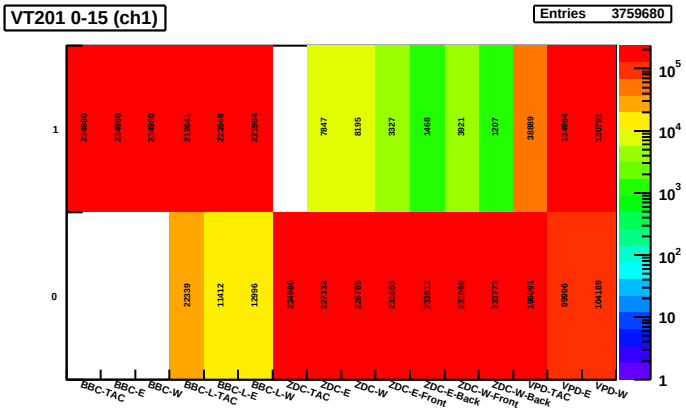

Input to FE003 QT board
Entries7519360
ADC
channel

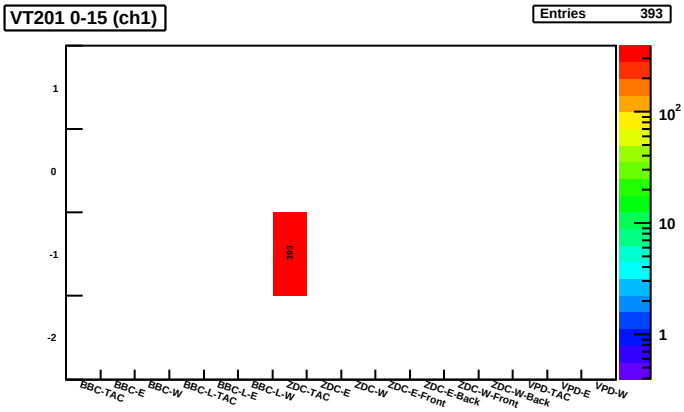

Input to FE004 QT board
Entries7519360
ADC
channel

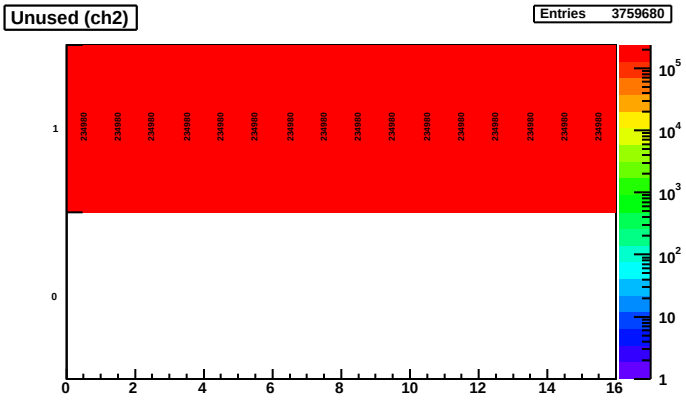

Input to FPE L1 DSM
Entries 939920
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

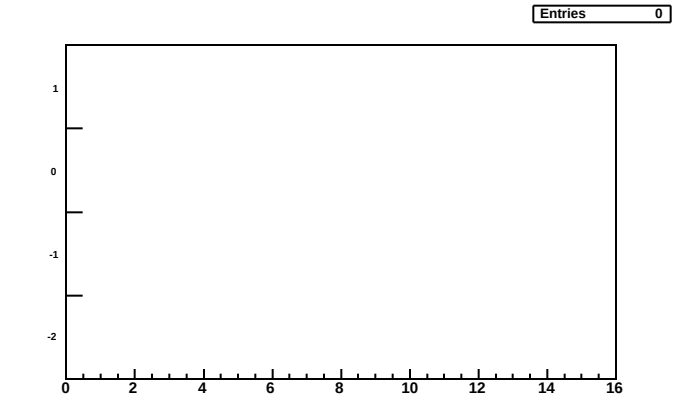

TF201 0-15 (ch0)
Entries 3759680
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

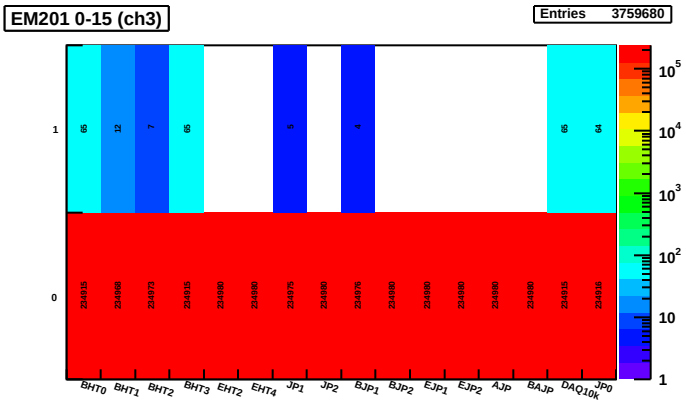

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3759680
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 393
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3759680


Entries 0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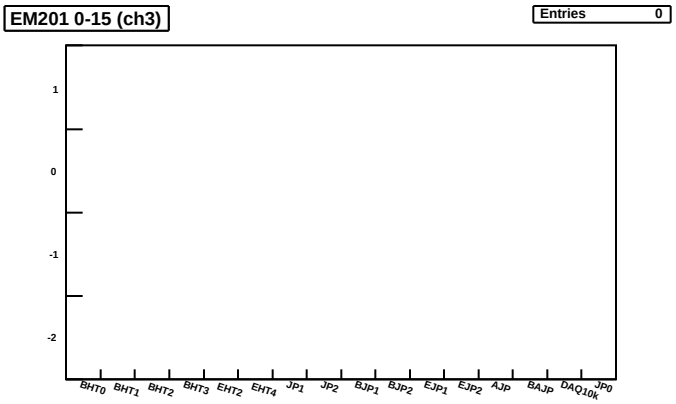
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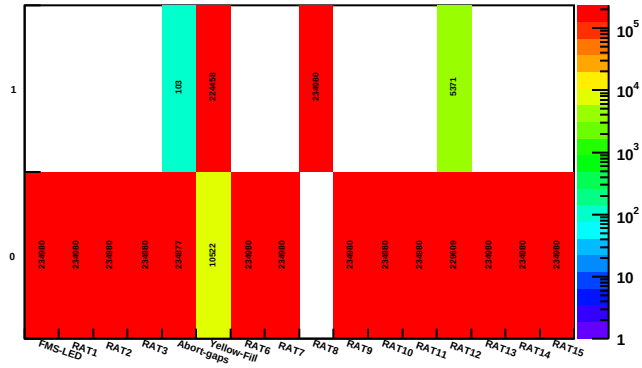
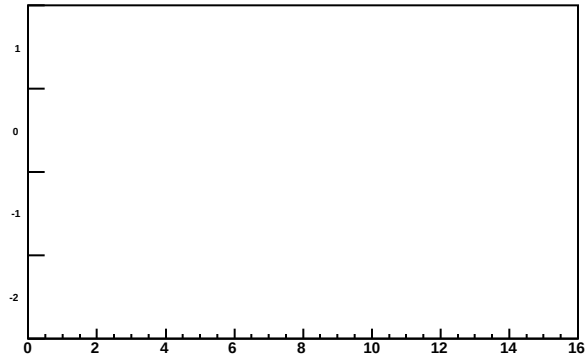
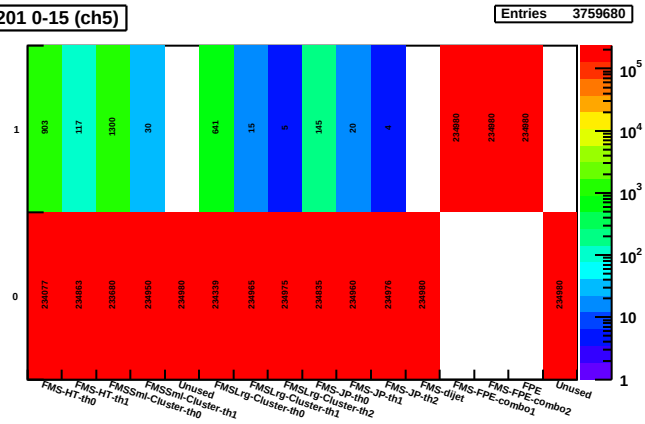
RAT board (ch4)





FP201 0-15 (ch5)



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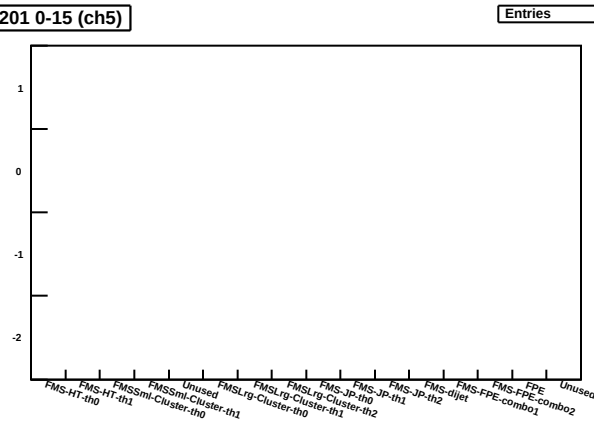

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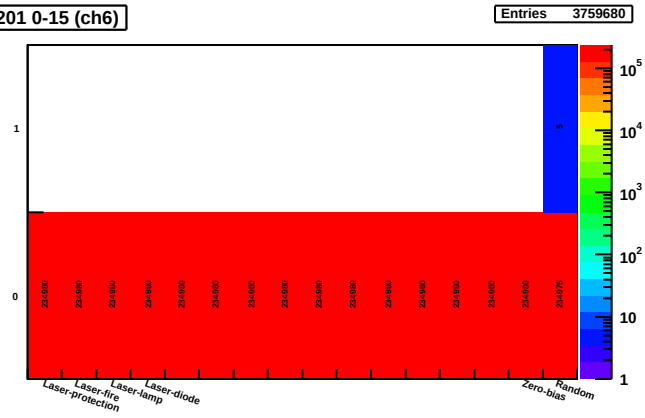

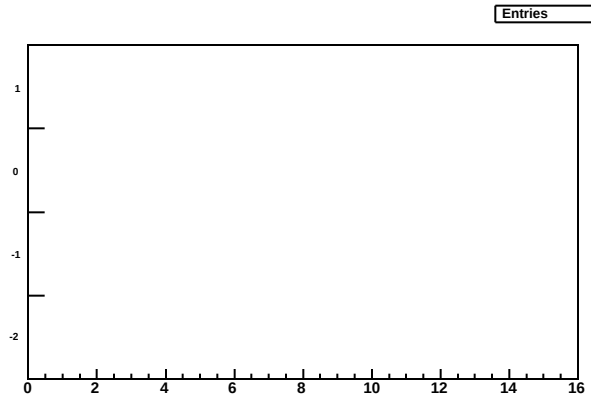
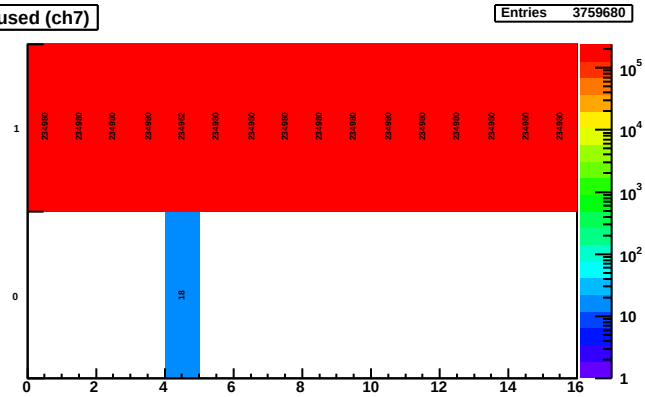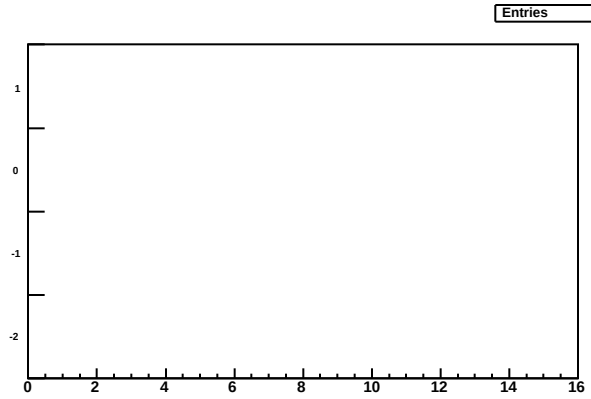
Unused (ch7)