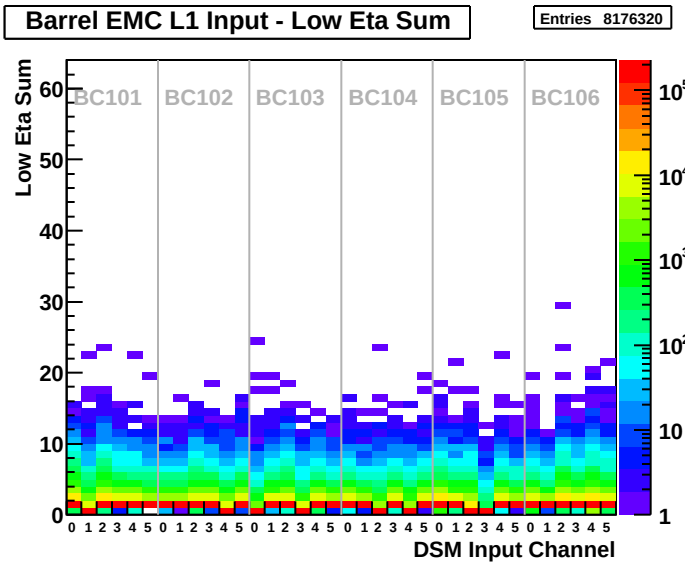
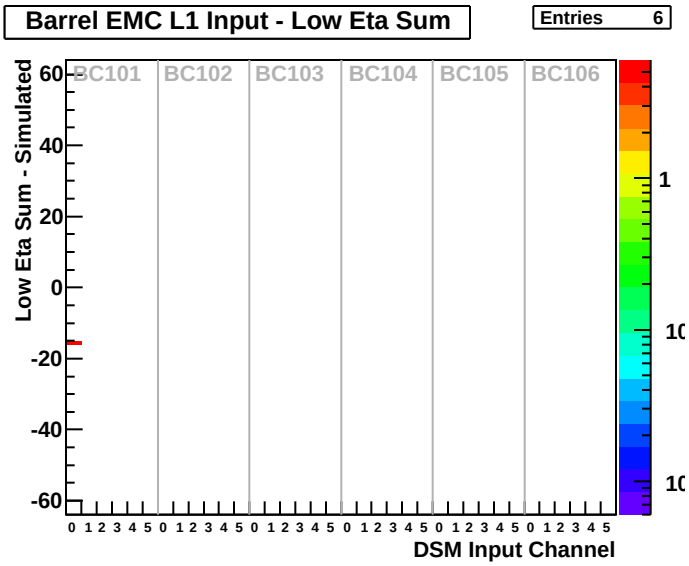
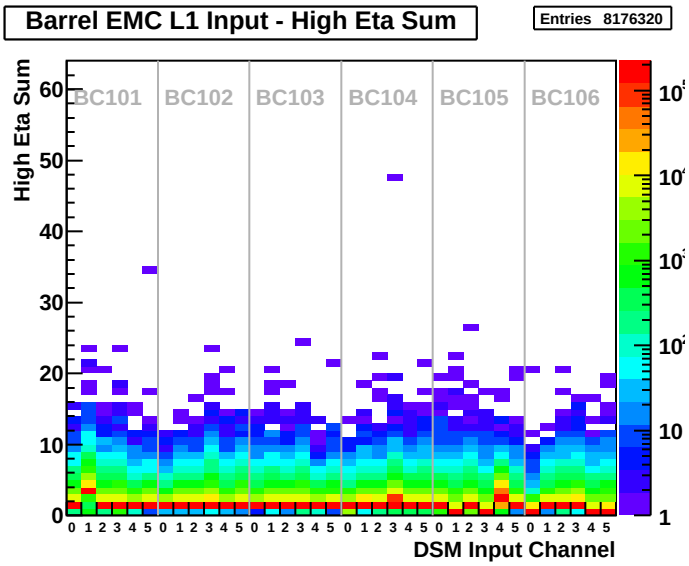
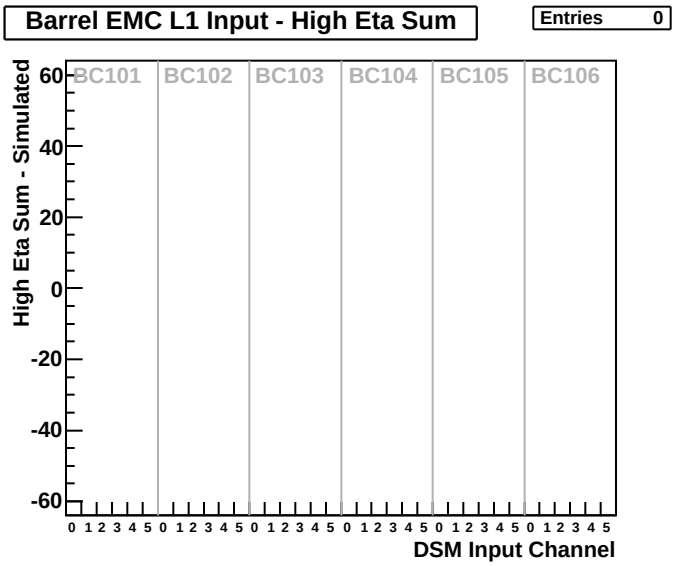
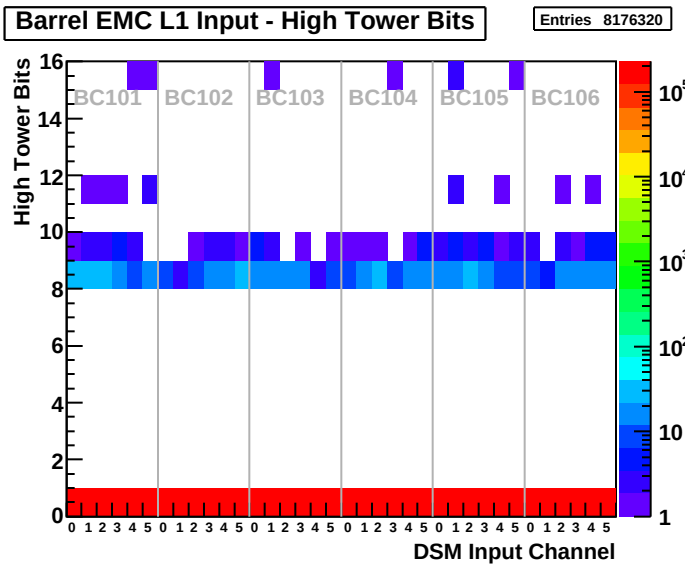
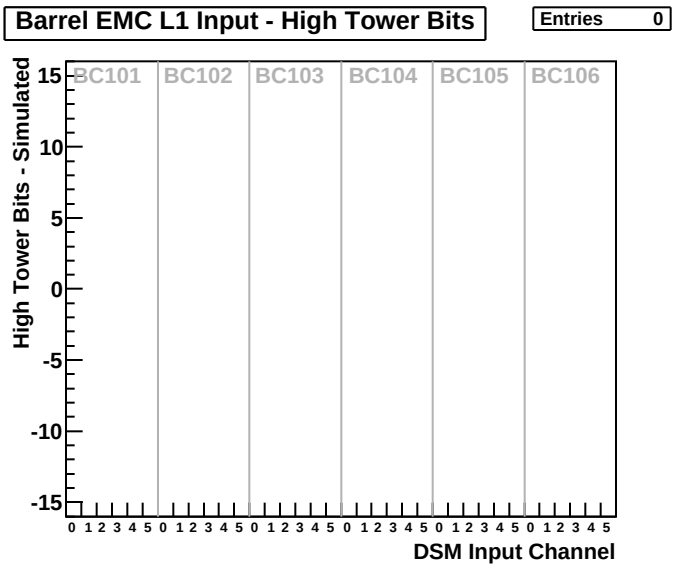

Barrel EMC L1 Input - Low Eta Sum
Entries 8176320
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

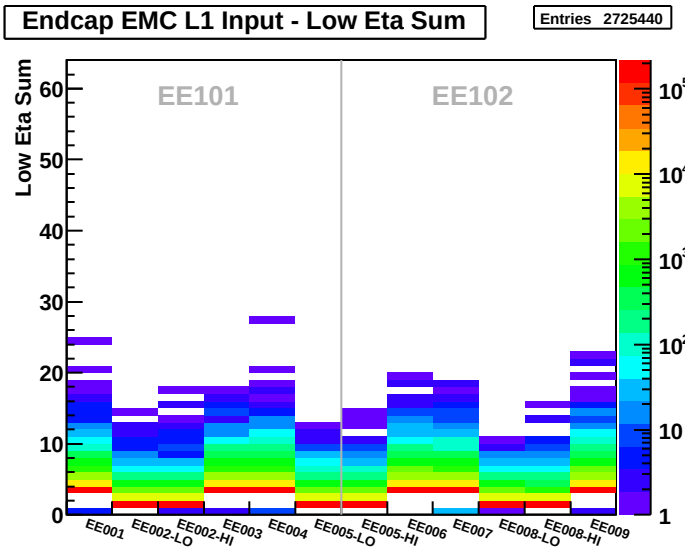

Barrel EMC L1 Input - Low Eta Sum
Entries 6
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

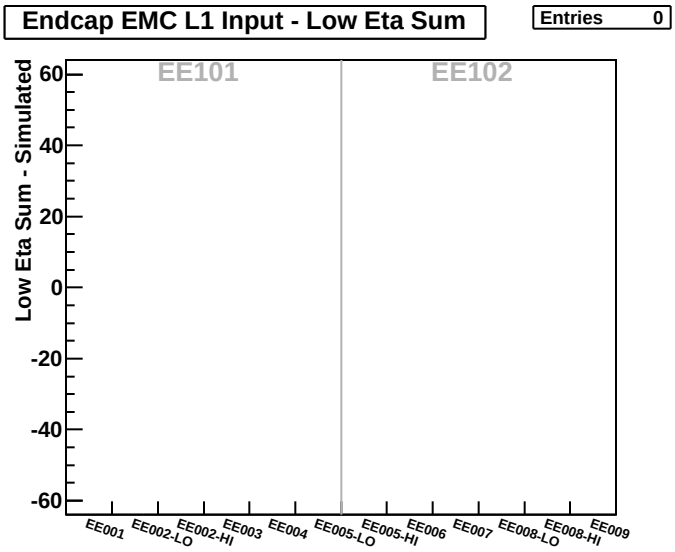

Barrel EMC L1 Input - High Eta Sum
Entries 8176320
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

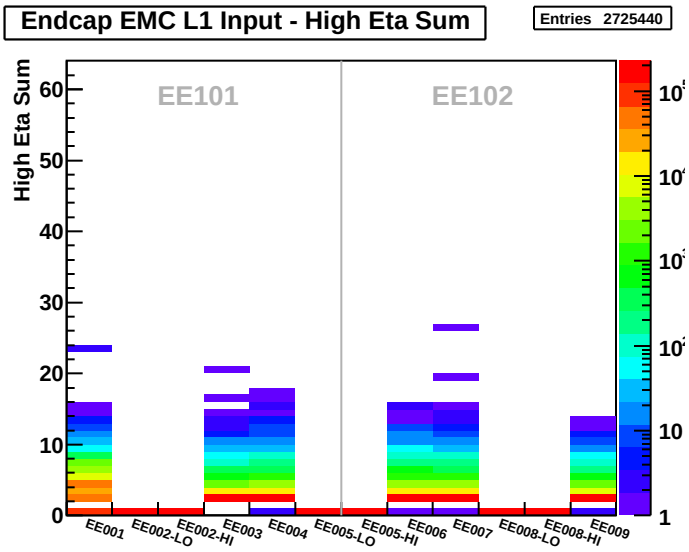

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

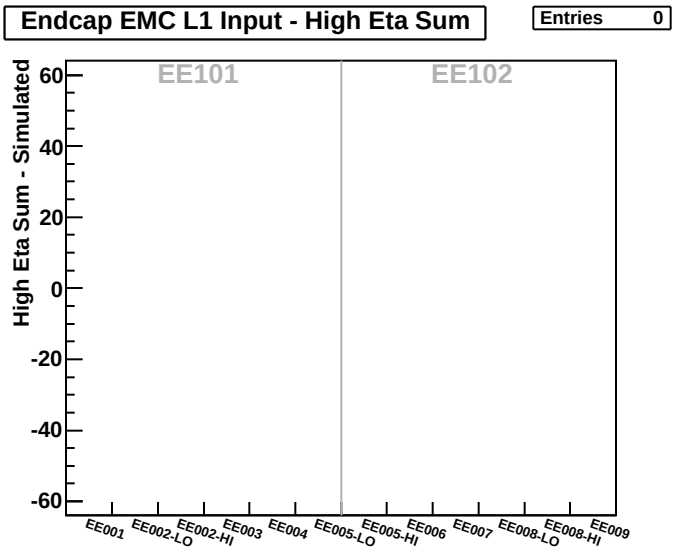

Barrel EMC L1 Input - High Tower Bits
Entries 8176320
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

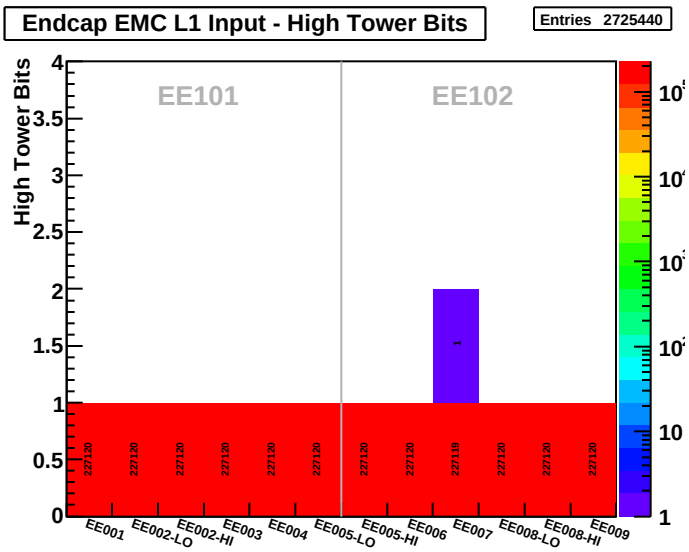

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

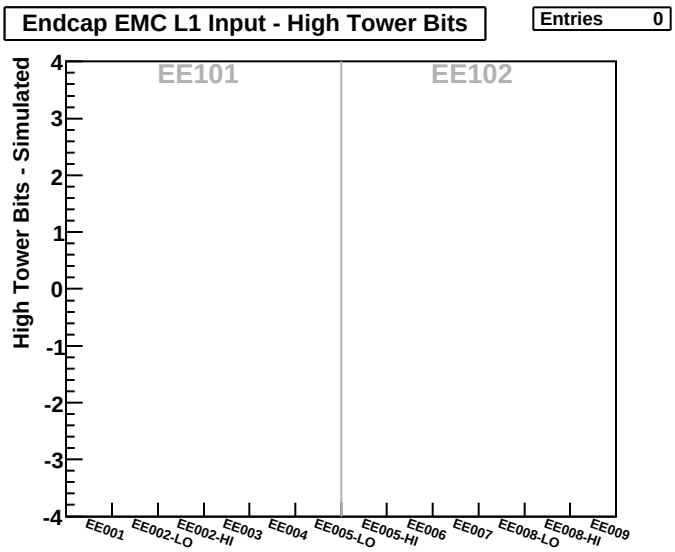

Endcap EMC L1 Input - Low Eta Sum
Entries 2725440
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

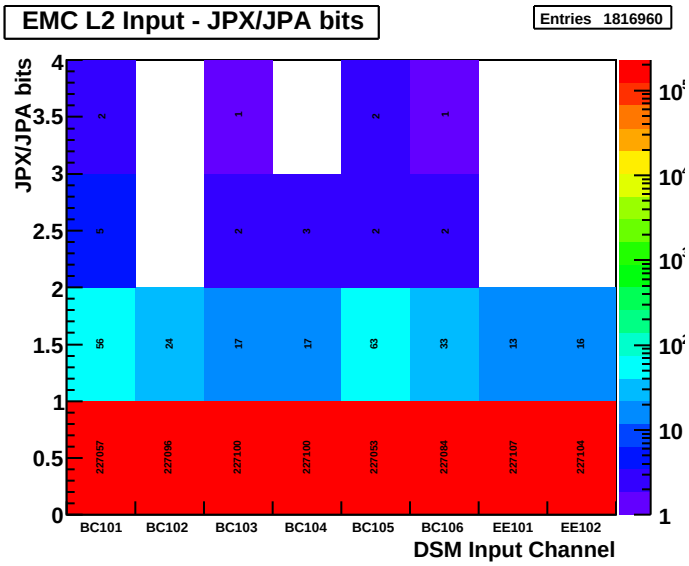

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

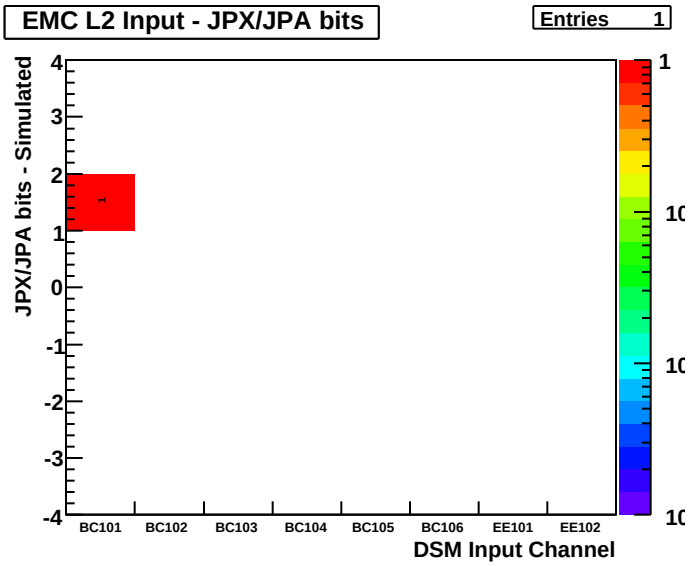

Endcap EMC L1 Input - High Eta Sum
Entries 2725440
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

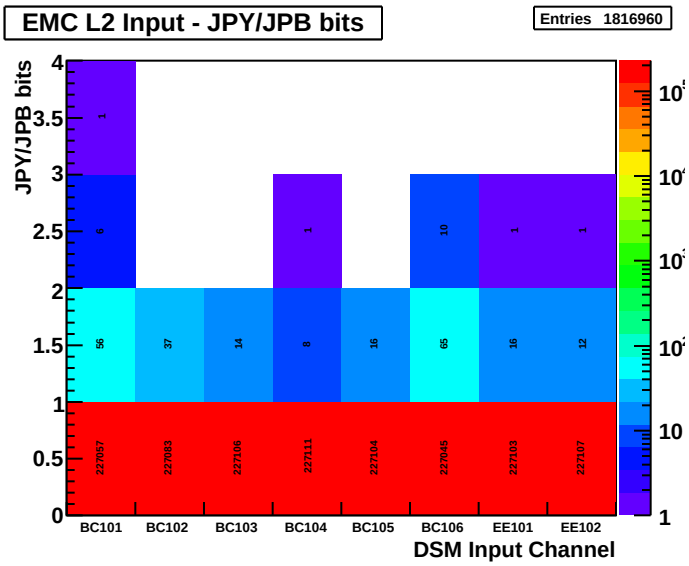

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

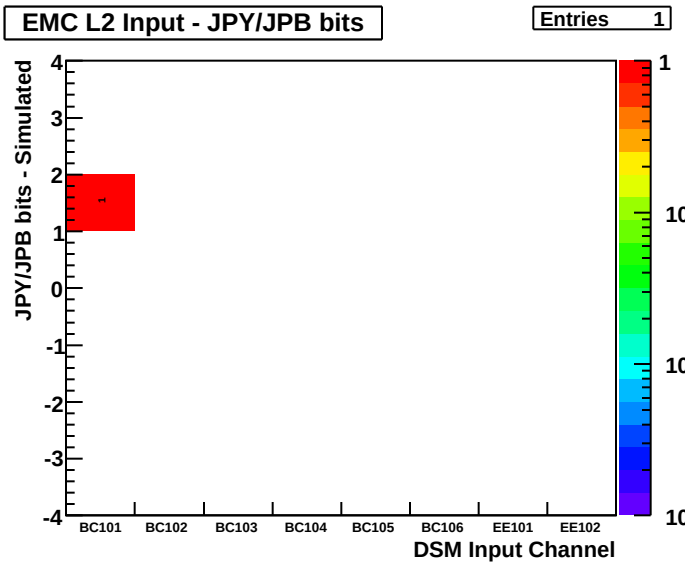

Endcap EMC L1 Input - High Tower Bits
Entries 2725440
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

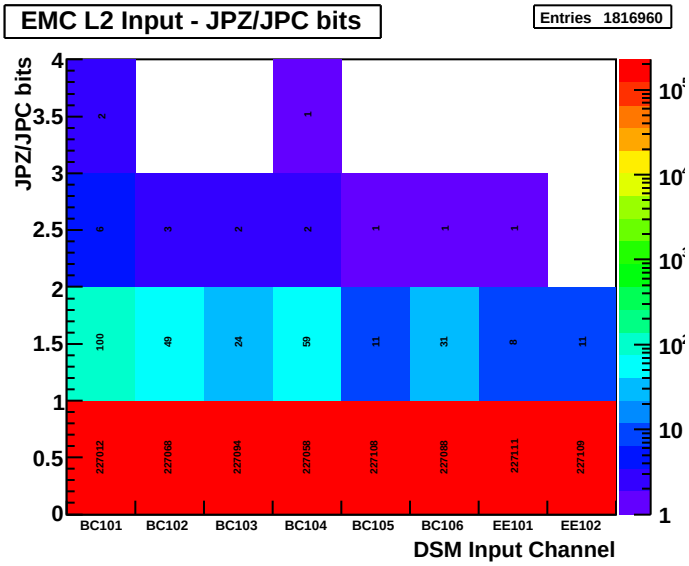

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

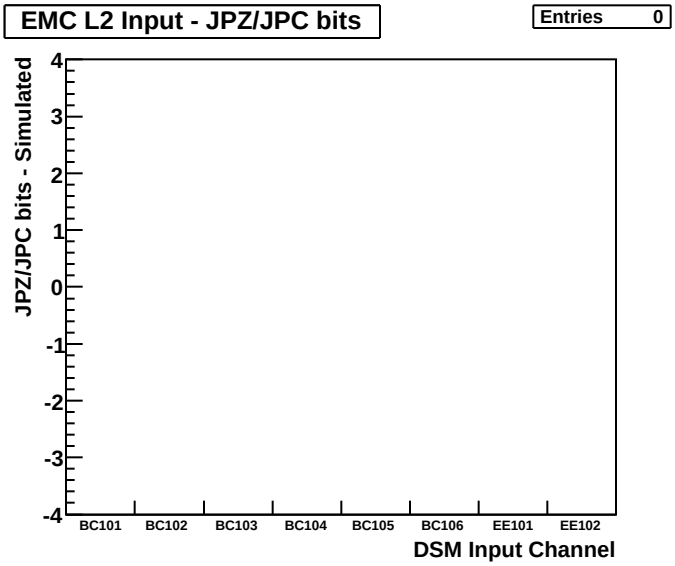

EMC L2 Input - JPX/JPA bits
Entries 1816960
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

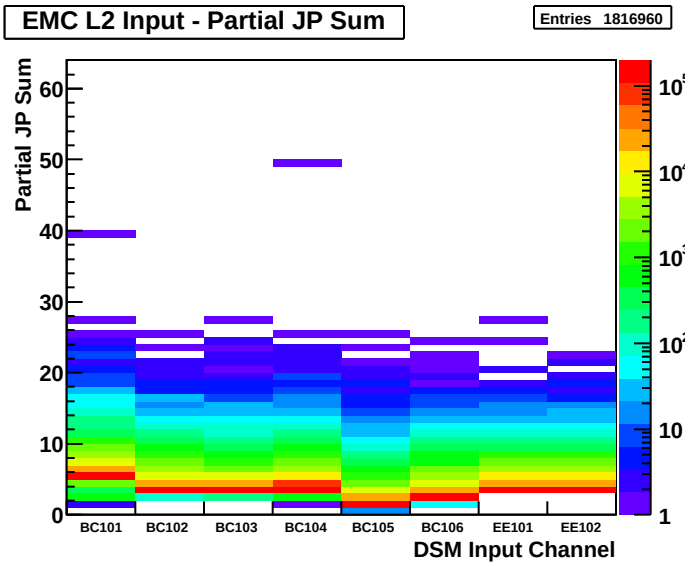

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

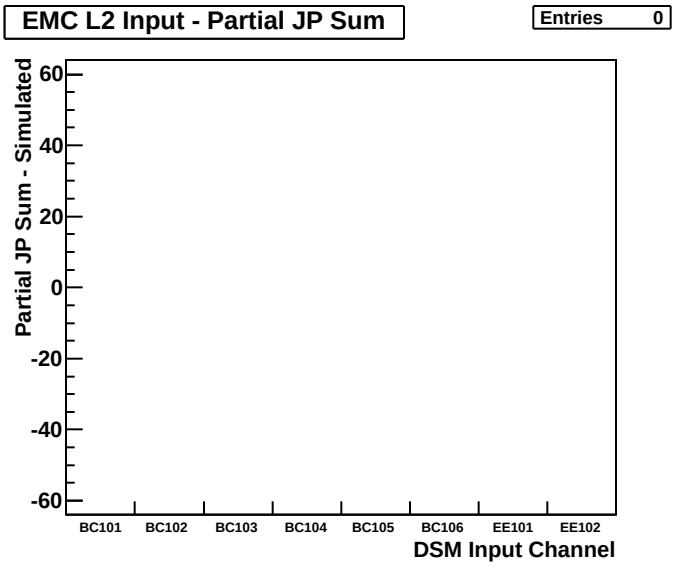

EMC L2 Input - JPY/JPB bits
Entries 1816960
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

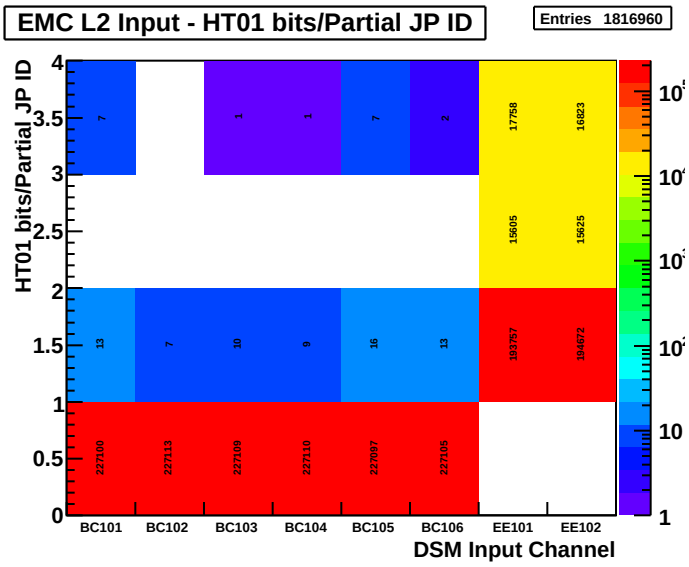

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

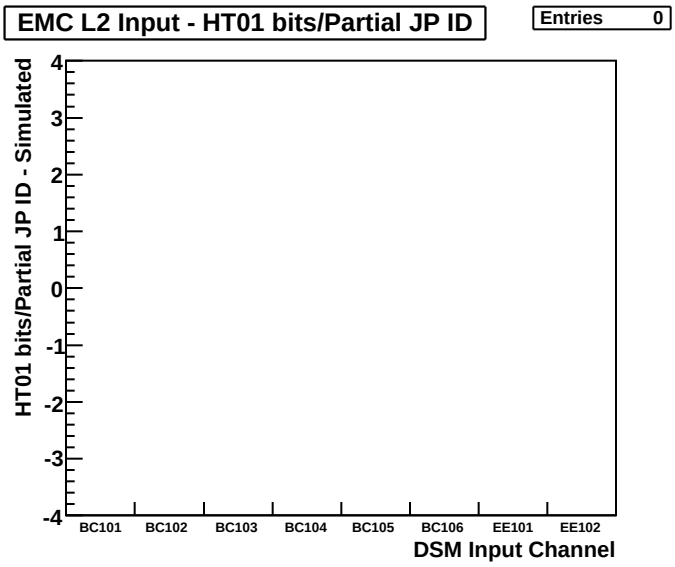

EMC L2 Input - JPZ/JPC bits
Entries 1816960
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

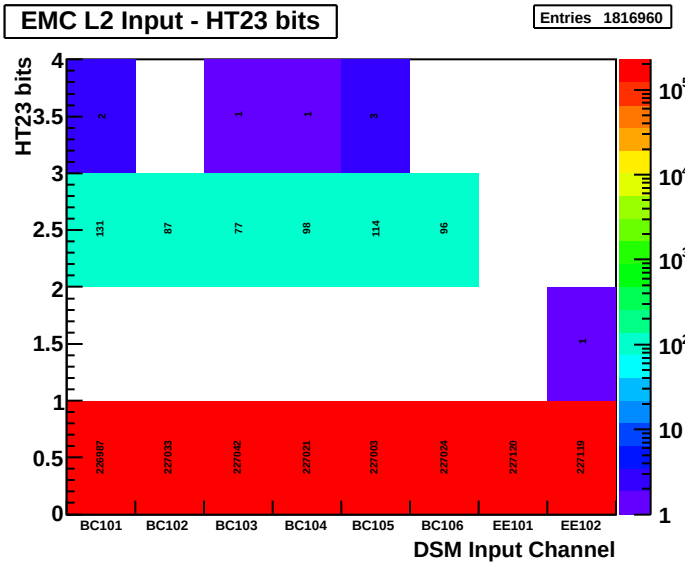

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

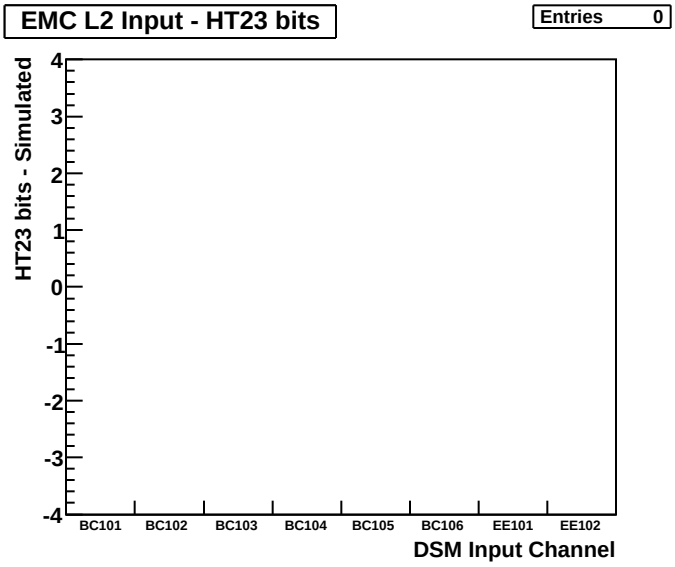

EMC L2 Input - Partial JP Sum
Entries 1816960
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

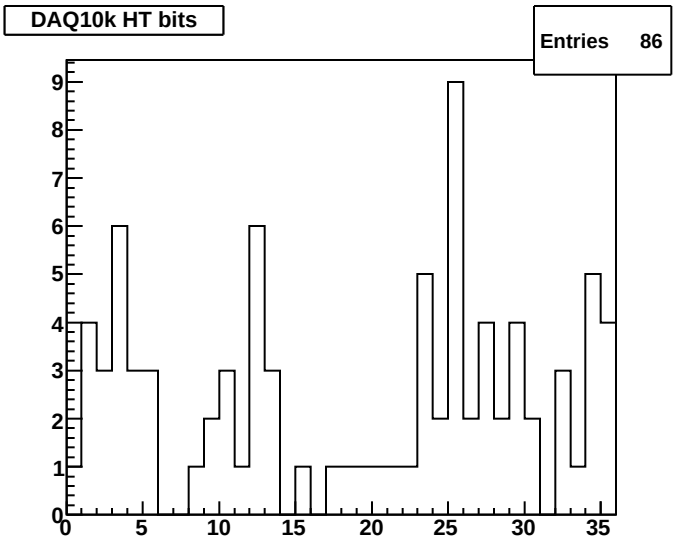

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

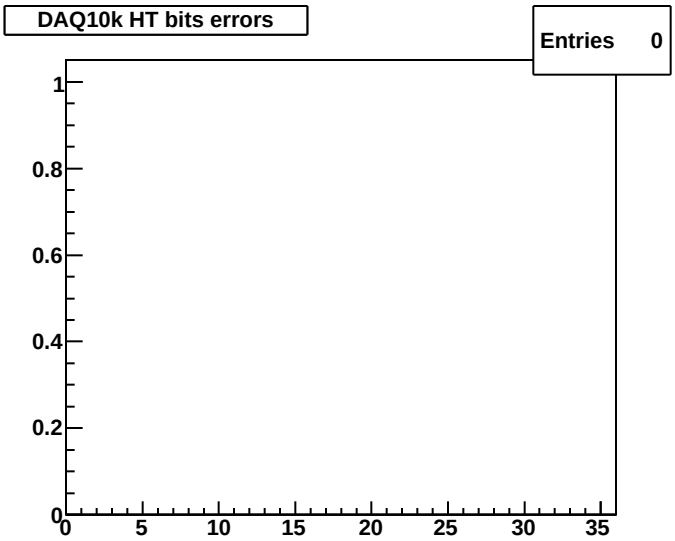

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1816960
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

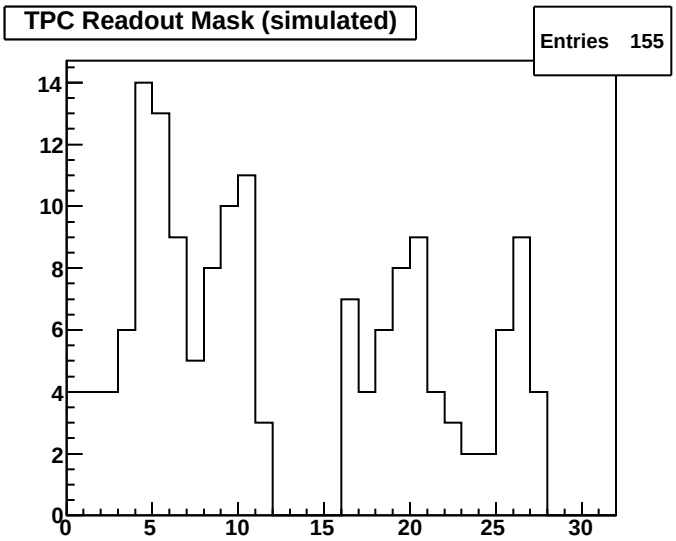

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

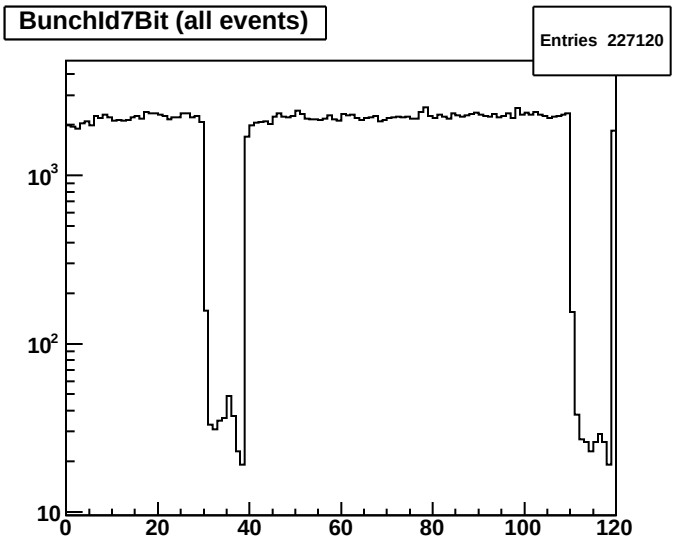

EMC L2 Input - HT23 bits
Entries 1816960
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

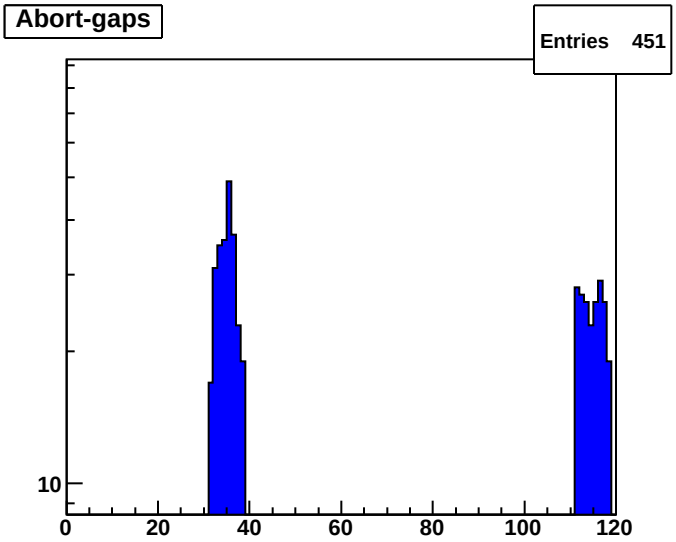

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

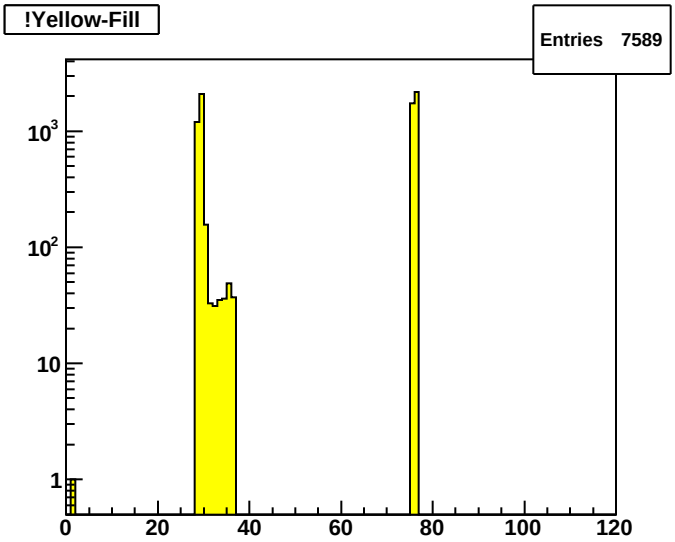

DAQ10k HT bits
Entries 86

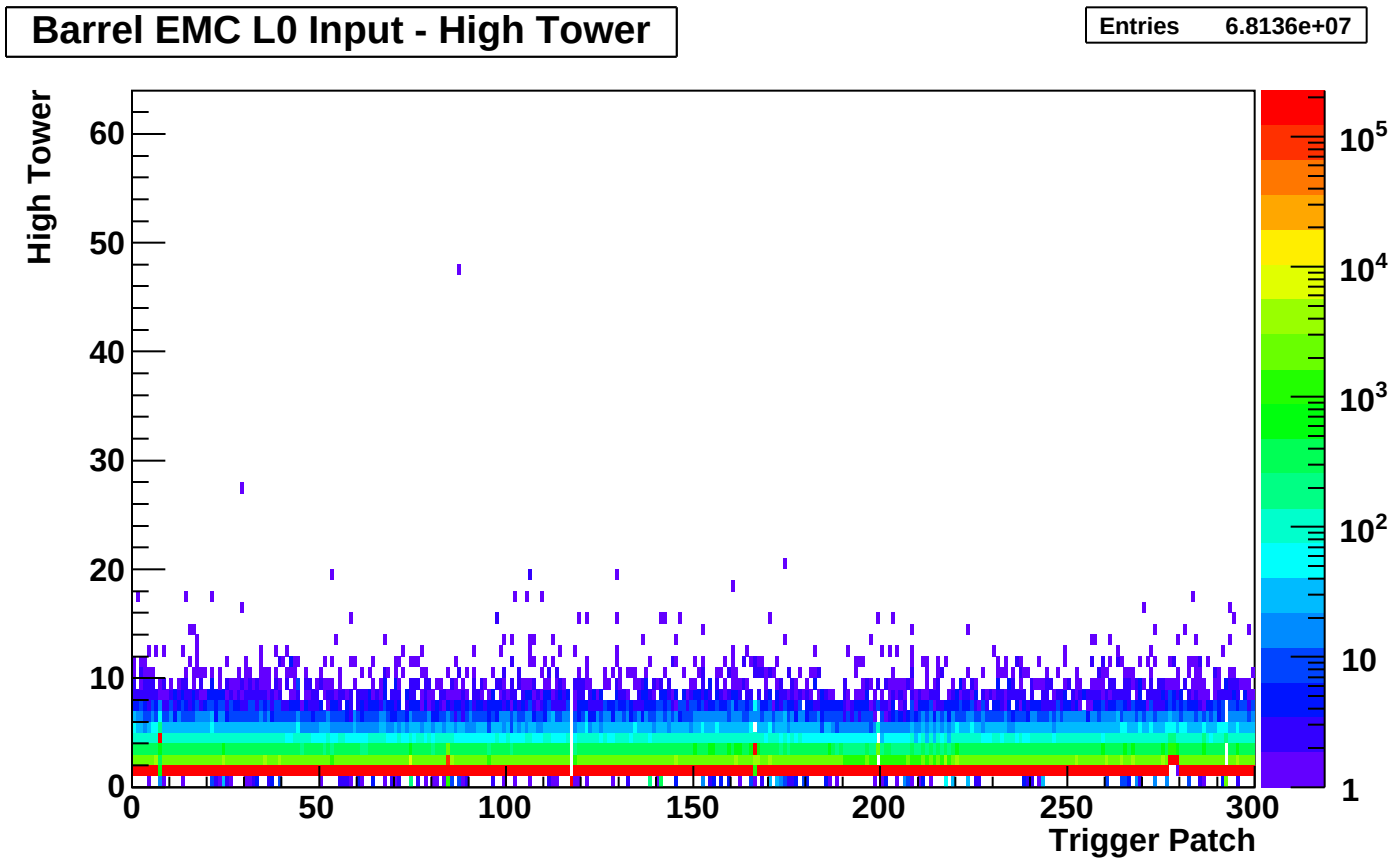

DAQ10k HT bits errors
Entries 0

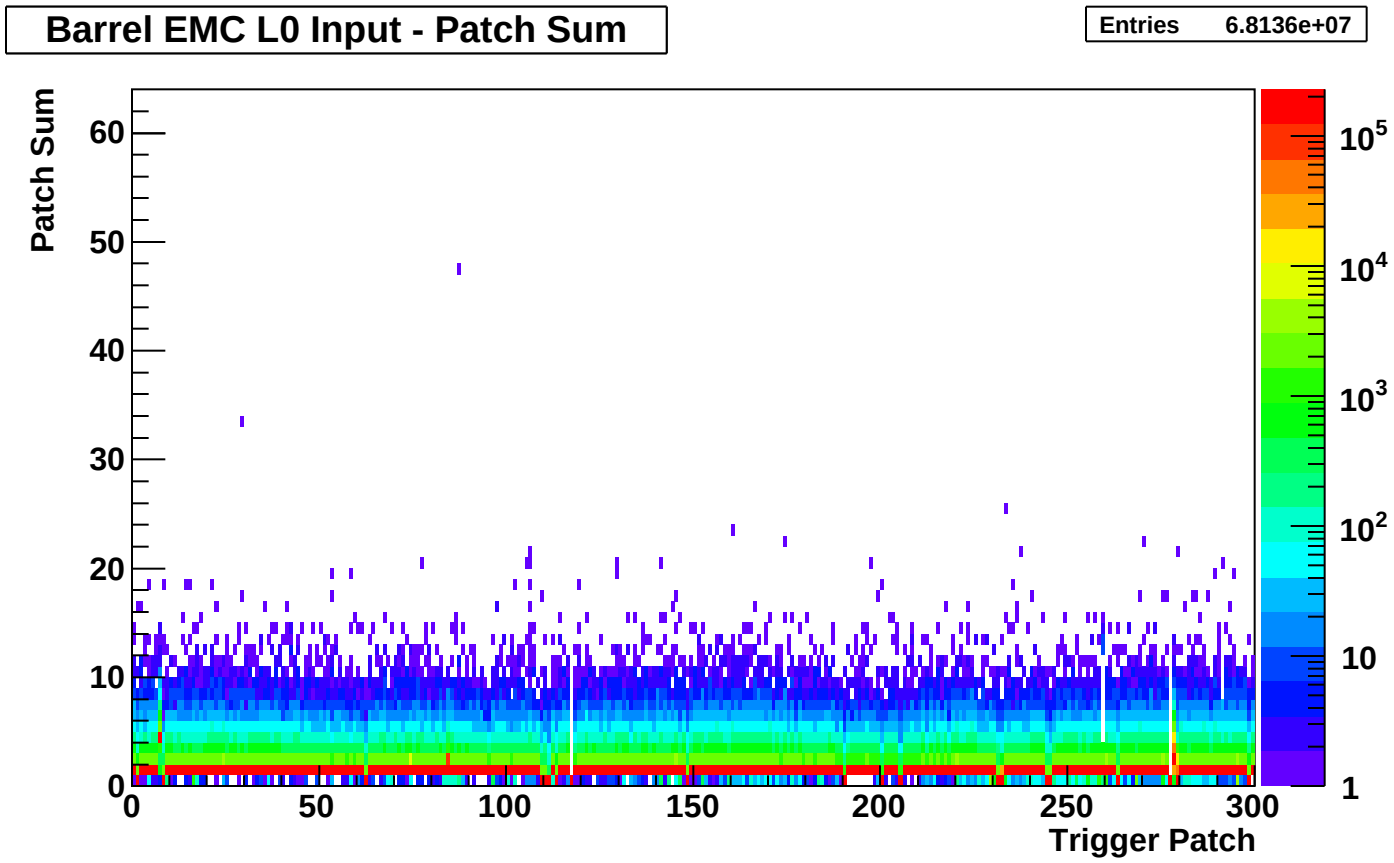

TPC Readout Mask (simulated)
Entries 155

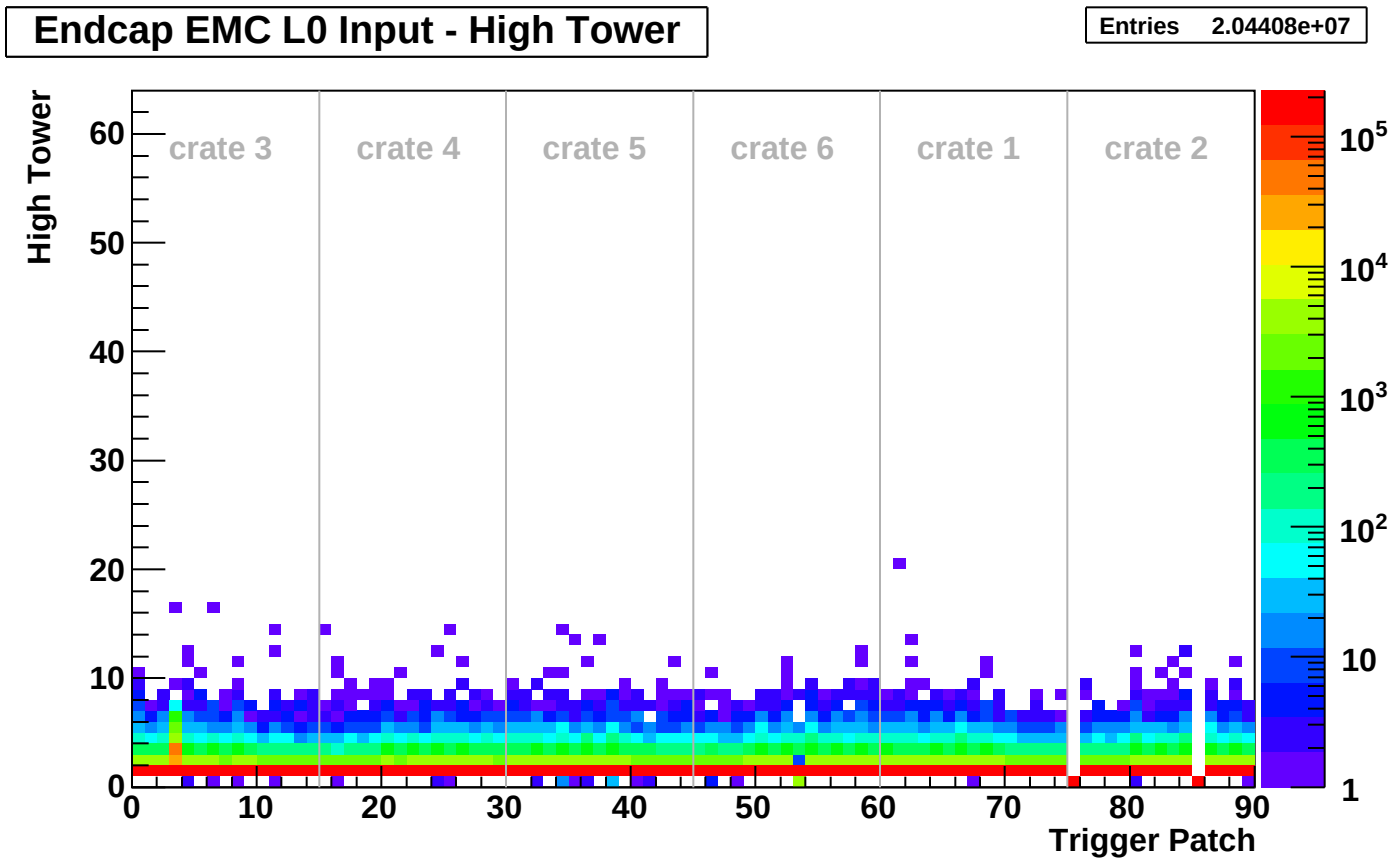

BunchId7Bit (all events)
Entries 227120

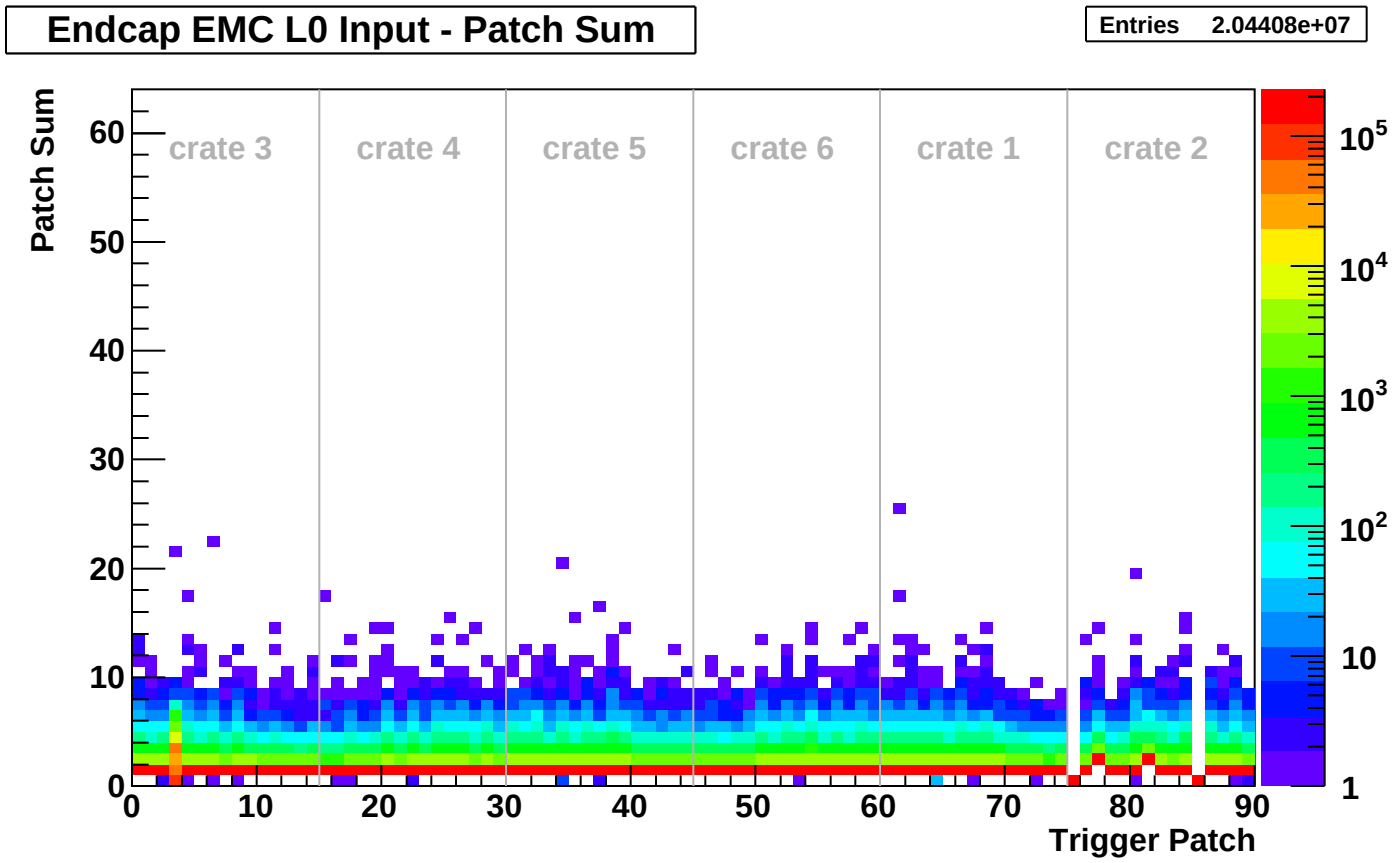

Abort-gaps
Entries 451

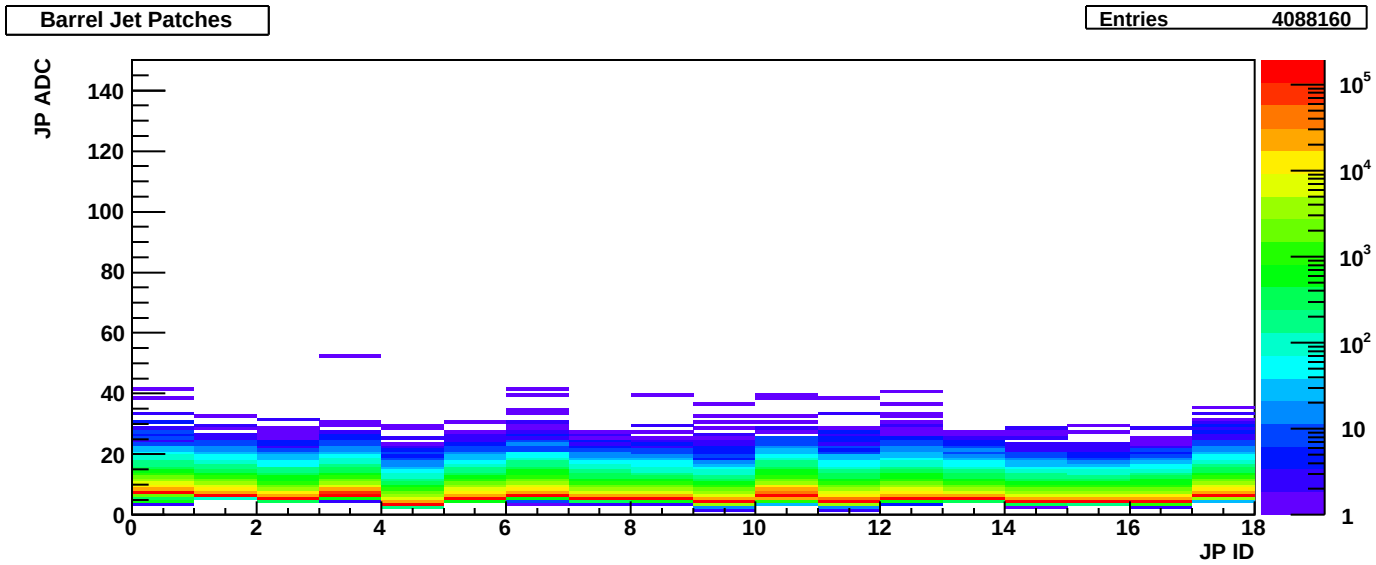

!Yellow-Fill
Entries 7589

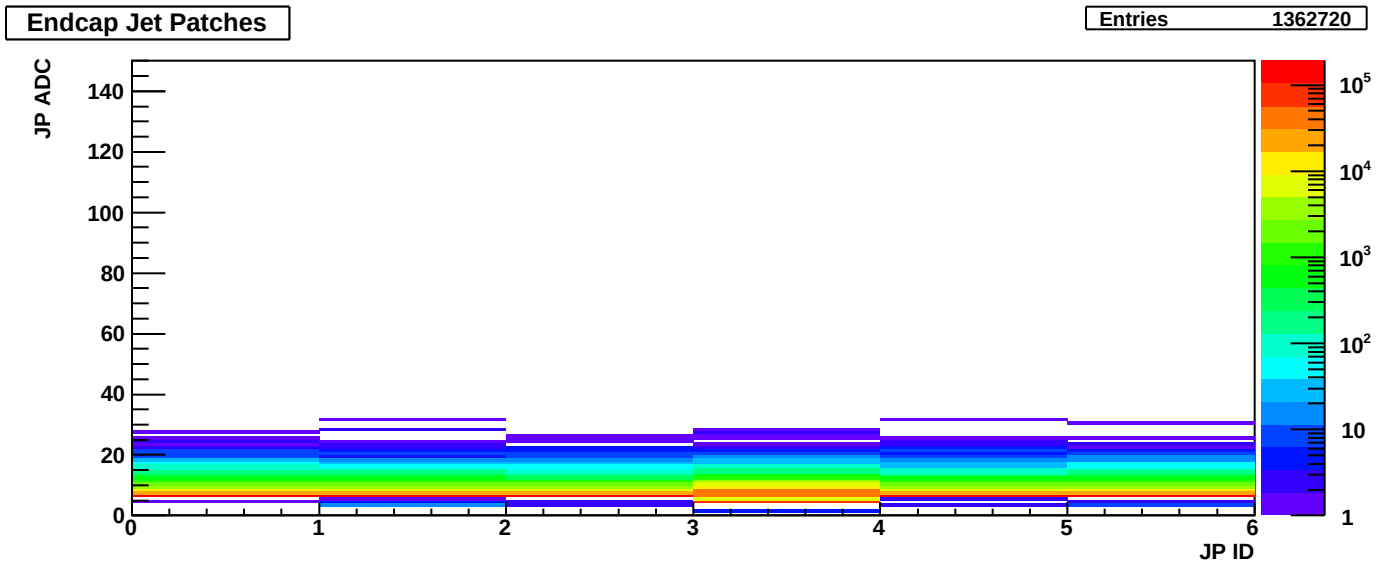

Barrel EMC L0 Input - High Tower
Entries 6.8136e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

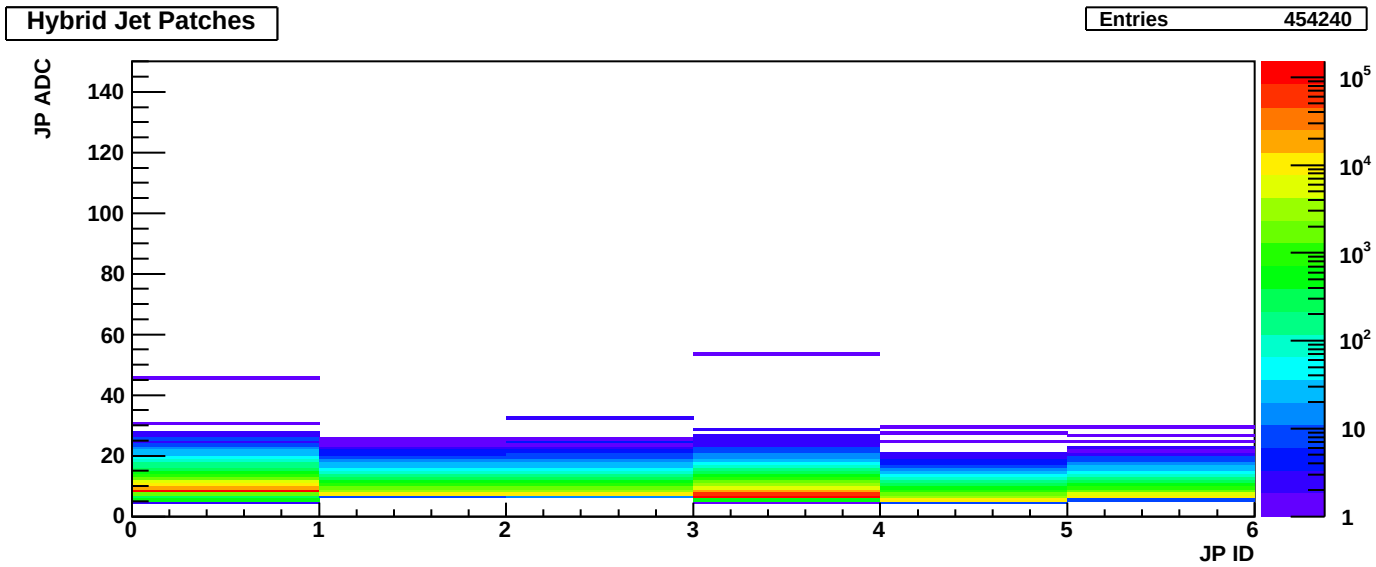

Barrel EMC L0 Input - Patch Sum
Entries 6.8136e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

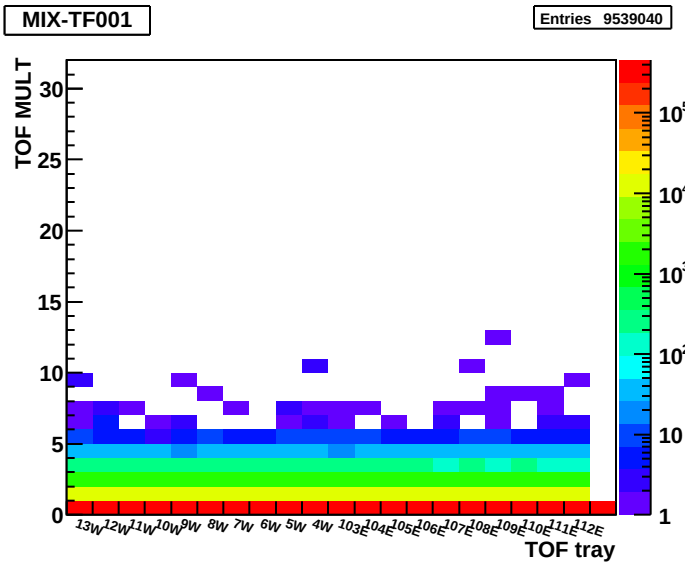

Endcap EMC L0 Input - High Tower
Entries 2.04408e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

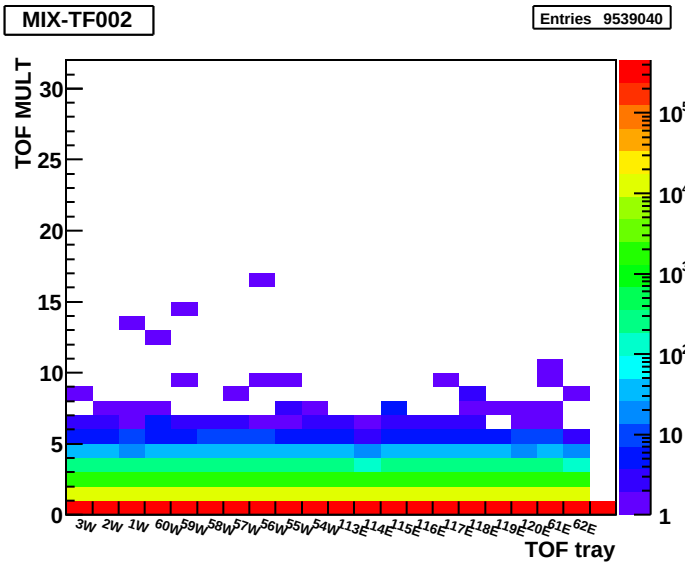

Endcap EMC L0 Input - Patch Sum
Entries 2.04408e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

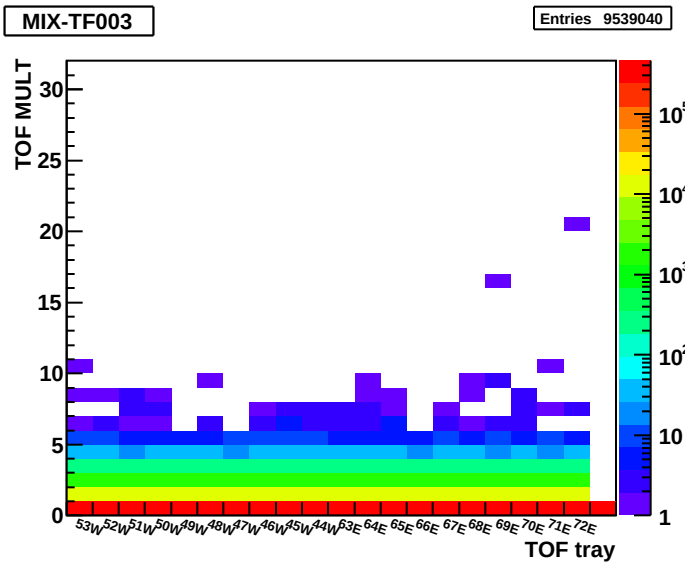

Barrel Jet Patches
Entries 4088160
JP ADC
JP ID

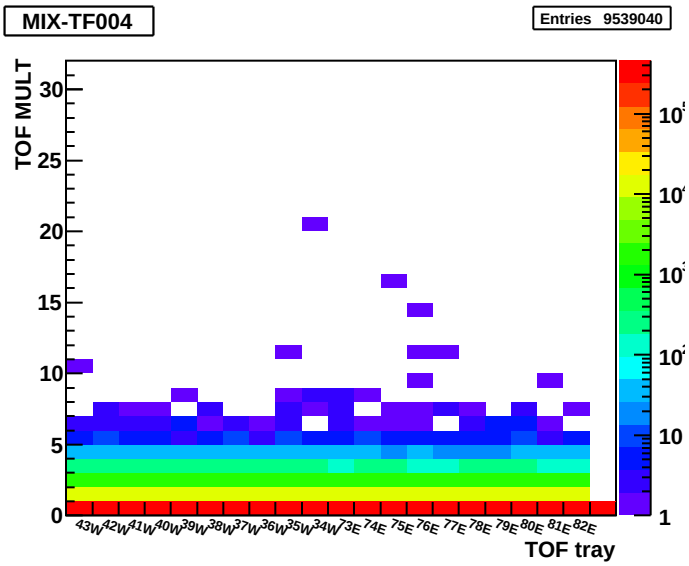

Endcap Jet Patches
Entries 1362720
JP ADC
JP ID

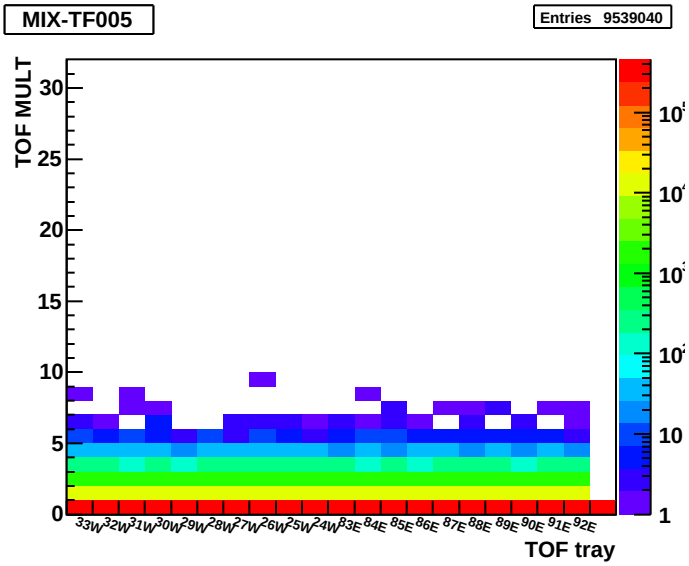

Hybrid Jet Patches
Entries 454240
JP ADC
JP ID

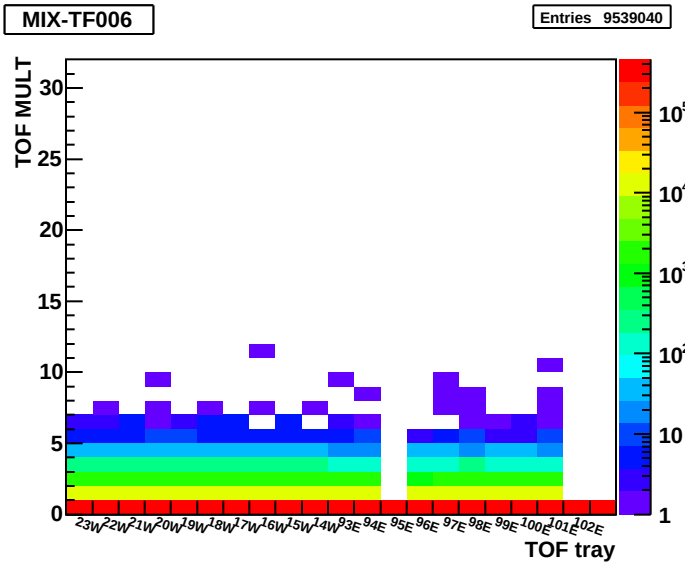

MIX-TF001
Entries 9539040
TOF MULT
TOF tray

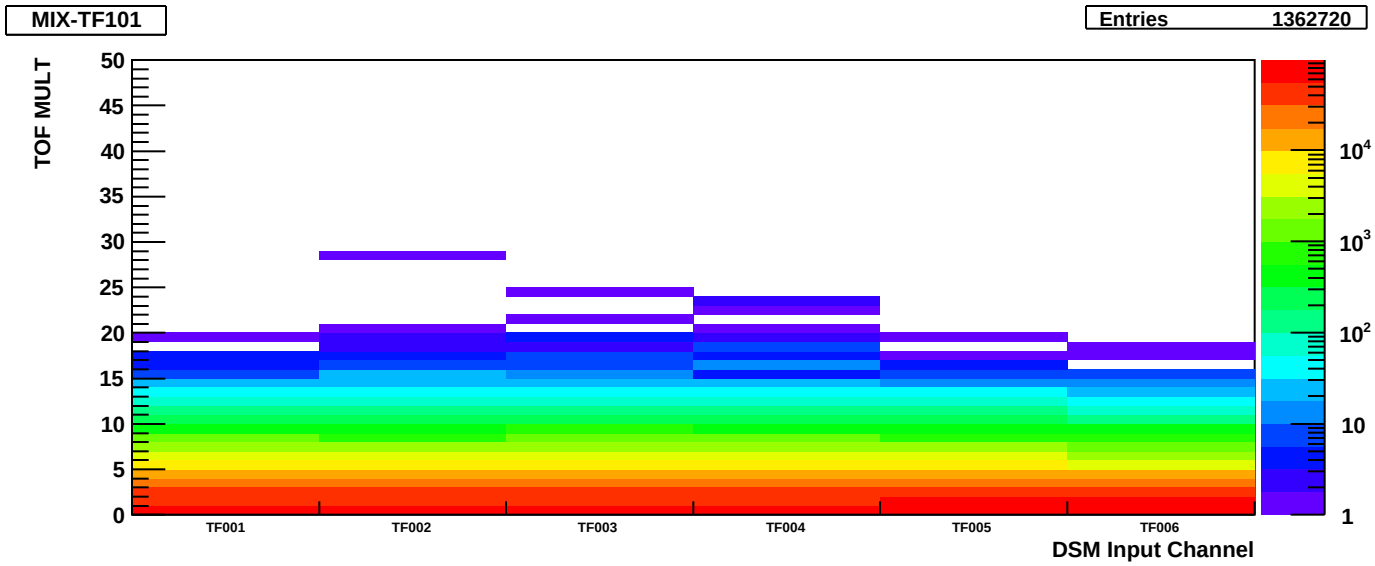

MIX-TF002
Entries 9539040
TOF MULT
TOF tray

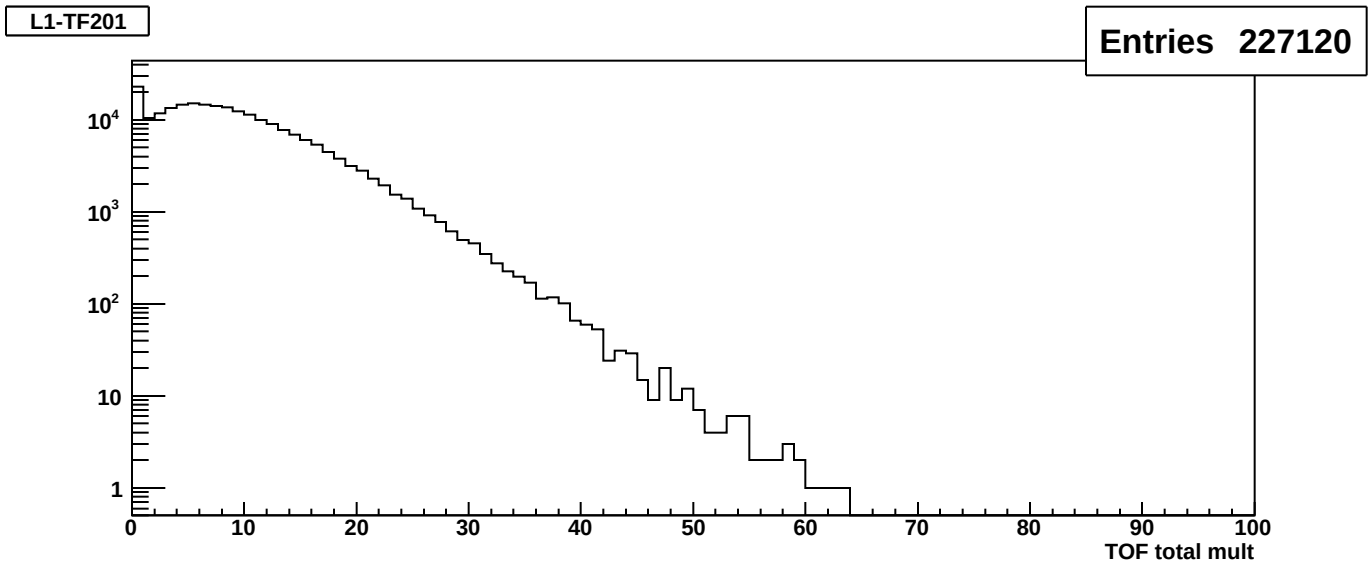

MIX-TF003
Entries 9539040
TOF MULT
TOF tray

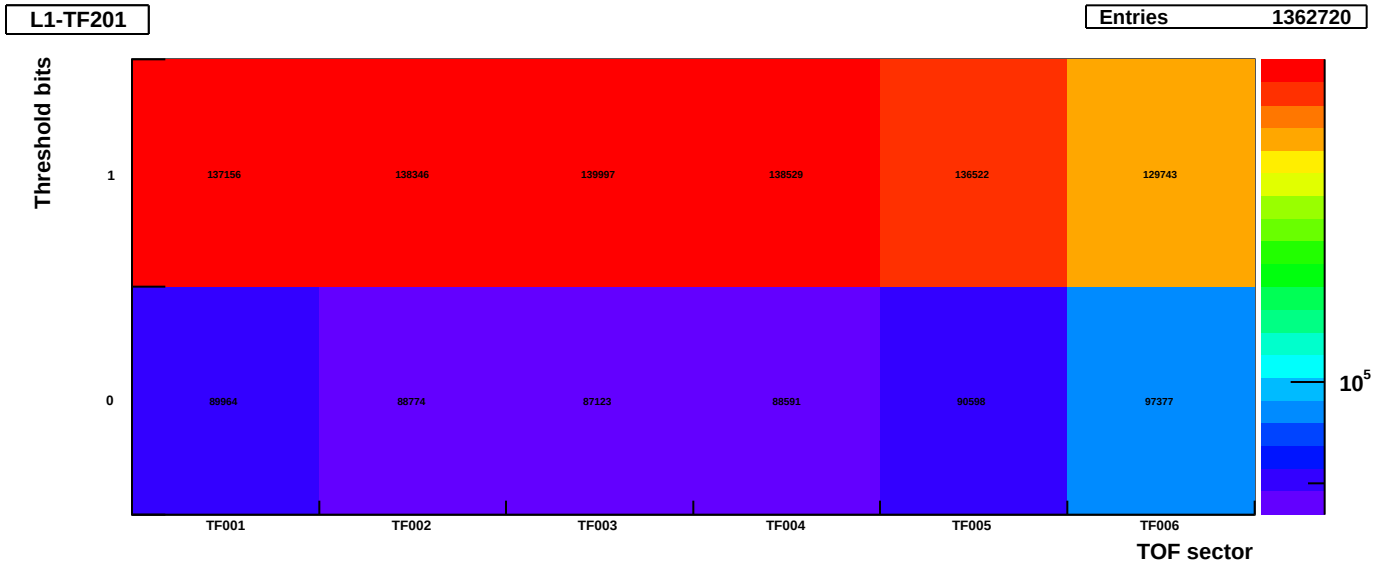

MIX-TF004
Entries 9539040
TOF MULT
TOF tray

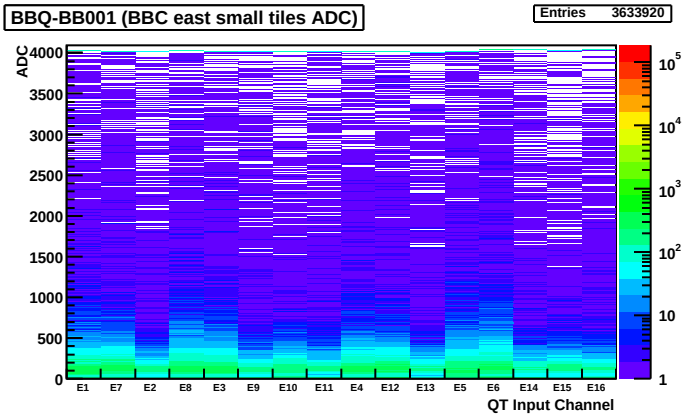

MIX-TF005
Entries 9539040
TOF MULT
TOF tray

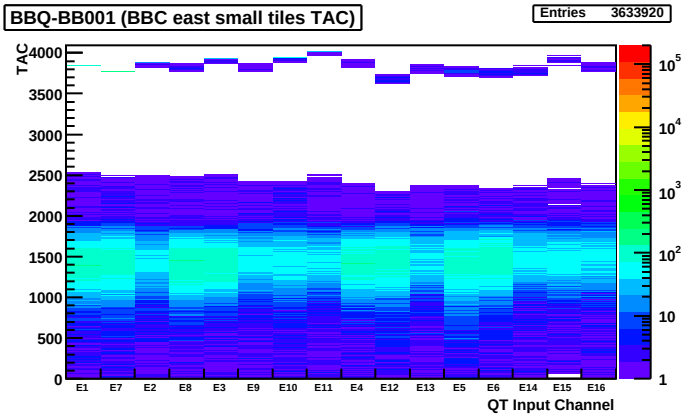

MIX-TF006
Entries 9539040
TOF MULT
TOF tray

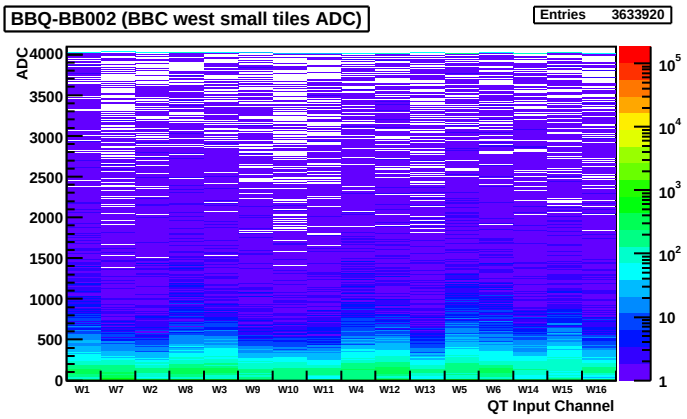

MIX-TF101
Entries 1362720
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

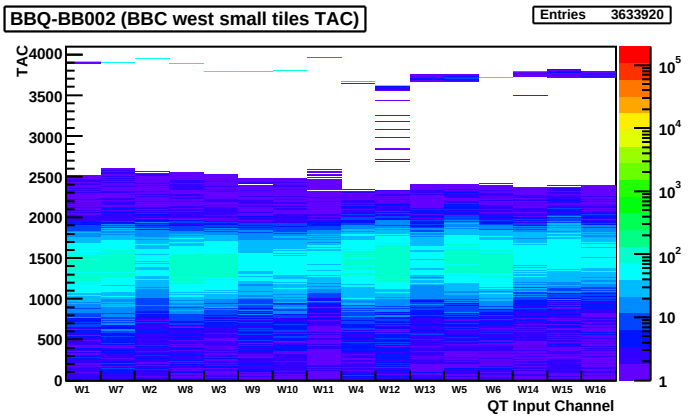

L1-TF201
Entries 227120
TOF total mult

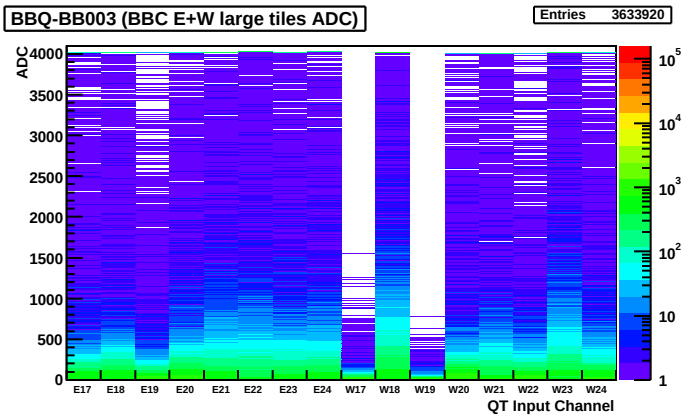

L1-TF201
Entries 1362720
Threshold bits
137156
138346
139997
138529
136522
129743
89964
88774
87123
88591
90598
97377
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

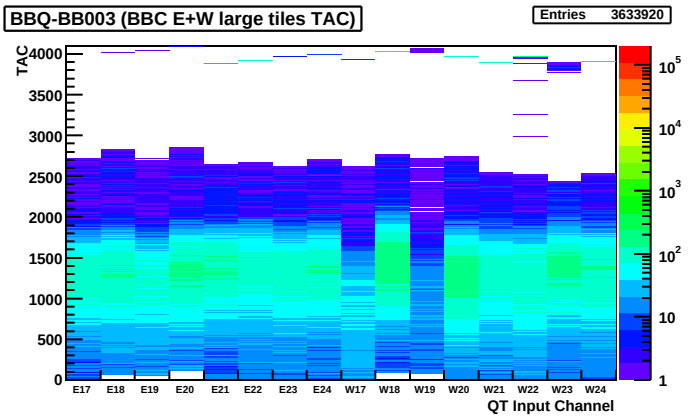

BBQ-BB001 (BBC east small tiles ADC)
Entries 3633920
ADC
QT Input Channel

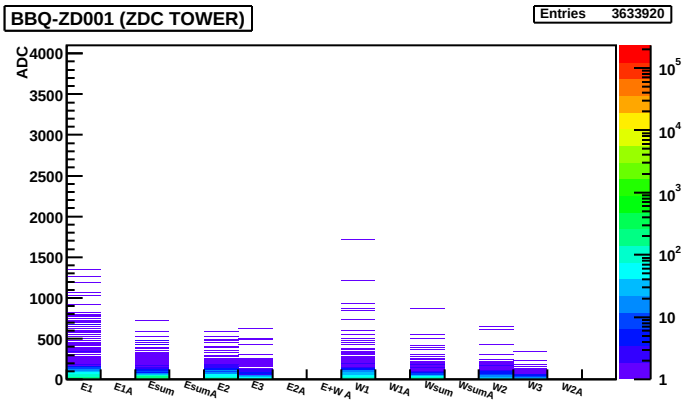

BBQ-BB001 (BBC east small tiles TAC)
Entries 3633920
TAC
QT Input Channel

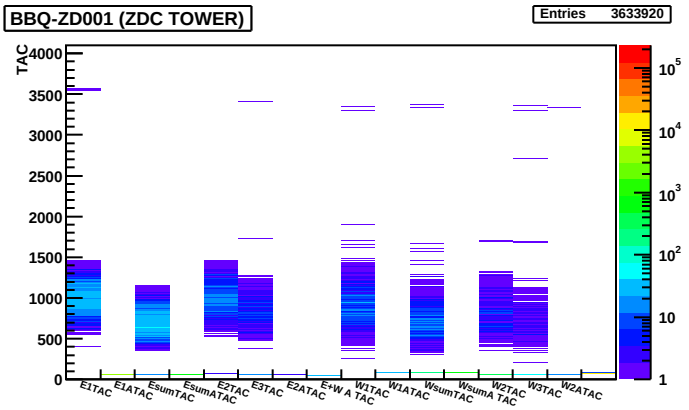

BBQ-BB002 (BBC west small tiles ADC)
Entries 3633920
ADC
QT Input Channel

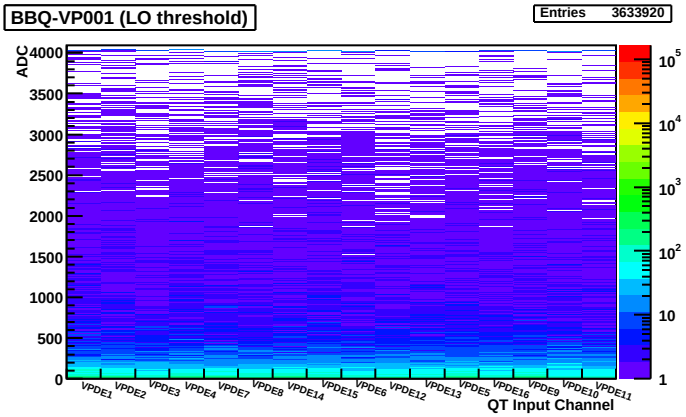

BBQ-BB002 (BBC west small tiles TAC)
Entries 3633920
TAC
QT Input Channel

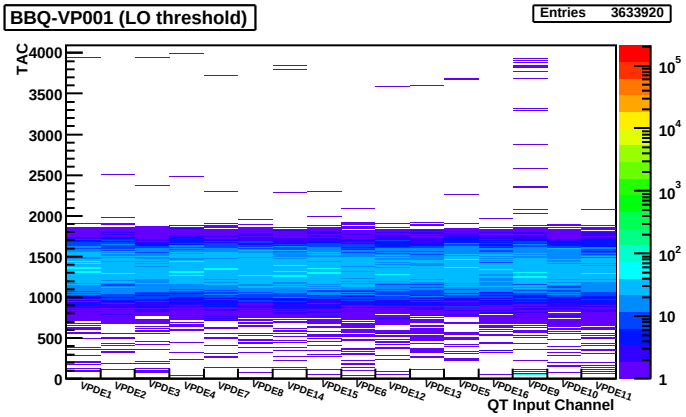

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3633920
ADC
QT Input Channel

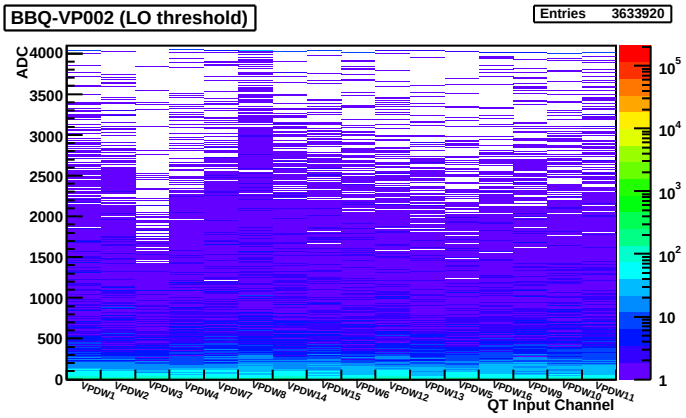

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3633920
TAC
QT Input Channel

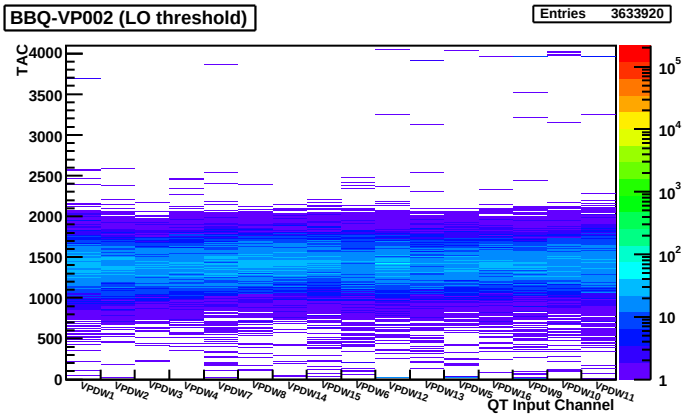

BBQ-ZD001 (ZDC TOWER)
Entries 3633920
ADC

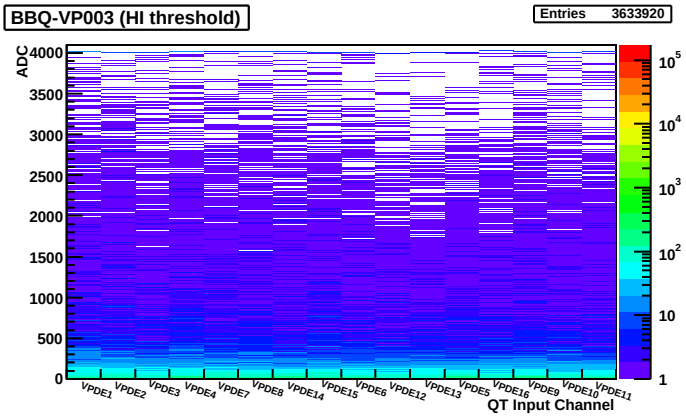

BBQ-ZD001 (ZDC TOWER)
Entries 3633920
TAC

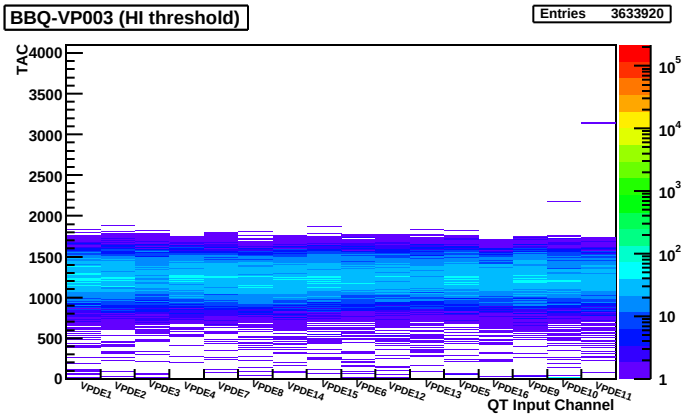

BBQ-VP001 (LO threshold)
Entries 3633920
ADC
QT Input Channel

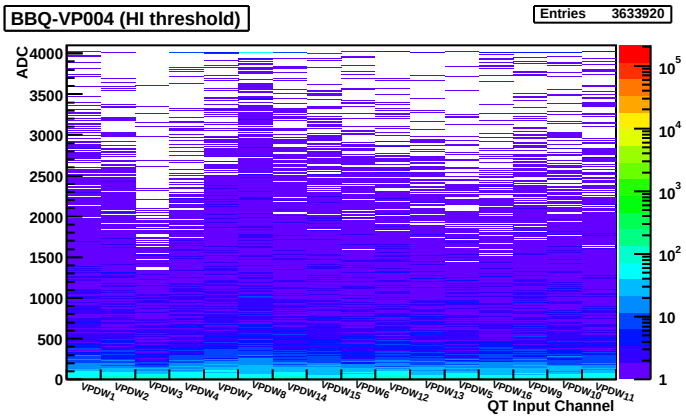

BBQ-VP001 (LO threshold)
Entries 3633920
TAC
QT Input Channel

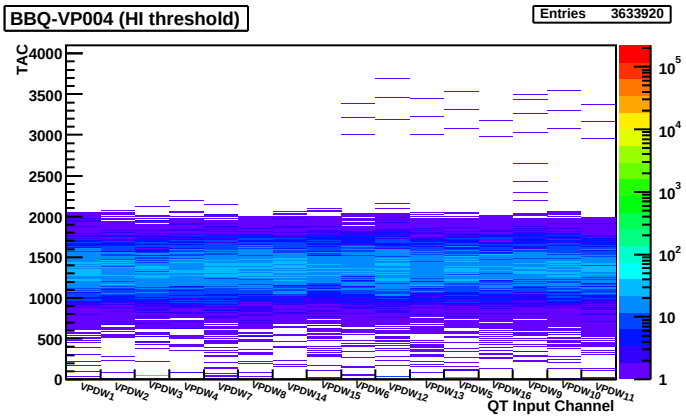

BBQ-VP002 (LO threshold)
Entries 3633920
ADC
QT Input Channel

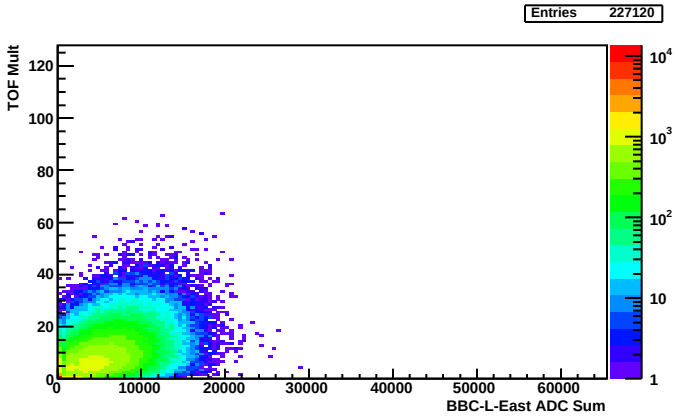

BBQ-VP002 (LO threshold)
Entries 3633920
TAC
QT Input Channel

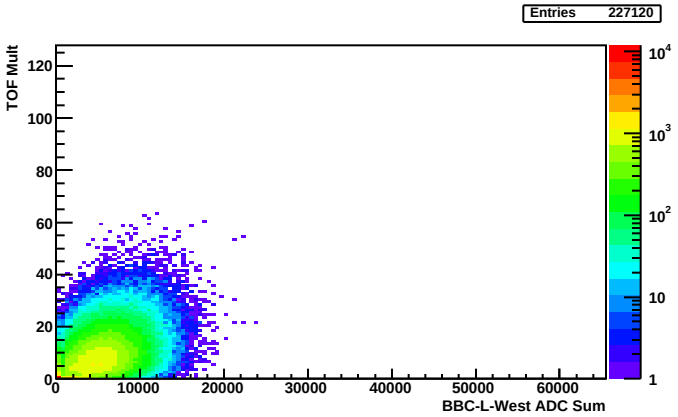

BBQ-VP003 (HI threshold)
Entries 3633920
ADC
QT Input Channel

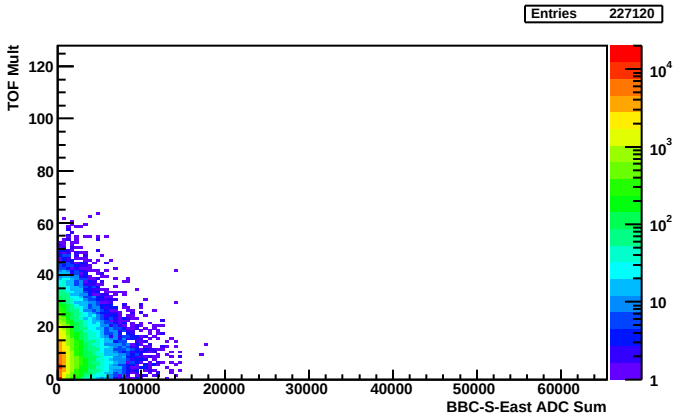

BBQ-VP003 (HI threshold)
Entries 3633920
TAC
QT Input Channel

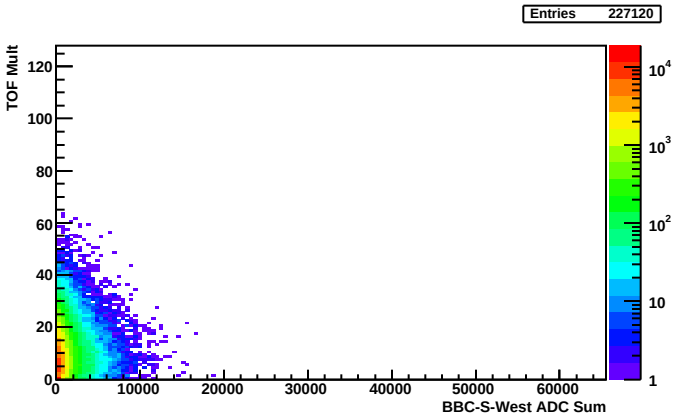

BBQ-VP004 (HI threshold)
Entries 3633920
ADC
QT Input Channel

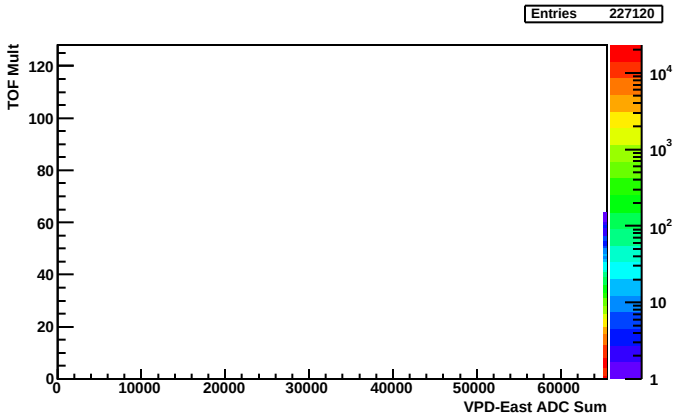

BBQ-VP004 (HI threshold)
Entries 3633920
TAC
QT Input Channel

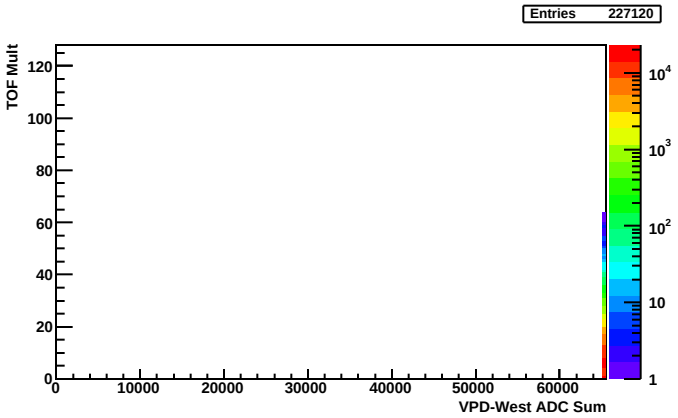

Entries 227120
TOF Mult
BBC-L-East ADC Sum

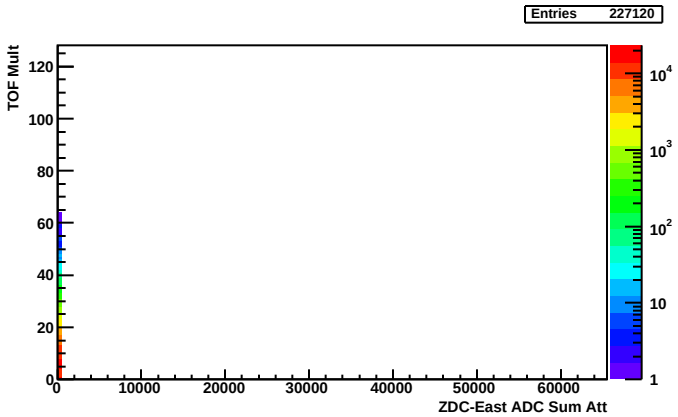

Entries 227120
TOF Mult
BBC-L-West ADC Sum

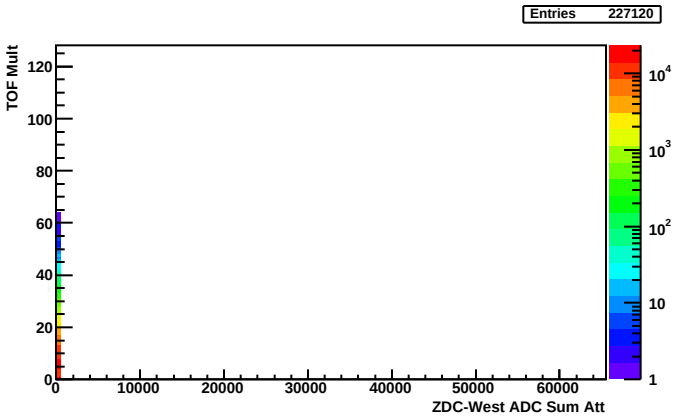

Entries 227120
TOF Mult
BBC-S-East ADC Sum

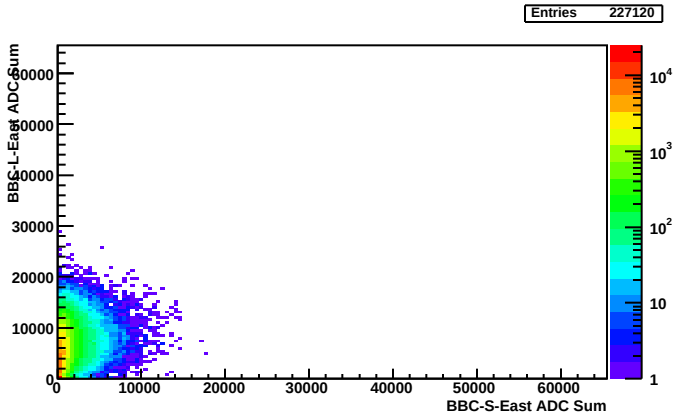

Entries 227120
TOF Mult
BBC-S-West ADC Sum

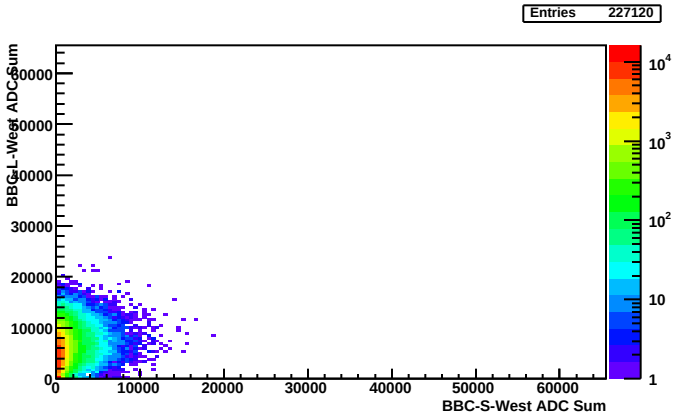

Entries 227120
TOF Mult
VPD-East ADC Sum

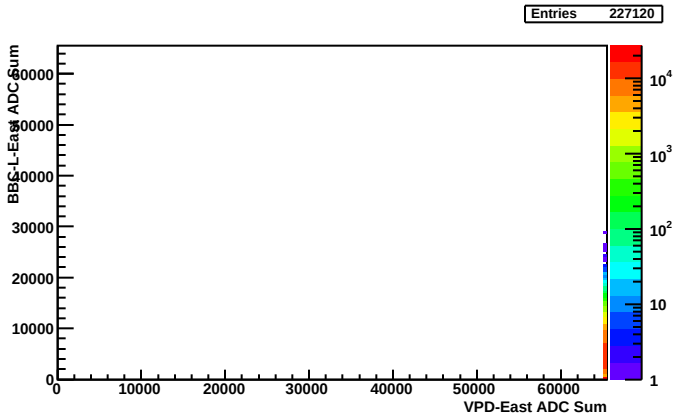

Entries 227120
TOF Mult
VPD-West ADC Sum

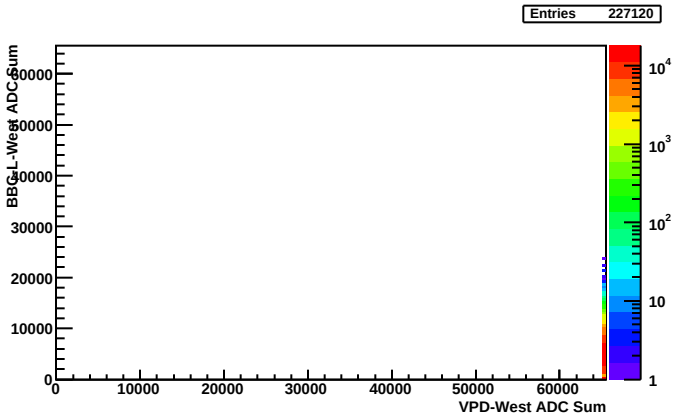

Entries 227120
TOF Mult
ZDC-East ADC Sum Att

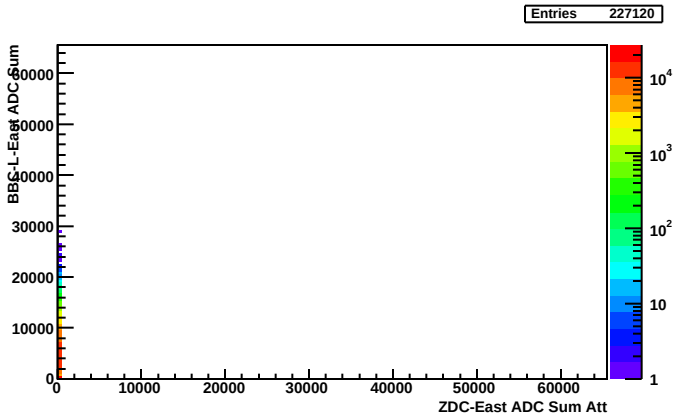

Entries 227120
TOF Mult
ZDC-West ADC Sum Att

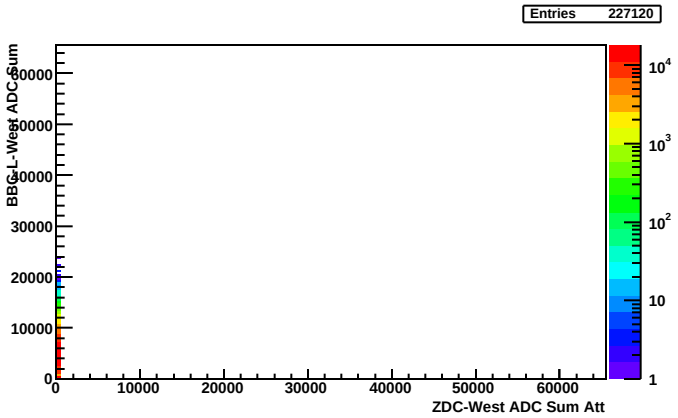

Entries 227120
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 227120
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 227120
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 227120
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 227120
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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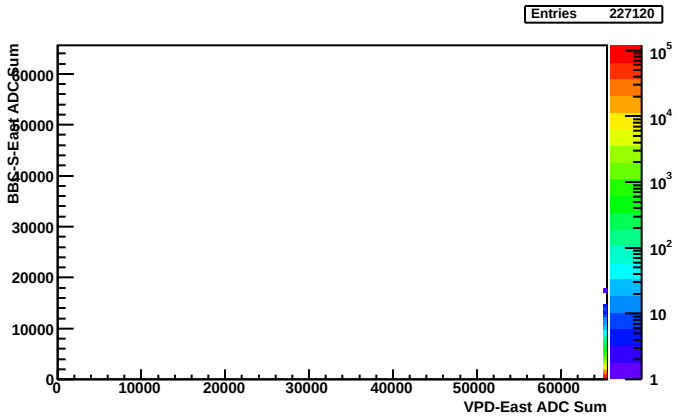
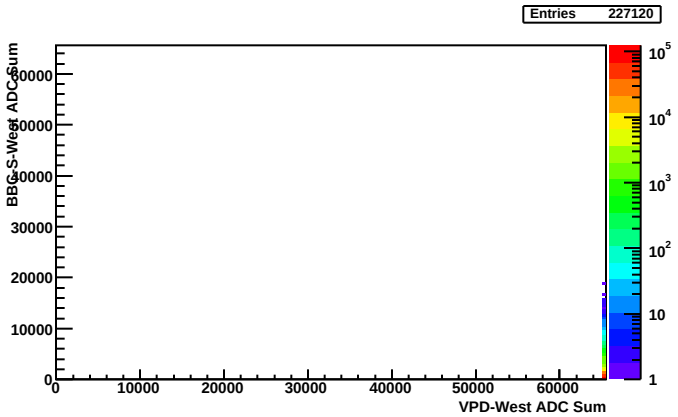
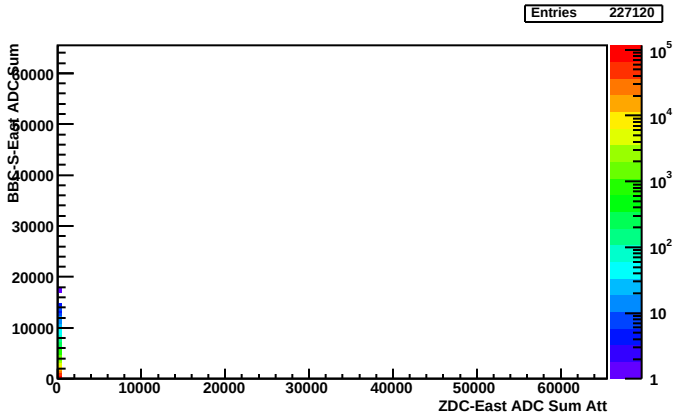
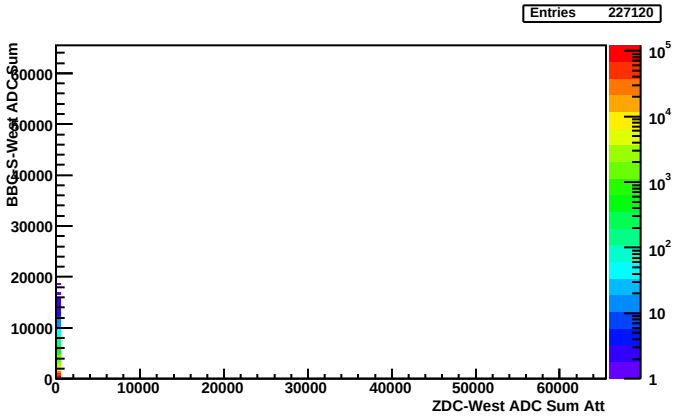
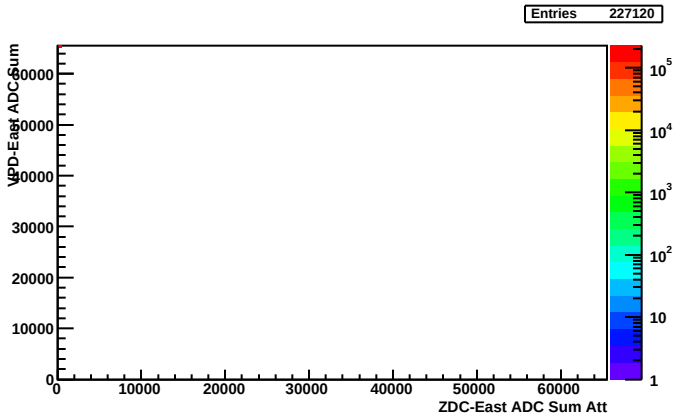
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

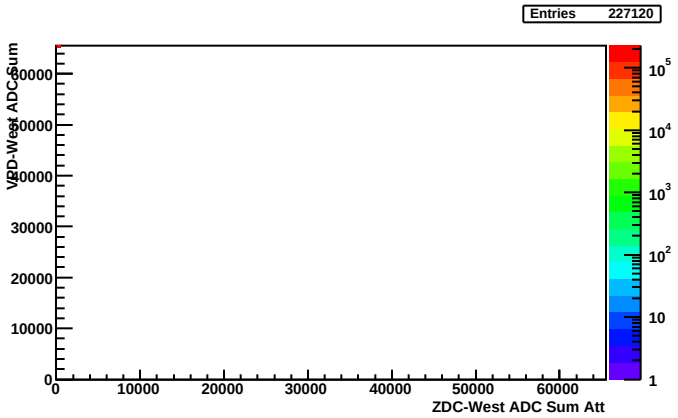

Entries 227120
BBC-S-East ADC Sum
VPD-East ADC Sum

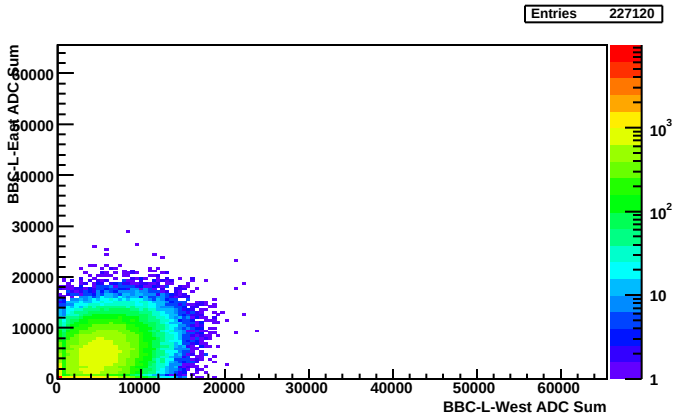

Entries 227120
BBC-S-West ADC Sum
VPD-West ADC Sum

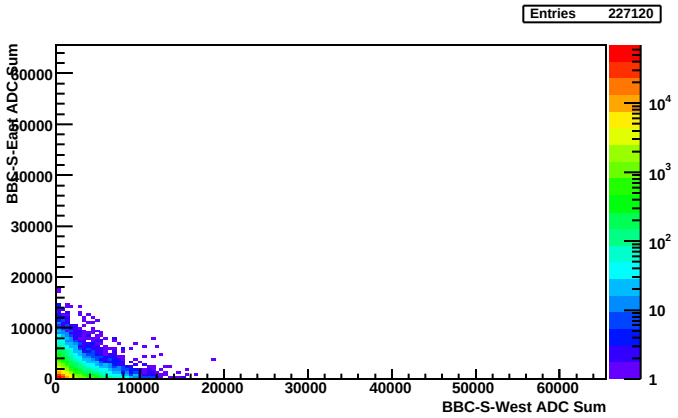

Entries 227120
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

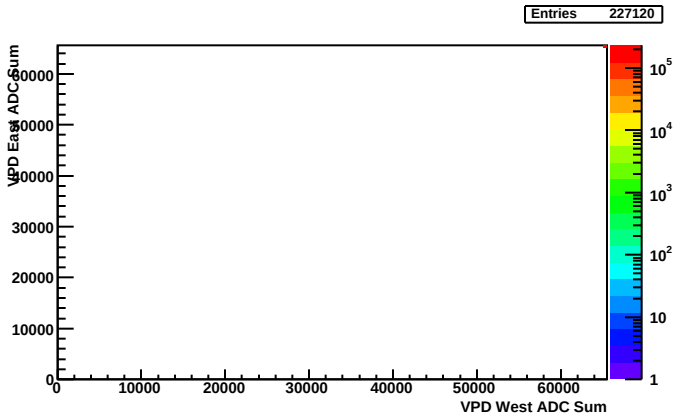

Entries 227120
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

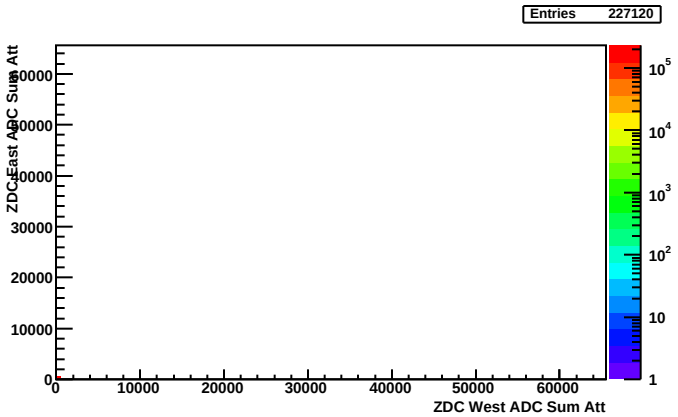

Entries 227120
VPD-East ADC Sum
ZDC-East ADC Sum Att

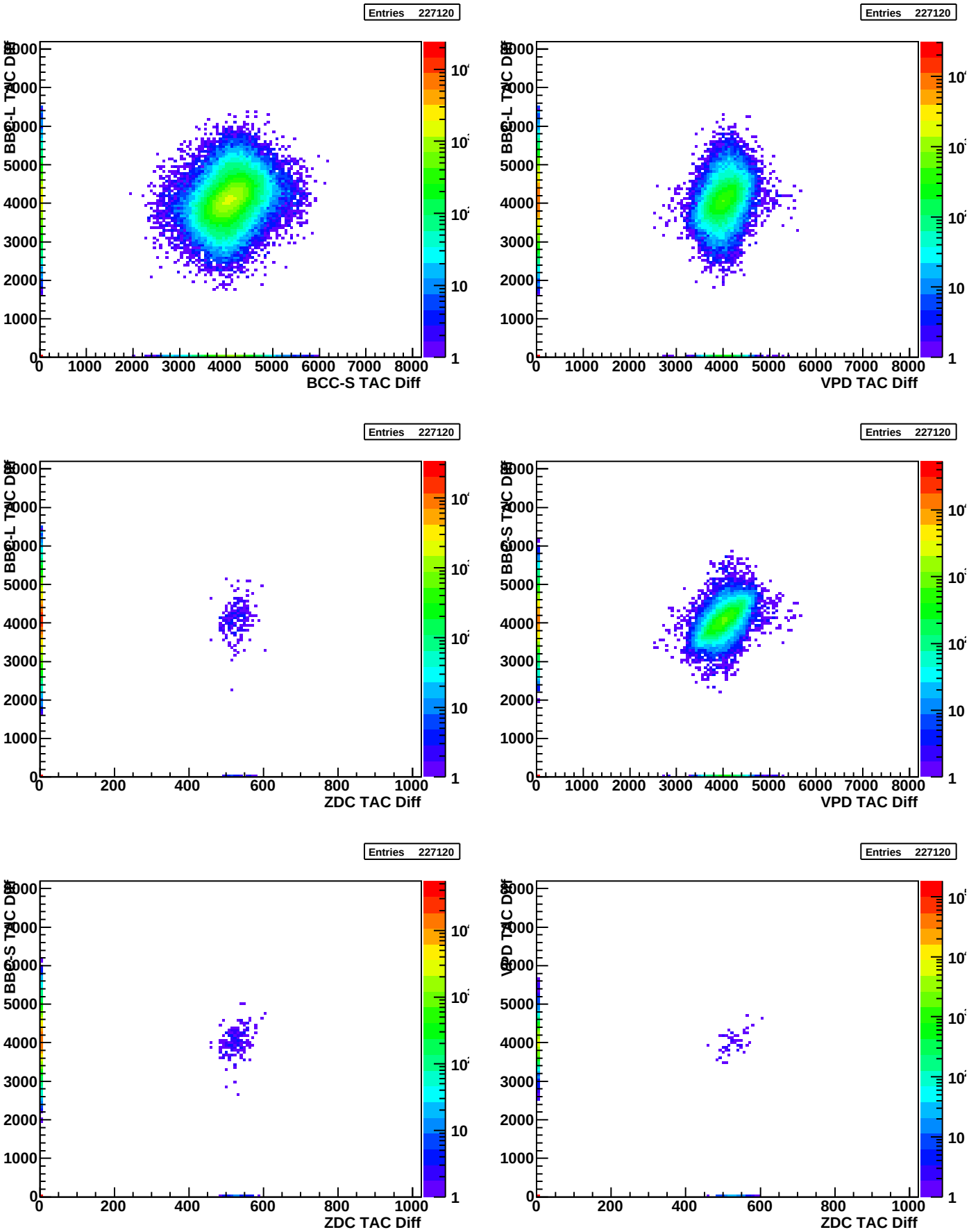

Entries 227120
VPD-West ADC Sum
ZDC-West ADC Sum Att

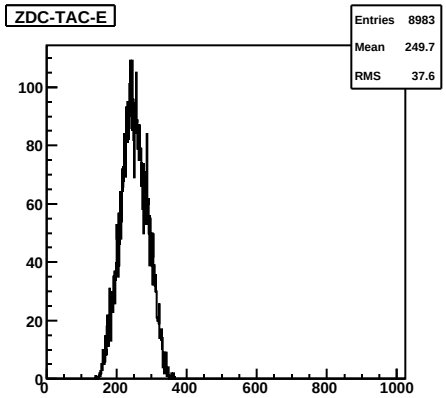

Entries 227120
BBC-L-East ADC Sum
BBC-L-West ADC Sum

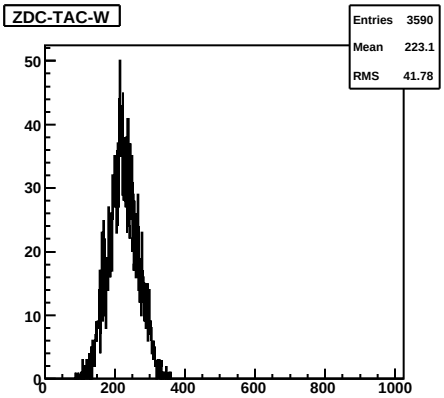

Entries 227120
BBC-S-East ADC Sum
BBC-S-West ADC Sum

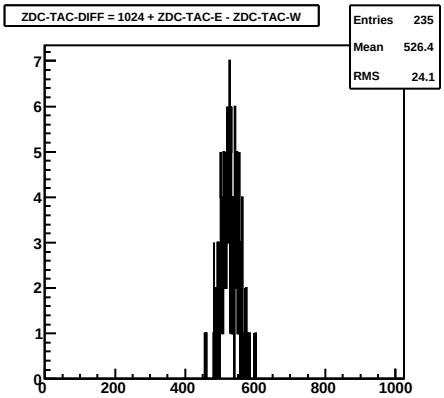

Entries 227120
VPD East ADC Sum
VPD West ADC Sum

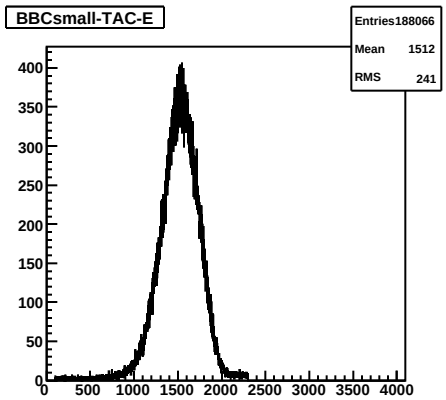

Entries 227120
ZDC East ADC Sum Att
ZDC West ADC Sum Att

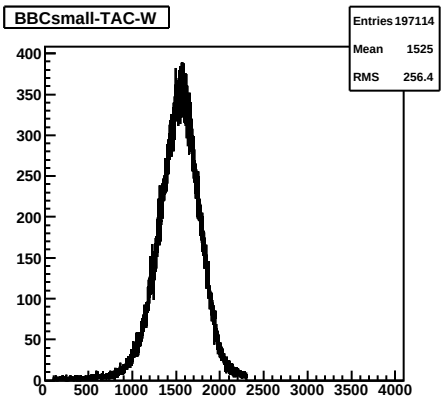

Entries 227120
BBC-L TAC Diff
BCC-S TAC Diff
Entries 227120
BBC-L TAC Diff
VPD TAC Diff
Entries 227120
BBC-L TAC Diff
ZDC TAC Diff
Entries 227120
BBC-S TAC Diff
VPD TAC Diff
Entries 227120
BBC-S TAC Diff
ZDC TAC Diff
Entries 227120
VPD TAC Diff
ZDC TAC Diff

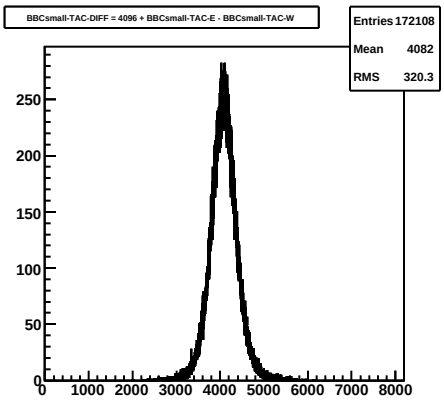

ZDC-TAC-E
Entries 8983
Mean 249.7
RMS 37.6

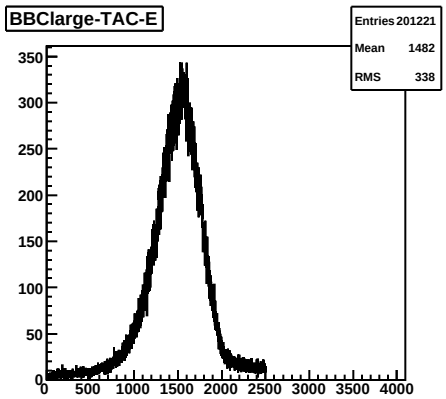

ZDC-TAC-W
Entries 3590
Mean 223.1
RMS 41.78

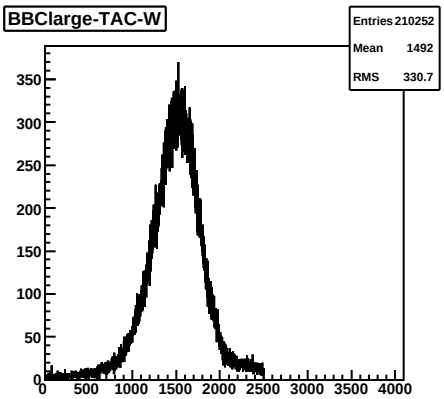

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 235
Mean 526.4
RMS 24.1

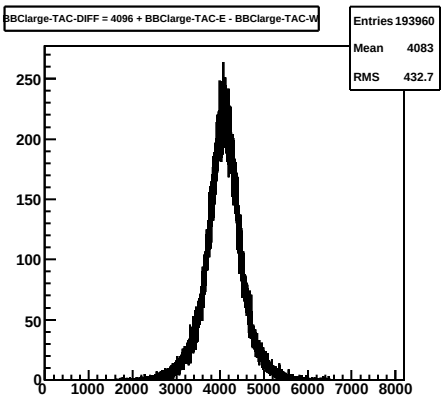

BBCsmall-TAC-E
Entries 188066
Mean 1512
RMS 241

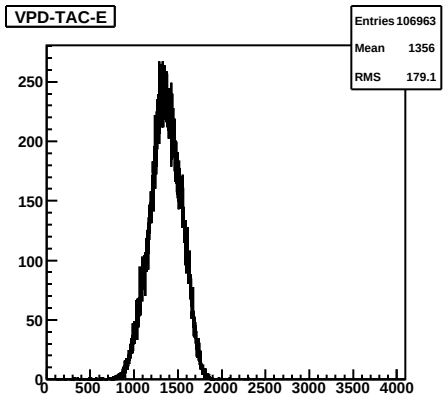

BBCsmall-TAC-W
Entries 197114
Mean 1525
RMS 256.4

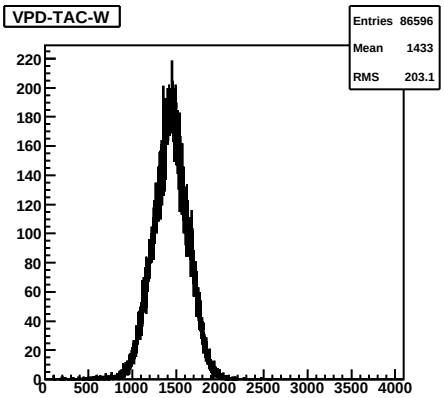

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 172108
Mean 4082
RMS 320.3

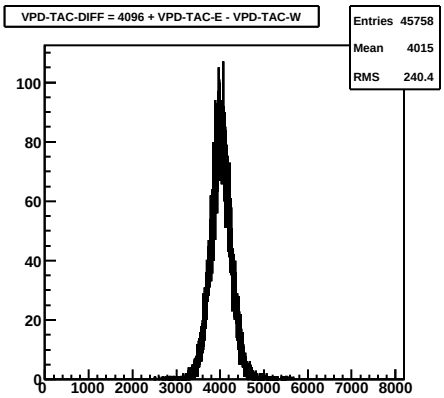

BBClarge-TAC-E
Entries 201221
Mean 1482
RMS 338

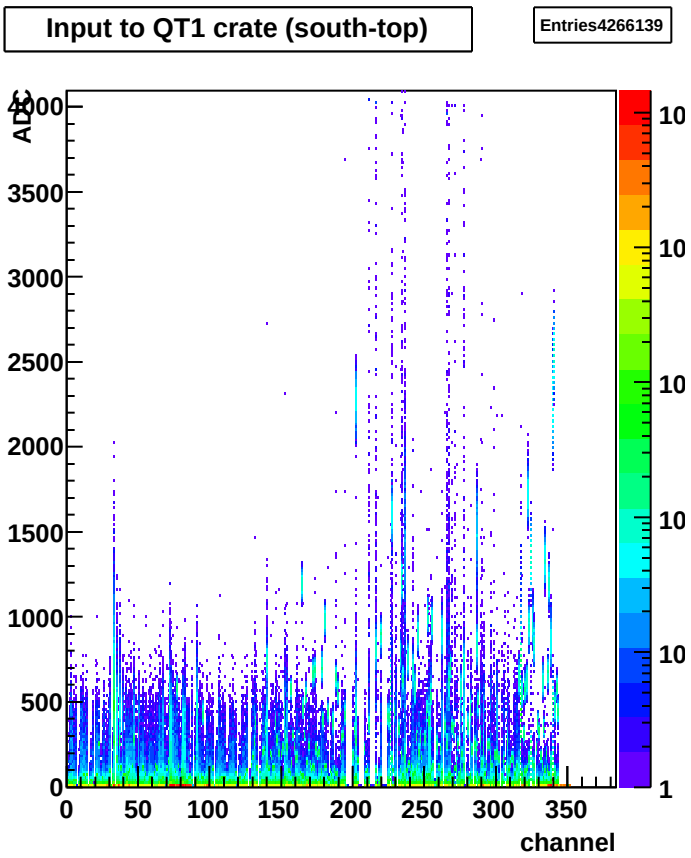

BBClarge-TAC-W
Entries 210252
Mean 1492
RMS 330.7

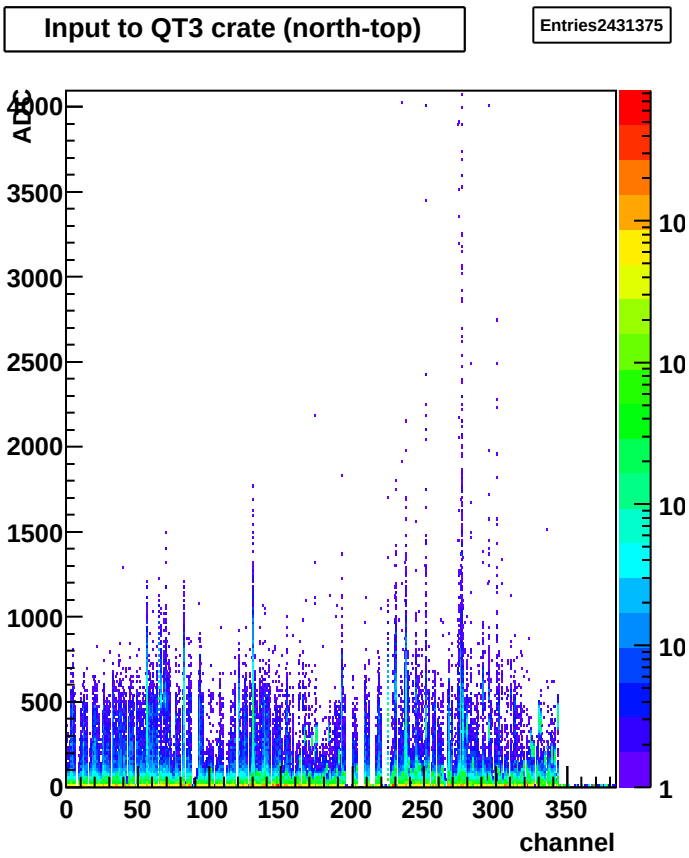

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 193960
Mean 4083
RMS 432.7

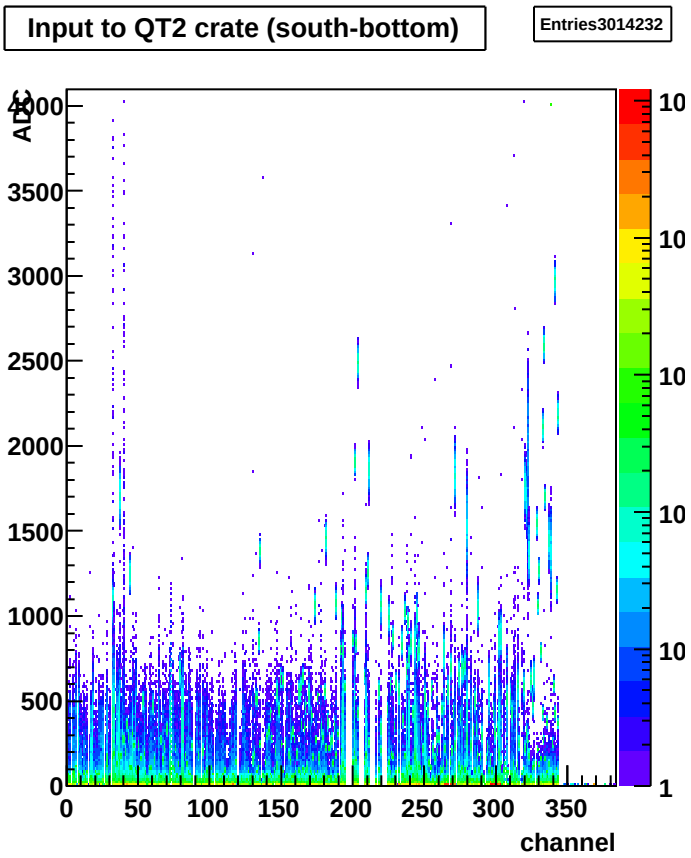

VPD-TAC-E
Entries 106963
Mean 1356
RMS 179.1

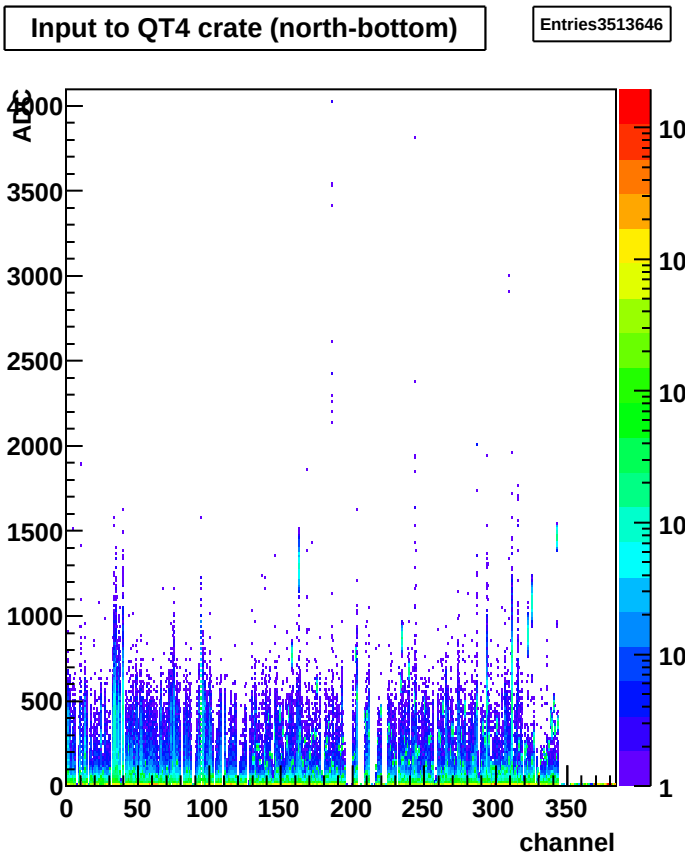

VPD-TAC-W
Entries 86596
Mean 1433
RMS 203.1

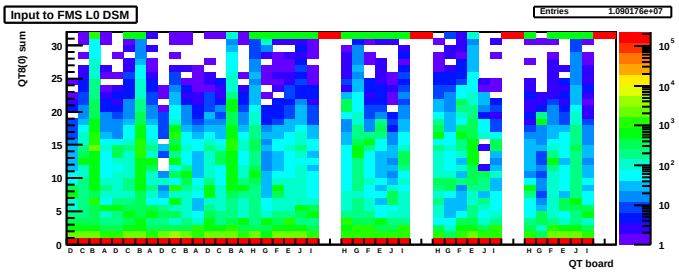

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 45758
Mean 4015
RMS 240.4

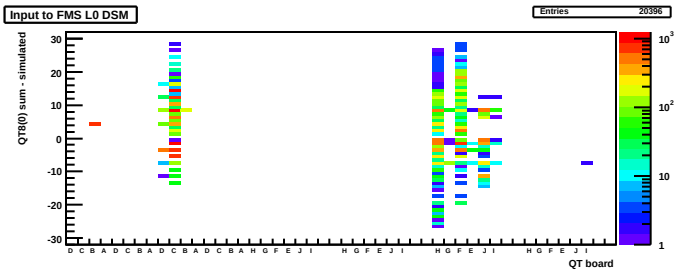

Input to QT1 crate (south-top)
Entries4266139
ADC
channel

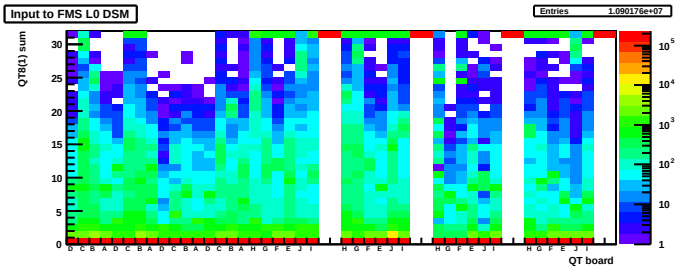

Input to QT3 crate (north-top)
Entries2431375
ADC
channel

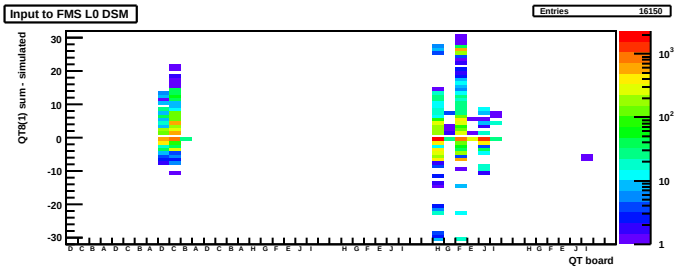

Input to QT2 crate (south-bottom)
Entries3014232
ADC
channel

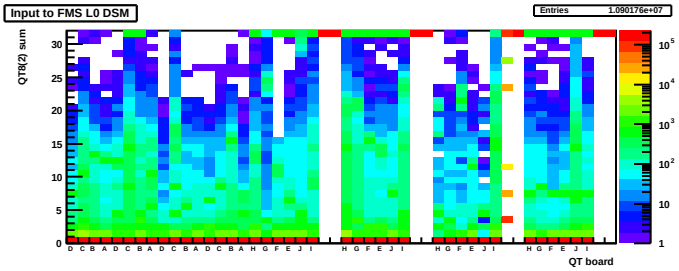

Input to QT4 crate (north-bottom)
Entries3513646
ADC
channel

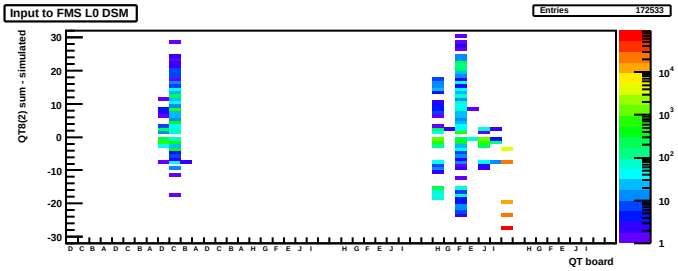

Input to FMS L0 DSM
Entries 1.090176e+07
QT8(0) sum
QT board

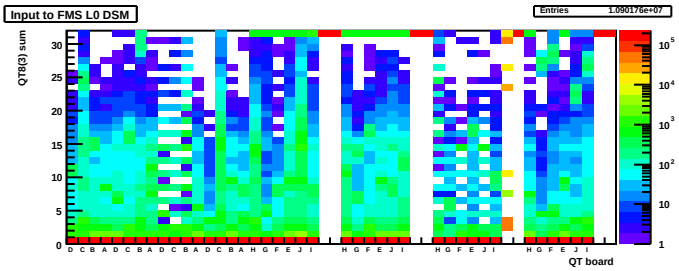

Input to FMS L0 DSM
Entries 20396
QT8(0) sum - simulated
QT board

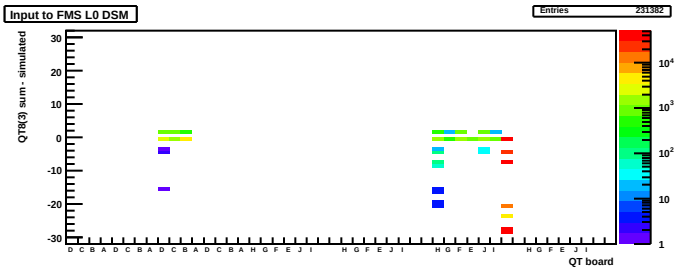

Input to FMS L0 DSM
Entries 1.090176e+07
QT8(1) sum
QT board

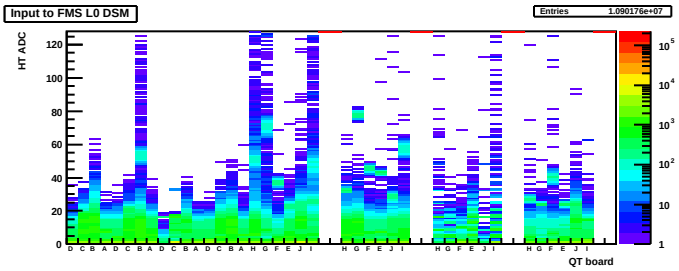

Input to FMS L0 DSM
Entries 16150
QT8(1) sum - simulated
QT board

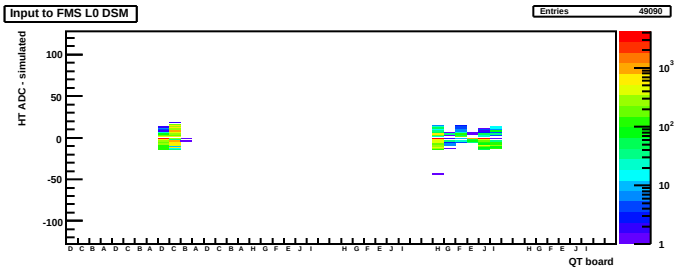

Input to FMS L0 DSM
Entries 1.090176e+07
QT8(2) sum
QT board

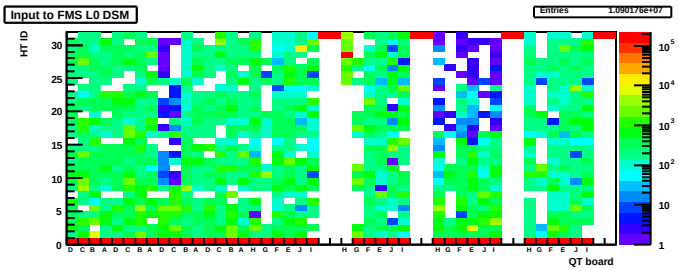

Input to FMS L0 DSM
Entries 172533
QT8(2) sum - simulated
QT board

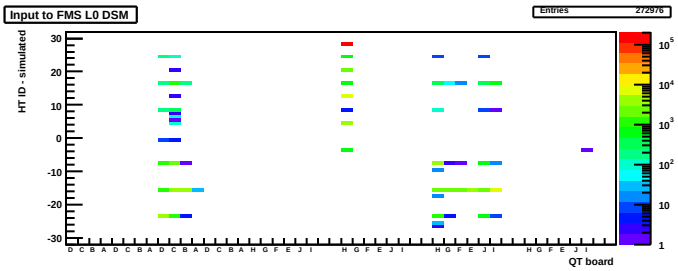

Input to FMS L0 DSM
Entries 1.090176e+07
QT8(3) sum
QT board

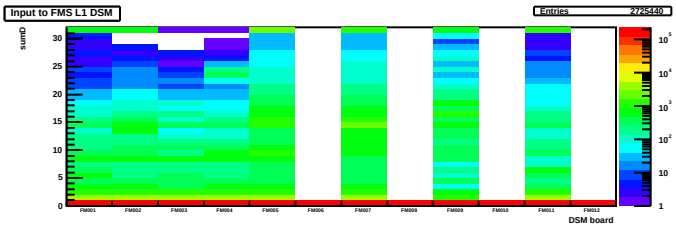

Input to FMS L0 DSM
Entries 231382
QT8(3) sum - simulated
QT board

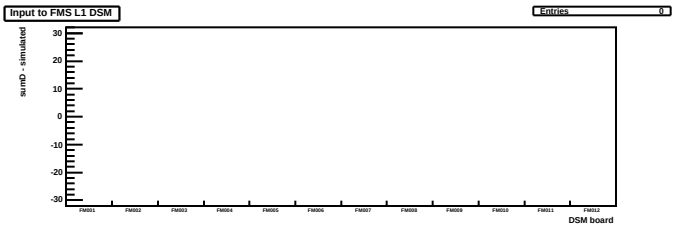

Input to FMS L0 DSM
Entries 1.090176e+07
HT ADC
QT board

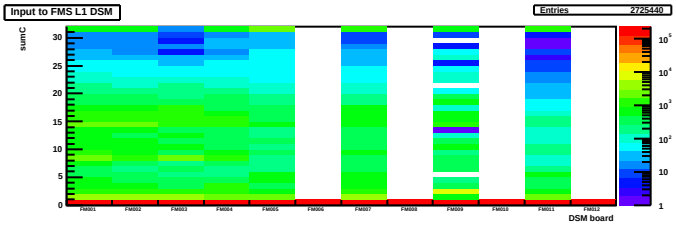

Input to FMS L0 DSM
Entries 49090
HT ADC - simulated
QT board

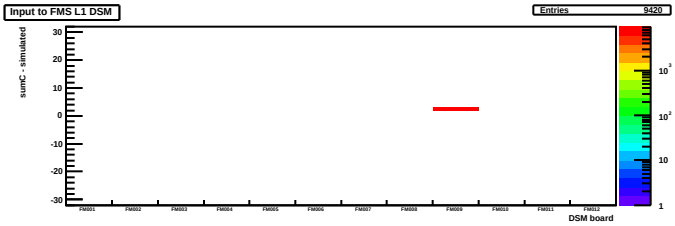

Input to FMS L0 DSM
Entries 1.090176e+07
HT ID
QT board

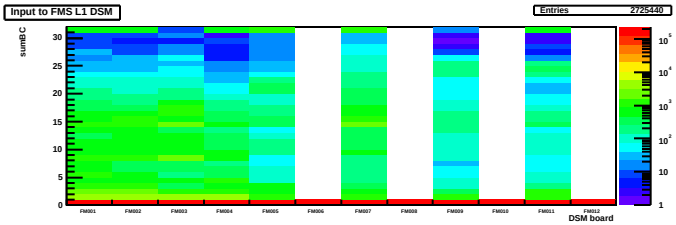

Input to FMS L0 DSM
Entries 272976
HT ID - simulated
QT board

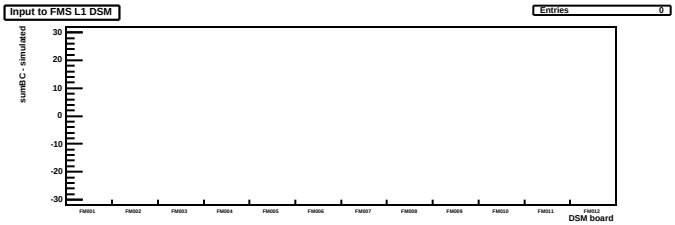

Input to FMS L1 DSM
Entries 2725440
sumD
DSM board

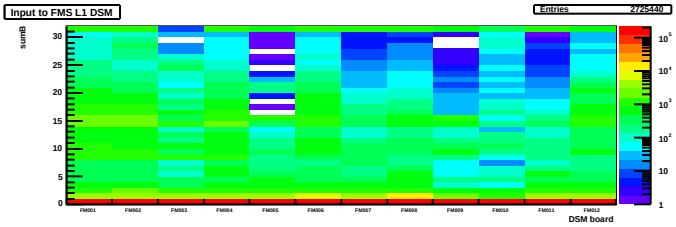

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

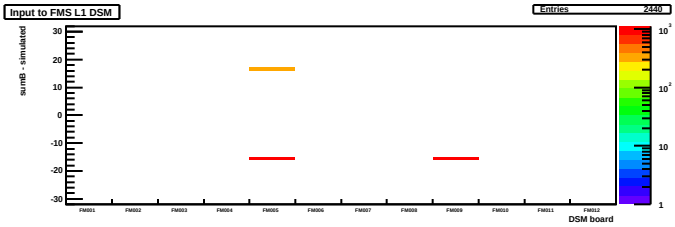

Input to FMS L1 DSM
Entries 2725440
sumC
DSM board

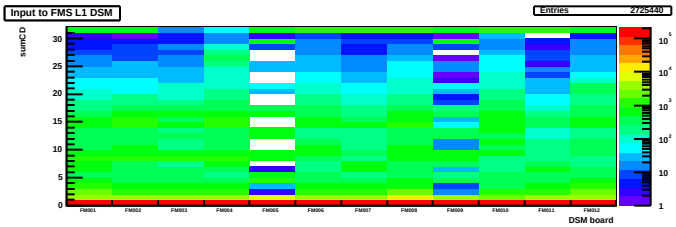

Input to FMS L1 DSM
Entries 9420
sumC - simulated
DSM board

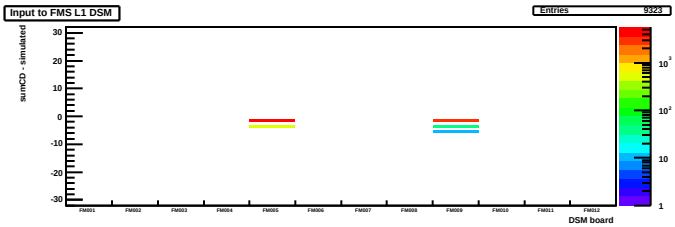

Input to FMS L1 DSM
Entries 2725440
sumBC
DSM board

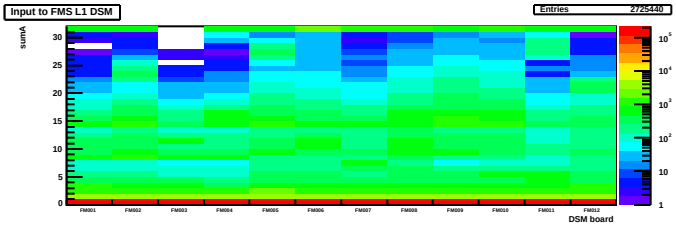

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

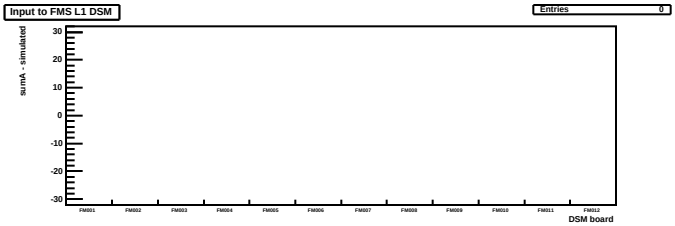

Input to FMS L1 DSM
Entries 2725440
sumB
DSM board

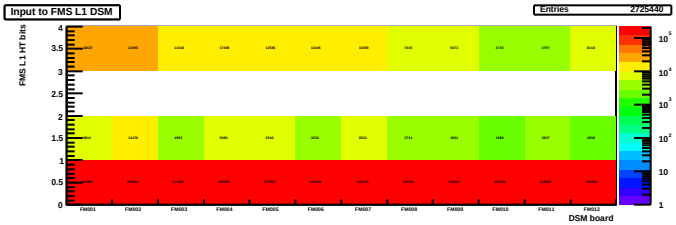

Input to FMS L1 DSM
Entries 2440
sumB - simulated
DSM board

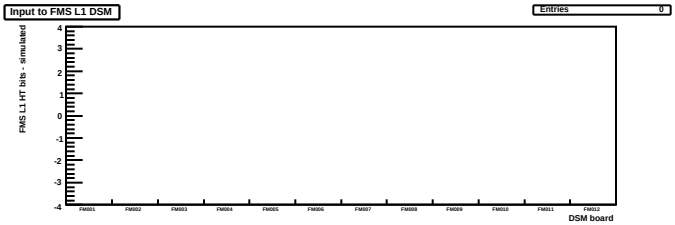

Input to FMS L1 DSM
Entries 2725440
sumCD
DSM board

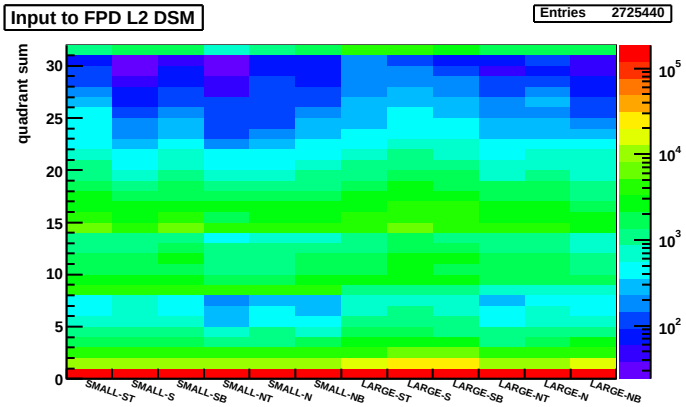

Input to FMS L1 DSM
Entries 9323
sumCD - simulated
DSM board

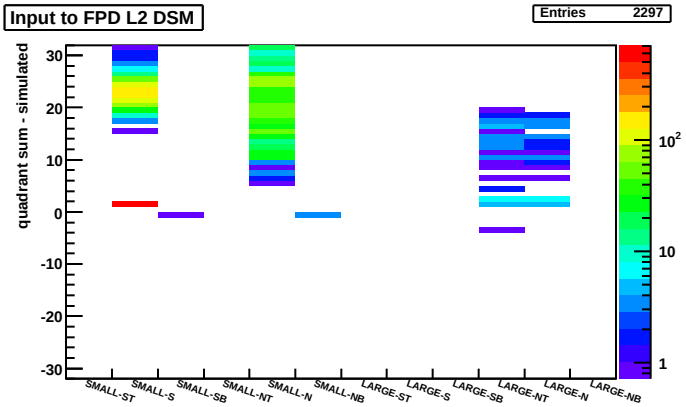

Input to FMS L1 DSM
Entries 2725440
sumA
DSM board

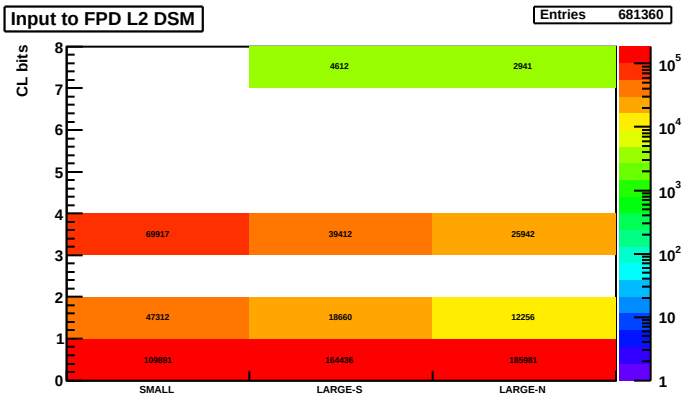

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

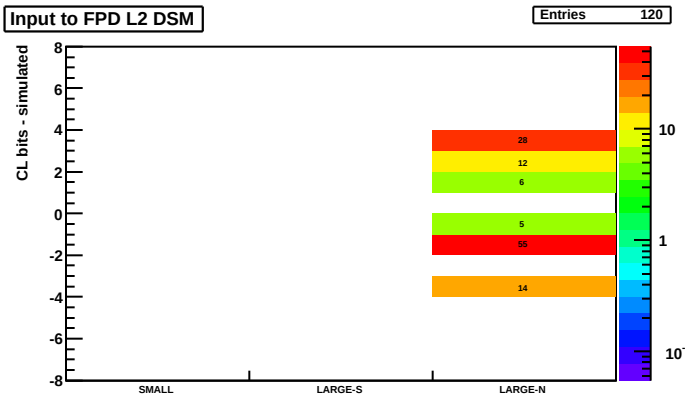

Input to FMS L1 DSM
Entries 2725440
FMS L1 HT bits
DSM board

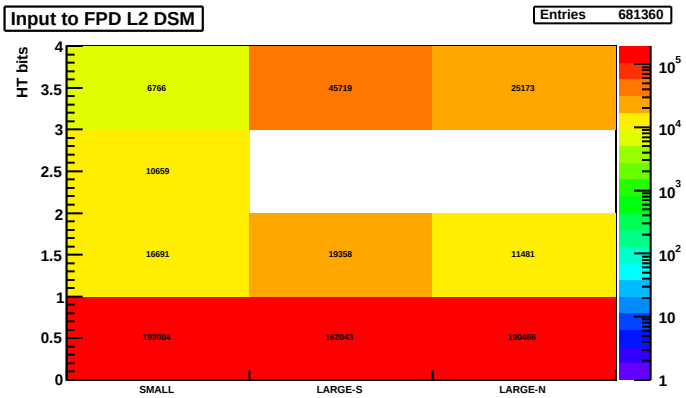

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

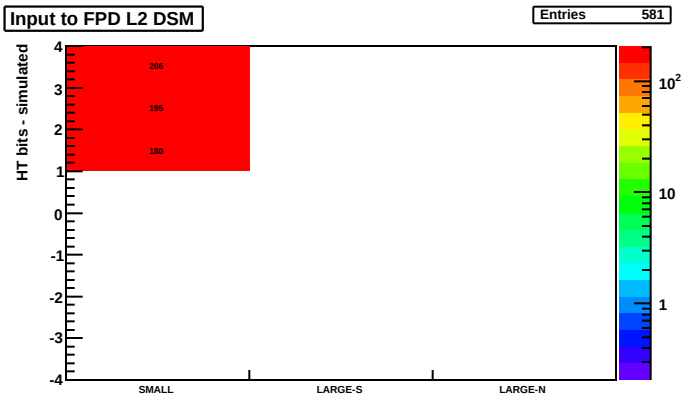

Input to FPD L2 DSM
Entries 2725440
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

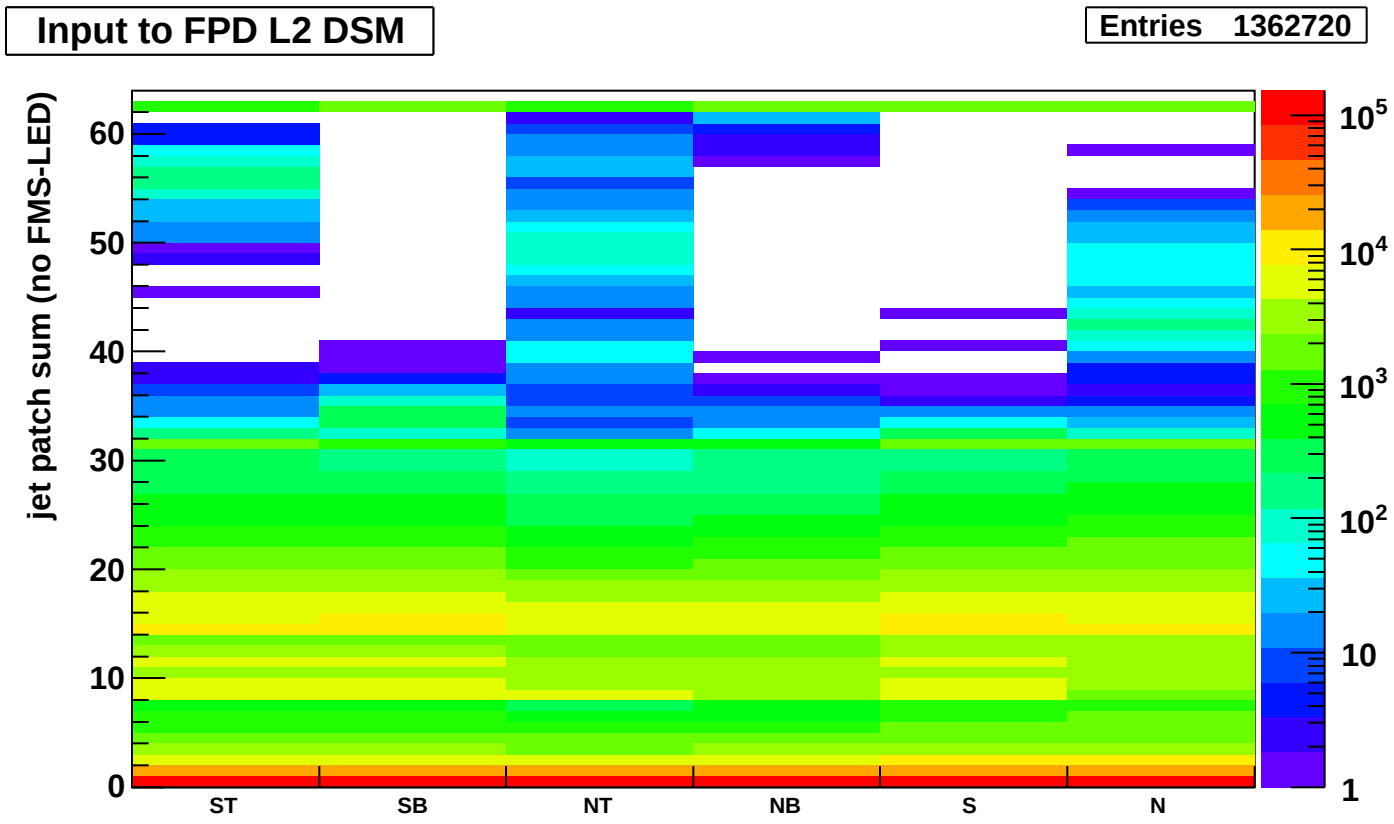

Input to FPD L2 DSM
Entries 2297
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

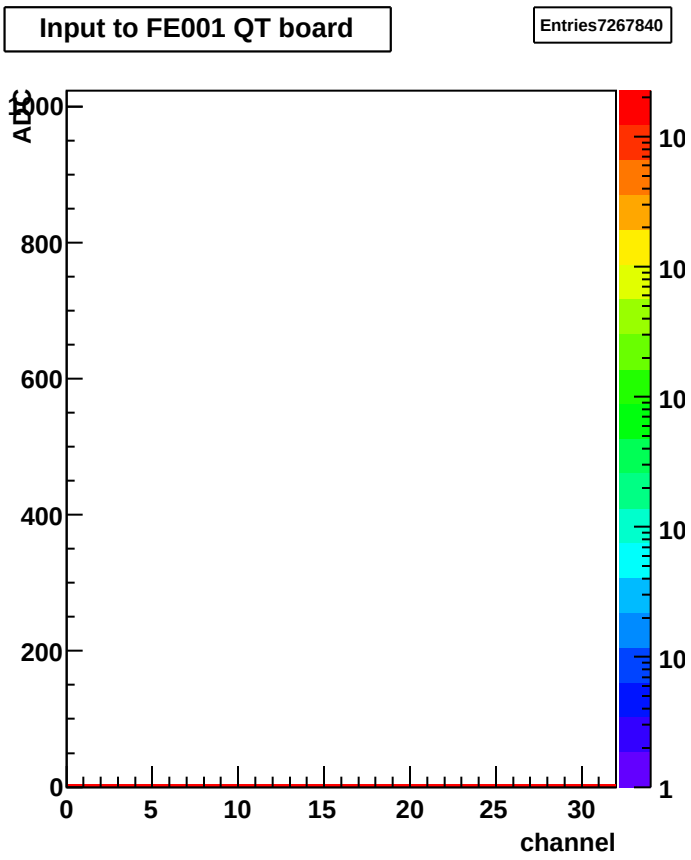

Input to FPD L2 DSM
Entries 681360
CL bits
4612
2941
69917
39412
25942
47312
18660
12256
109891
164436
185981
SMALL
LARGE-S
LARGE-N

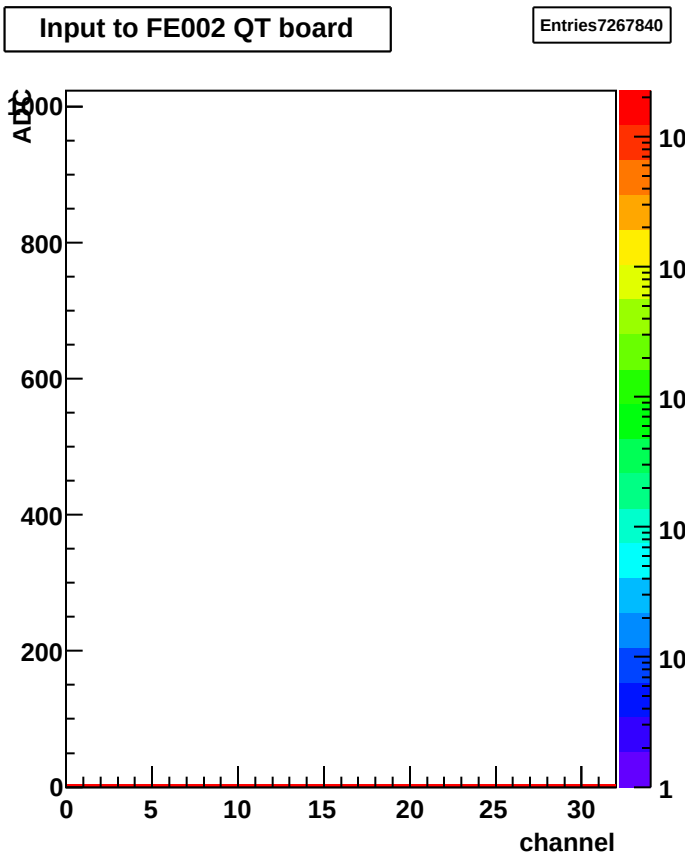

Input to FPD L2 DSM
Entries 120
CL bits - simulated
28
12
6
5
55
14
SMALL
LARGE-S
LARGE-N

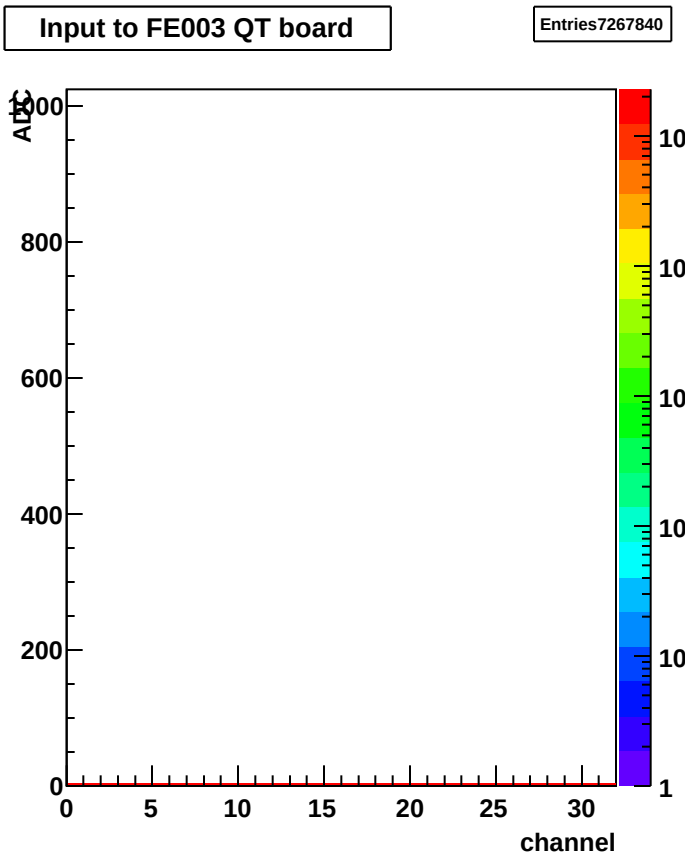

Input to FPD L2 DSM
Entries 681360
HT bits
6766
45719
25173
10659
16691
19358
11481
193004
162043
190466
SMALL
LARGE-S
LARGE-N

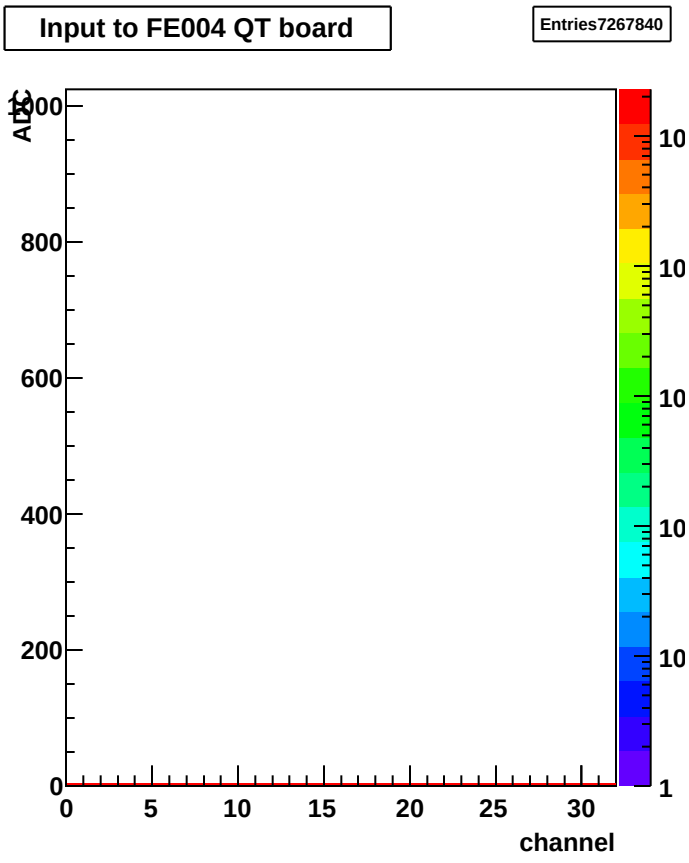

Input to FPD L2 DSM
Entries 581
HT bits - simulated
206
195
180
SMALL
LARGE-S
LARGE-N

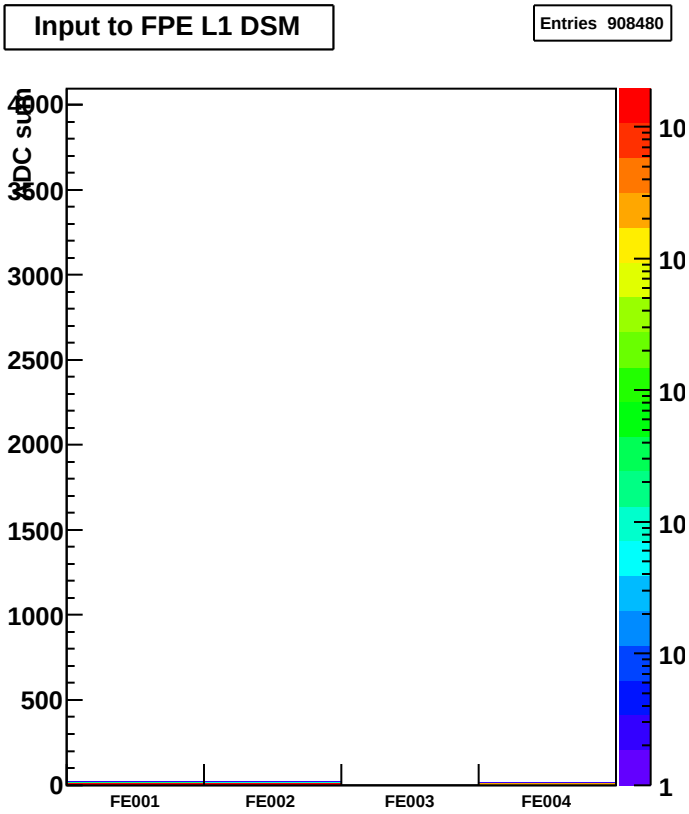

Input to FPD L2 DSM
Entries 1362720
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

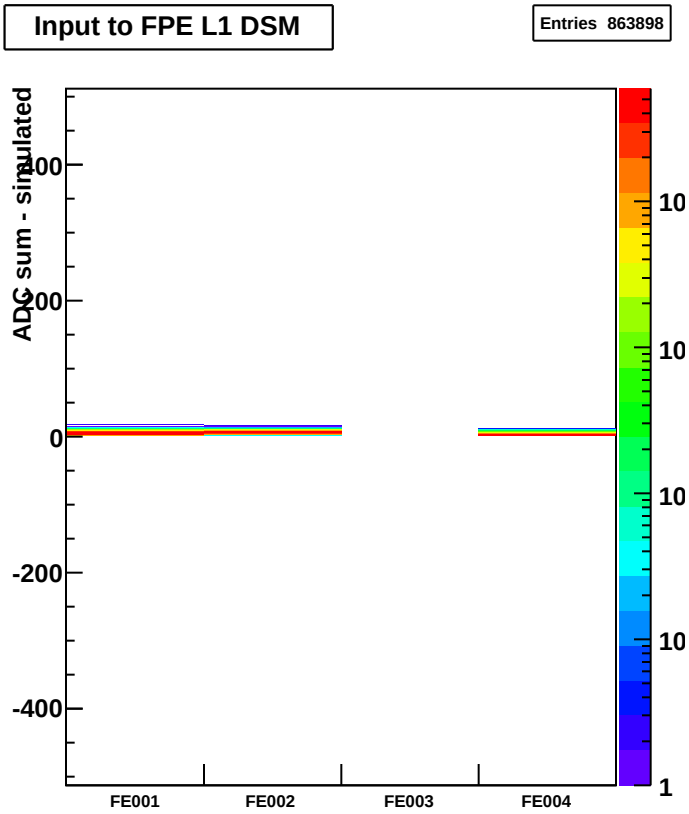

Input to FE001 QT board
Entries7267840
ADC
channel

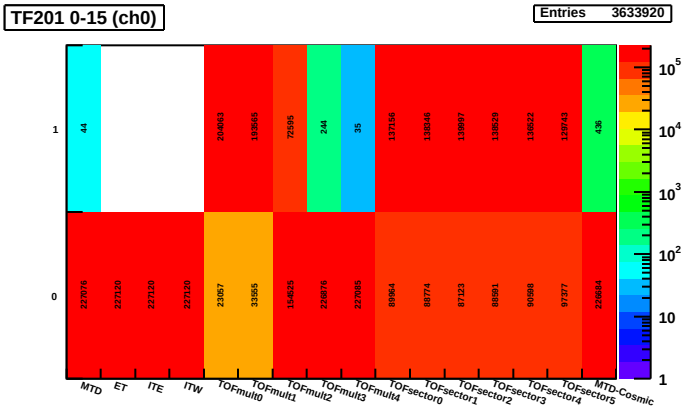

Input to FE002 QT board
Entries7267840
ADC
channel

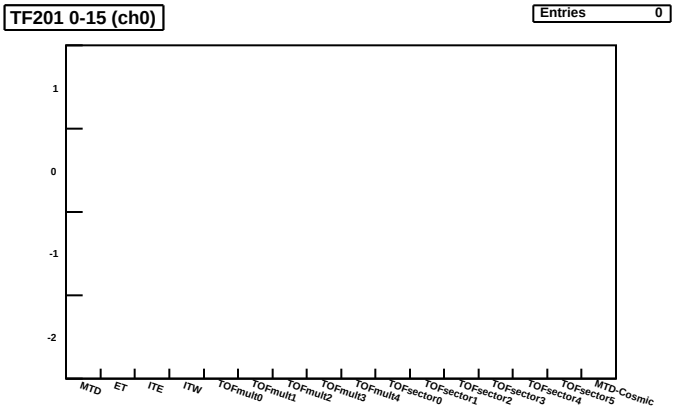

Input to FE003 QT board
Entries7267840
ADC
channel

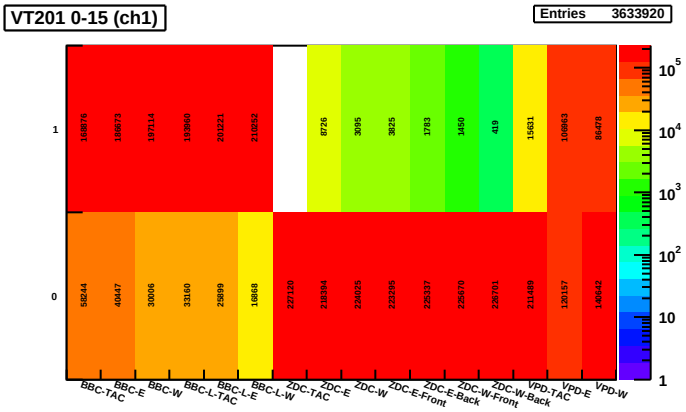

Input to FE004 QT board
Entries7267840
ADC
channel

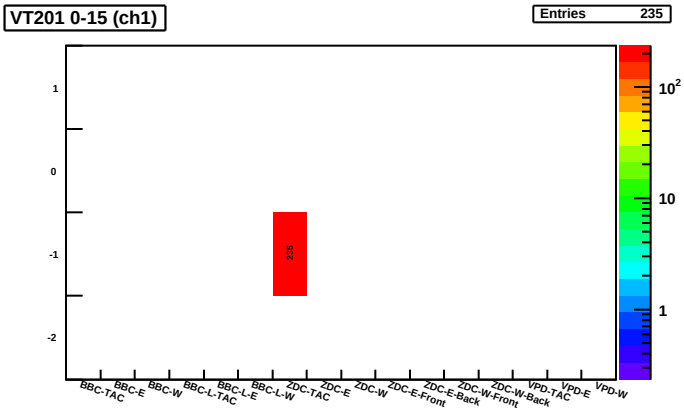

Input to FPE L1 DSM
Entries 908480
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

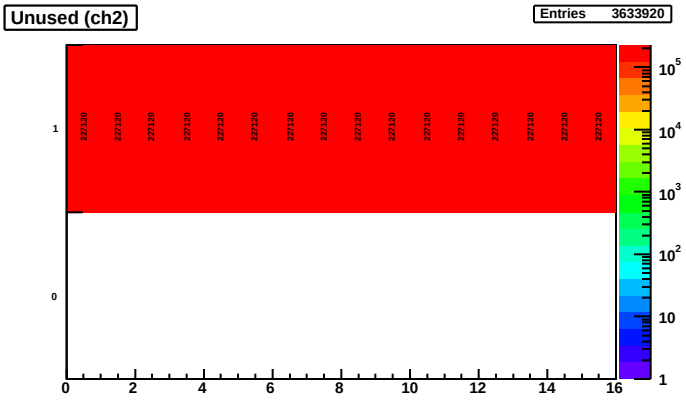

Input to FPE L1 DSM
Entries 863898
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

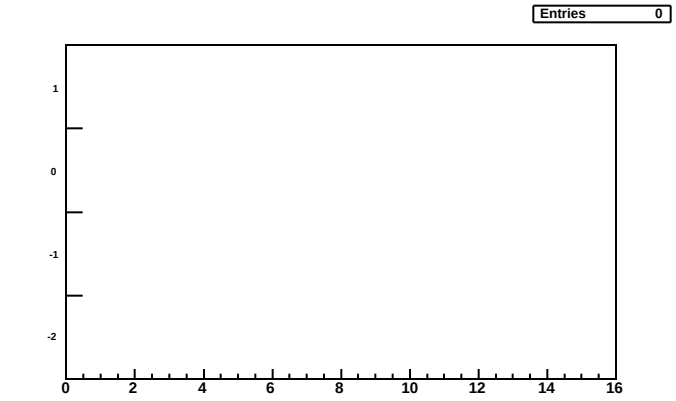

TF201 0-15 (ch0)
Entries 3633920
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3633920
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 235
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3633920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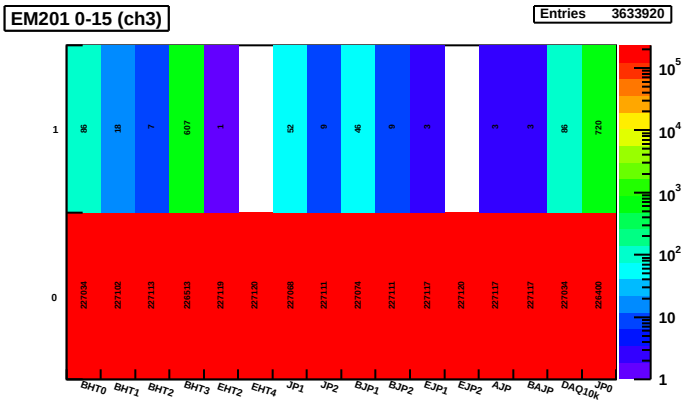
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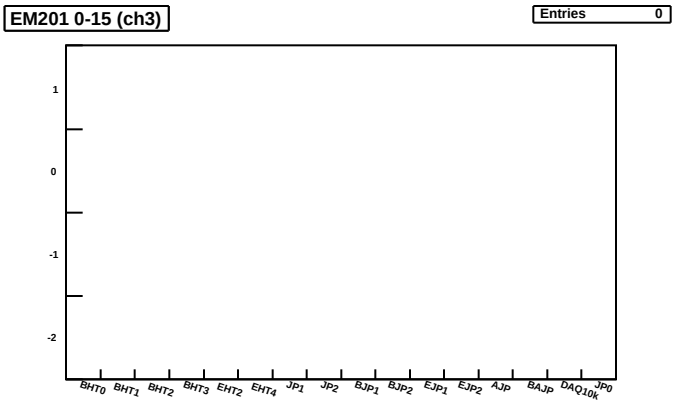
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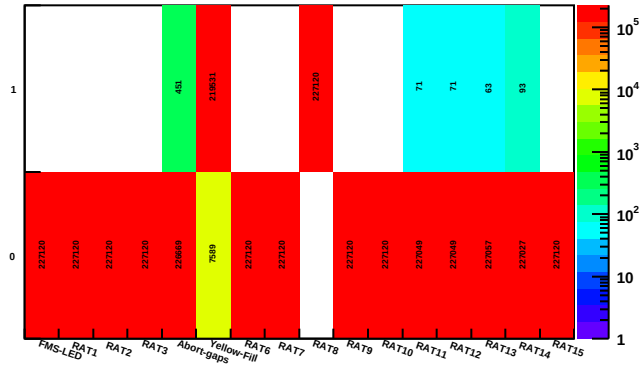
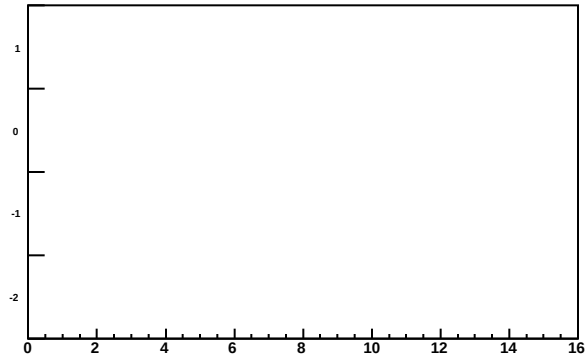
RAT board (ch4)





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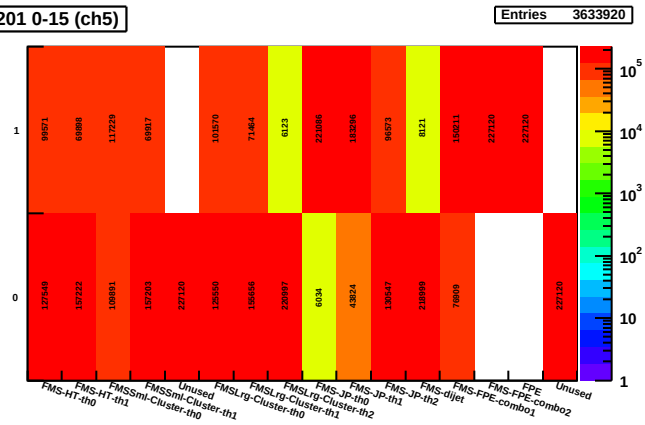

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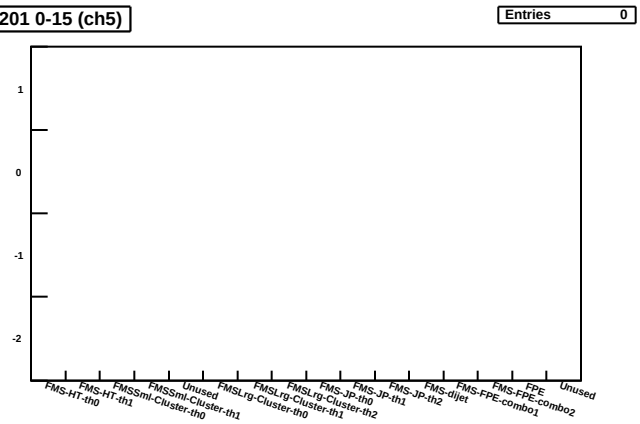

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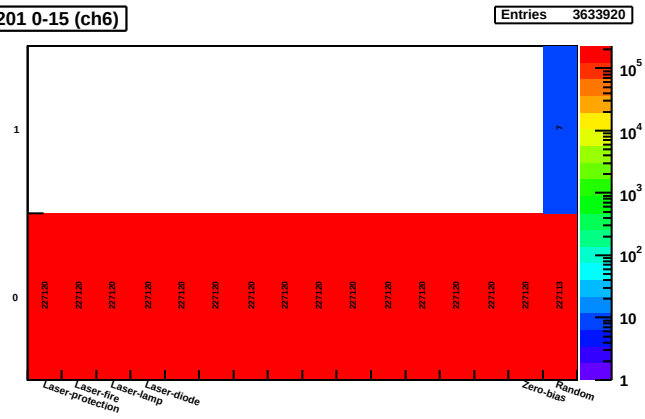

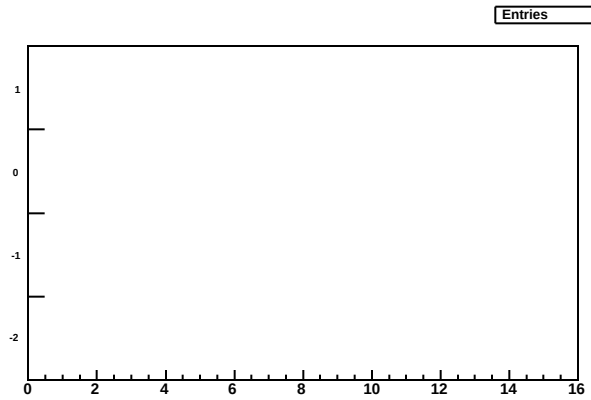
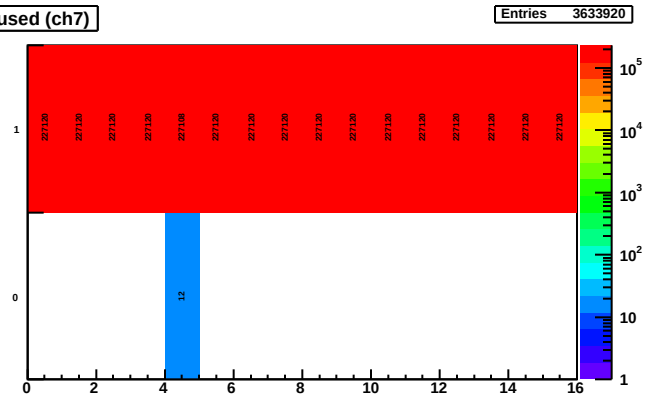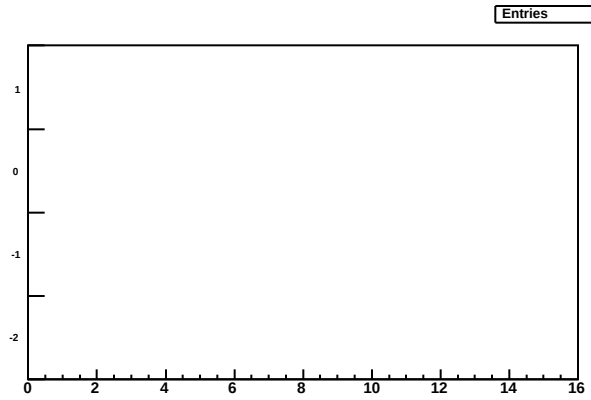
Unused (ch7)