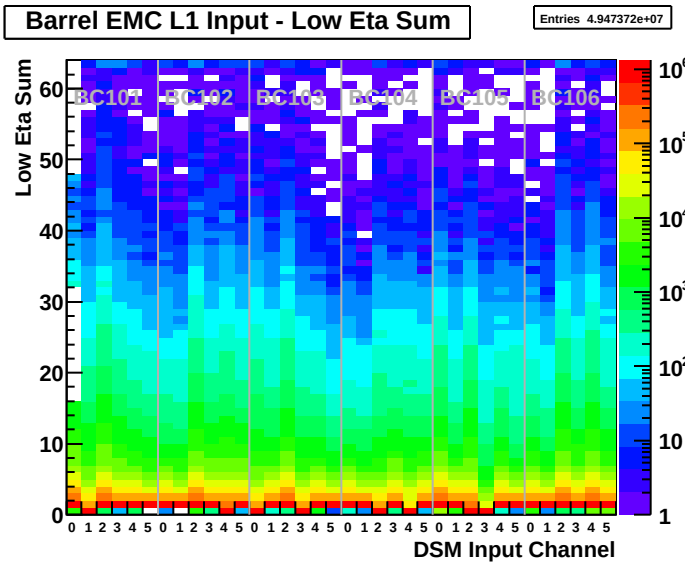
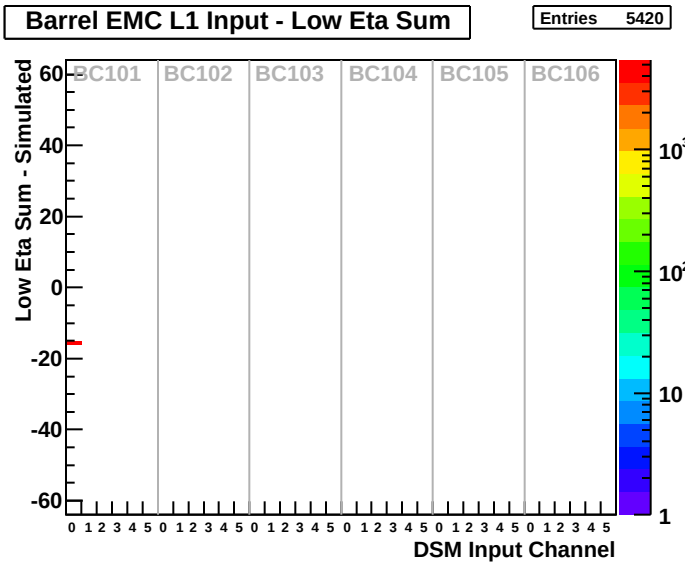
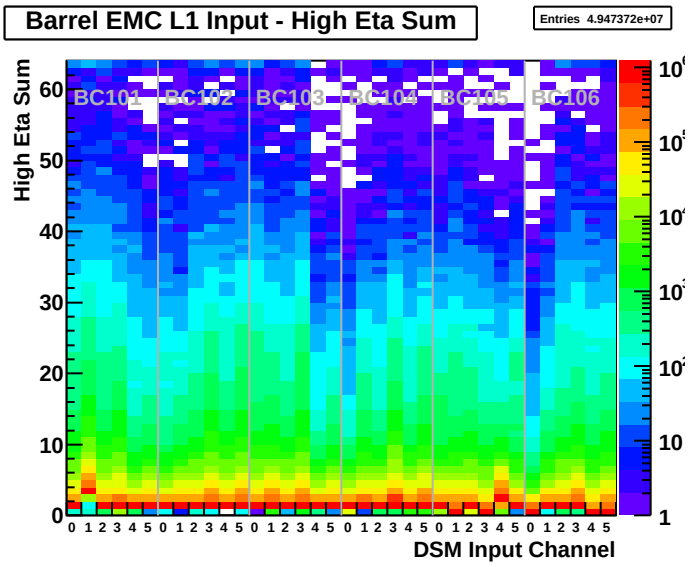
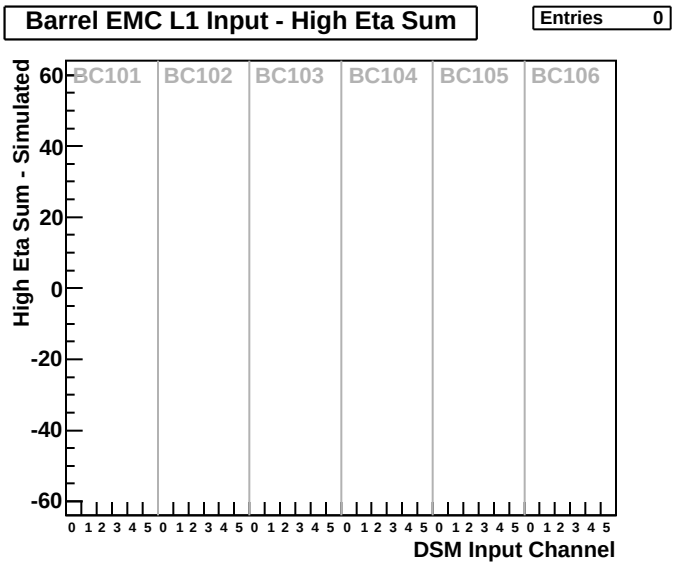
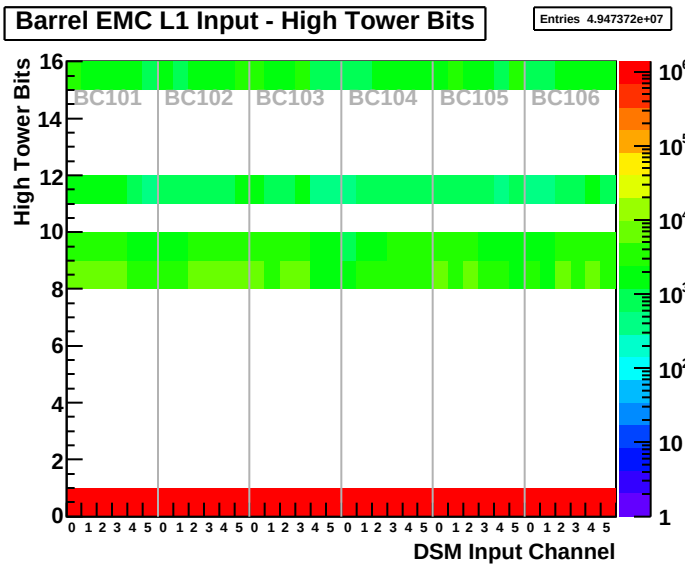
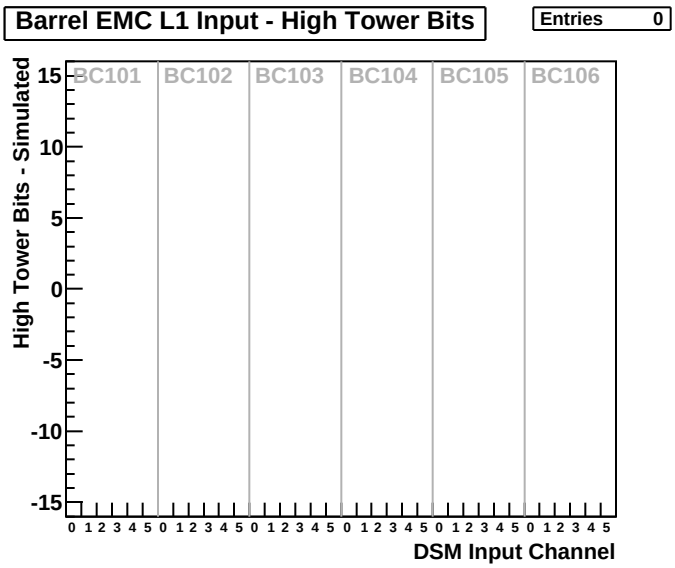

Barrel EMC L1 Input - Low Eta Sum
Entries 4.947372e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

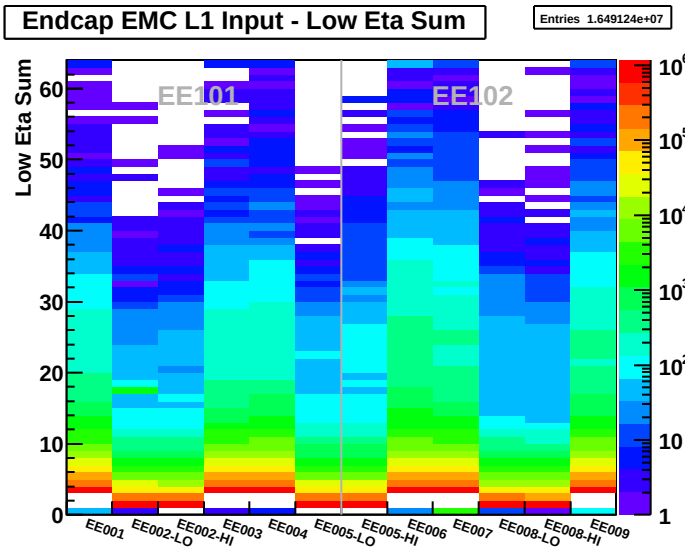

Barrel EMC L1 Input - Low Eta Sum
Entries 5420
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

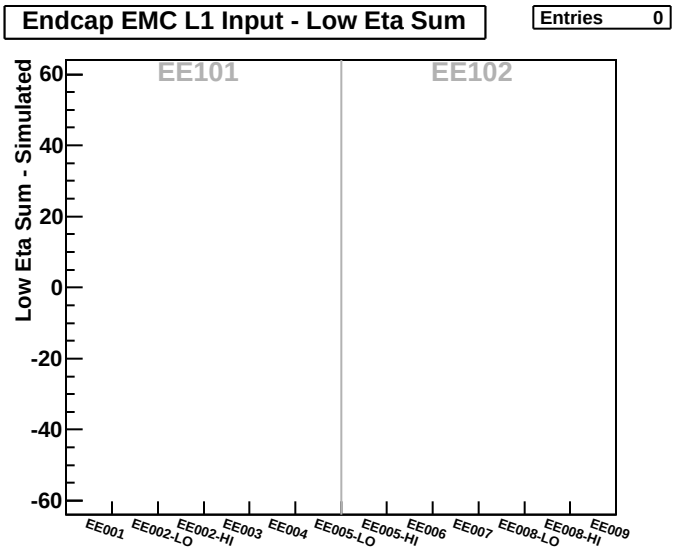

Barrel EMC L1 Input - High Eta Sum
Entries 4.947372e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

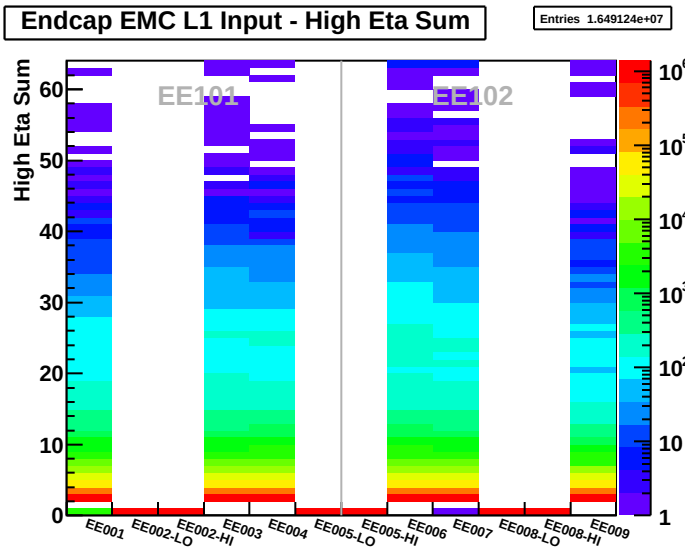

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

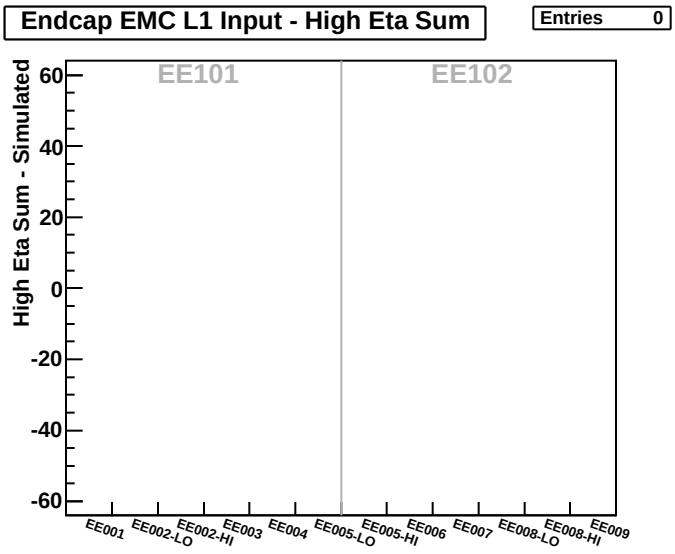

Barrel EMC L1 Input - High Tower Bits
Entries 4.947372e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

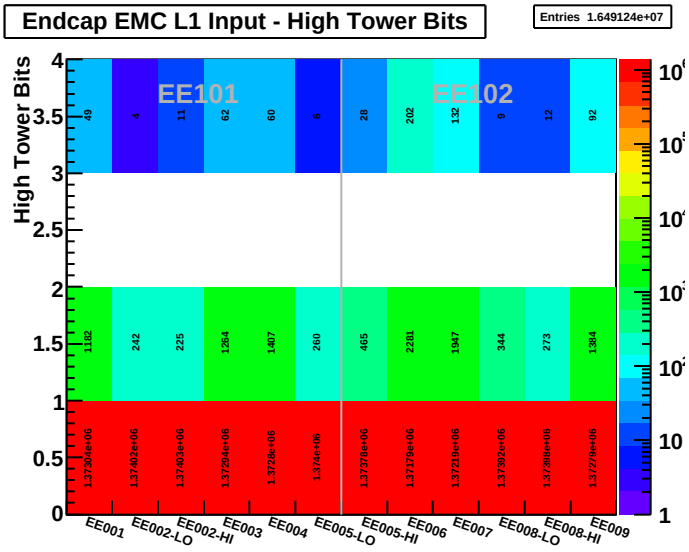

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

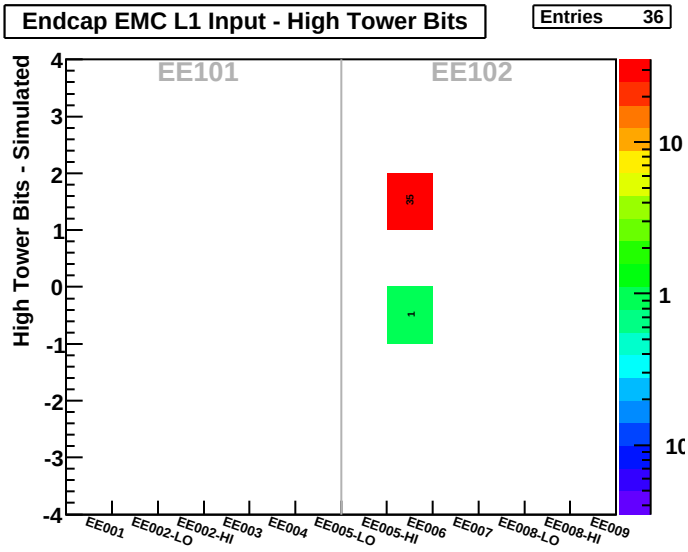

Endcap EMC L1 Input - Low Eta Sum
Entries 1.649124e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

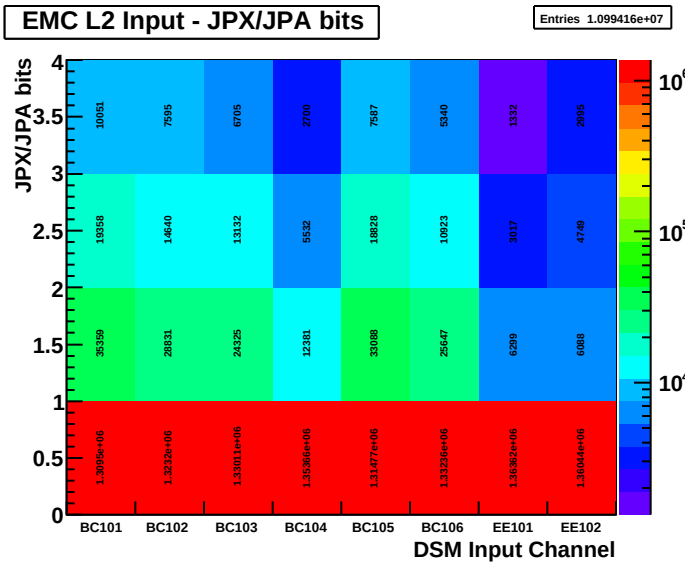

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

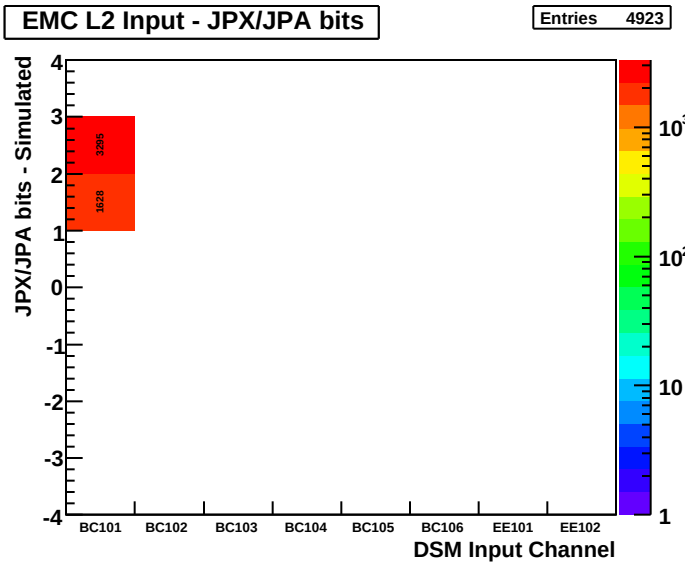

Endcap EMC L1 Input - High Eta Sum
Entries 1.649124e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

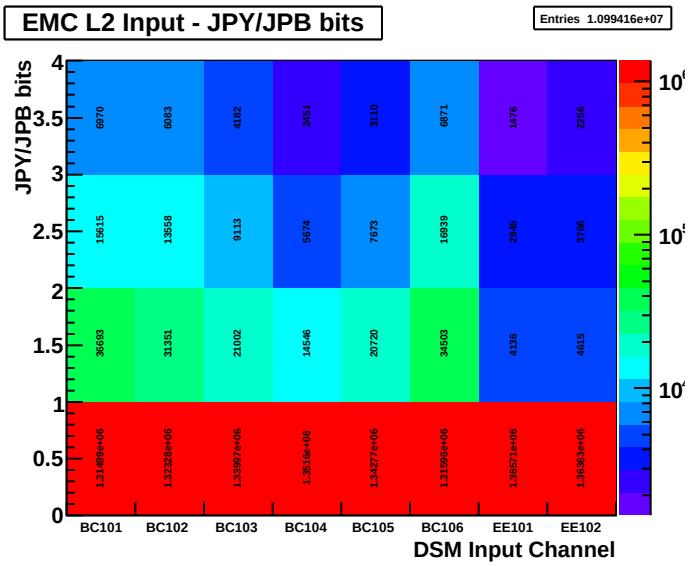

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

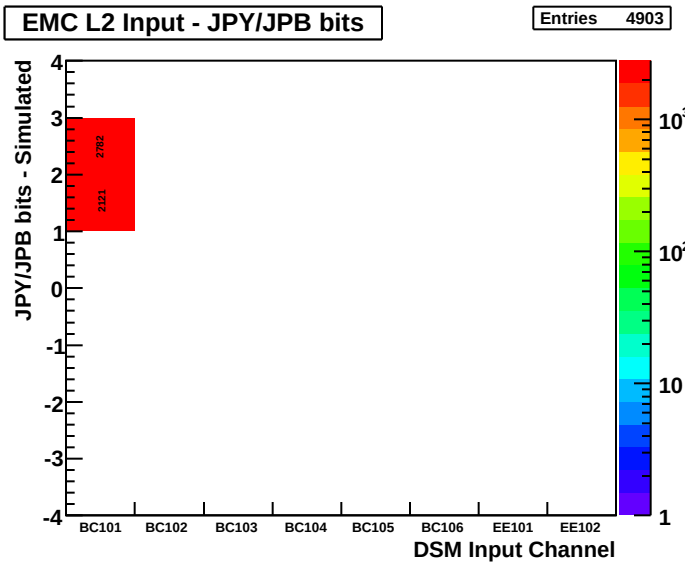

Endcap EMC L1 Input - High Tower Bits
Entries 1.649124e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

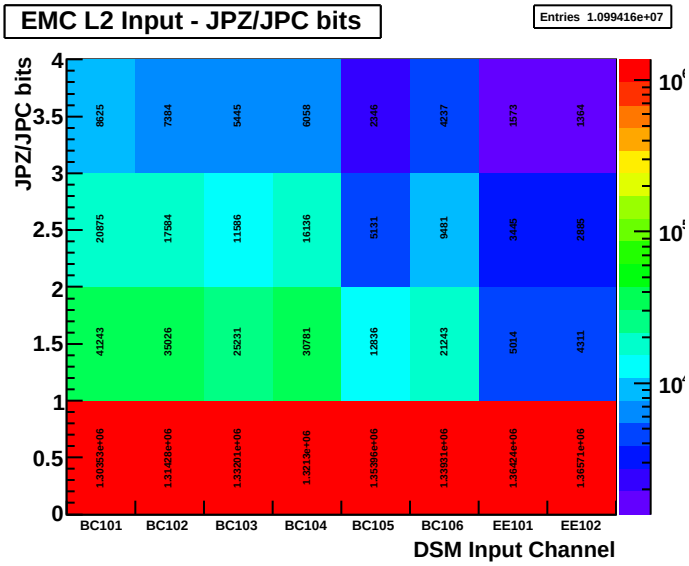

Endcap EMC L1 Input - High Tower Bits
Entries 36
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

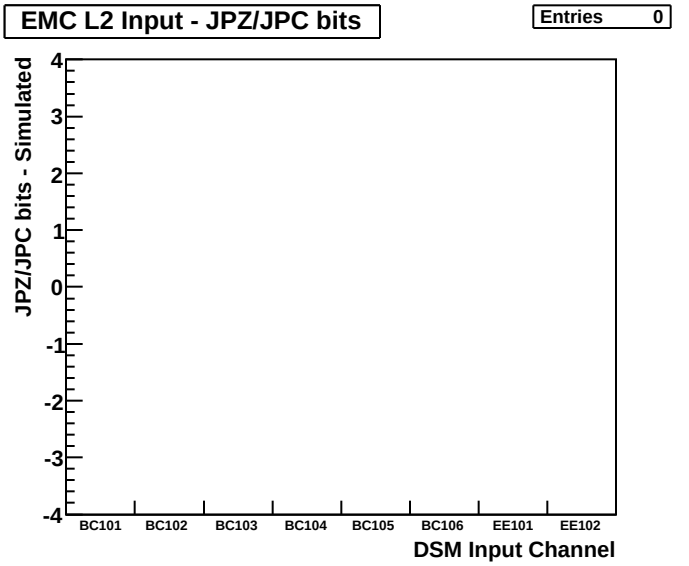

EMC L2 Input - JPX/JPA bits
Entries 1.099416e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10051 7595 6705 2700 7587 5340 1332 2995
19358 14640 13132 5532 18828 10923 3017 4749
35399 28831 24325 12381 33088 25647 6299 6088
1.3095e+06 1.3232e+06 1.33011e+06 1.35366e+06 1.31477e+06 1.33236e+06 1.36362e+06 1.36044e+06

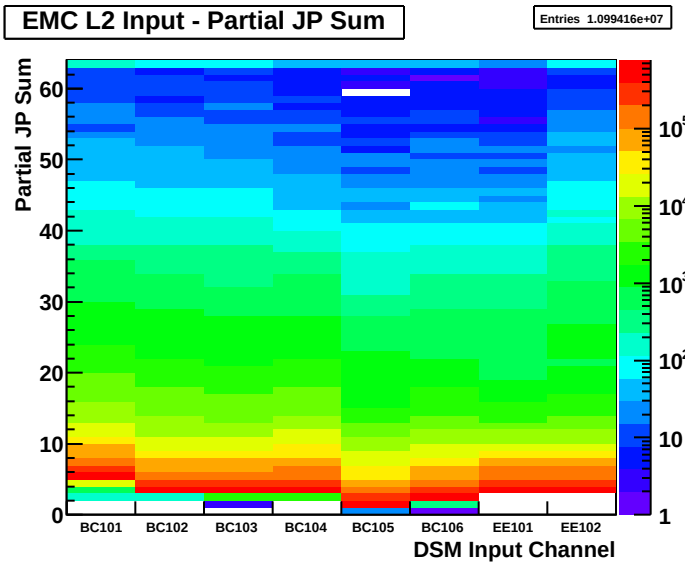

EMC L2 Input - JPX/JPA bits
Entries 4923
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3295
1628

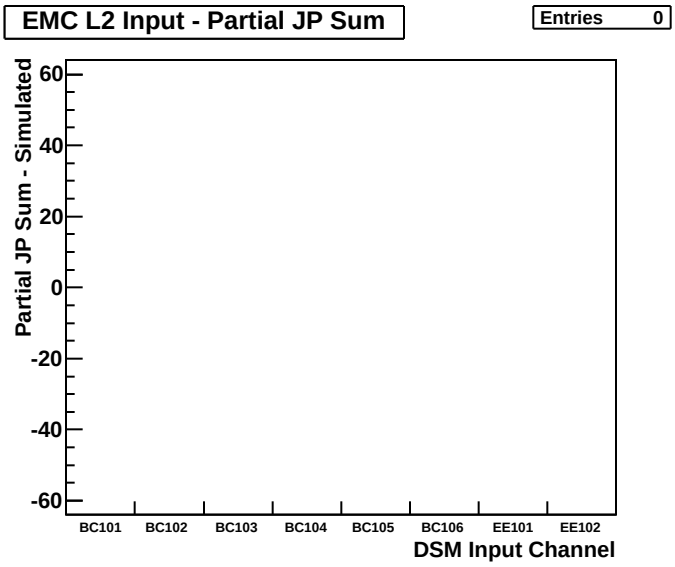

EMC L2 Input - JPY/JPB bits
Entries 1.099416e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6970 6083 4182 2451 3110 6871 1476 2256
15615 13558 9113 5674 7673 16939 2945 3766
36693 31351 21002 14546 20720 34503 4136 4615
1.31409e+06 1.33328e+06 1.33997e+06 1.3516e+06 1.34277e+06 1.31596e+06 1.36571e+06 1.36363e+06

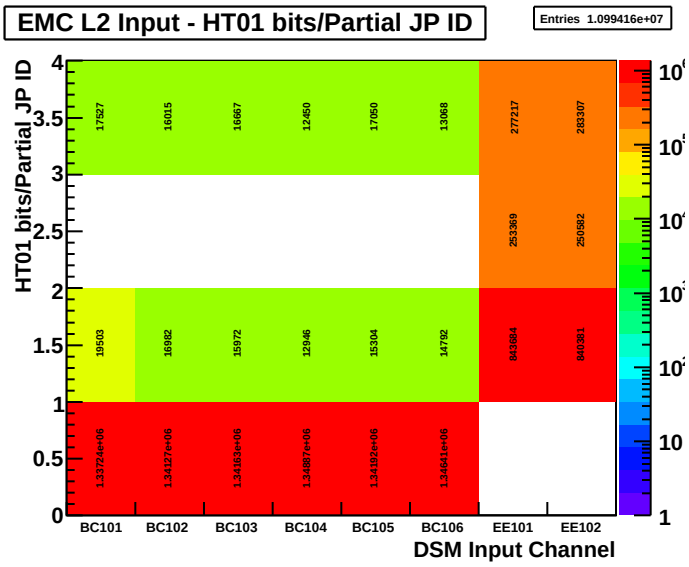

EMC L2 Input - JPY/JPB bits
Entries 4903
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2782
2121

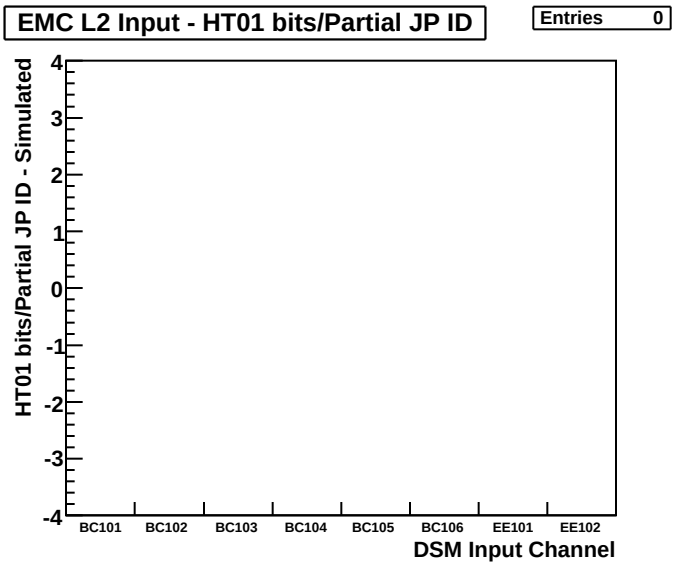

EMC L2 Input - JPZ/JPC bits
Entries 1.099416e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8625 7384 5445 6058 2346 4237 1573 1364
20875 17584 11586 16136 5131 9481 3445 2885
41243 35026 25231 30781 12836 21243 5014 4311
1.30353e+06 1.31428e+06 1.33201e+06 1.3213e+06 1.35396e+06 1.33931e+06 1.36424e+06 1.36571e+06

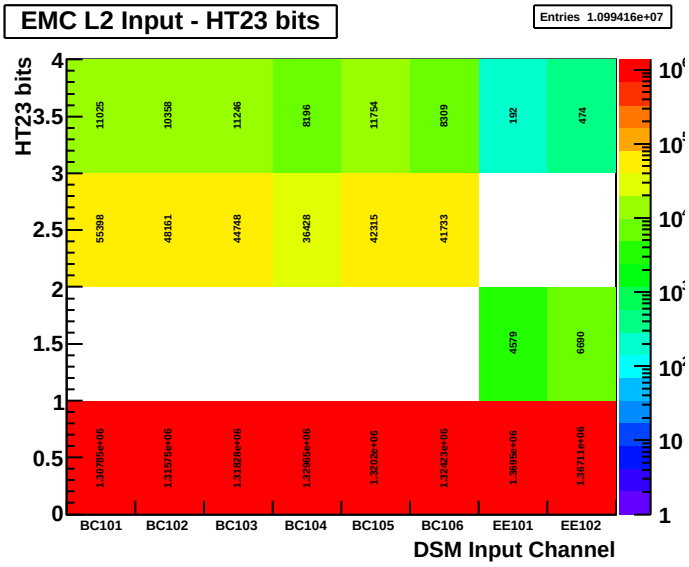

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

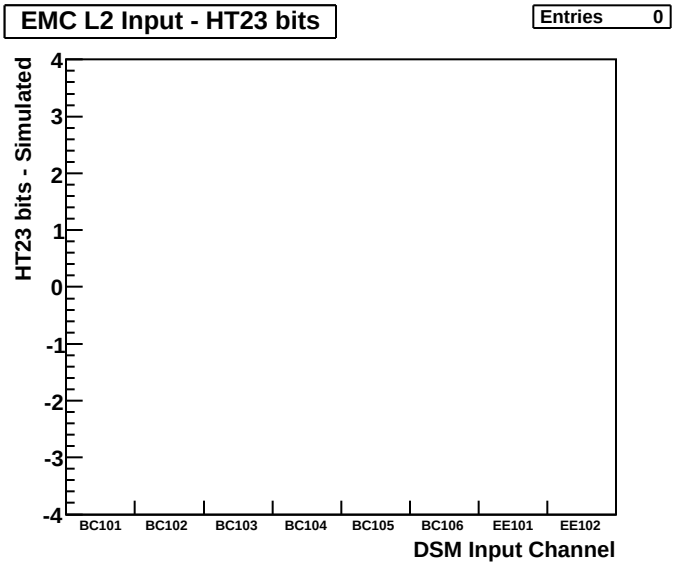

EMC L2 Input - Partial JP Sum
Entries 1.099416e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

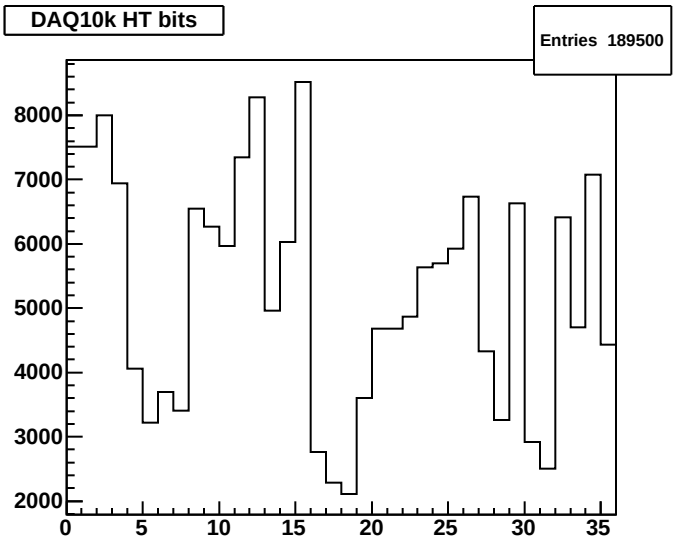

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

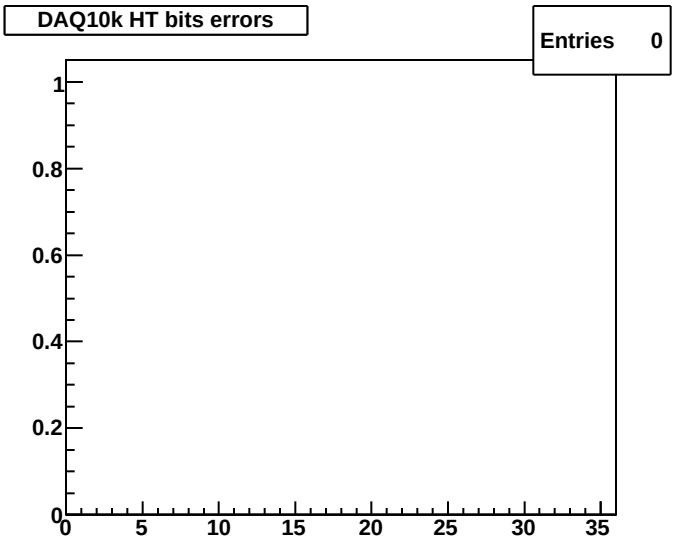

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.099416e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

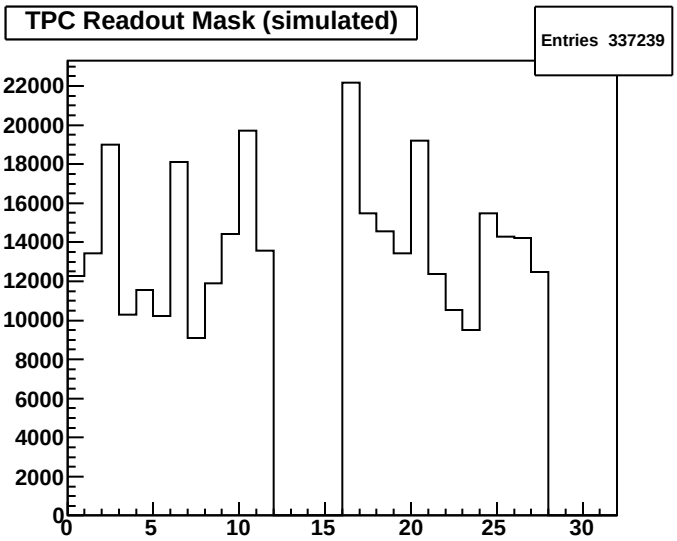

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

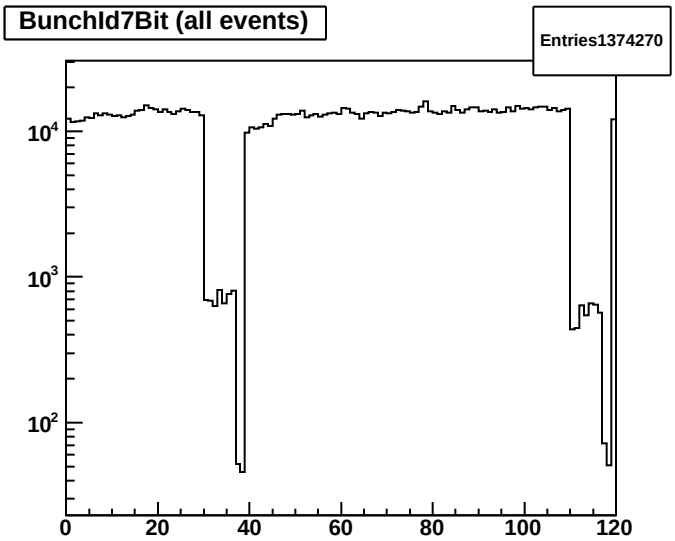

EMC L2 Input - HT23 bits
Entries 1.099416e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

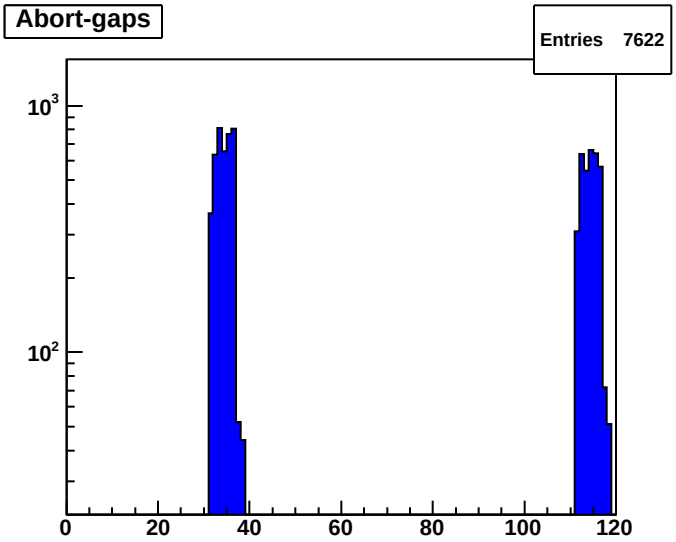

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

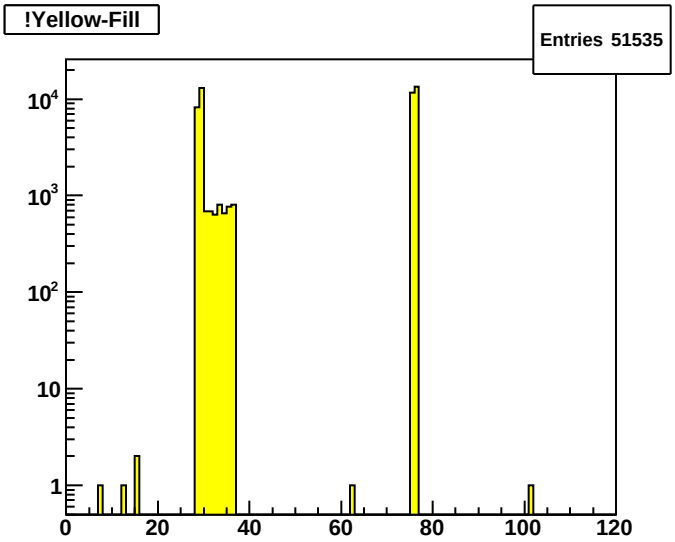

DAQ10k HT bits
Entries 189500

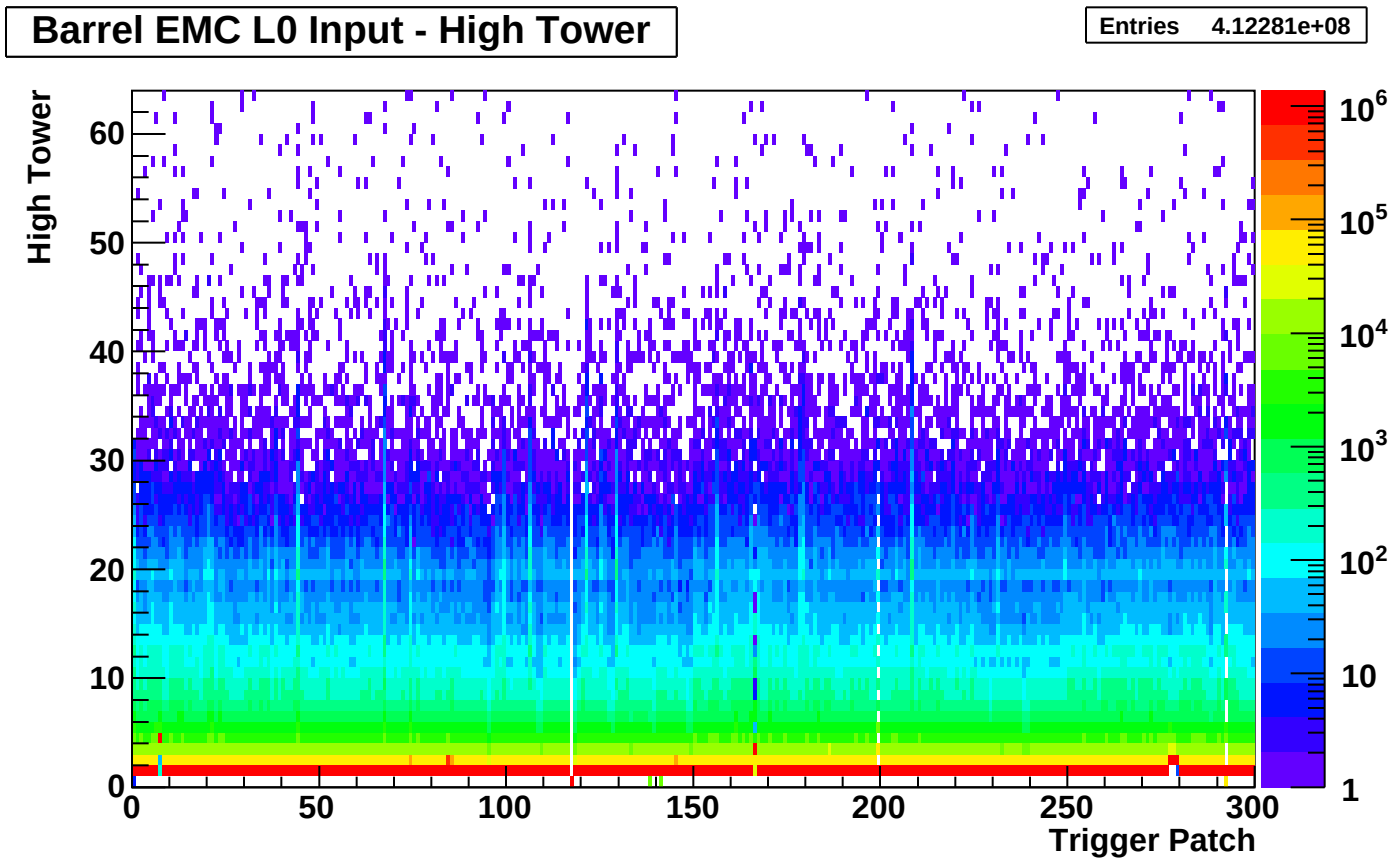

DAQ10k HT bits errors
Entries 0

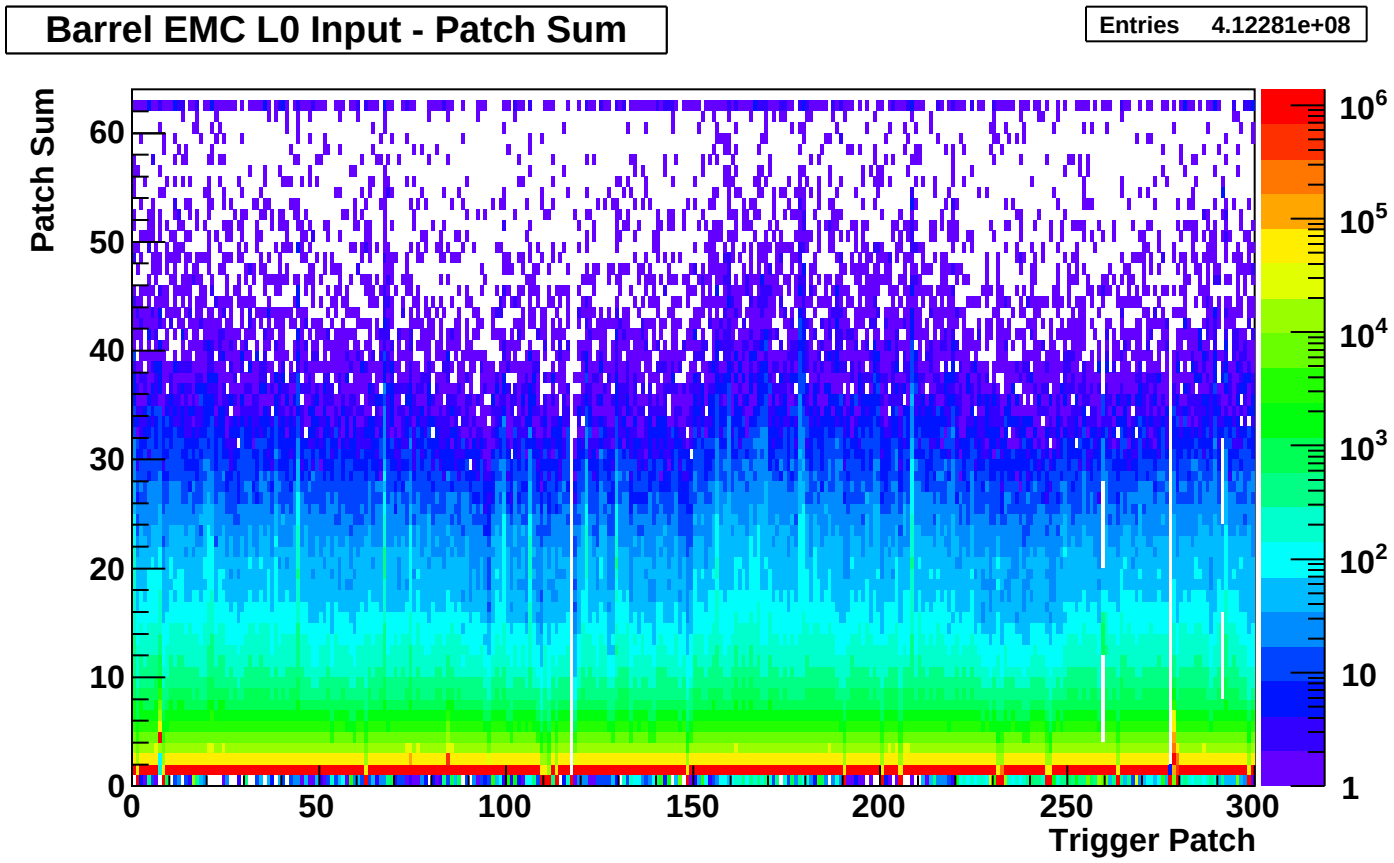

TPC Readout Mask (simulated)
Entries 337239

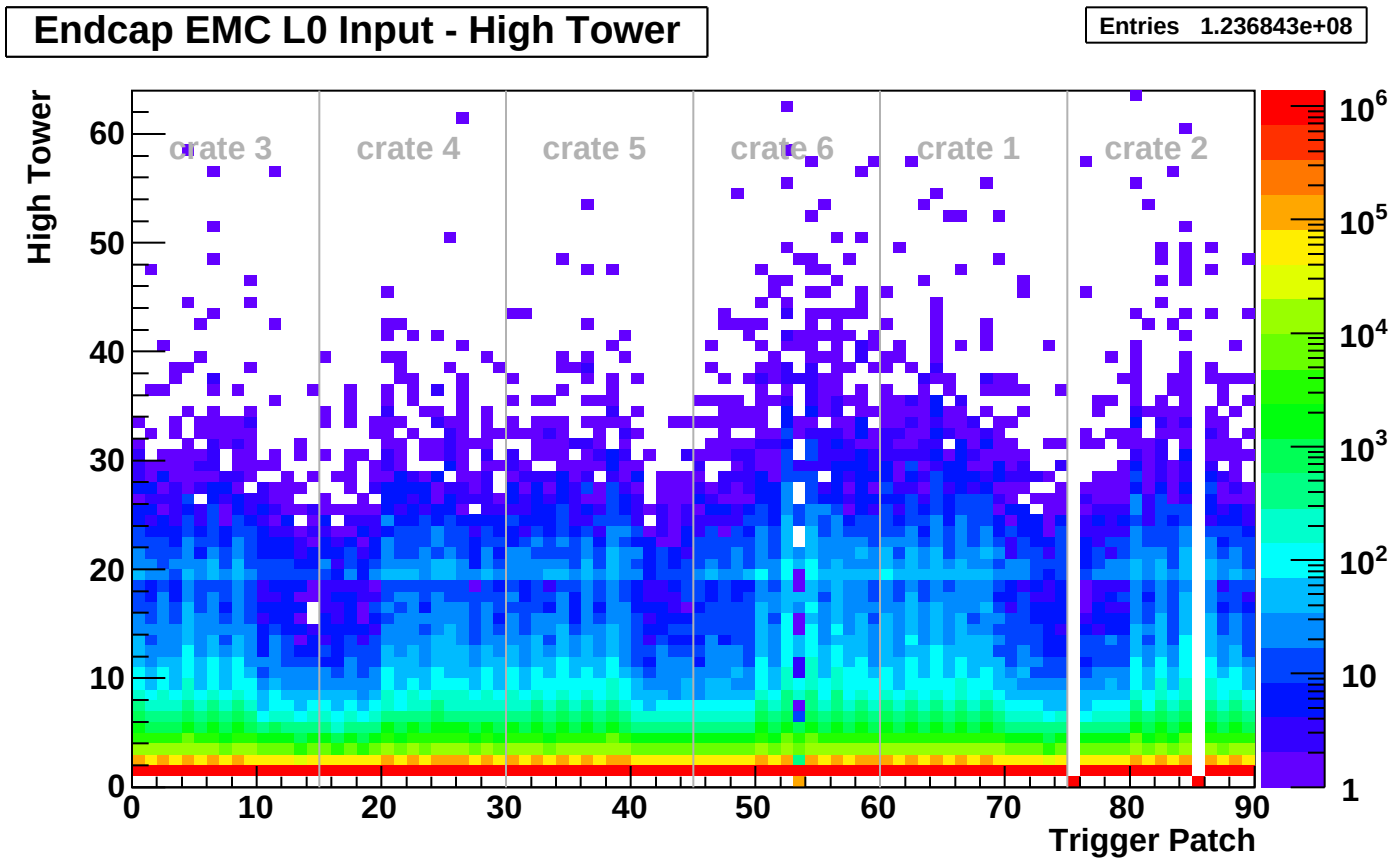

BunchId7Bit (all events)
Entries1374270

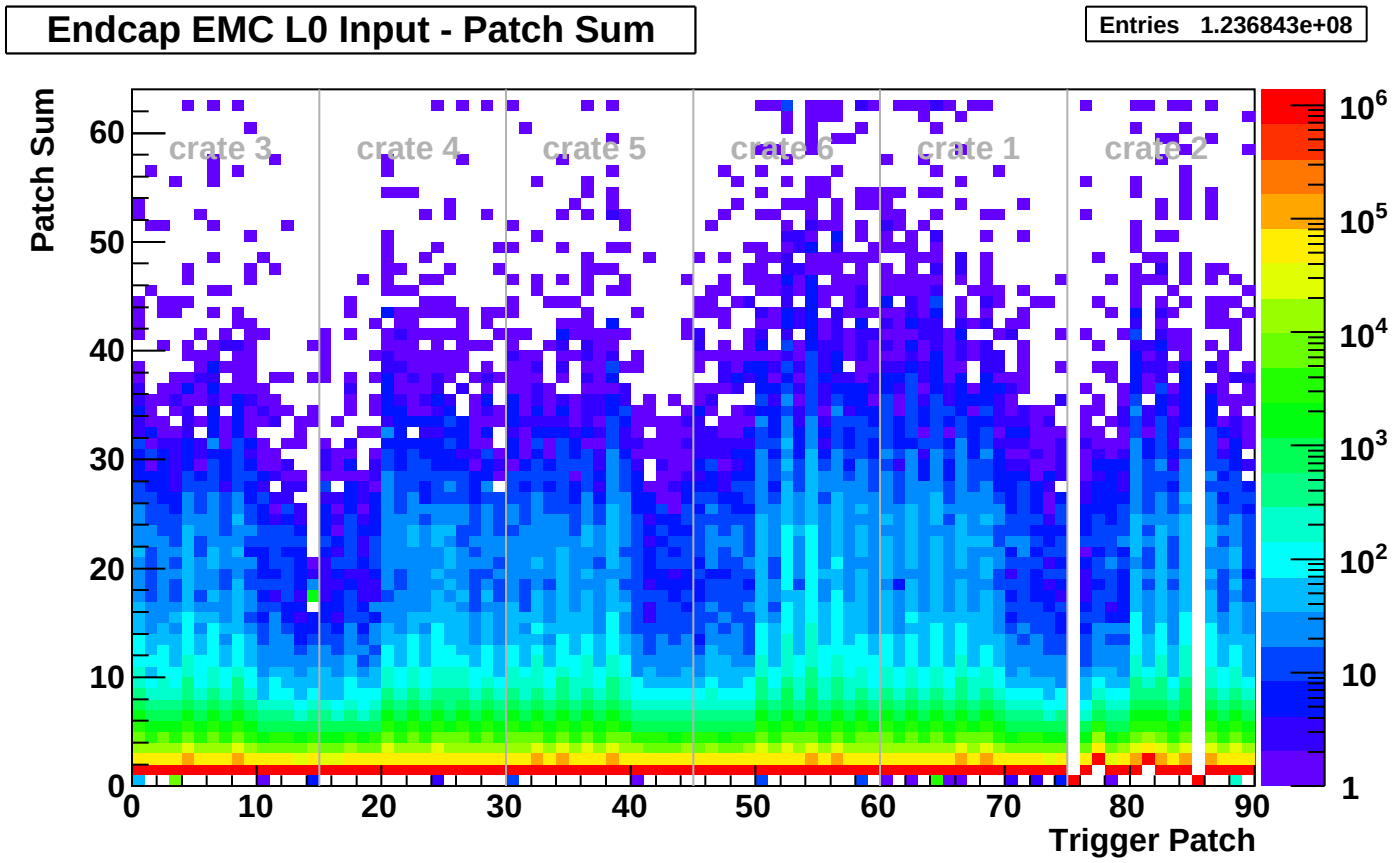

Abort-gaps
Entries 7622

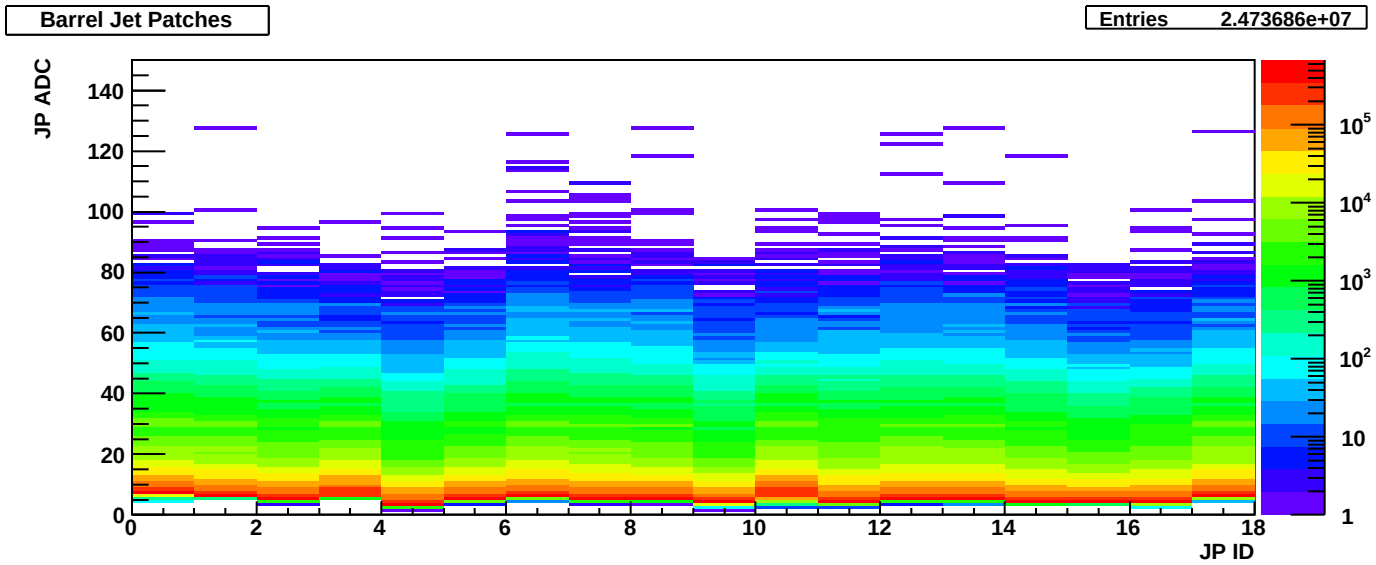

!Yellow-Fill
Entries 51535

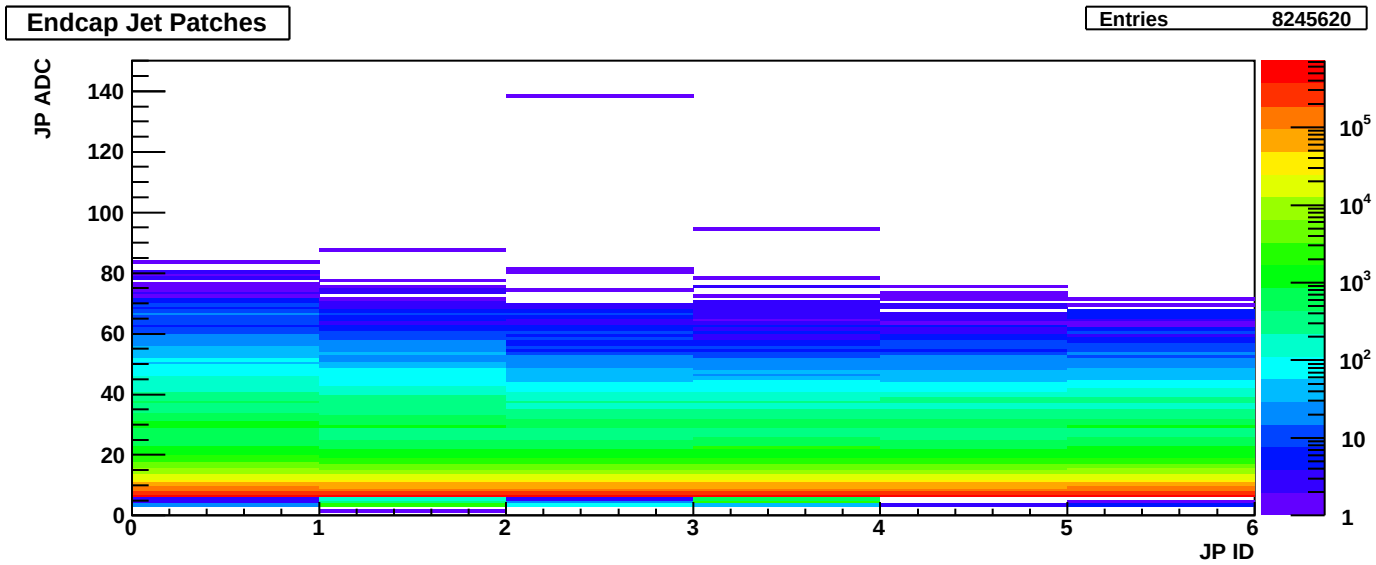

Barrel EMC L0 Input - High Tower
Entries 4.12281e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

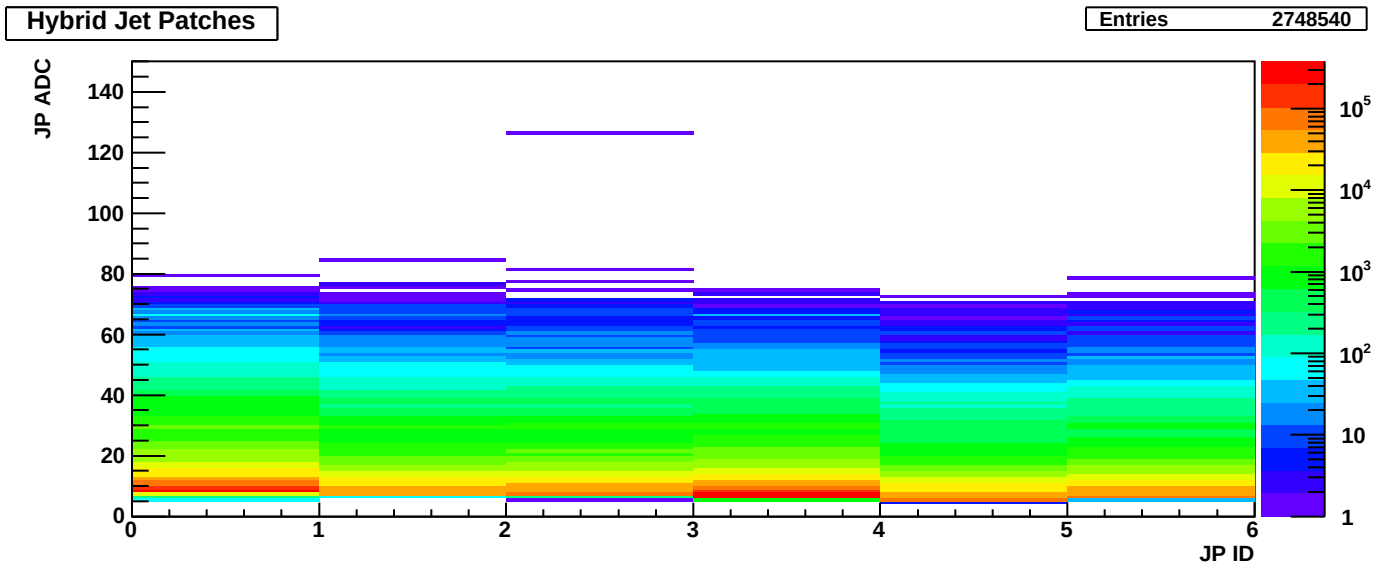

Barrel EMC L0 Input - Patch Sum
Entries 4.12281e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

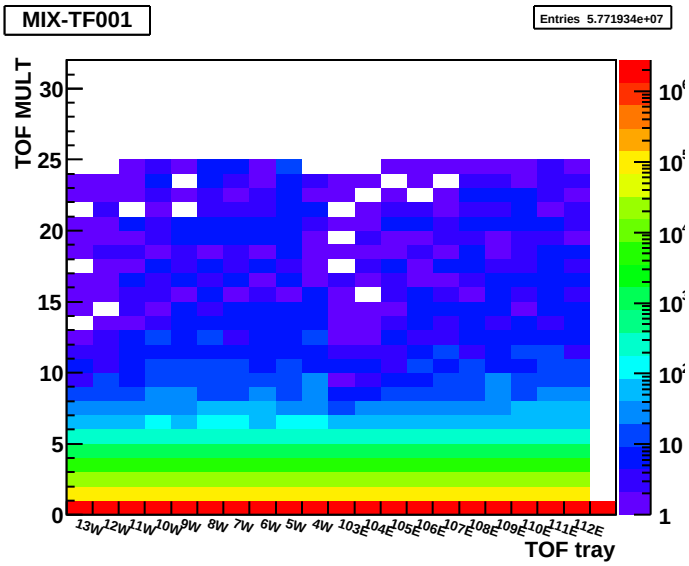

Endcap EMC L0 Input - High Tower
Entries 1.236843e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

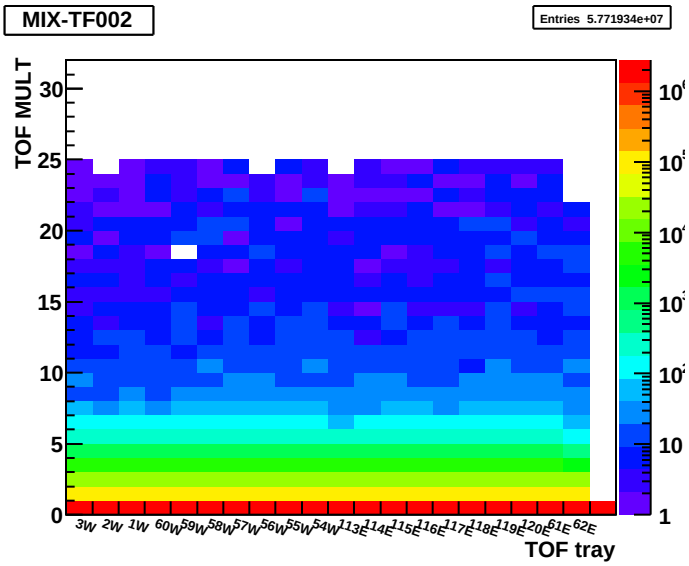

Endcap EMC L0 Input - Patch Sum
Entries 1.236843e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

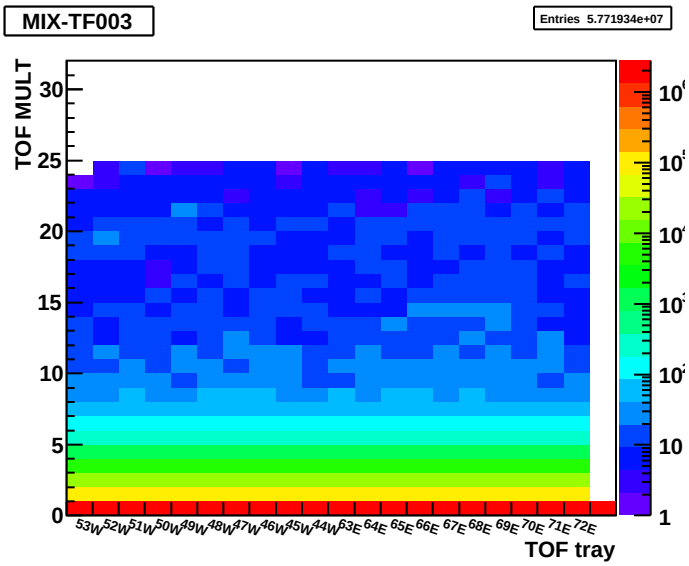

Barrel Jet Patches
Entries 2.473686e+07
JP ADC
JP ID

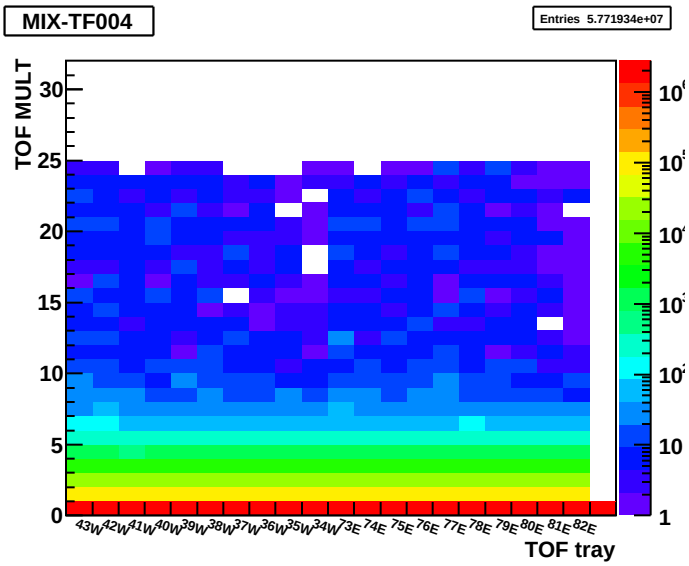

Endcap Jet Patches
Entries 8245620
JP ADC
JP ID

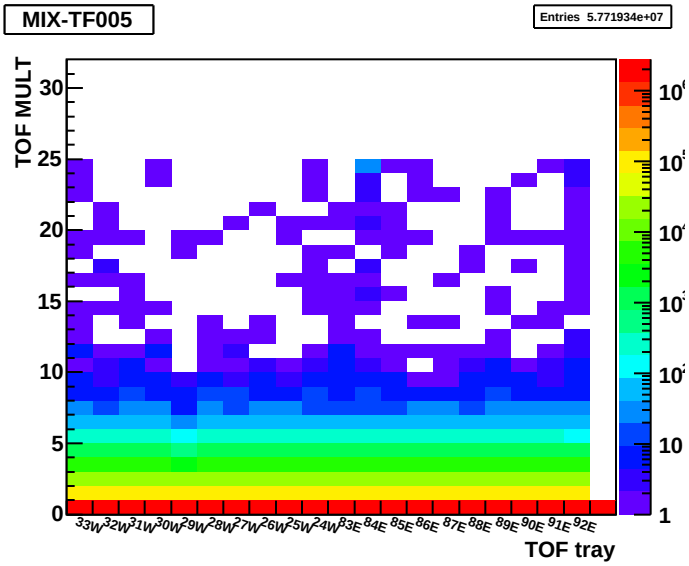

Hybrid Jet Patches
Entries 2748540
JP ADC
JP ID

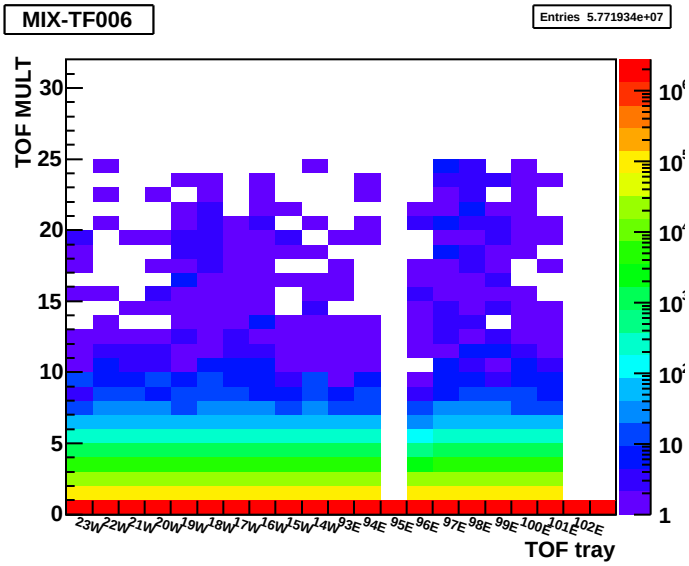

MIX-TF001
Entries 5.771934e+07
TOF MULT
TOF tray

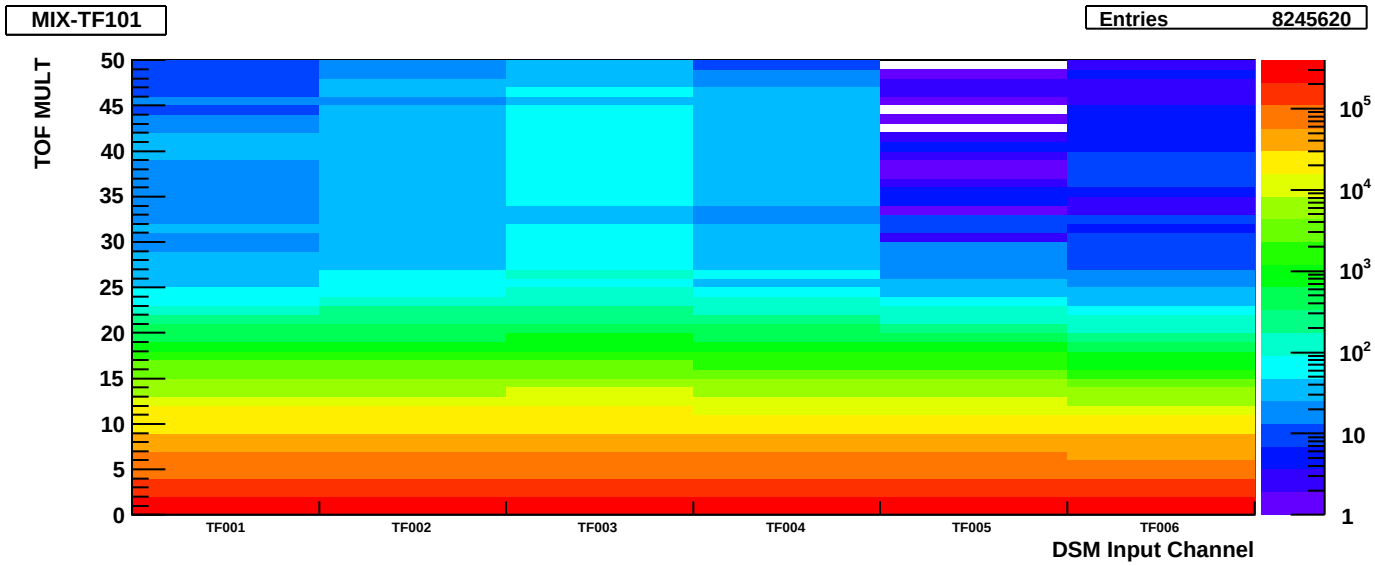

MIX-TF002
Entries 5.771934e+07
TOF MULT
TOF tray

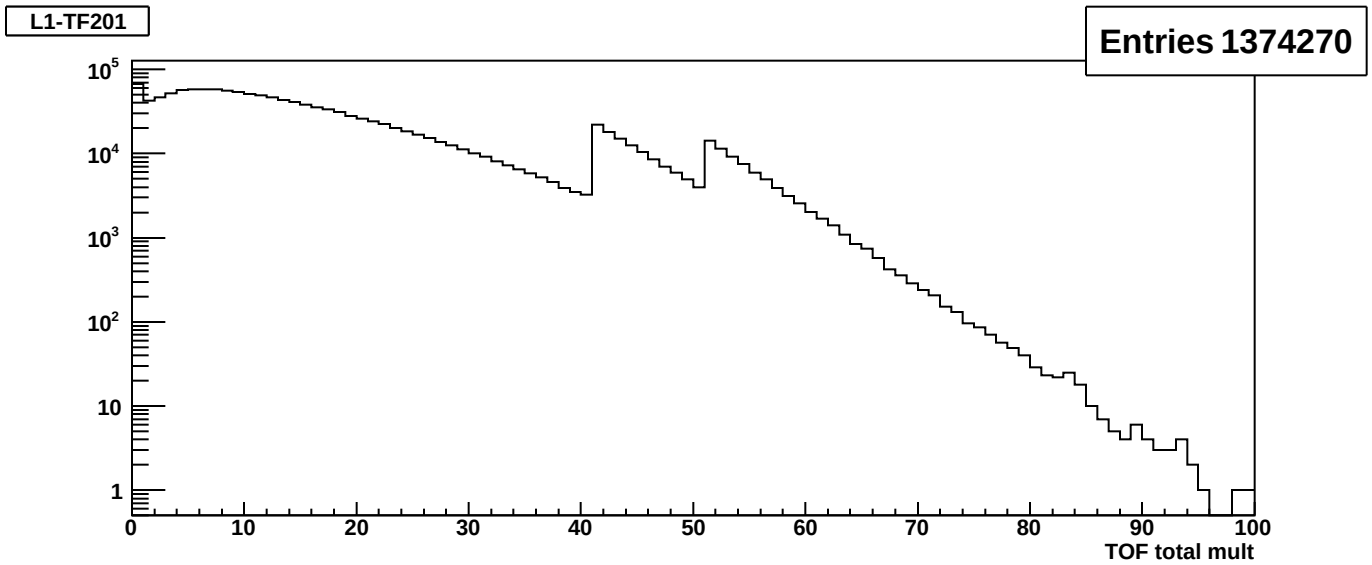

MIX-TF003
Entries 5.771934e+07
TOF MULT
TOF tray

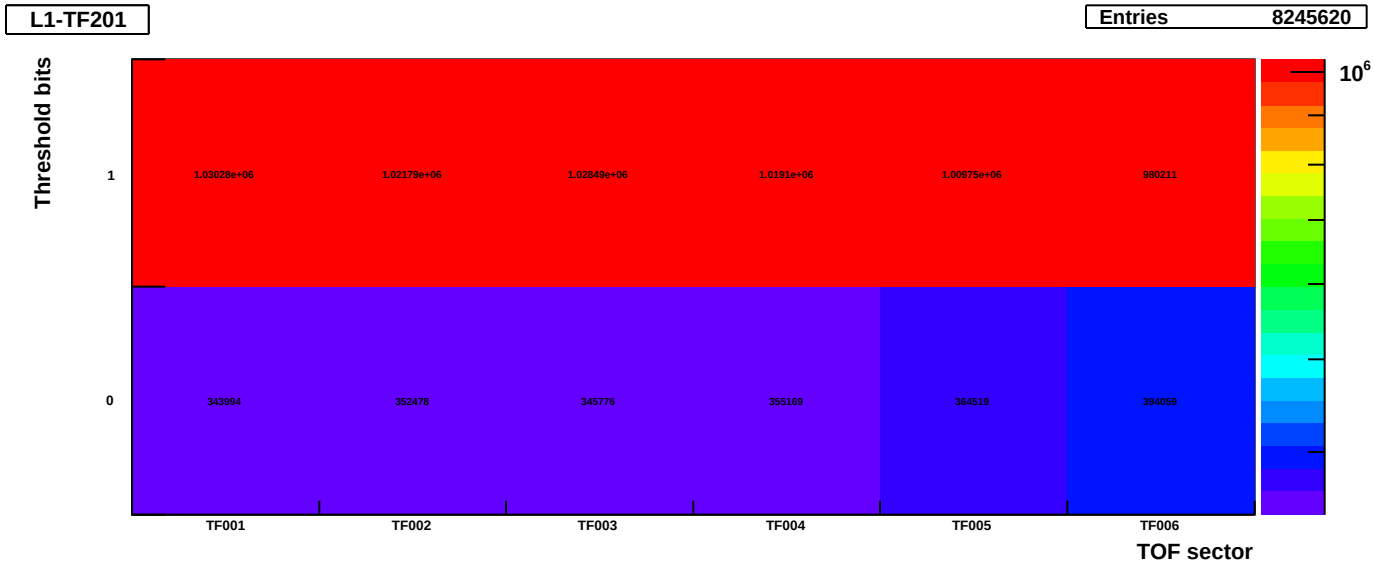

MIX-TF004
Entries 5.771934e+07
TOF MULT
TOF tray

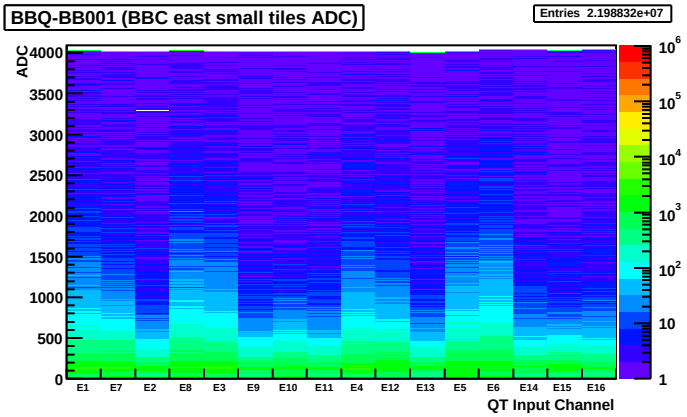

MIX-TF005
Entries 5.771934e+07
TOF MULT
TOF tray

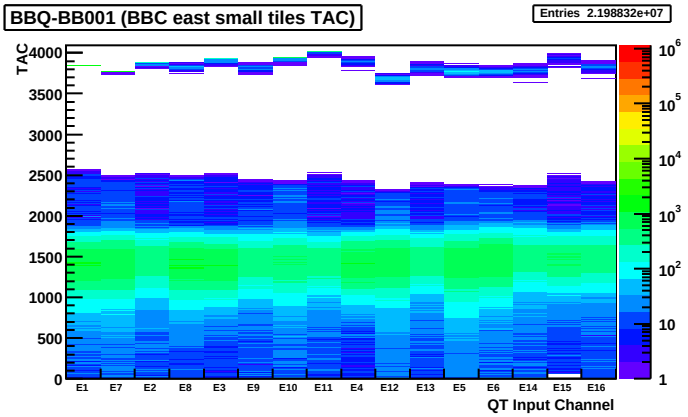

MIX-TF006
Entries 5.771934e+07
TOF MULT
TOF tray

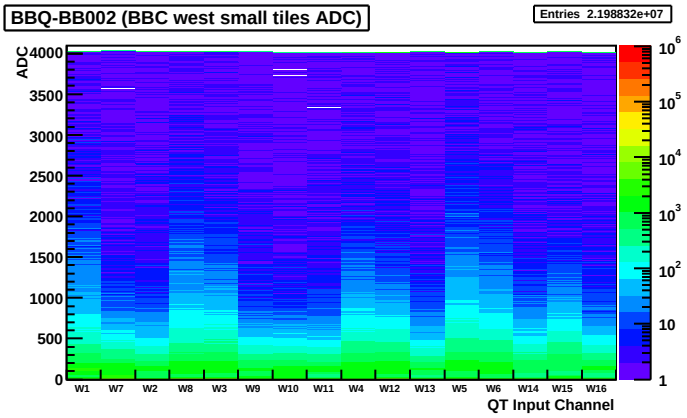

MIX-TF101
Entries 8245620
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

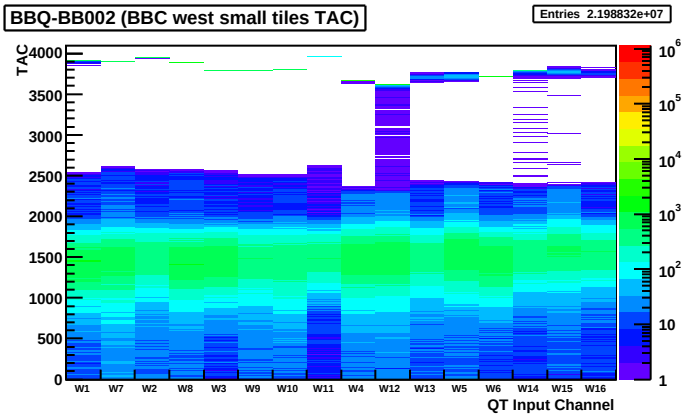

L1-TF201
Entries 1374270
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

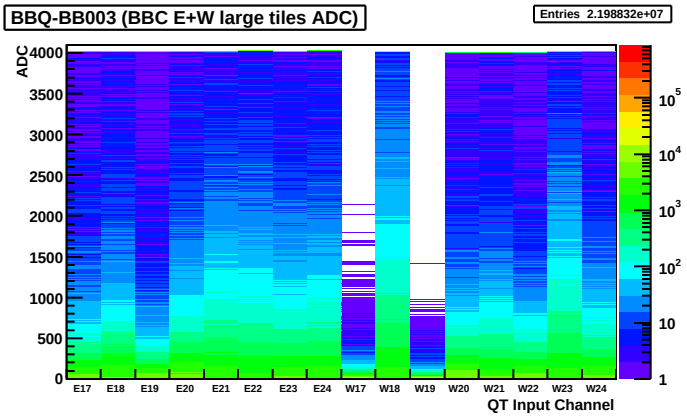

L1-TF201
Entries 8245620
Threshold bits
1
0
1.03028e+06
1.02179e+06
1.02849e+06
1.0191e+06
1.00975e+06
980211
343994
352478
345776
355169
364519
394059
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

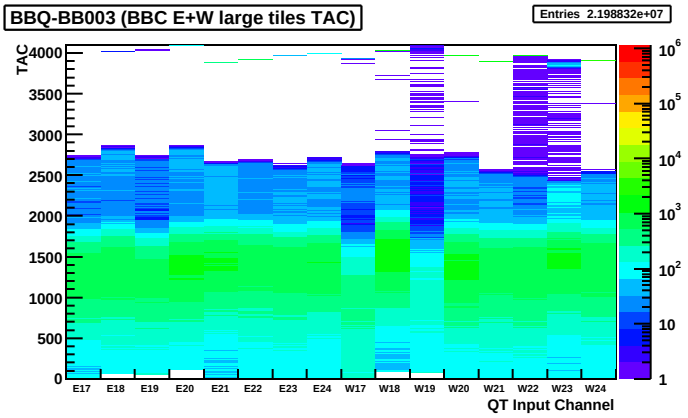

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.198832e+07
ADC
QT Input Channel

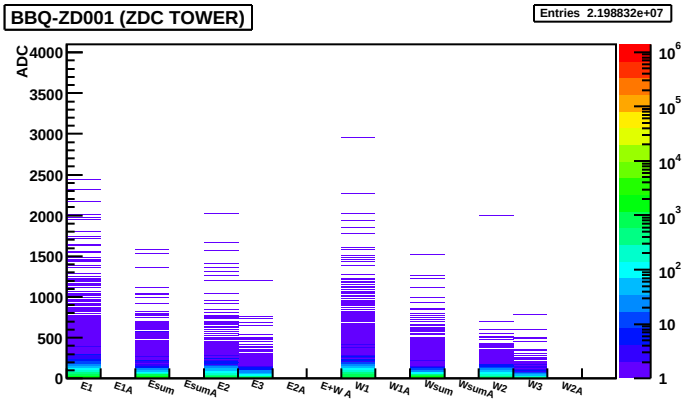

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.198832e+07
TAC
QT Input Channel

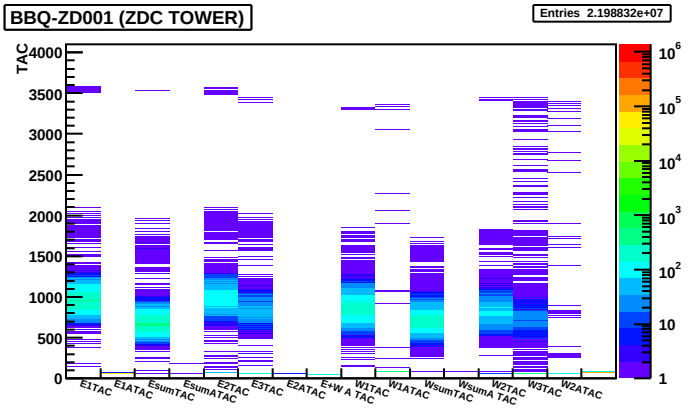

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.198832e+07
ADC
QT Input Channel

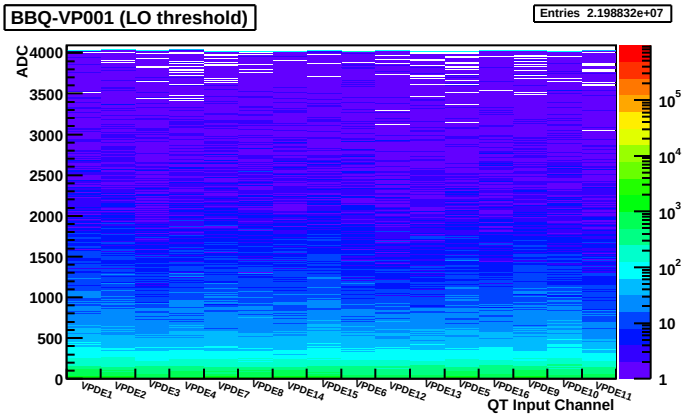

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.198832e+07
TAC
QT Input Channel

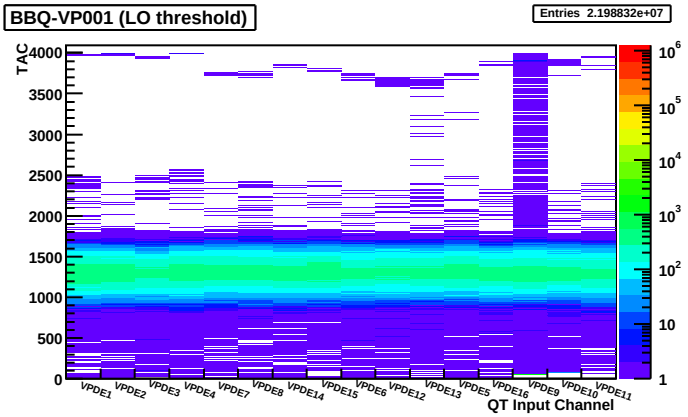

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.198832e+07
ADC
QT Input Channel

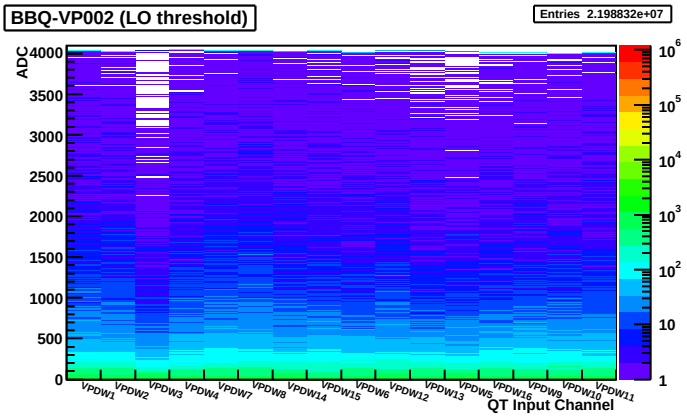

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.198832e+07
TAC
QT Input Channel

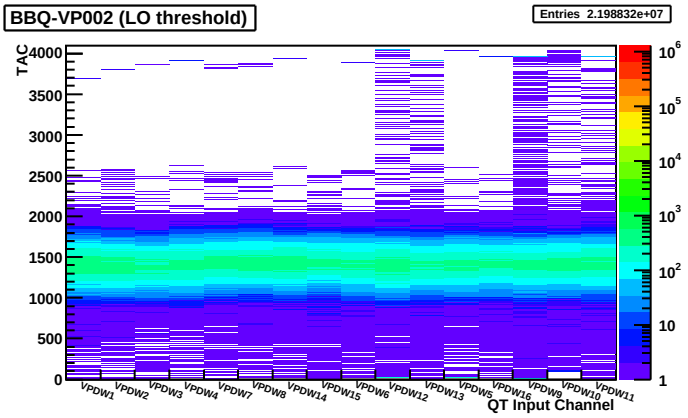

BBQ-ZD001 (ZDC TOWER)
Entries 2.198832e+07
ADC

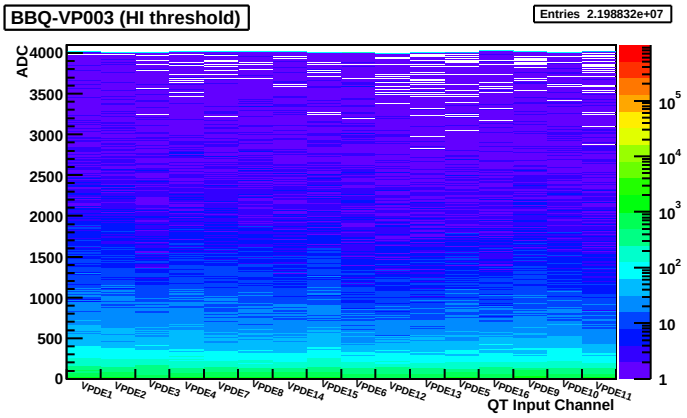

BBQ-ZD001 (ZDC TOWER)
Entries 2.198832e+07
TAC

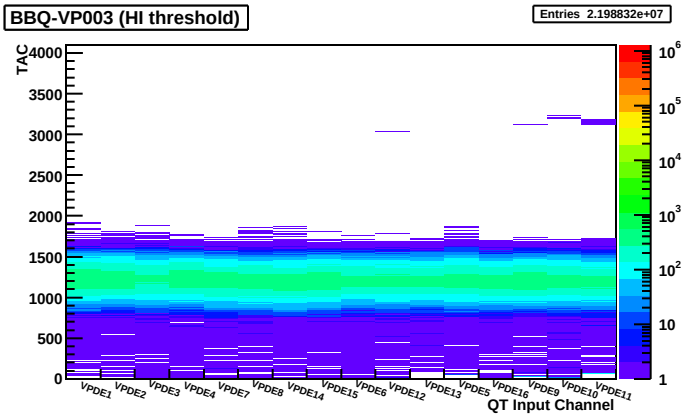

BBQ-VP001 (LO threshold)
Entries 2.198832e+07
ADC
QT Input Channel

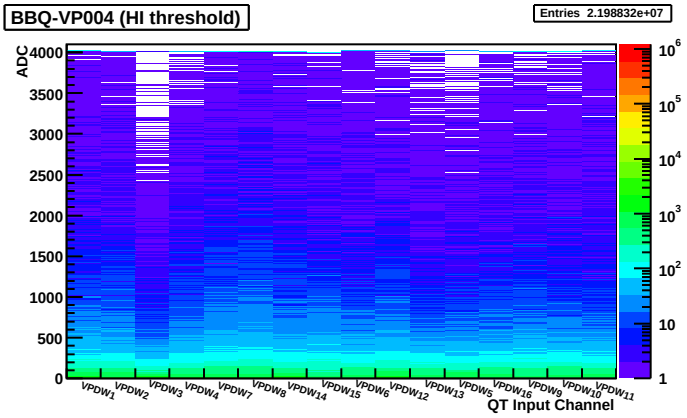

BBQ-VP001 (LO threshold)
Entries 2.198832e+07
TAC
QT Input Channel

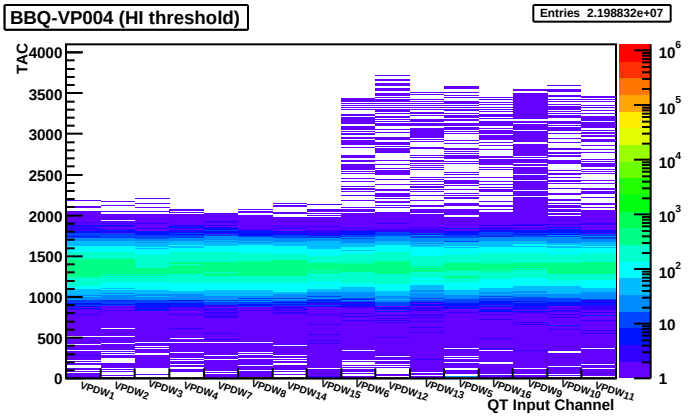

BBQ-VP002 (LO threshold)
Entries 2.198832e+07
ADC
QT Input Channel

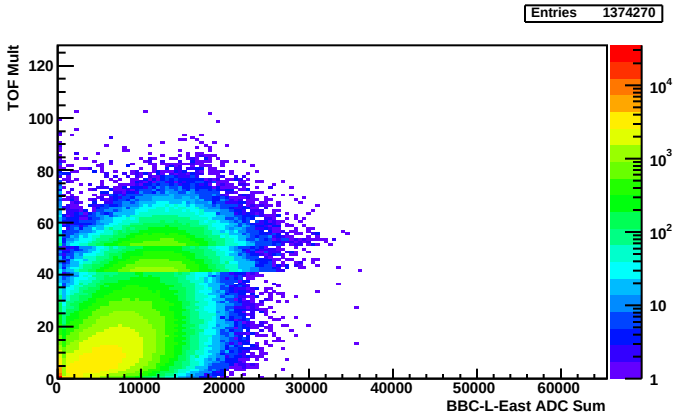

BBQ-VP002 (LO threshold)
Entries 2.198832e+07
TAC
QT Input Channel

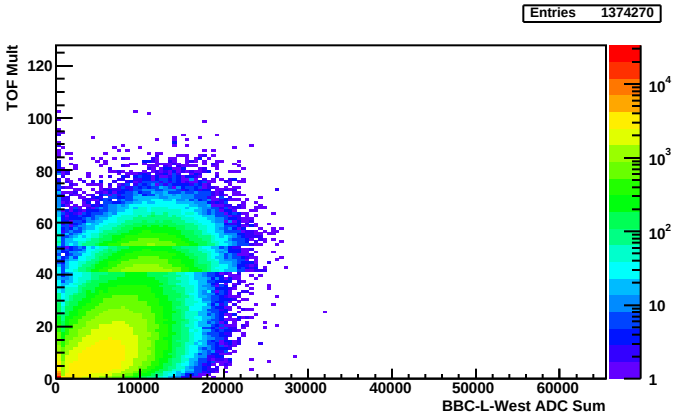

BBQ-VP003 (HI threshold)
Entries 2.198832e+07
ADC
QT Input Channel

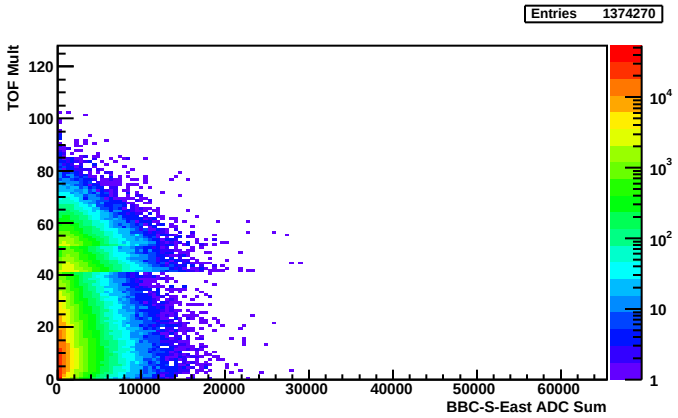

BBQ-VP003 (HI threshold)
Entries 2.198832e+07
TAC
QT Input Channel

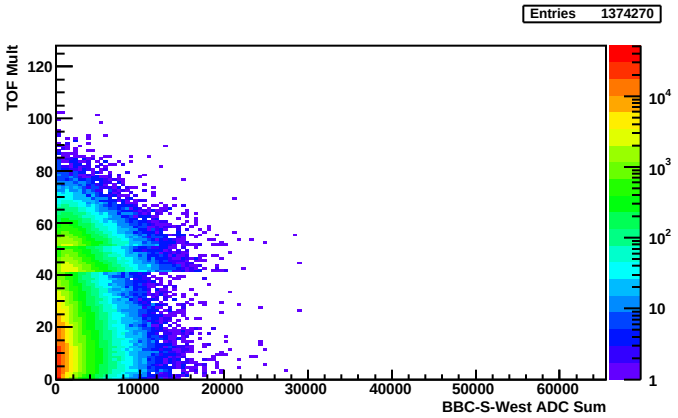

BBQ-VP004 (HI threshold)
Entries 2.198832e+07
ADC
QT Input Channel

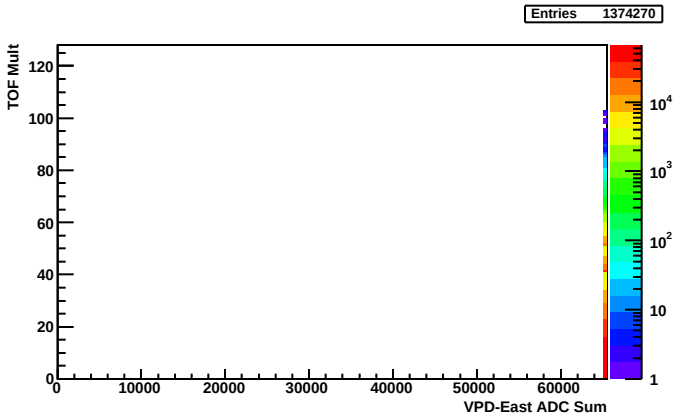

BBQ-VP004 (HI threshold)
Entries 2.198832e+07
TAC
QT Input Channel

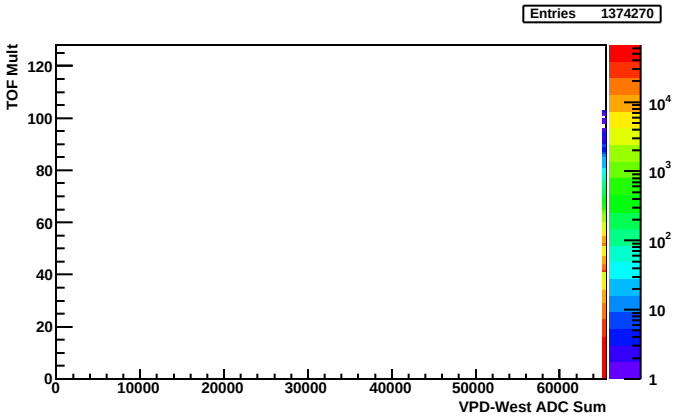

Entries 1374270
TOF Mult
BBC-L-East ADC Sum

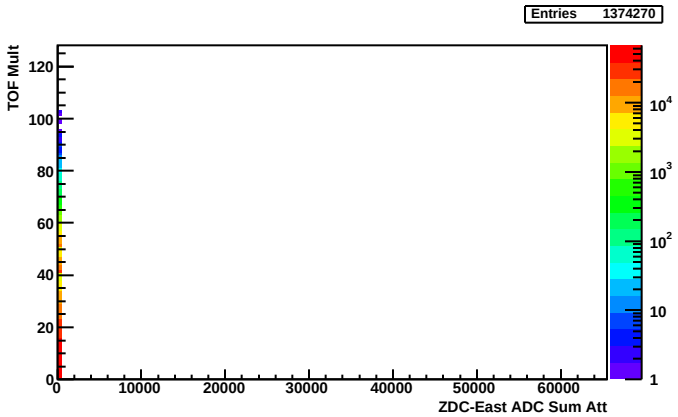

Entries 1374270
TOF Mult
BBC-L-West ADC Sum

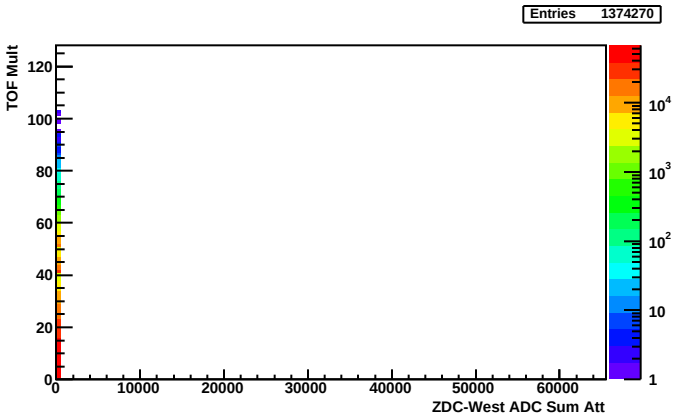

Entries 1374270
TOF Mult
BBC-S-East ADC Sum

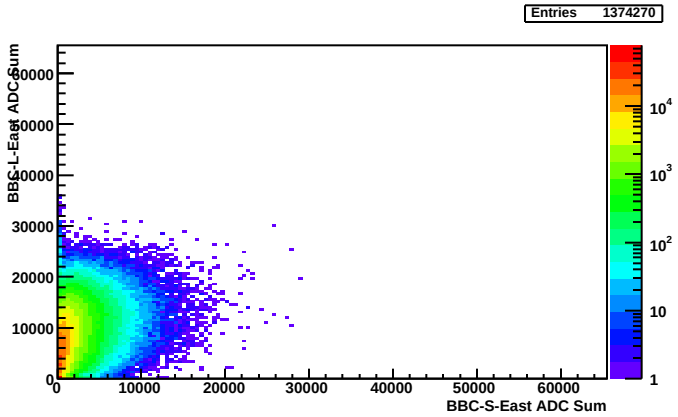

Entries 1374270
TOF Mult
BBC-S-West ADC Sum

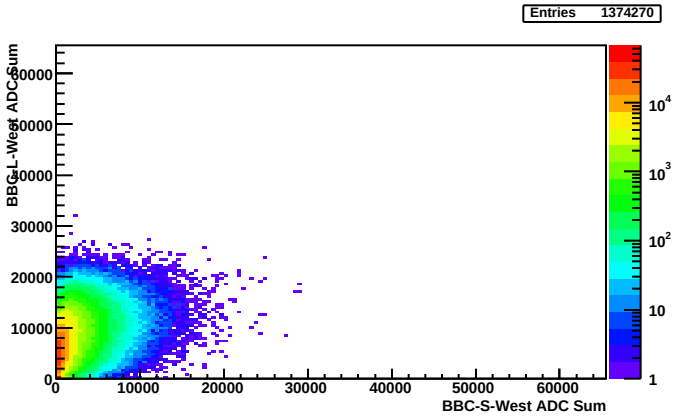

Entries 1374270
TOF Mult
VPD-East ADC Sum

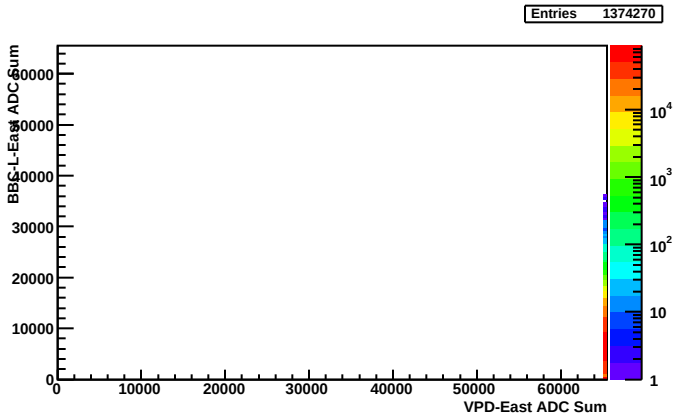

Entries 1374270
TOF Mult
VPD-West ADC Sum

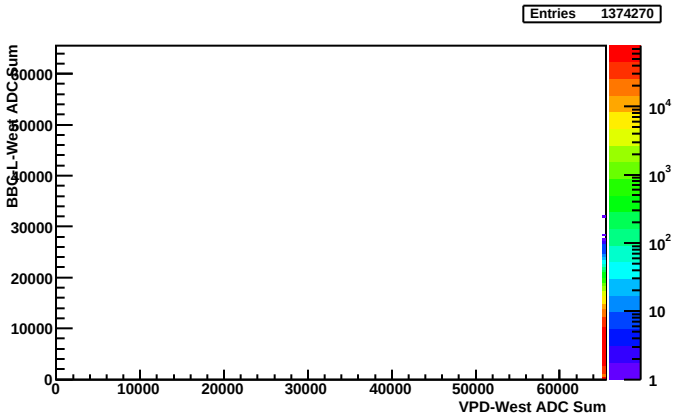

Entries 1374270
TOF Mult
ZDC-East ADC Sum Att

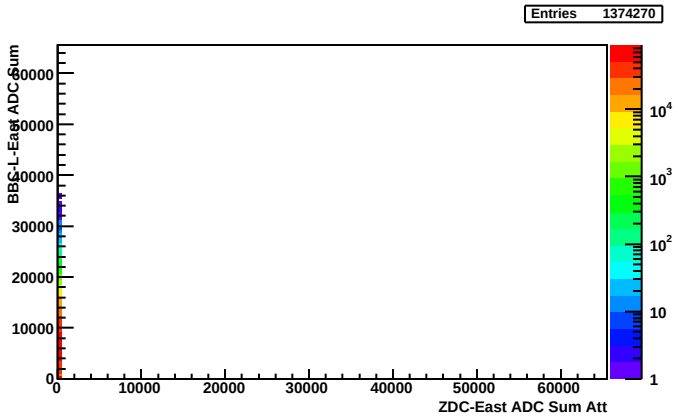

Entries 1374270
TOF Mult
ZDC-West ADC Sum Att

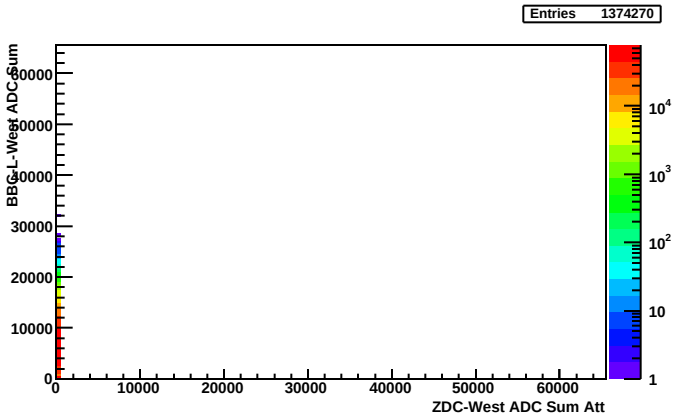

Entries 1374270
BBC-L-East ADC Sum
BBC-S-East ADC Sum

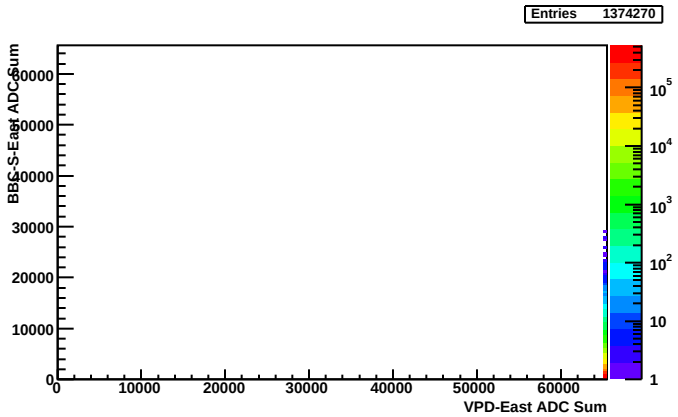

Entries 1374270
BBC-L-West ADC Sum
BBC-S-West ADC Sum

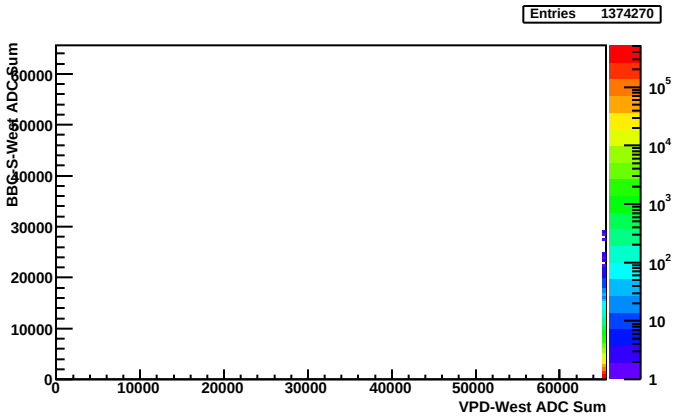

Entries 1374270
BBC-L-East ADC Sum
VPD-East ADC Sum

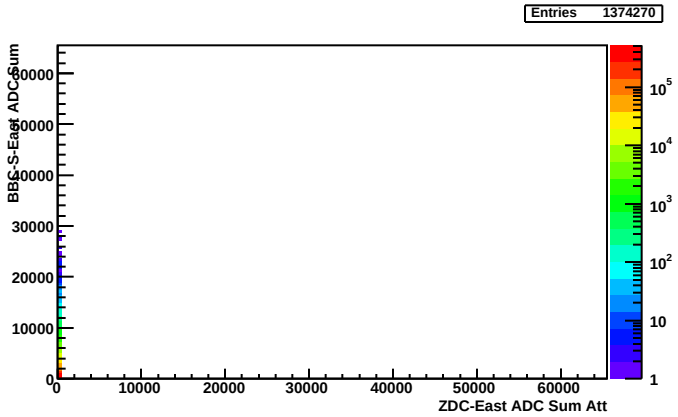

Entries 1374270
BBC-L-West ADC Sum
VPD-West ADC Sum

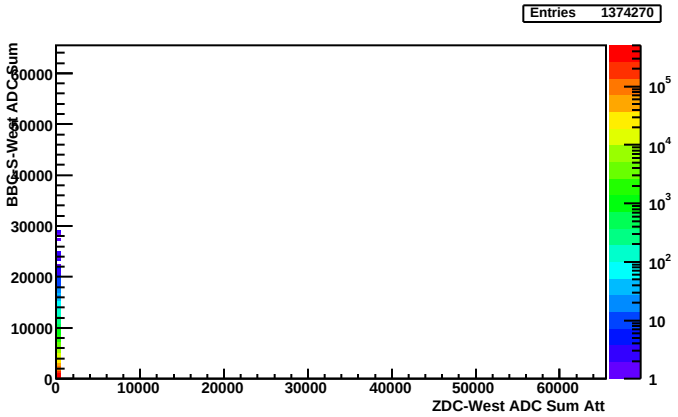

Entries 1374270
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

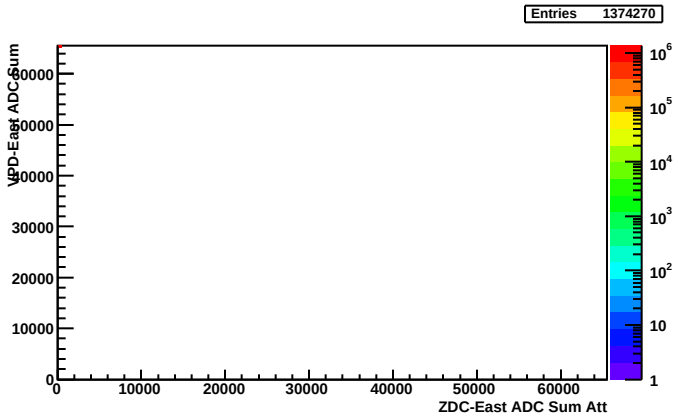

Entries 1374270
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

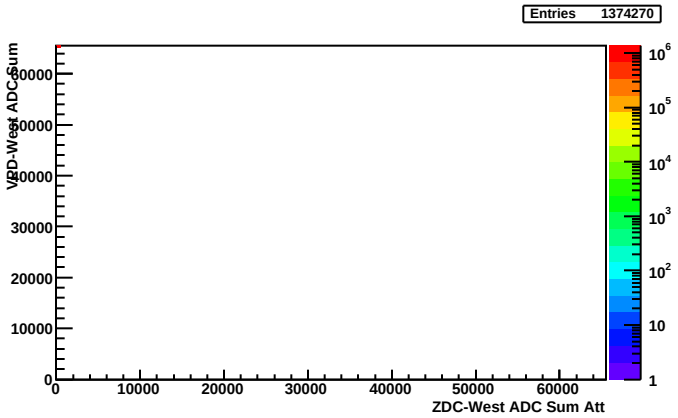

Entries 1374270
BBC-S-East ADC Sum
VPD-East ADC Sum

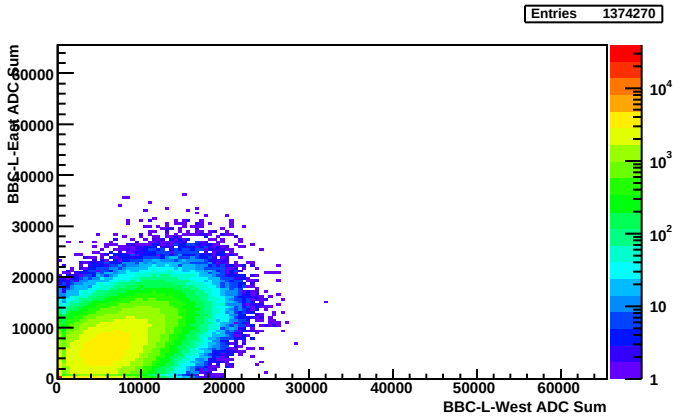

Entries 1374270
BBC-S-West ADC Sum
VPD-West ADC Sum

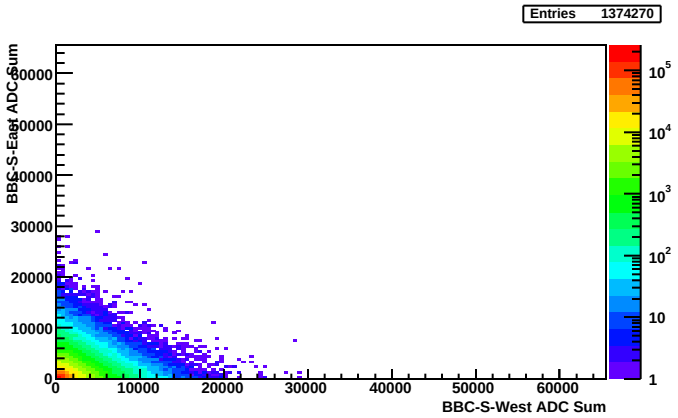

Entries 1374270
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

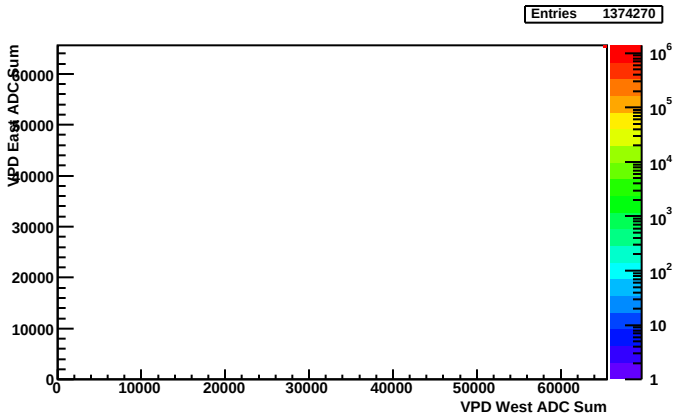

Entries 1374270
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

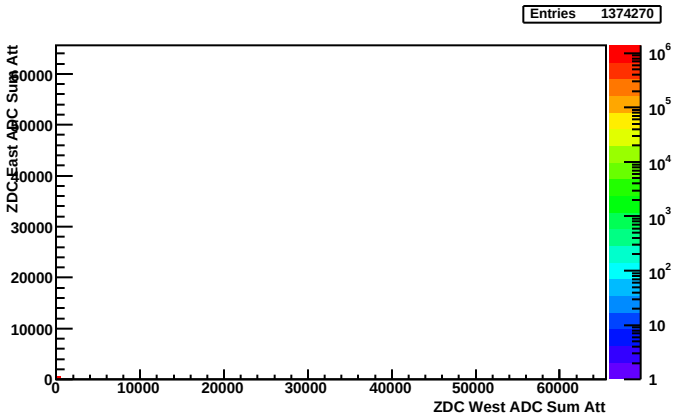

Entries 1374270
VPD-East ADC Sum
ZDC-East ADC Sum Att

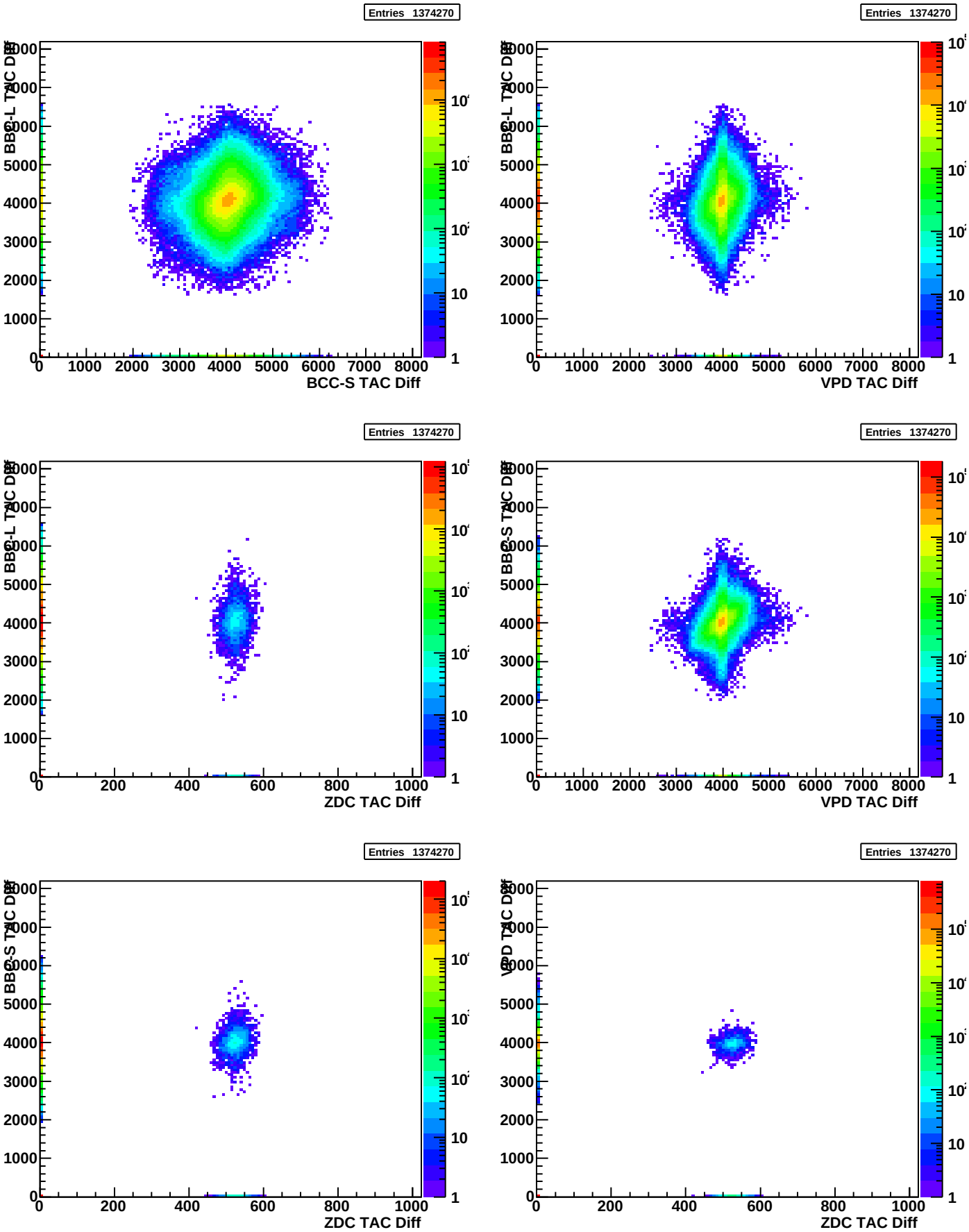

Entries 1374270
VPD-West ADC Sum
ZDC-West ADC Sum Att

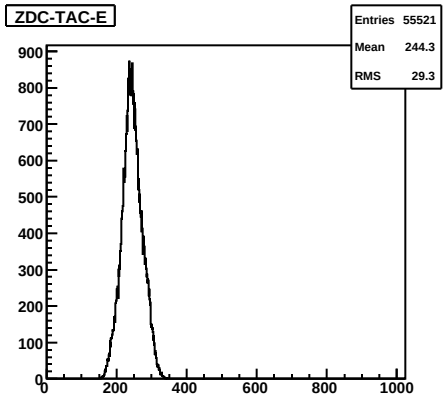

Entries 1374270
BBC-L-East ADC Sum
BBC-L-West ADC Sum

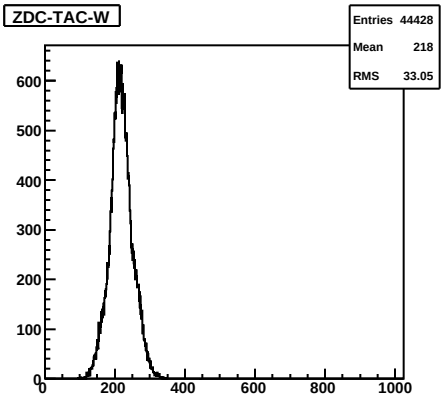

Entries 1374270
BBC-S-East ADC Sum
BBC-S-West ADC Sum

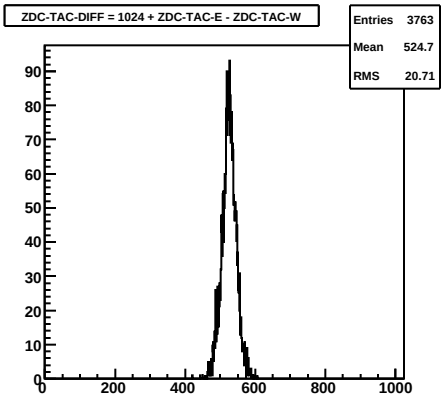

Entries 1374270
VPD East ADC Sum
VPD West ADC Sum

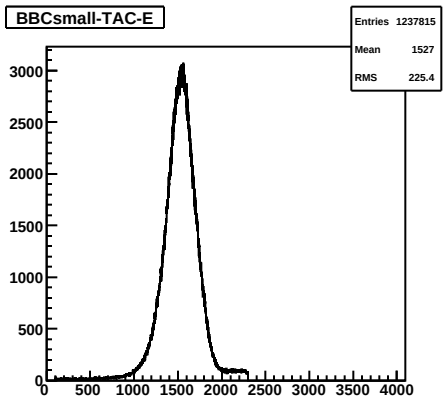

Entries 1374270
ZDC East ADC Sum Att
ZDC West ADC Sum Att

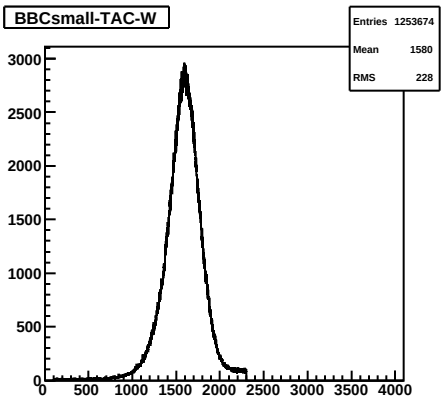

Entries 1374270
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1374270
BBC-L TAC Diff
VPD TAC Diff
Entries 1374270
BBC-L TAC Diff
ZDC TAC Diff
Entries 1374270
BBC-S TAC Diff
VPD TAC Diff
Entries 1374270
BBC-S TAC Diff
ZDC TAC Diff
Entries 1374270
VPD TAC Diff
ZDC TAC Diff

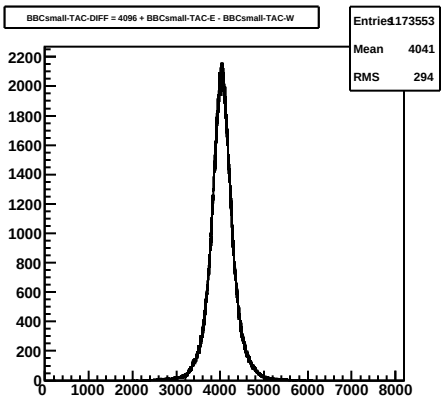

ZDC-TAC-E
Entries 55521
Mean 244.3
RMS 29.3

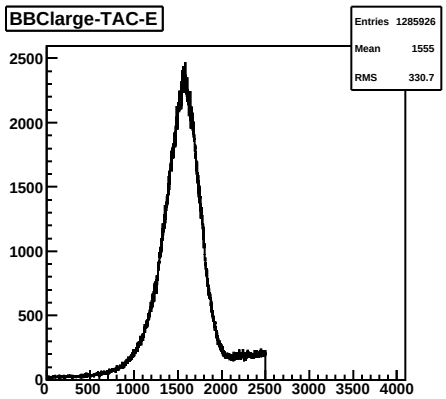

ZDC-TAC-W
Entries 44428
Mean 218
RMS 33.05

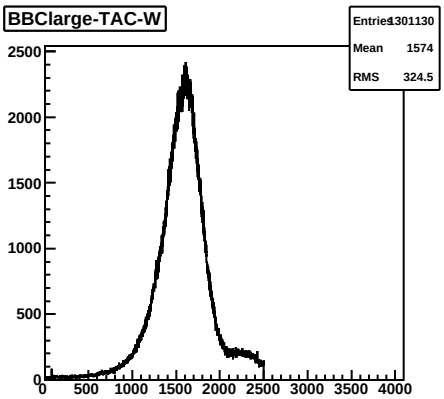

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3763
Mean 524.7
RMS 20.71

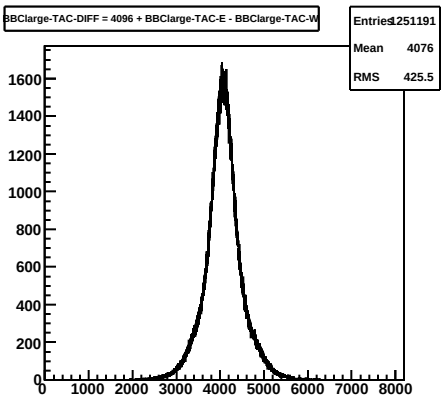

BBCsmall-TAC-E
Entries 1237815
Mean 1527
RMS 225.4

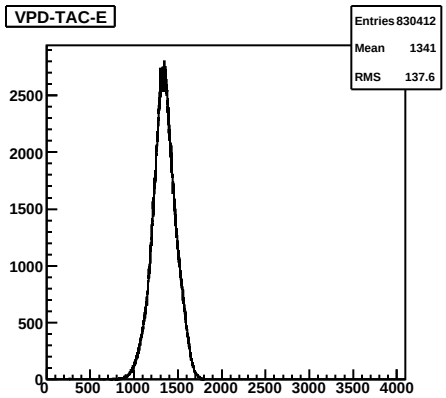

BBCsmall-TAC-W
Entries 1253674
Mean 1580
RMS 228

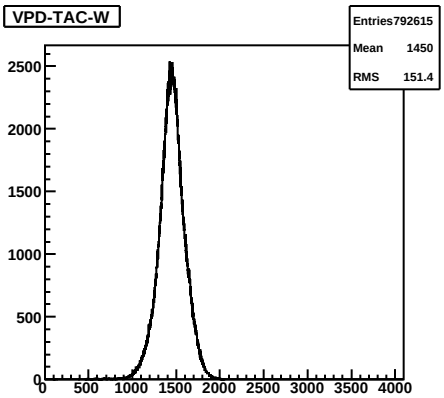

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1173553
Mean 4041
RMS 294

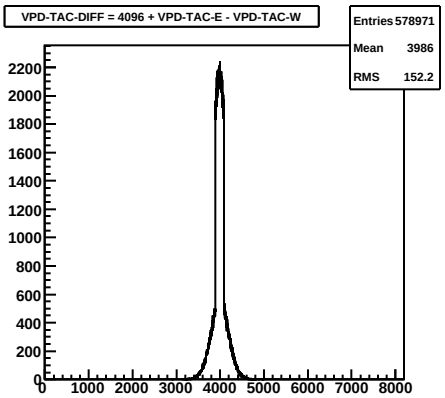

BBClarge-TAC-E
Entries 1285926
Mean 1555
RMS 330.7

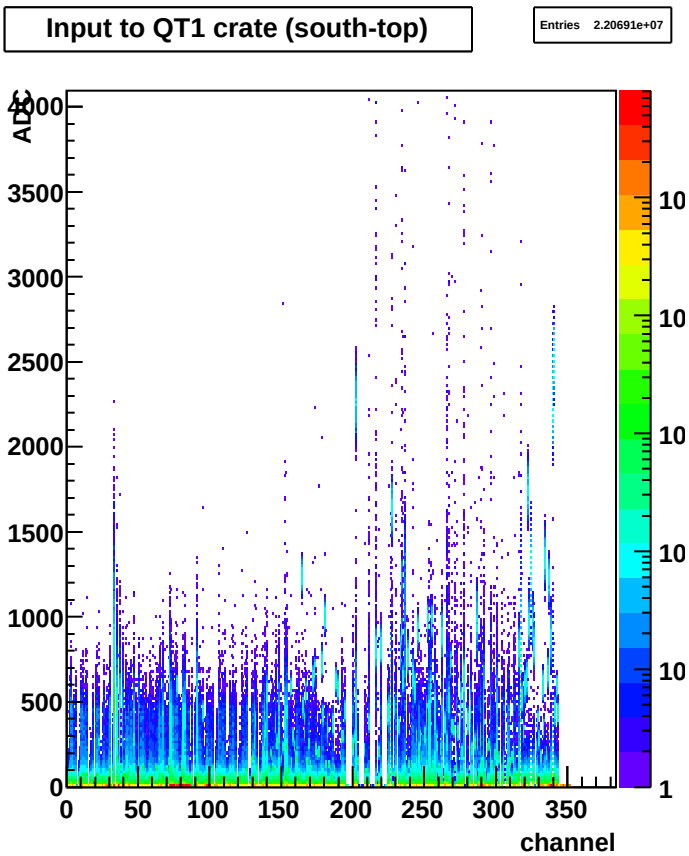

BBClarge-TAC-W
Entries 1301130
Mean 1574
RMS 324.5

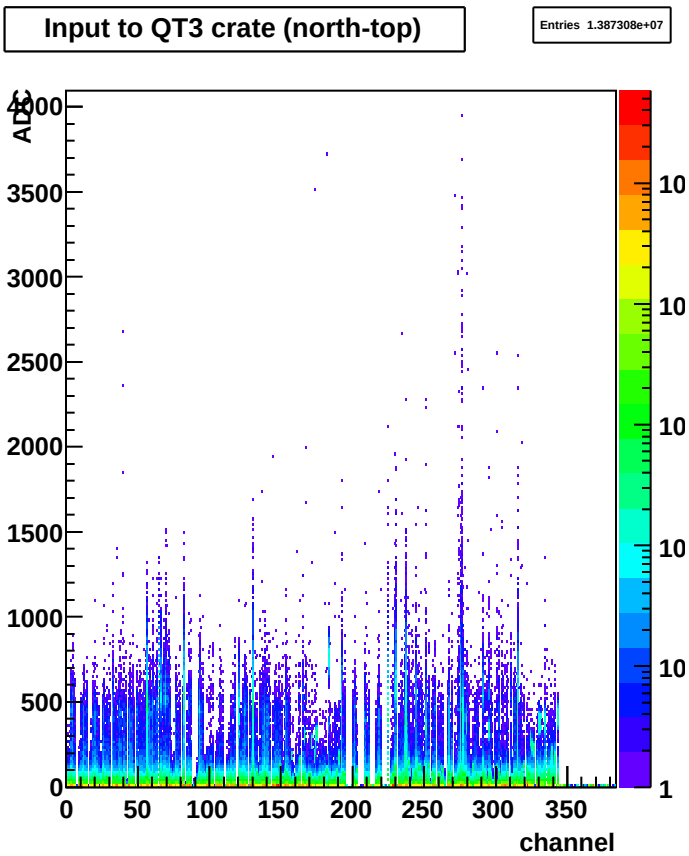

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1251191
Mean 4076
RMS 425.5

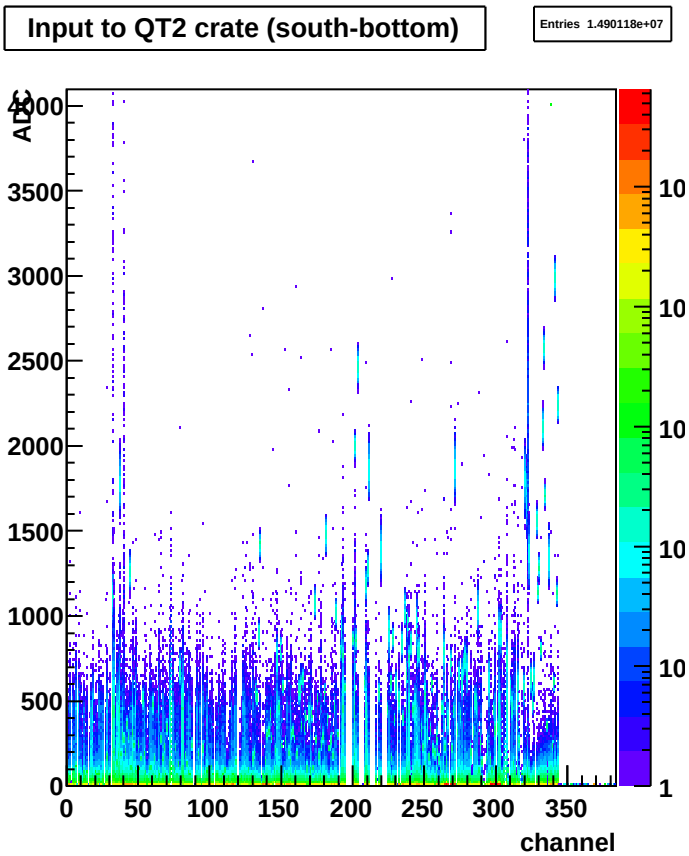

VPD-TAC-E
Entries 830412
Mean 1341
RMS 137.6

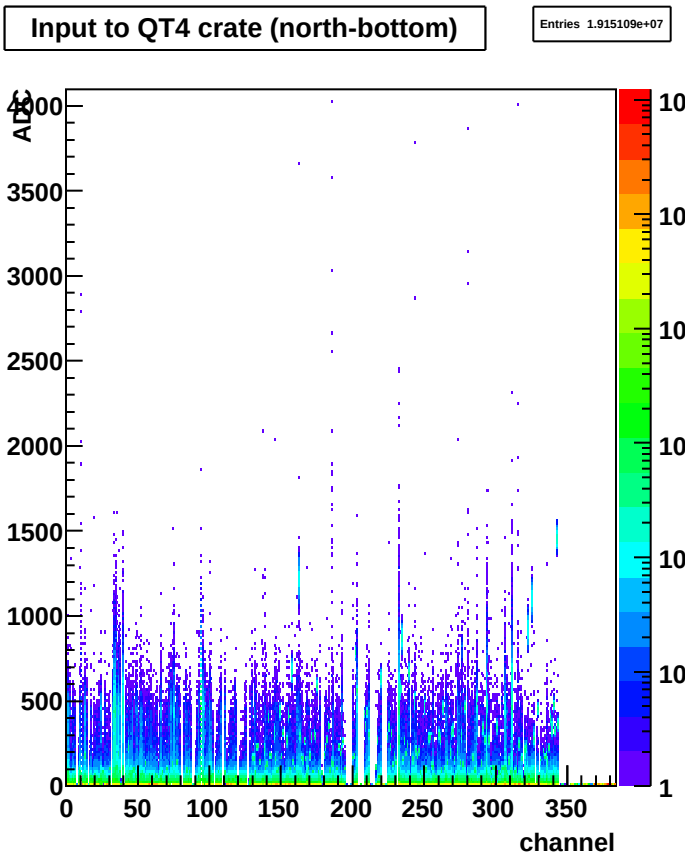

VPD-TAC-W
Entries 792615
Mean 1450
RMS 151.4

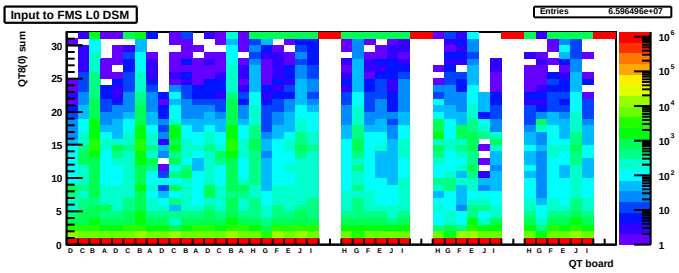

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 578971
Mean 3986
RMS 152.2

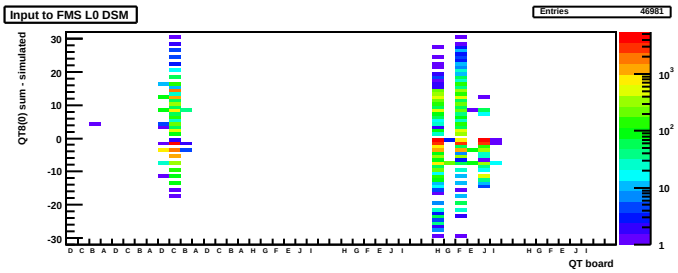

Input to QT1 crate (south-top)
Entries 2.20691e+07
ADC
channel

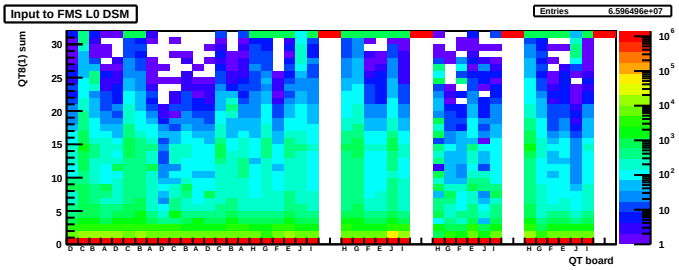

Input to QT3 crate (north-top)
Entries 1.387308e+07
ADC
channel

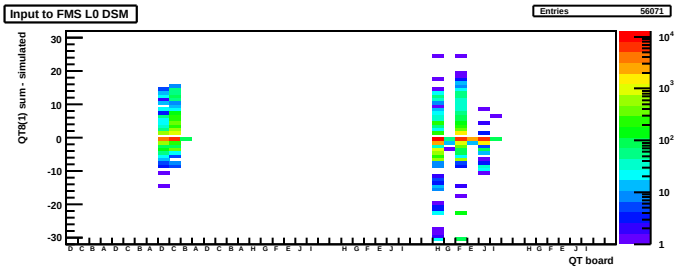

Input to QT2 crate (south-bottom)
Entries 1.490118e+07
ADC
channel

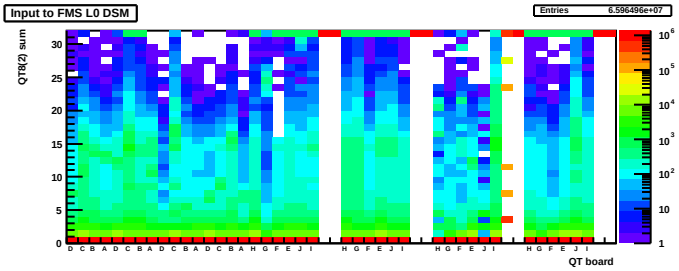

Input to QT4 crate (north-bottom)
Entries 1.915109e+07
ADC
channel

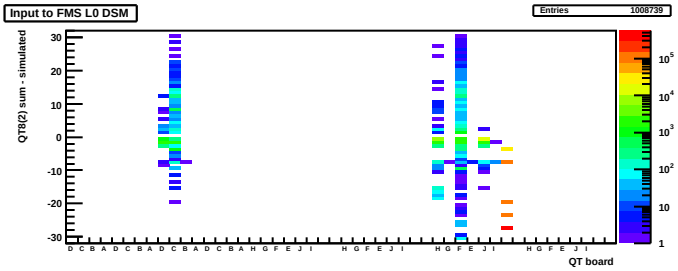

Input to FMS L0 DSM
Entries 6.596496e+07
QT8(0) sum
QT board

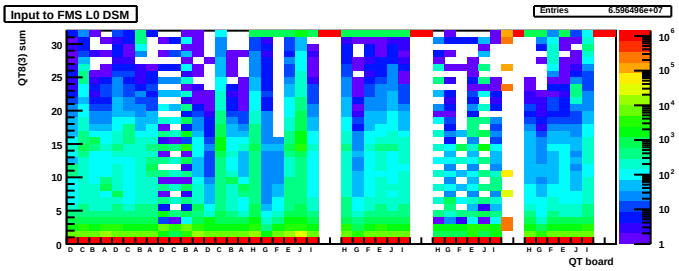

Input to FMS L0 DSM
Entries 46981
QT8(0) sum - simulated
QT board

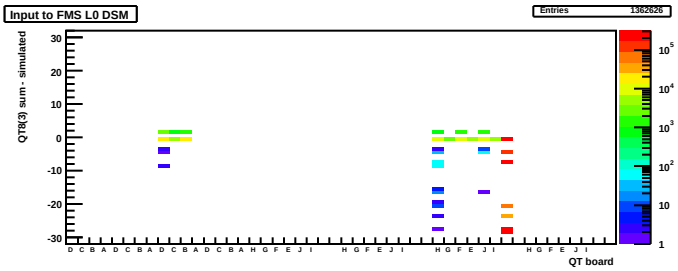

Input to FMS L0 DSM
Entries 6.596496e+07
QT8(1) sum
QT board

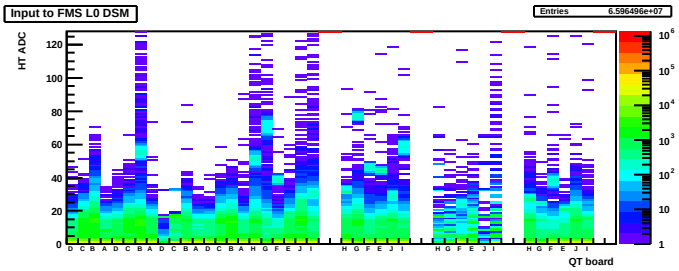

Input to FMS L0 DSM
Entries 56071
QT8(1) sum - simulated
QT board

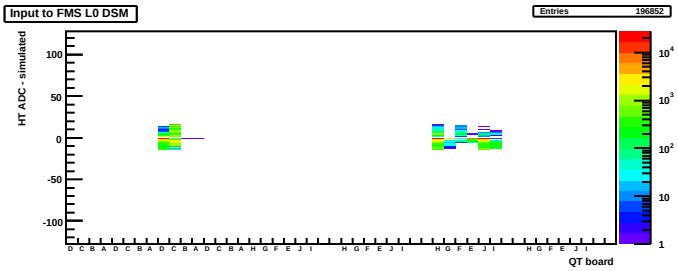

Input to FMS L0 DSM
Entries 6.596496e+07
QT8(2) sum
QT board

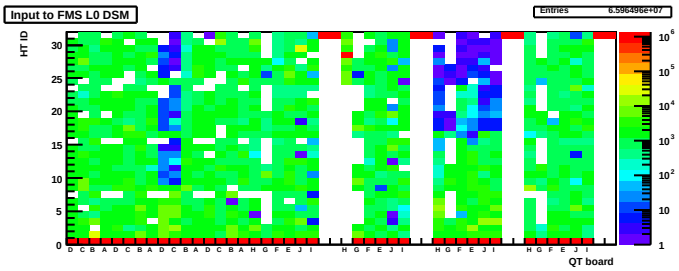

Input to FMS L0 DSM
Entries 1008739
QT8(2) sum - simulated
QT board

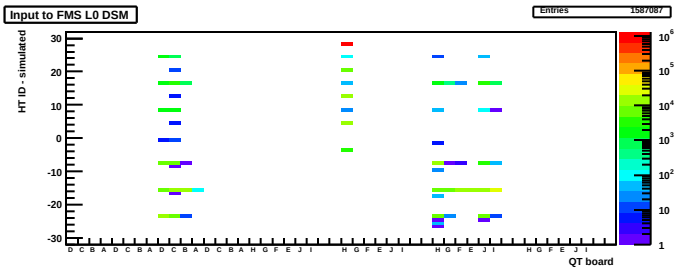

Input to FMS L0 DSM
Entries 6.596496e+07
QT8(3) sum
QT board

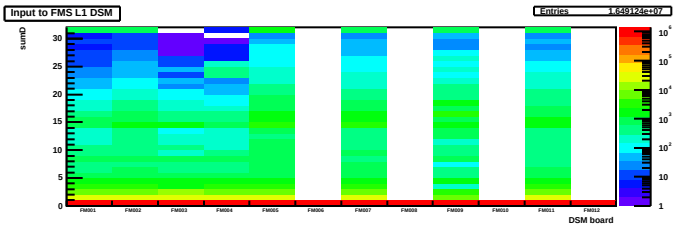

Input to FMS L0 DSM
Entries 1362626
QT8(3) sum - simulated
QT board

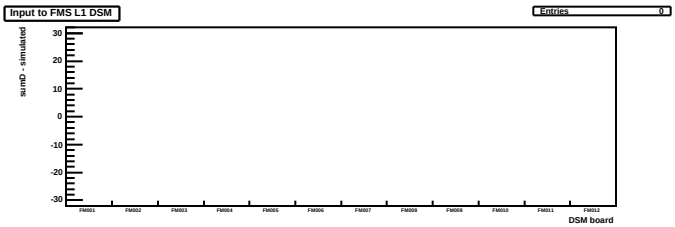

Input to FMS L0 DSM
Entries 6.596496e+07
HT ADC
QT board

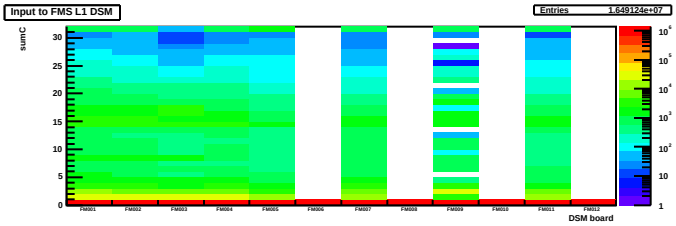

Input to FMS L0 DSM
Entries 196852
HT ADC - simulated
QT board

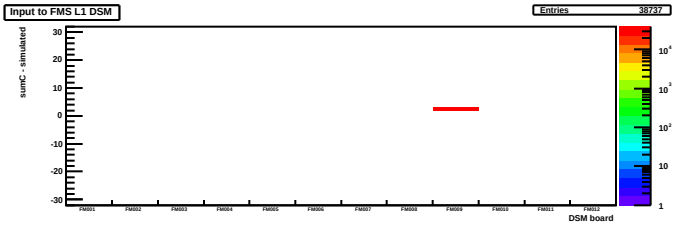

Input to FMS L0 DSM
Entries 6.596496e+07
HT ID
QT board

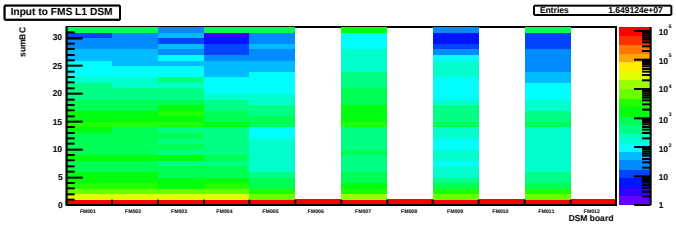

Input to FMS L0 DSM
Entries 1587087
HT ID - simulated
QT board

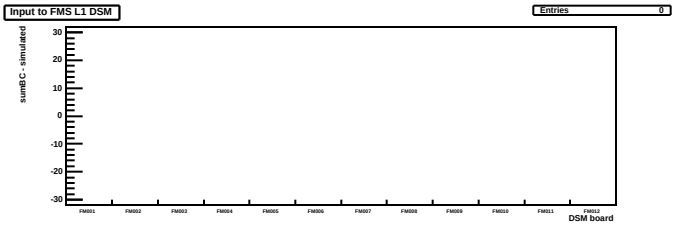

Input to FMS L1 DSM
Entries 1.649124e+07
sumD
DSM board

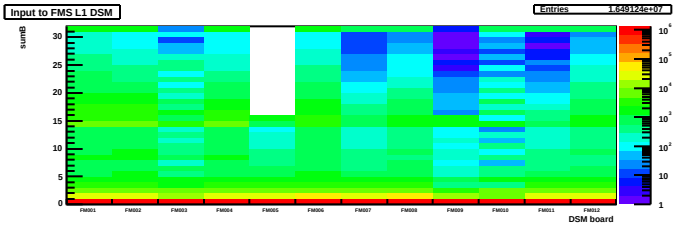

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

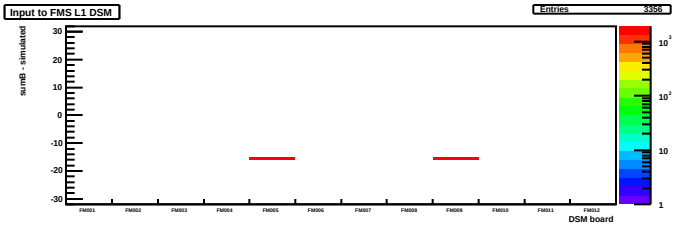

Input to FMS L1 DSM
Entries 1.649124e+07
sumC
DSM board

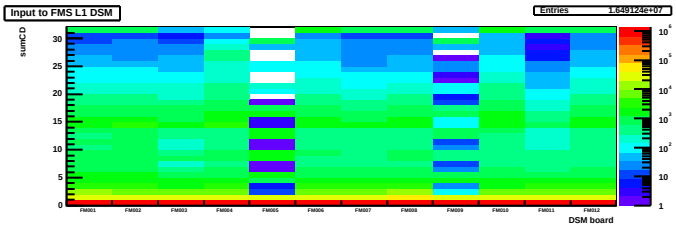

Input to FMS L1 DSM
Entries 38737
sumC - simulated
DSM board

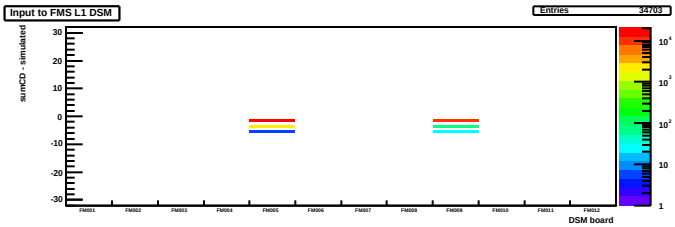

Input to FMS L1 DSM
Entries 1.649124e+07
sumBC
DSM board

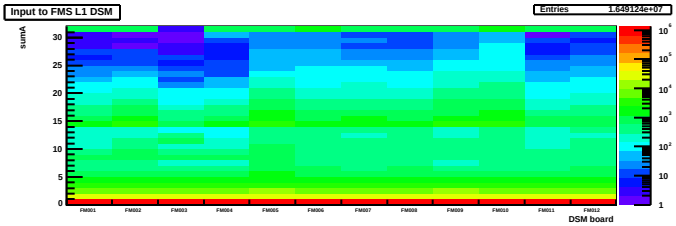

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

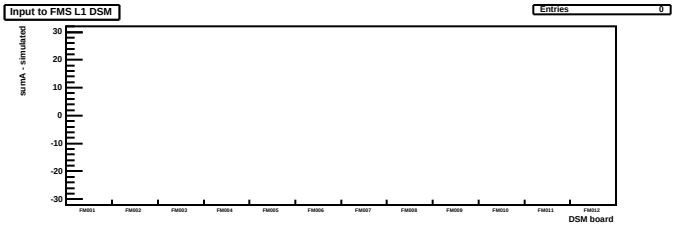

Input to FMS L1 DSM
Entries 1.649124e+07
sumB
DSM board

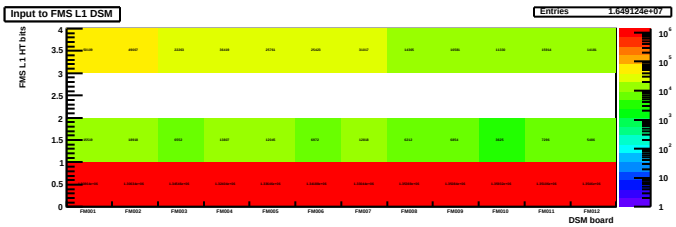

Input to FMS L1 DSM
Entries 3356
sumB - simulated
DSM board

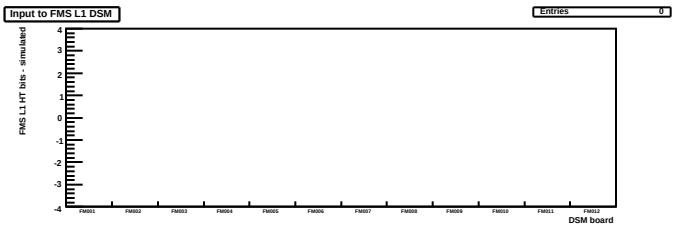

Input to FMS L1 DSM
Entries 1.649124e+07
sumCD
DSM board

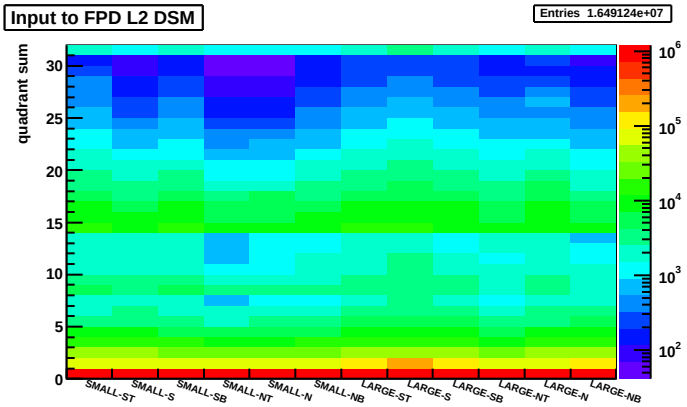

Input to FMS L1 DSM
Entries 34703
sumCD - simulated
DSM board

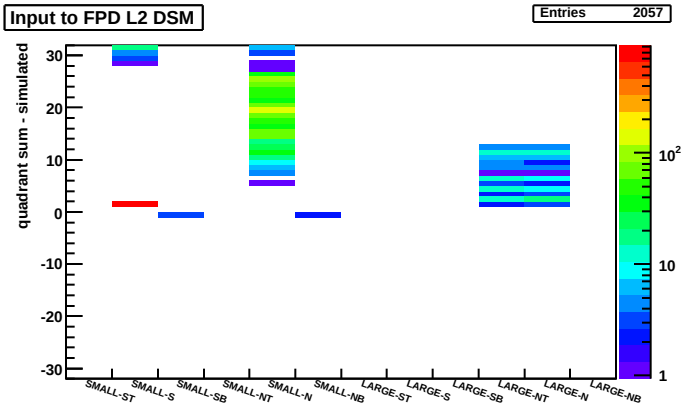

Input to FMS L1 DSM
Entries 1.649124e+07
sumA
DSM board

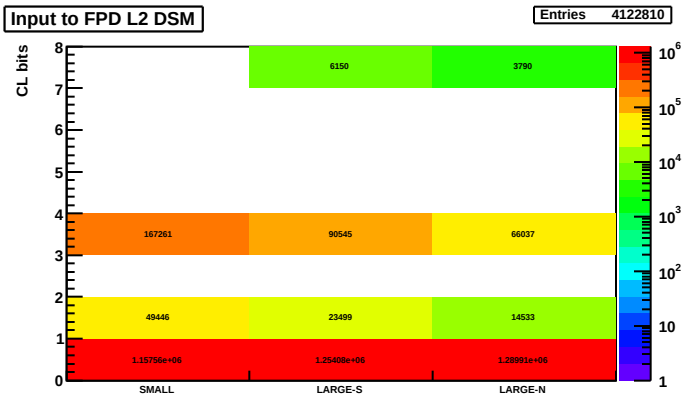

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

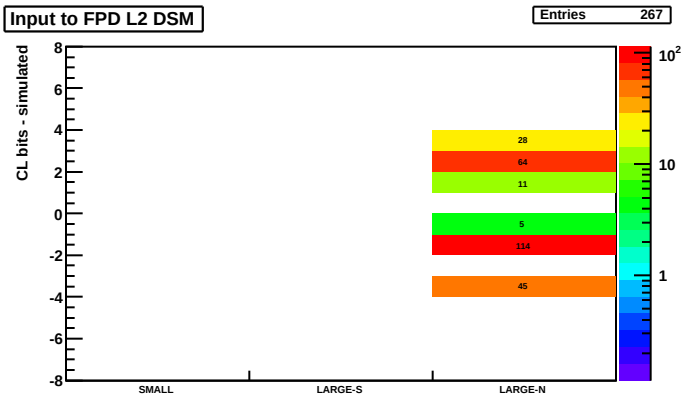

Input to FMS L1 DSM
Entries 1.649124e+07
FMS L1 HT bits
DSM board

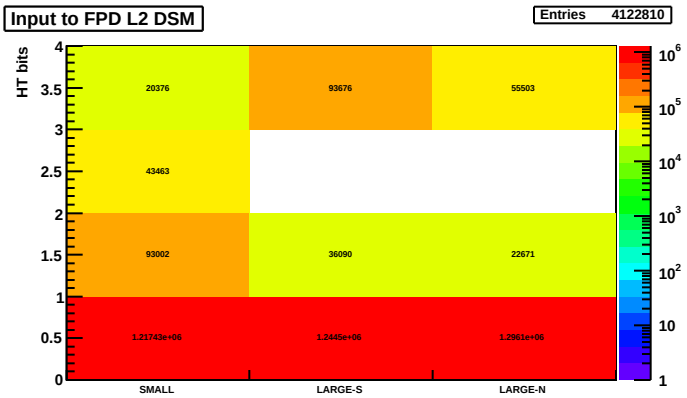

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

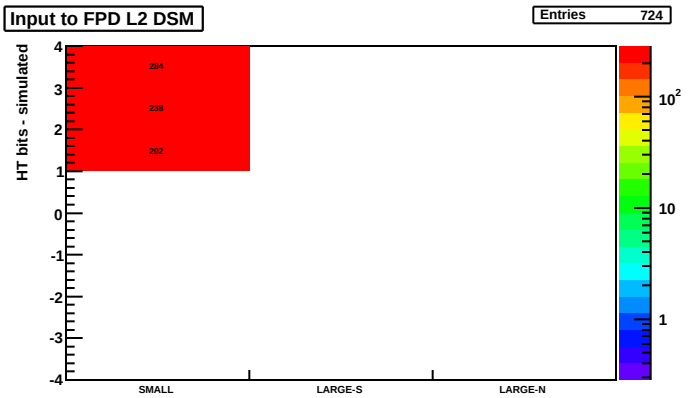

Input to FPD L2 DSM
Entries 1.649124e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

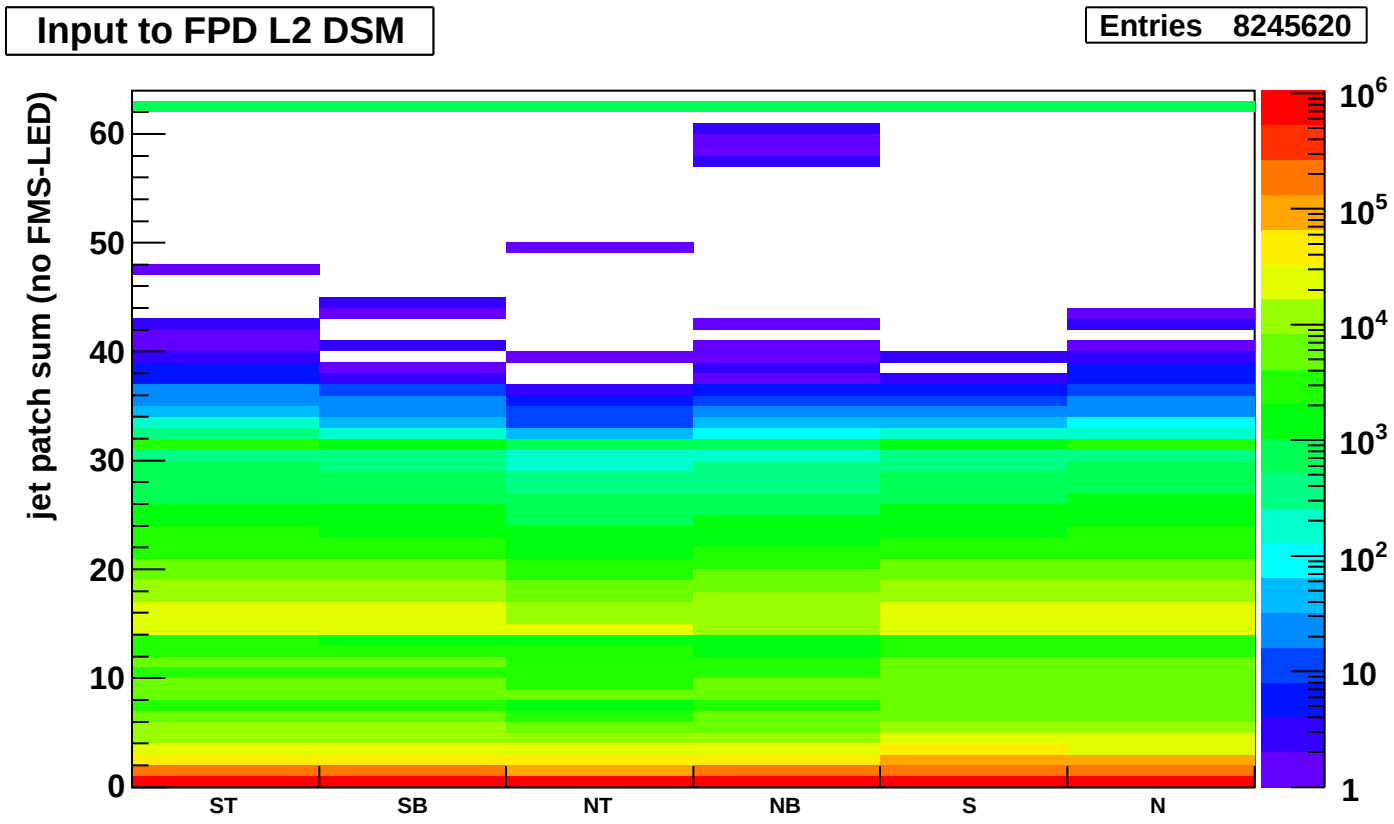

Input to FPD L2 DSM
Entries 2057
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

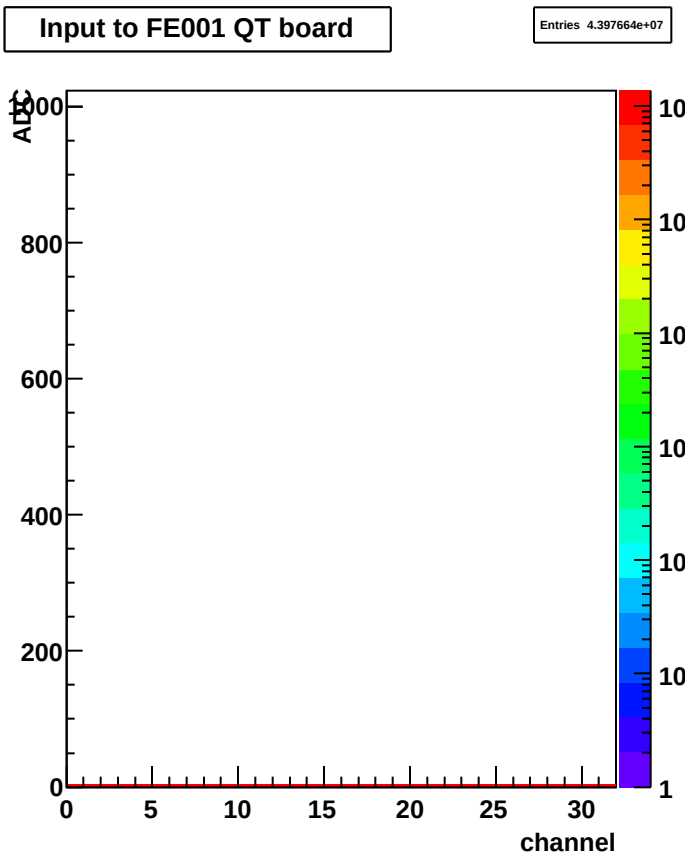

Input to FPD L2 DSM
Entries 4122810
CL bits
6150
3790
167261
90545
66037
49446
23499
14533
1.15756e+06
1.25408e+06
1.28991e+06
SMALL
LARGE-S
LARGE-N

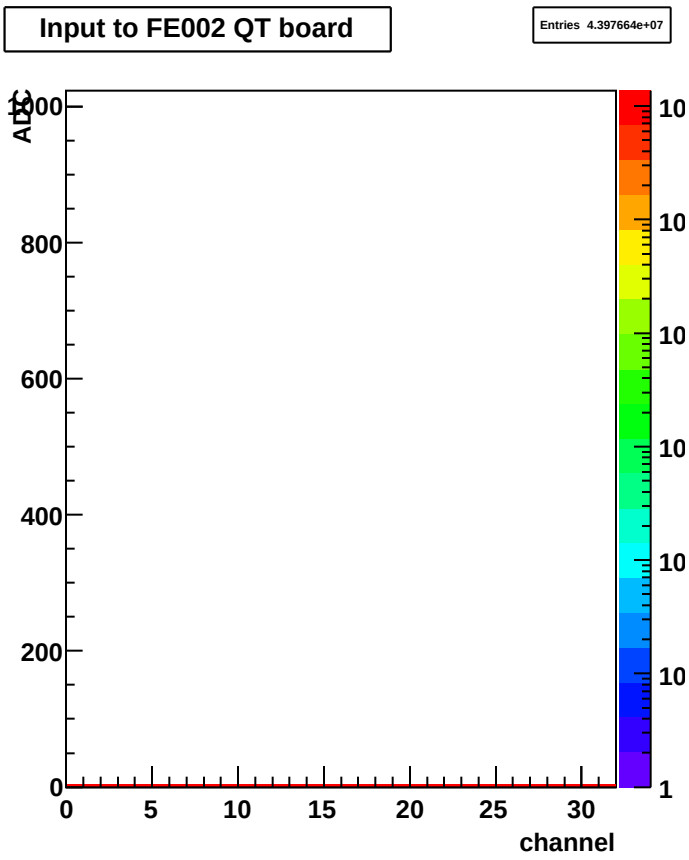

Input to FPD L2 DSM
Entries 267
CL bits - simulated
28
64
11
5
114
45
SMALL
LARGE-S
LARGE-N

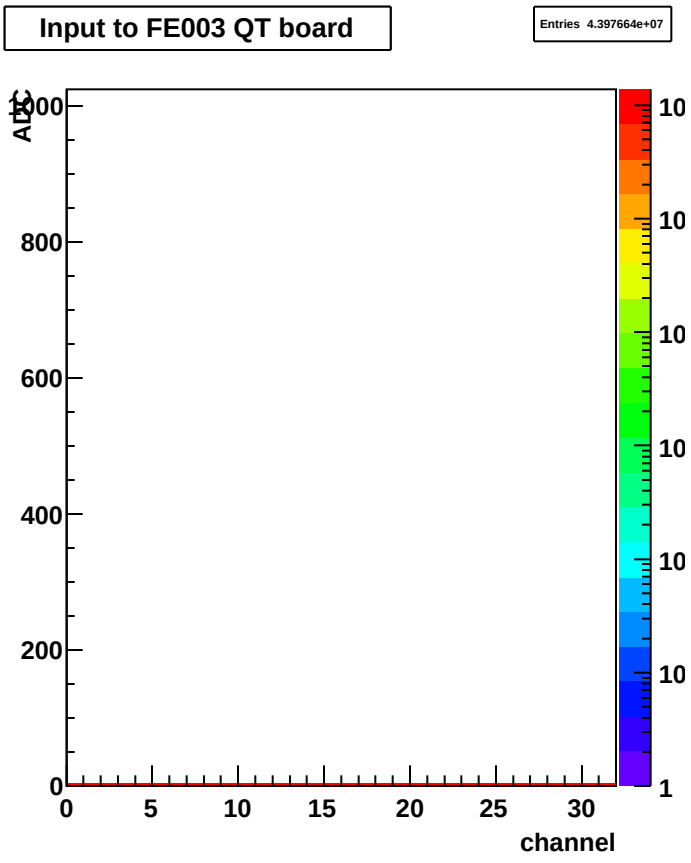

Input to FPD L2 DSM
Entries 4122810
HT bits
20376
93676
55503
43463
93002
36090
22671
1.21743e+06
1.2445e+06
1.2961e+06
SMALL
LARGE-S
LARGE-N

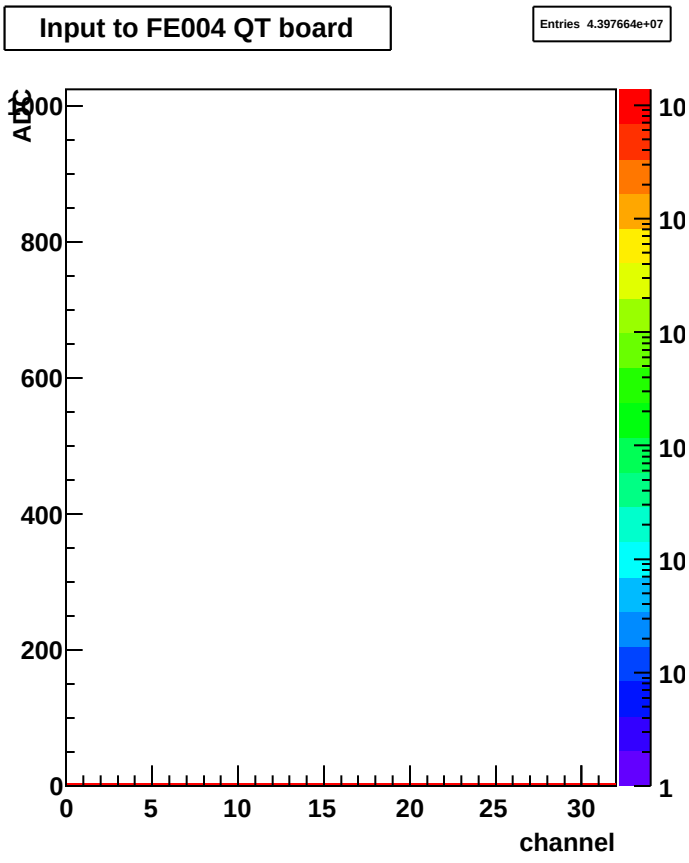

Input to FPD L2 DSM
Entries 724
HT bits - simulated
284
238
202
SMALL
LARGE-S
LARGE-N

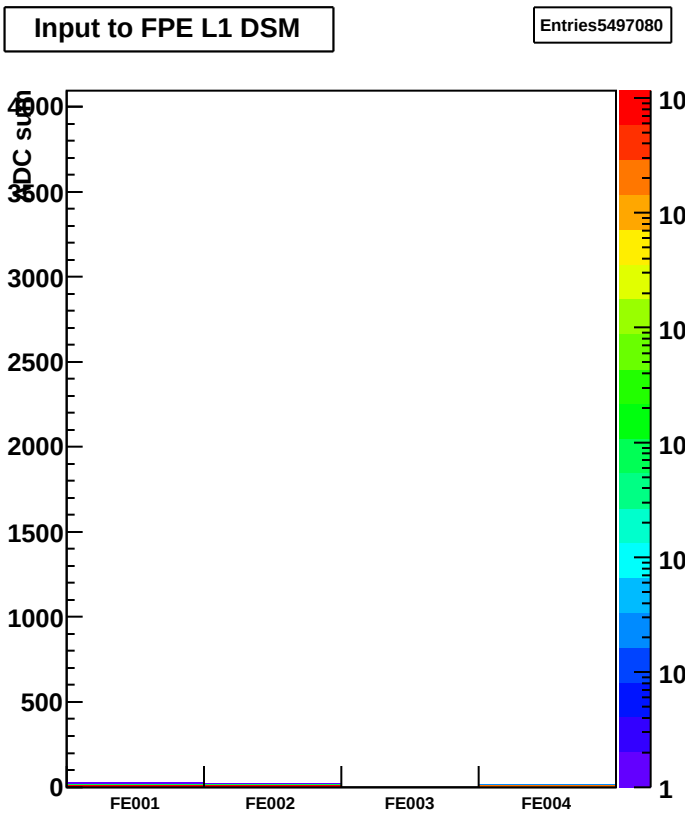

Input to FPD L2 DSM
Entries 8245620
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

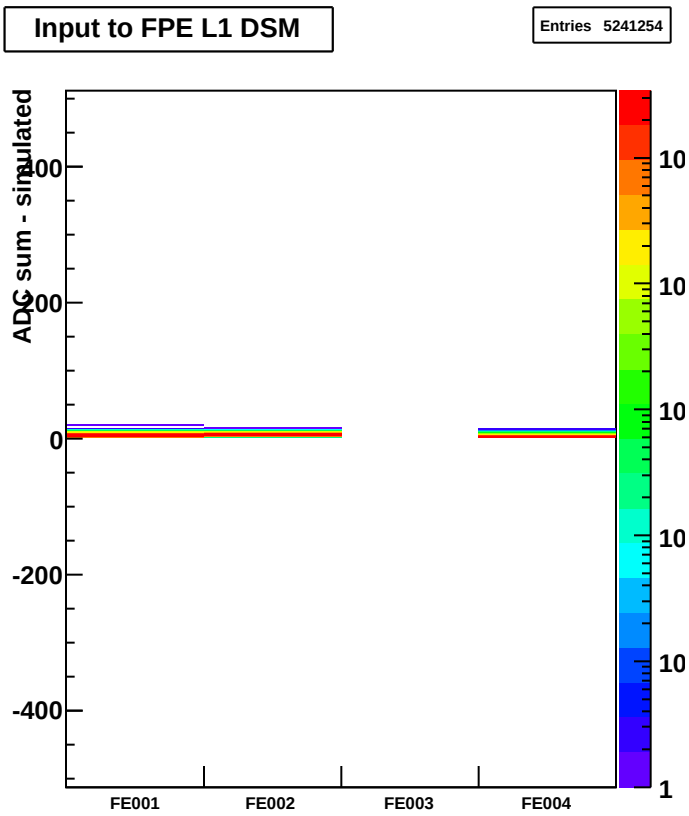

Input to FE001 QT board
Entries 4.397664e+07
ADC
channel

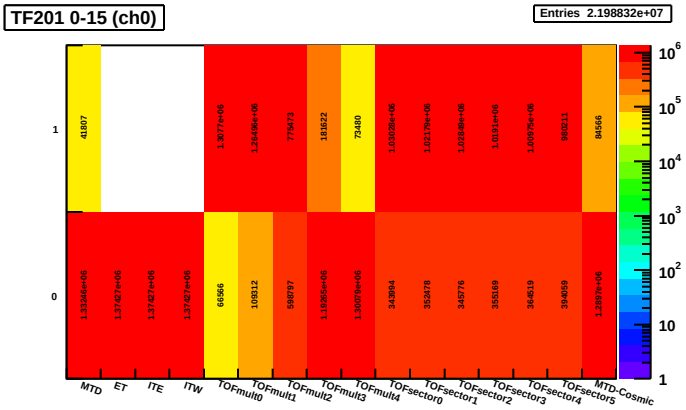

Input to FE002 QT board
Entries 4.397664e+07
ADC
channel

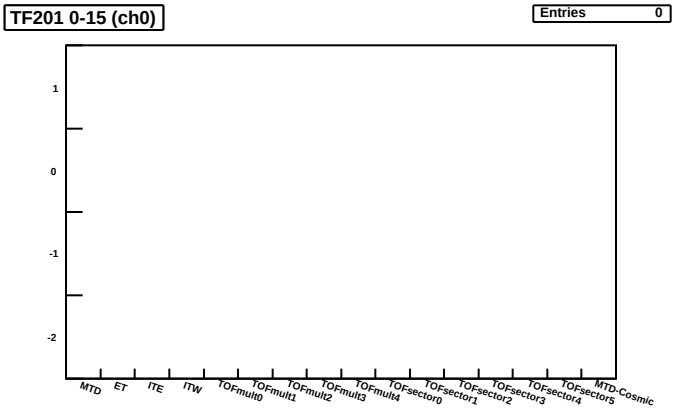

Input to FE003 QT board
Entries 4.397664e+07
ADC
channel


Input to FE004 QT board
Entries 4.397664e+07
ADC
channel


Input to FPE L1 DSM
Entries5497080
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 5241254
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.198832e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
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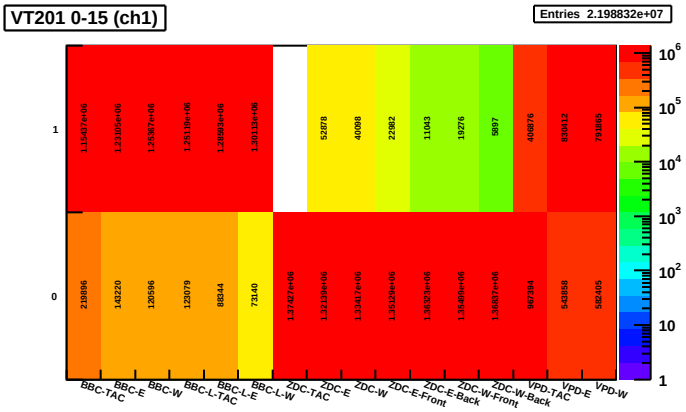
VT201 0-15 (ch1)
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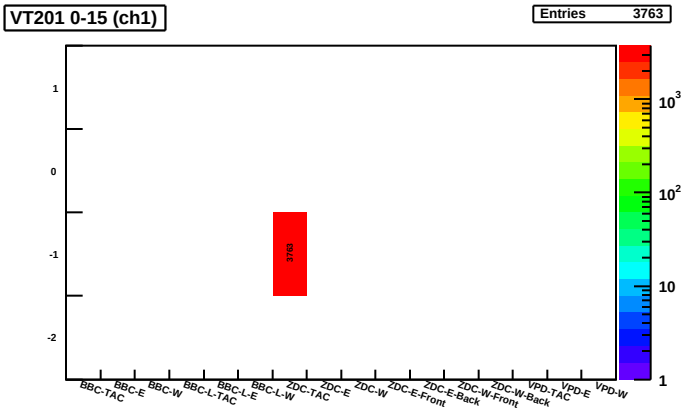
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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Entries 3763
3763
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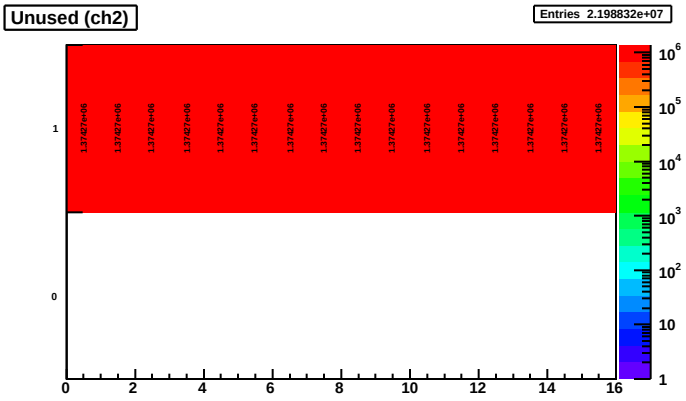
Unused (ch2)
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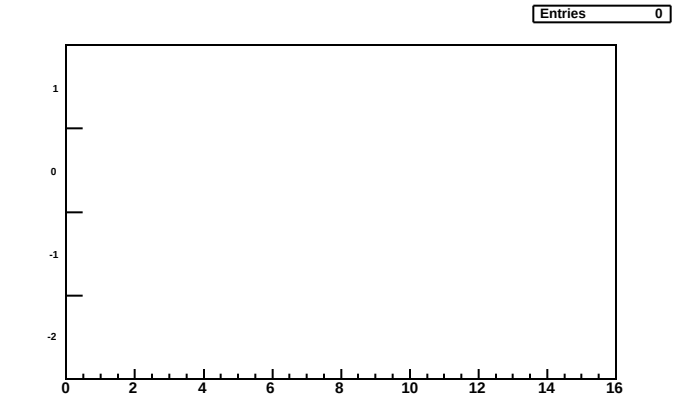
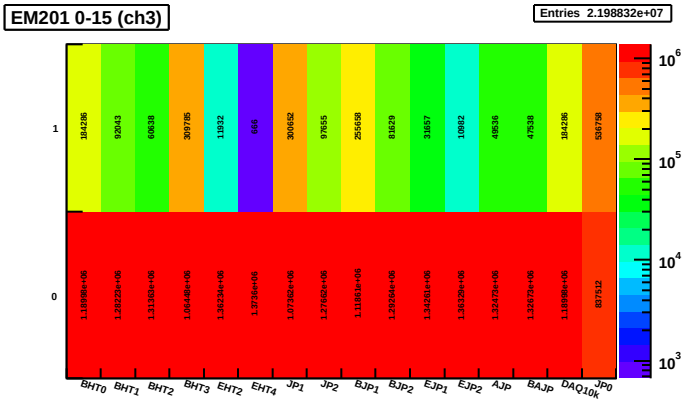

Entries 0


EM201 0-15 (ch3)
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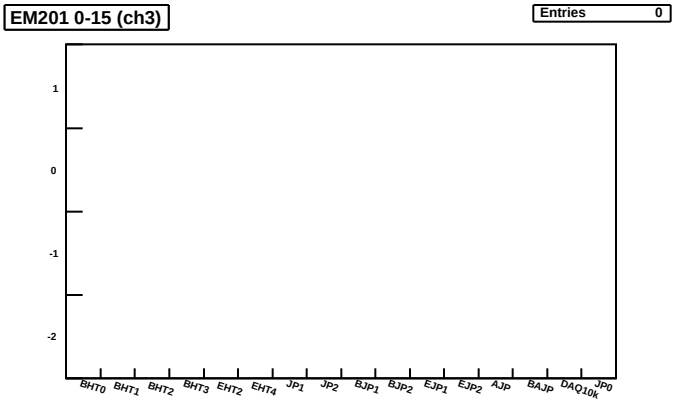
EM201 0-15 (ch3)
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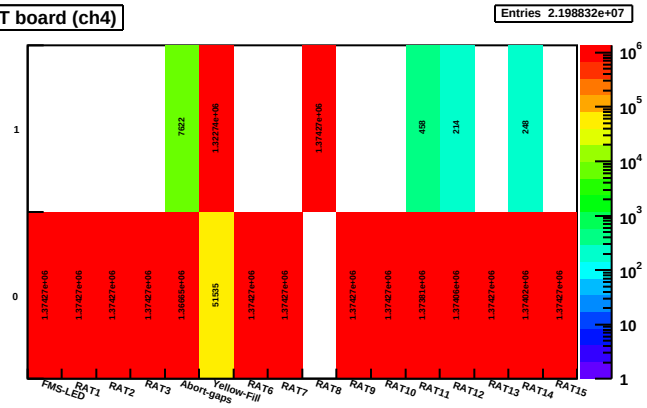
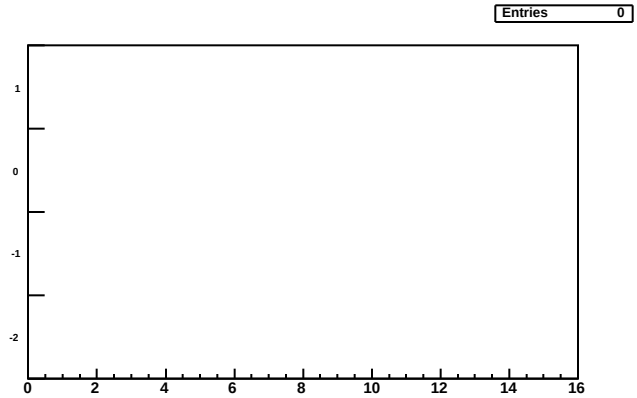
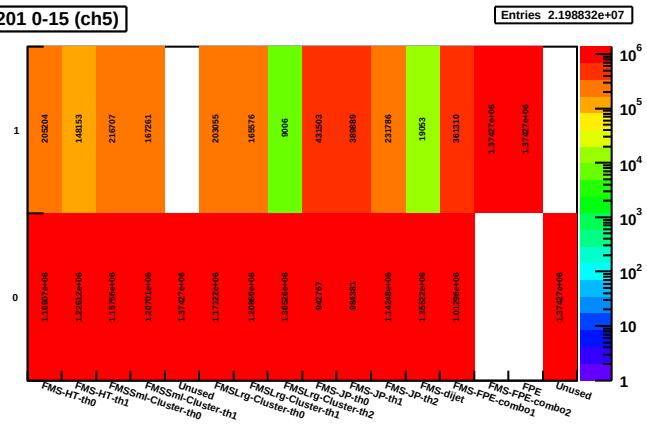
RAT board (ch4)





FP201 0-15 (ch5)



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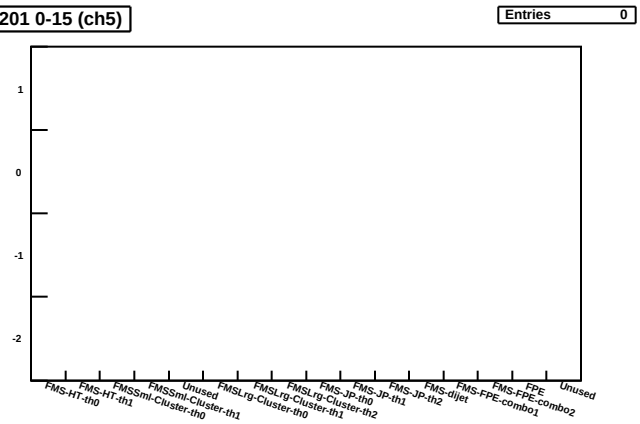
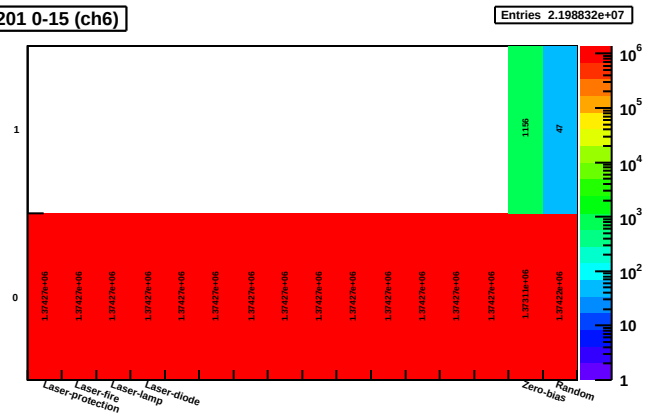
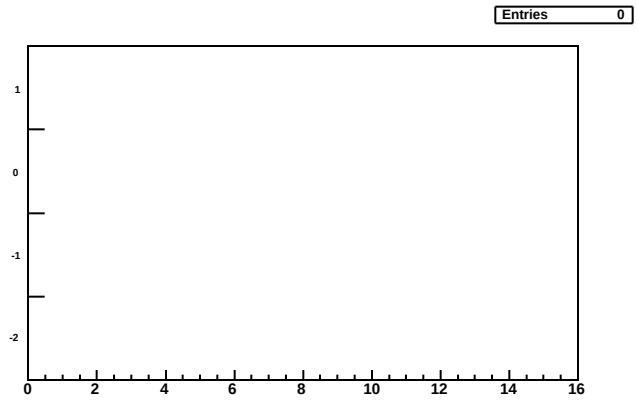
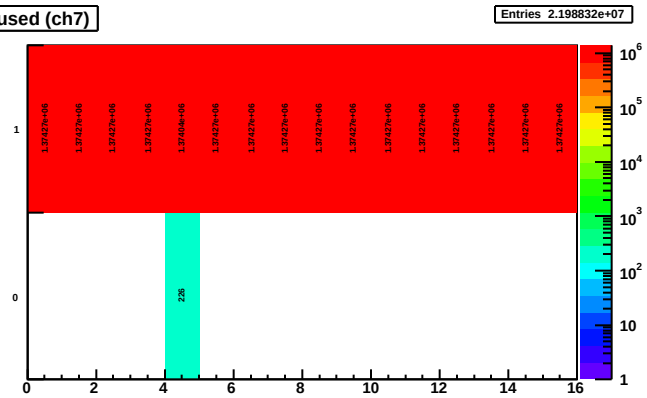
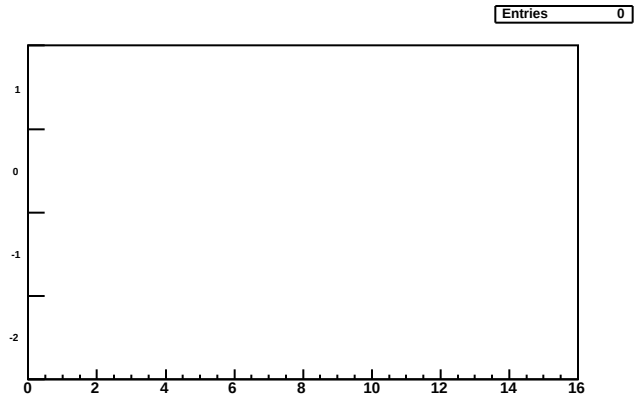


ST201 0-15 (ch6)





Unused (ch7)