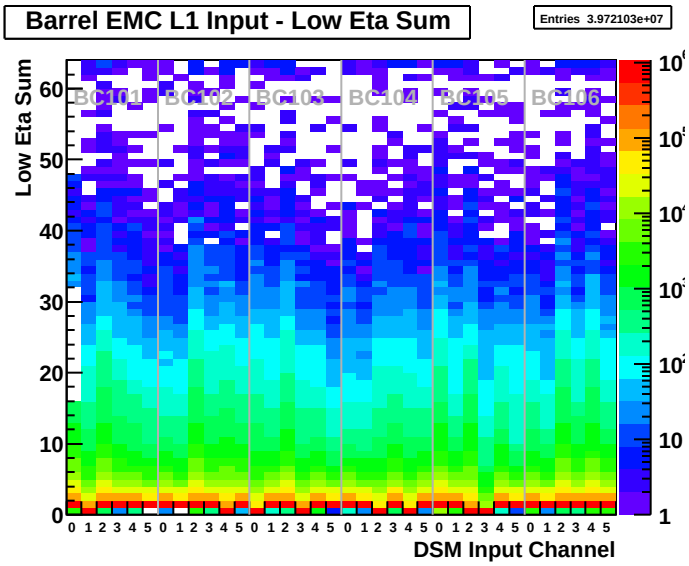
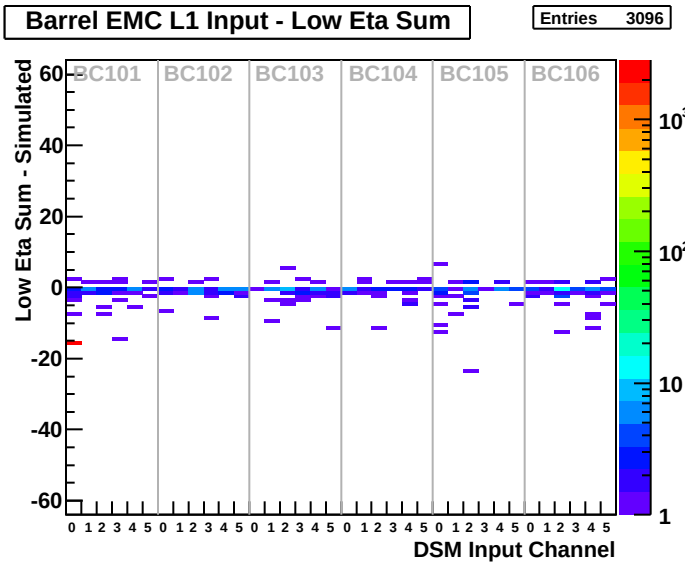
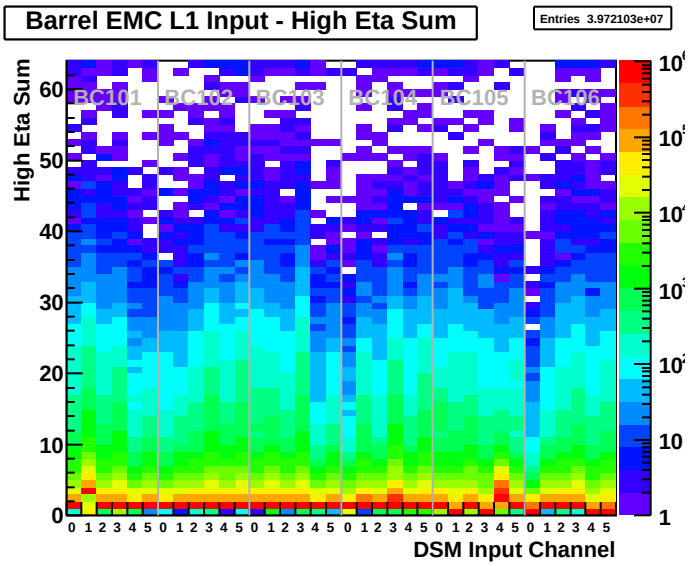
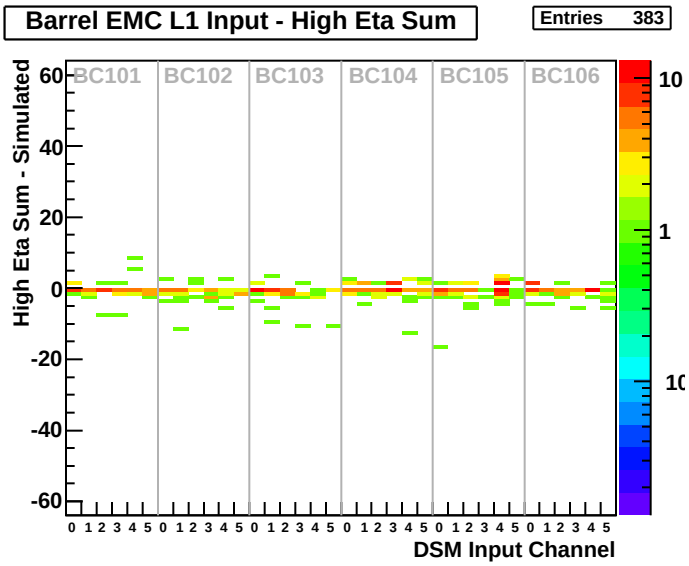
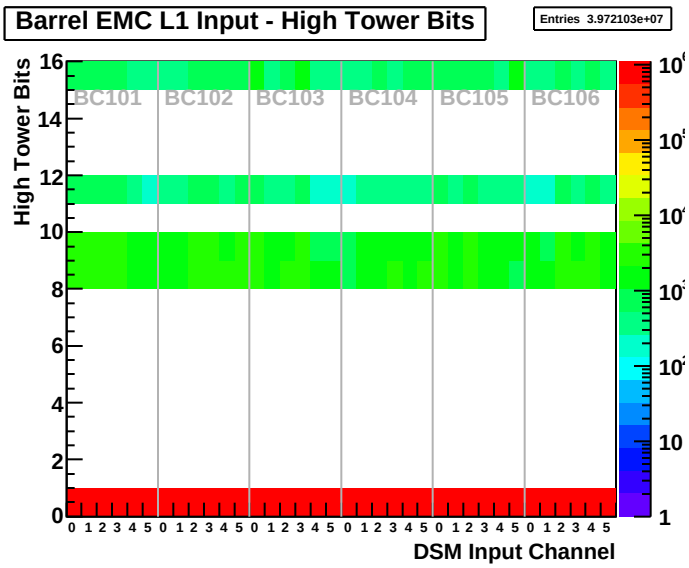
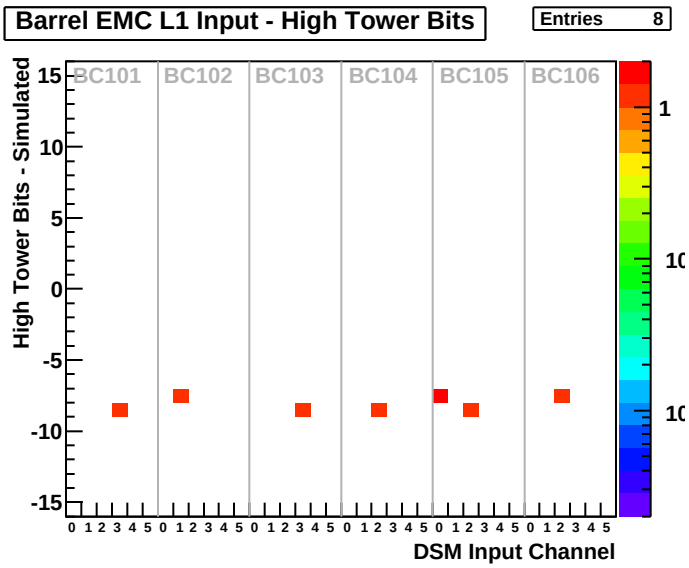

Barrel EMC L1 Input - Low Eta Sum
Entries 3.972103e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

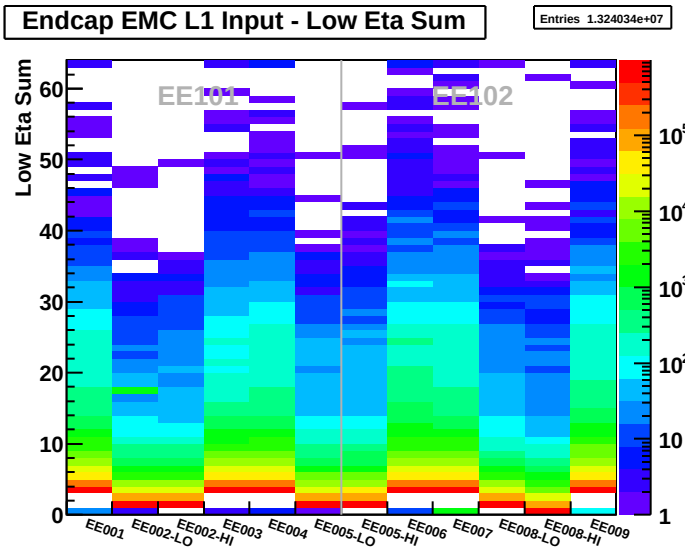

Barrel EMC L1 Input - Low Eta Sum
Entries 3096
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

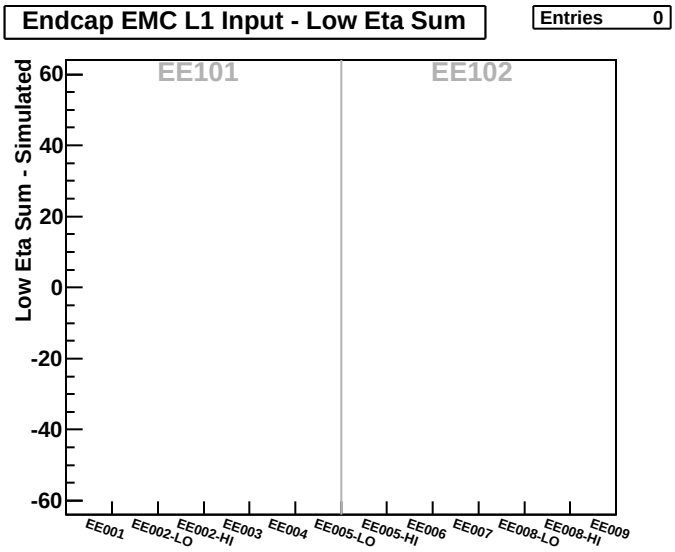

Barrel EMC L1 Input - High Eta Sum
Entries 3.972103e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

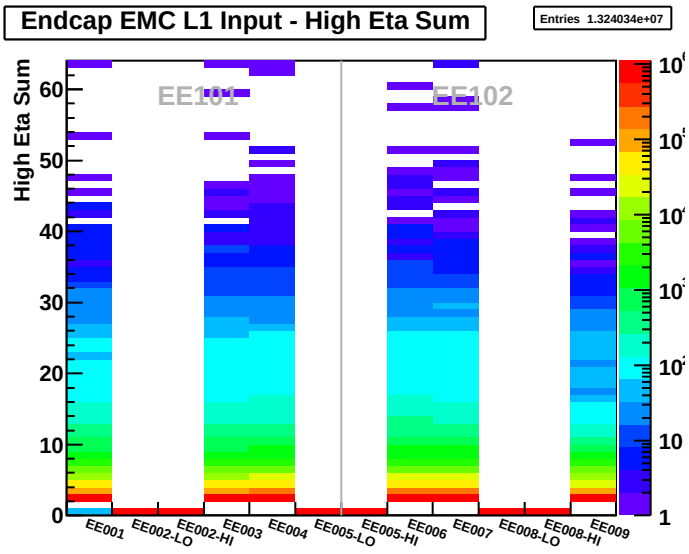

Barrel EMC L1 Input - High Eta Sum
Entries 383
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

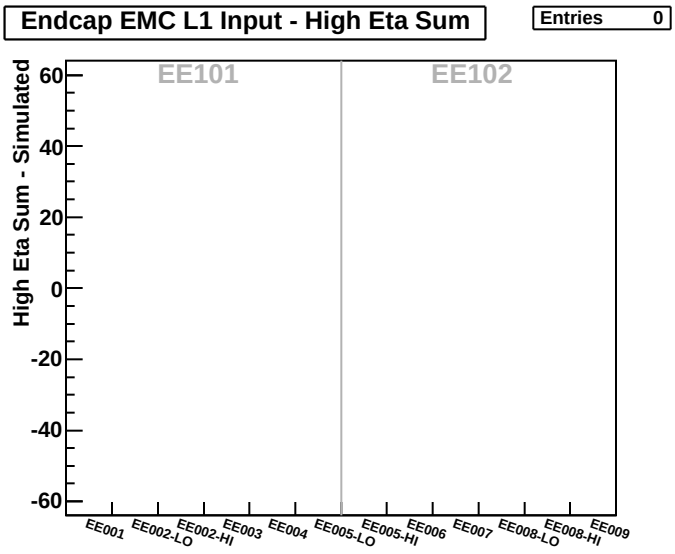

Barrel EMC L1 Input - High Tower Bits
Entries 3.972103e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

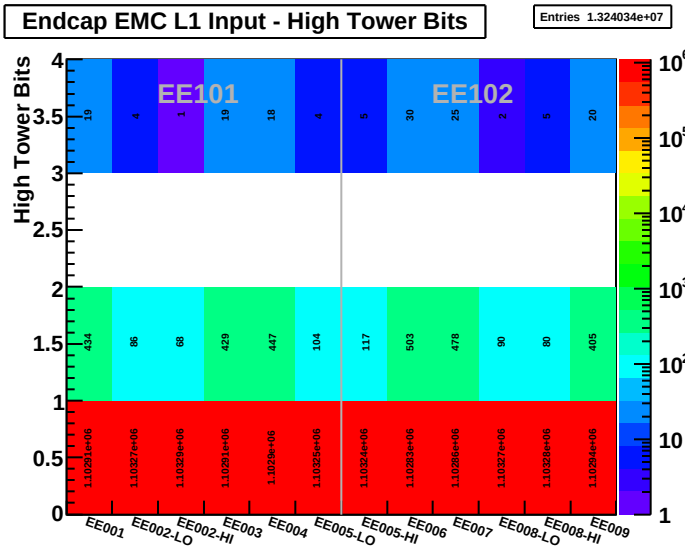

Barrel EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

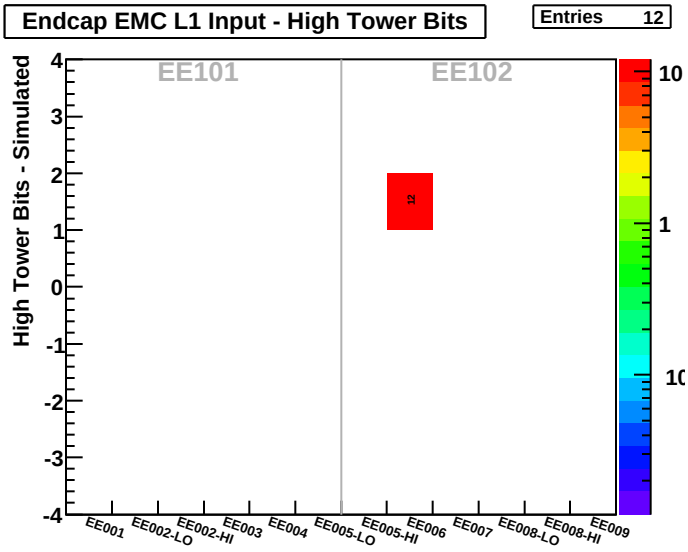

Endcap EMC L1 Input - Low Eta Sum
Entries 1.324034e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

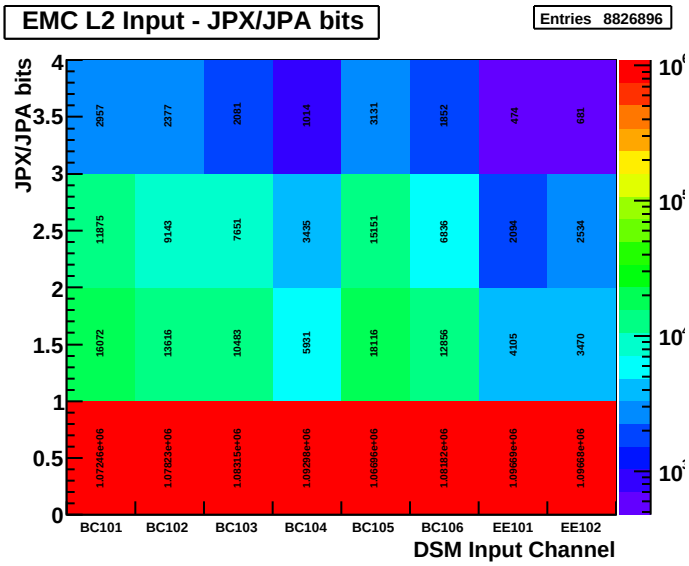

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

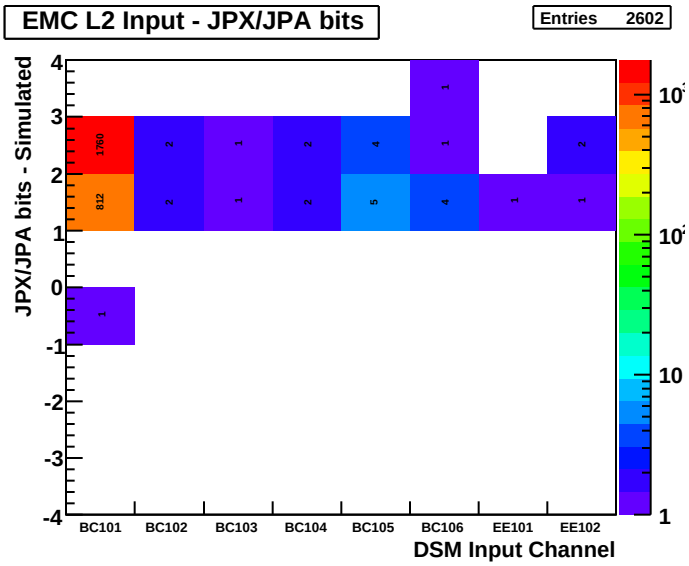

Endcap EMC L1 Input - High Eta Sum
Entries 1.324034e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

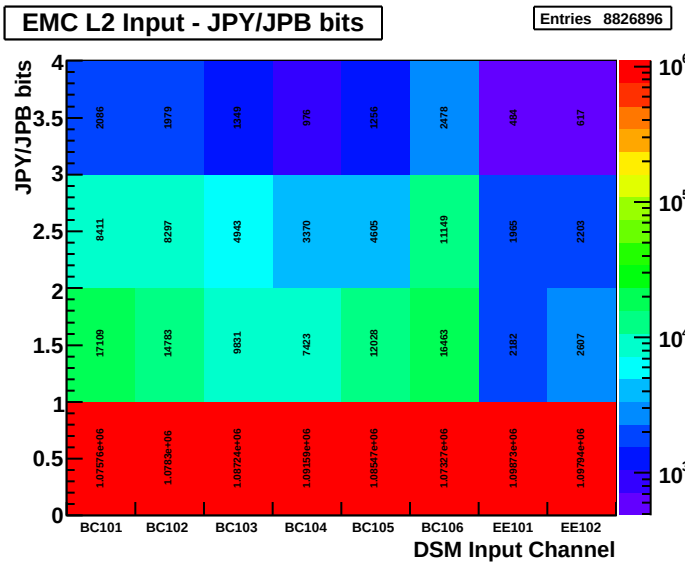

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

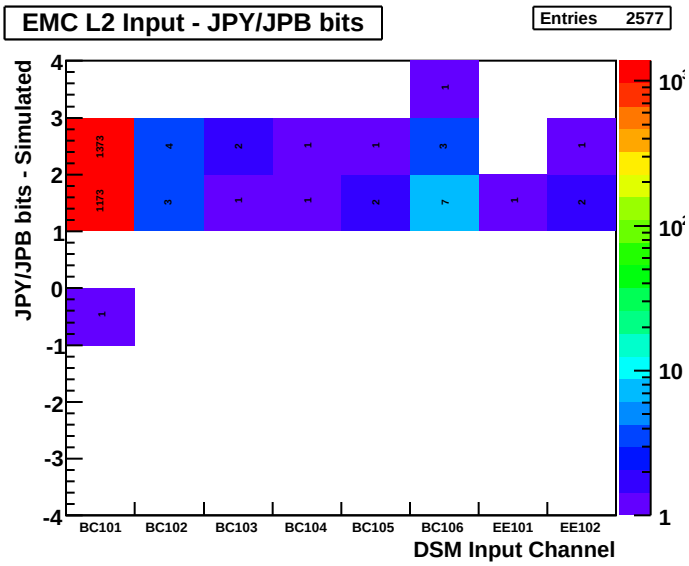

Endcap EMC L1 Input - High Tower Bits
Entries 1.324034e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

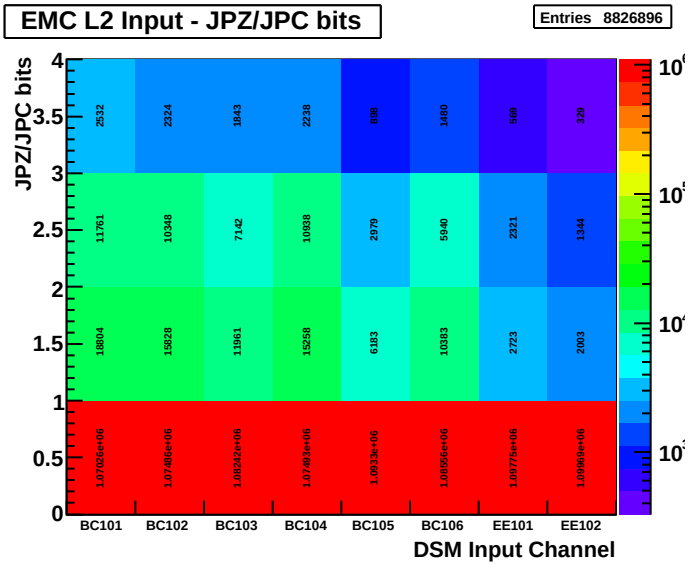

Endcap EMC L1 Input - High Tower Bits
Entries 12
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

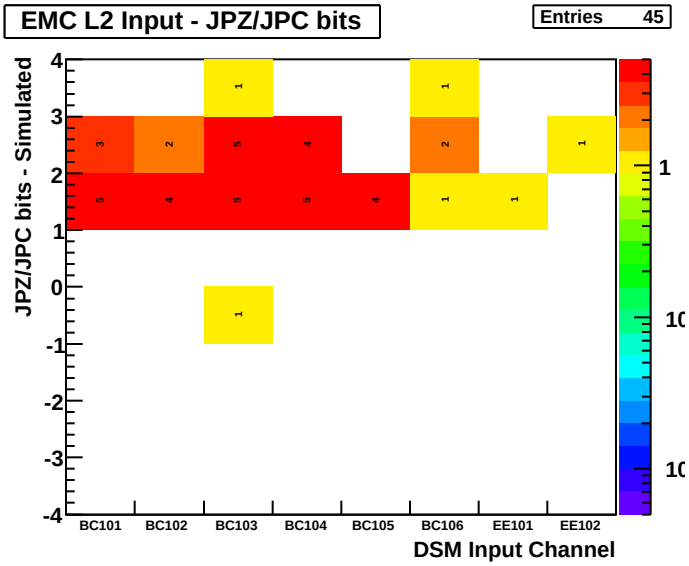

EMC L2 Input - JPX/JPA bits
Entries 8826896
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

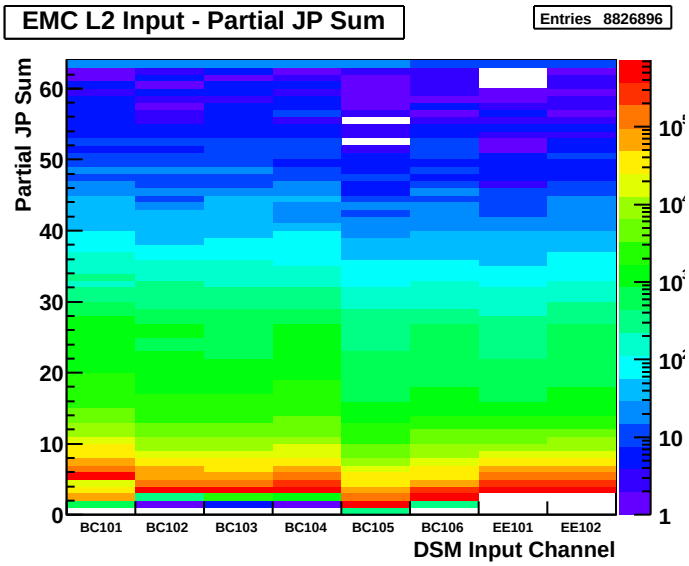

EMC L2 Input - JPX/JPA bits
Entries 2602
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

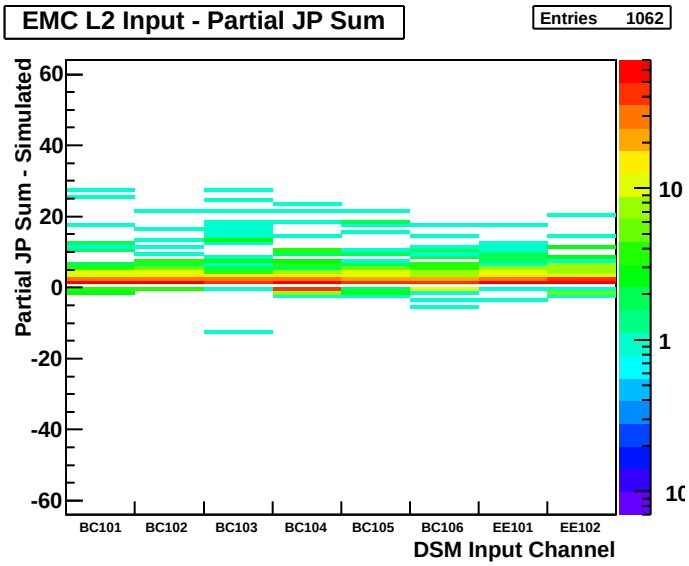

EMC L2 Input - JPY/JPB bits
Entries 8826896
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

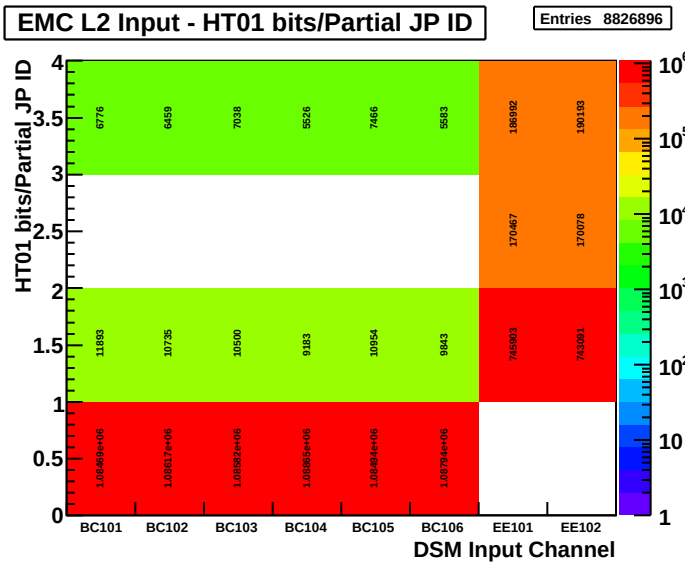

EMC L2 Input - JPY/JPB bits
Entries 2577
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

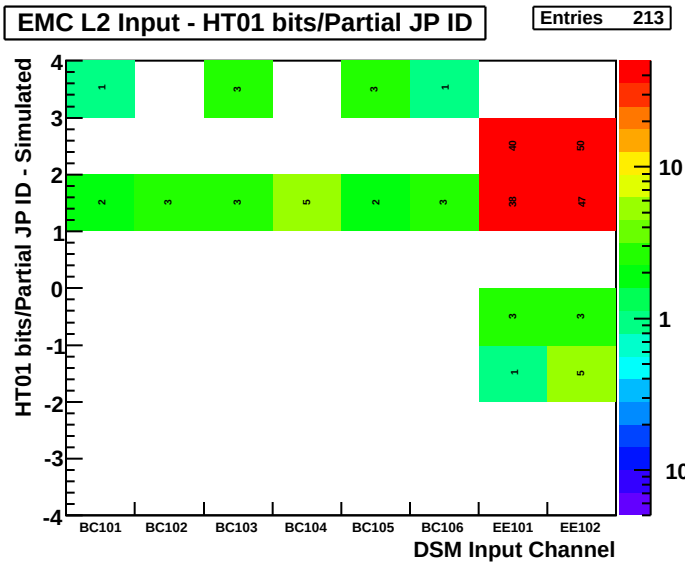

EMC L2 Input - JPZ/JPC bits
Entries 8826896
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

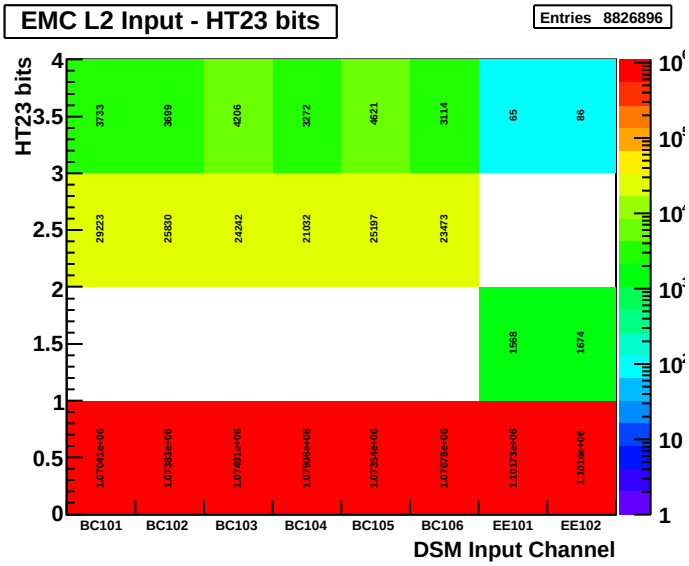

EMC L2 Input - JPZ/JPC bits
Entries 45
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

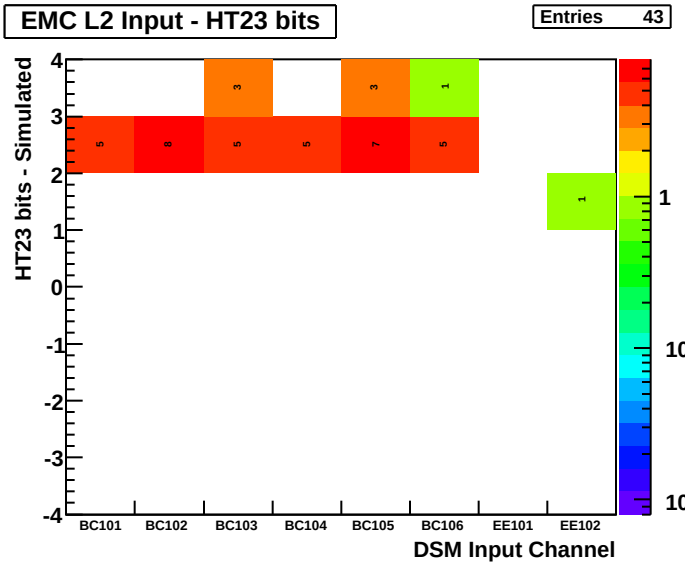

EMC L2 Input - Partial JP Sum
Entries 8826896
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

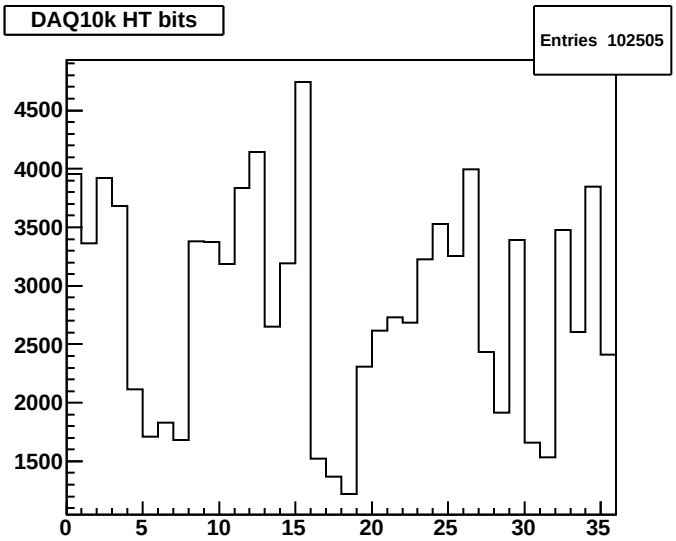

EMC L2 Input - Partial JP Sum
Entries 1062
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

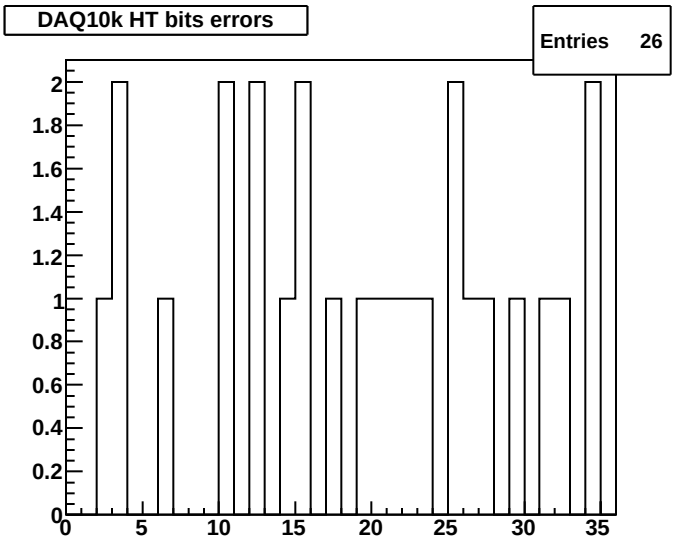

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8826896
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

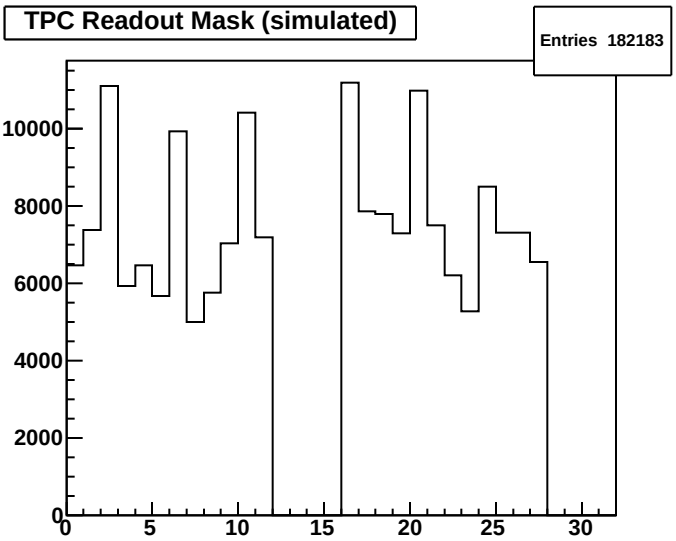

EMC L2 Input - HT01 bits/Partial JP ID
Entries 213
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

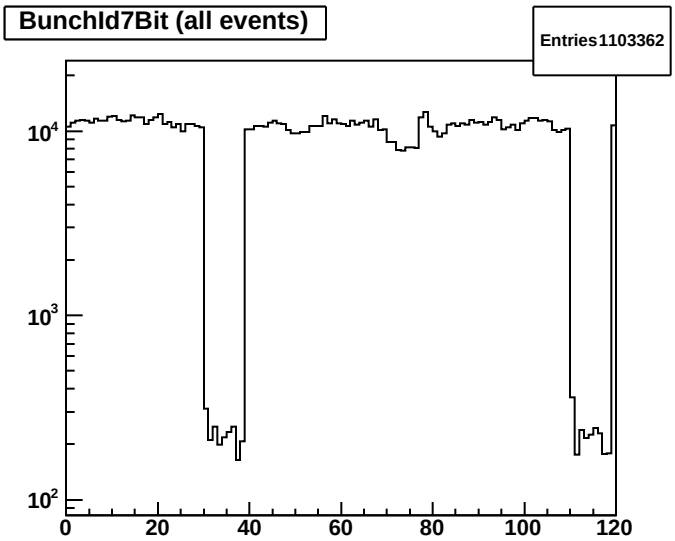

EMC L2 Input - HT23 bits
Entries 8826896
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

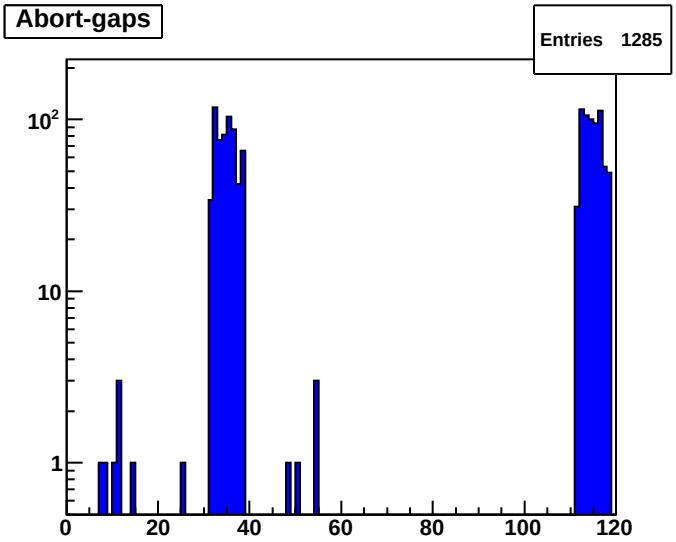

EMC L2 Input - HT23 bits
Entries 43
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

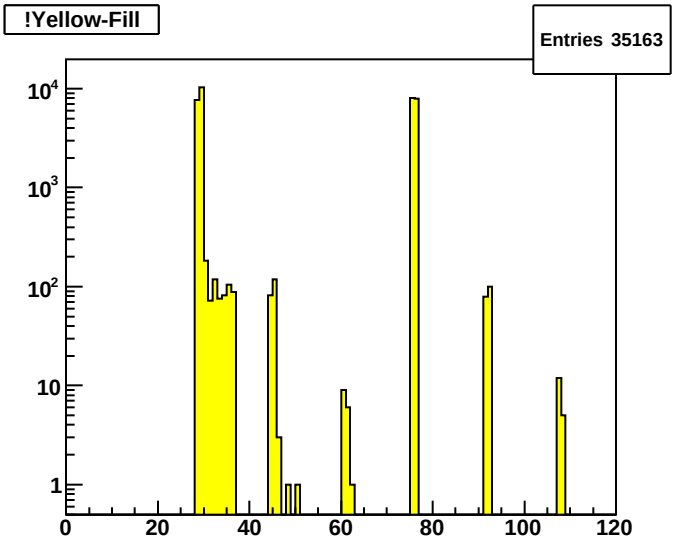

DAQ10k HT bits
Entries 102505

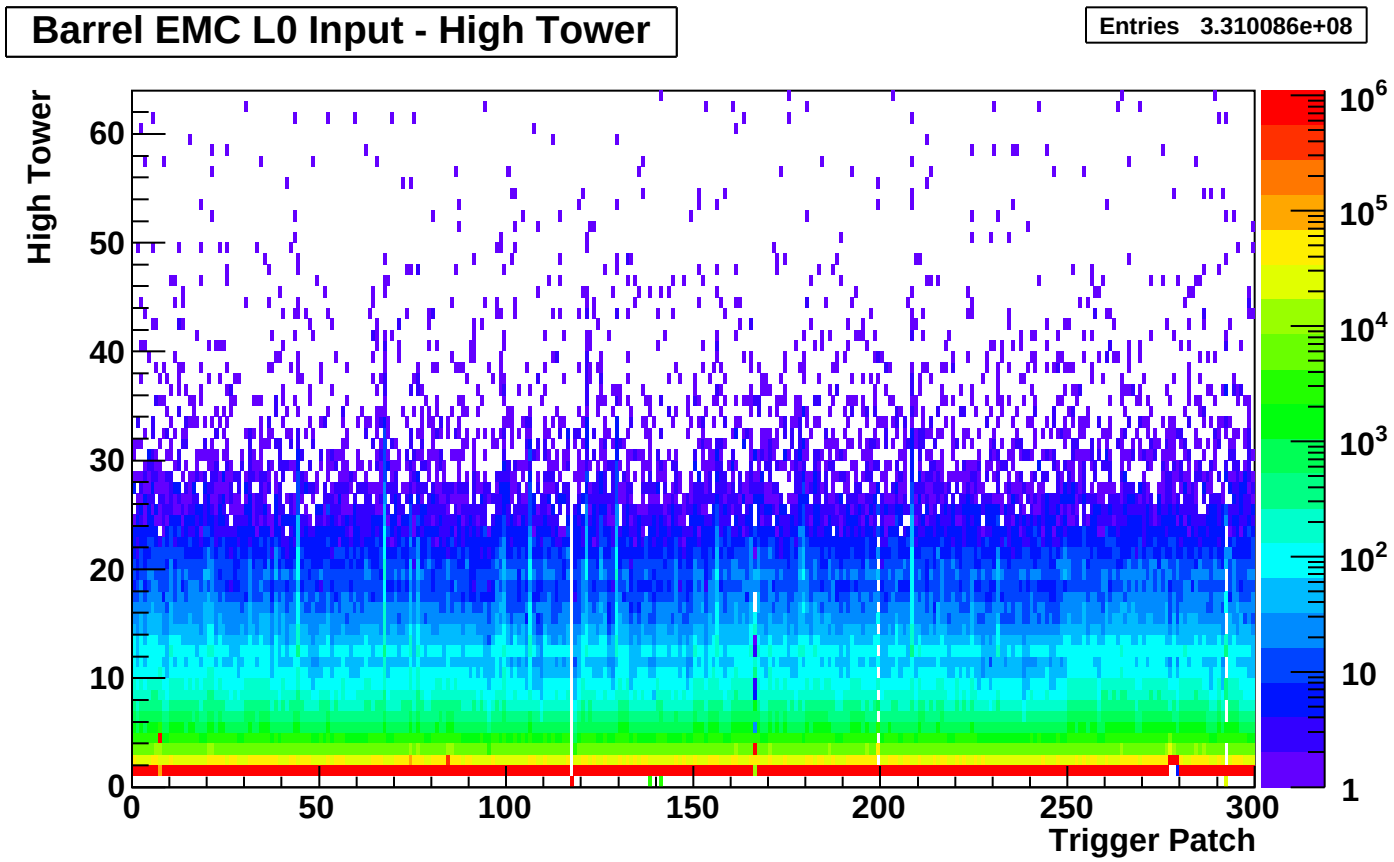

DAQ10k HT bits errors
Entries 26

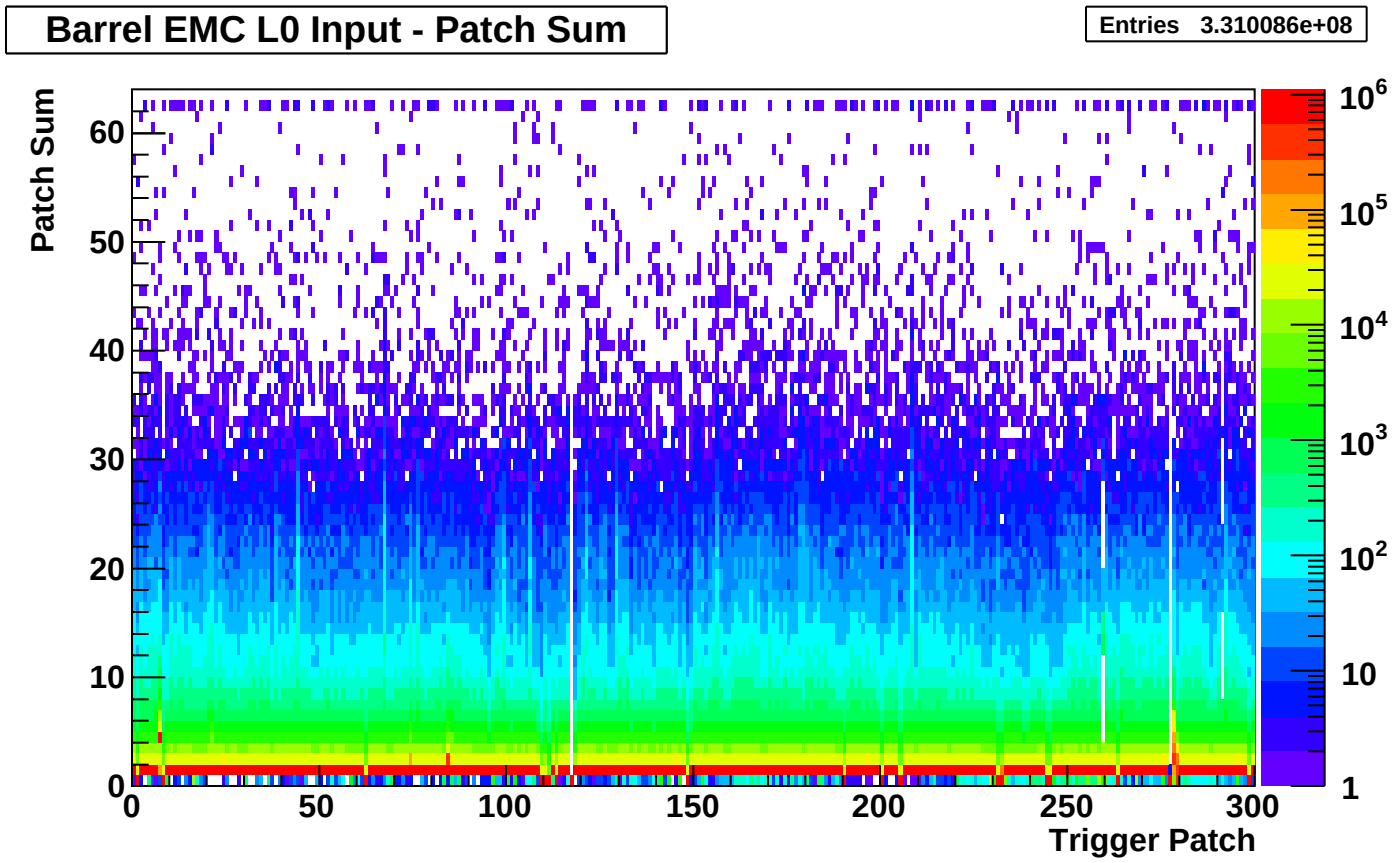

TPC Readout Mask (simulated)
Entries 182183

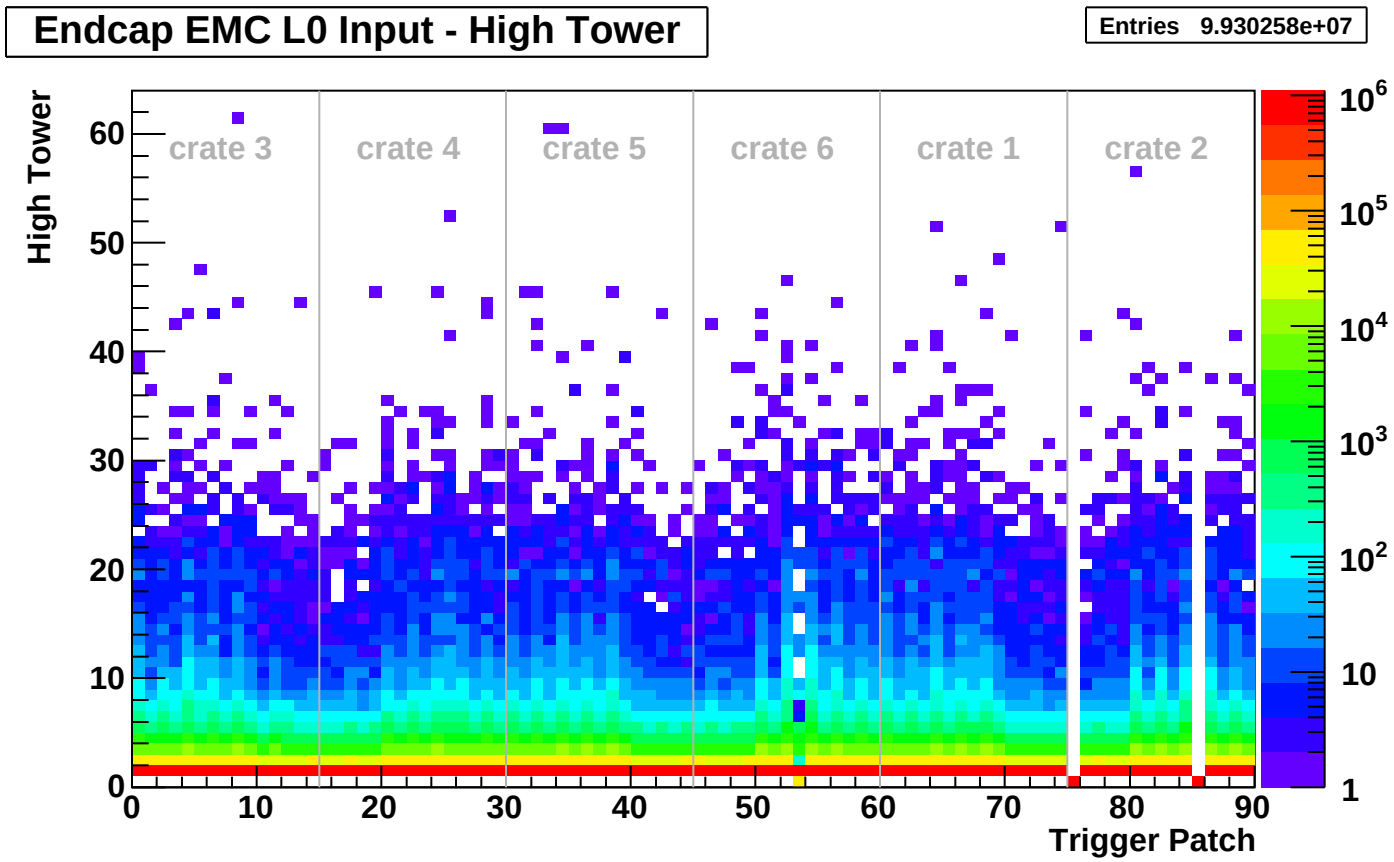

BunchId7Bit (all events)
Entries 1103362

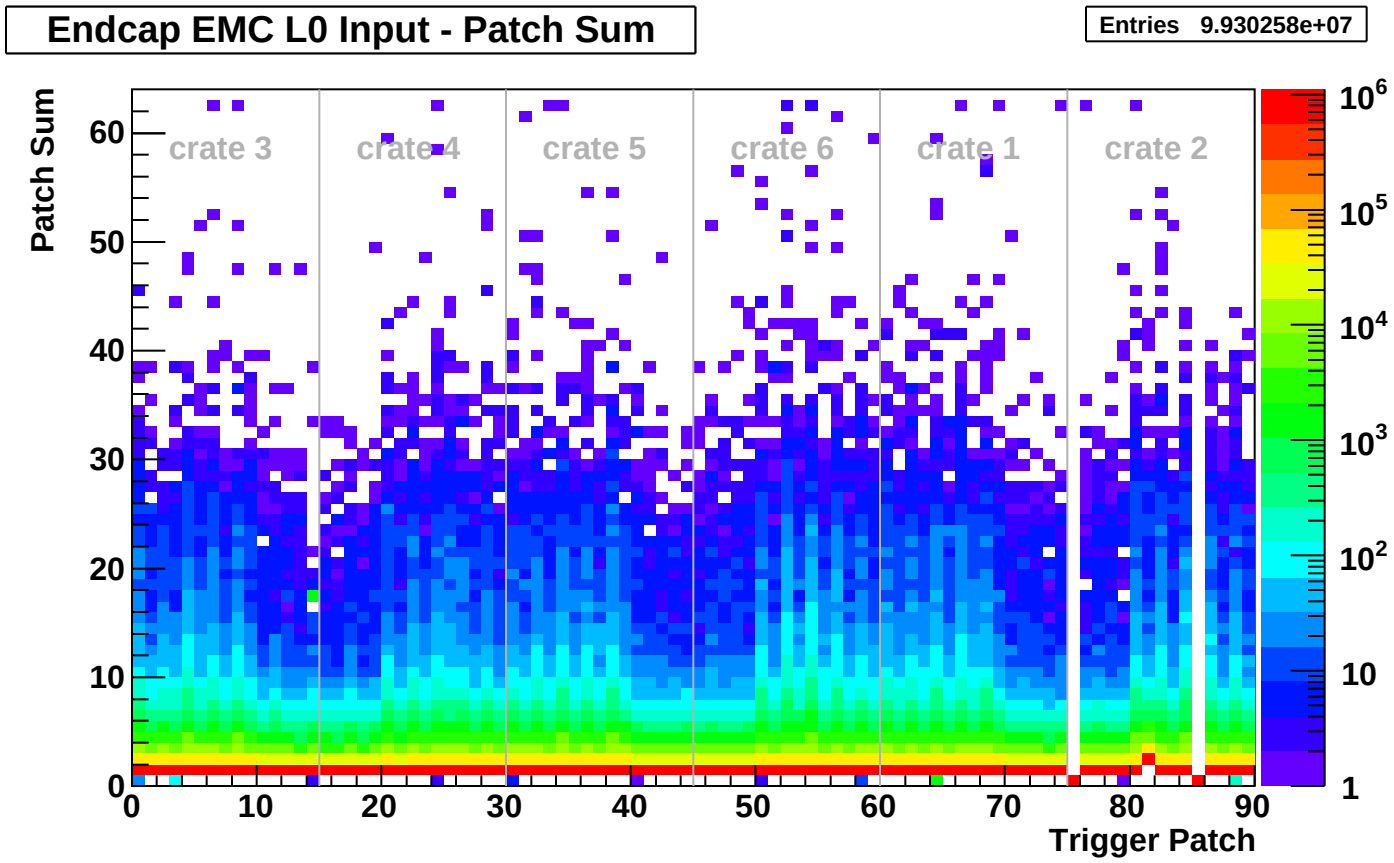

Abort-gaps
Entries 1285

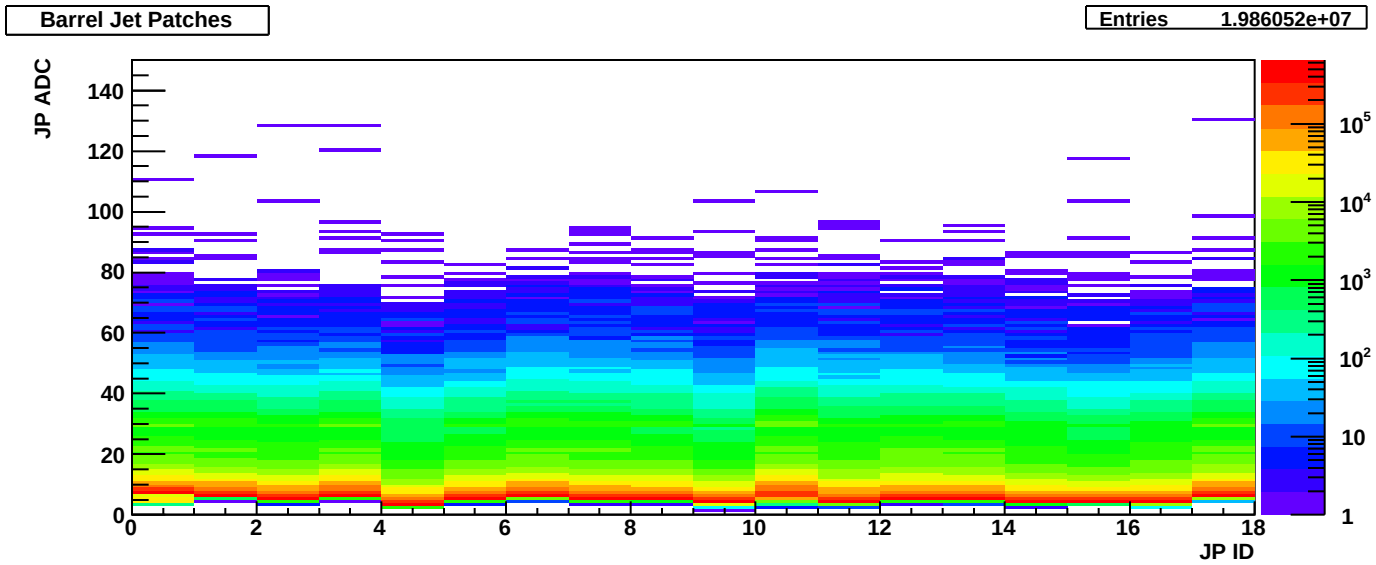

!Yellow-Fill
Entries 35163

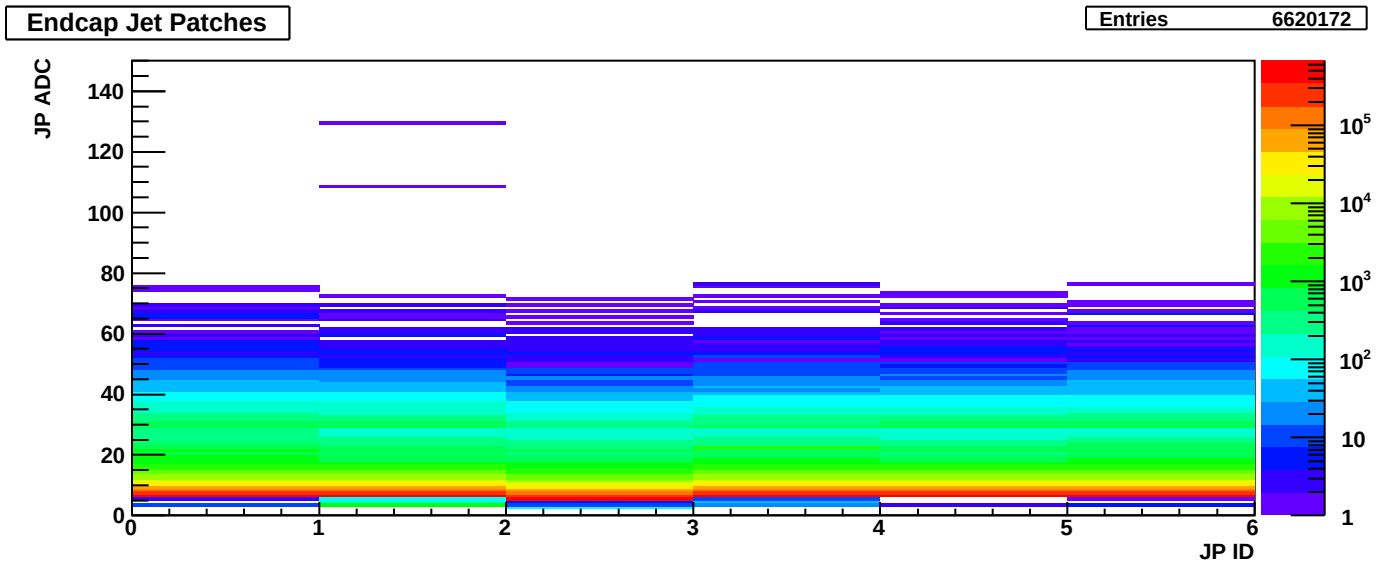

Barrel EMC L0 Input - High Tower
Entries 3.310086e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

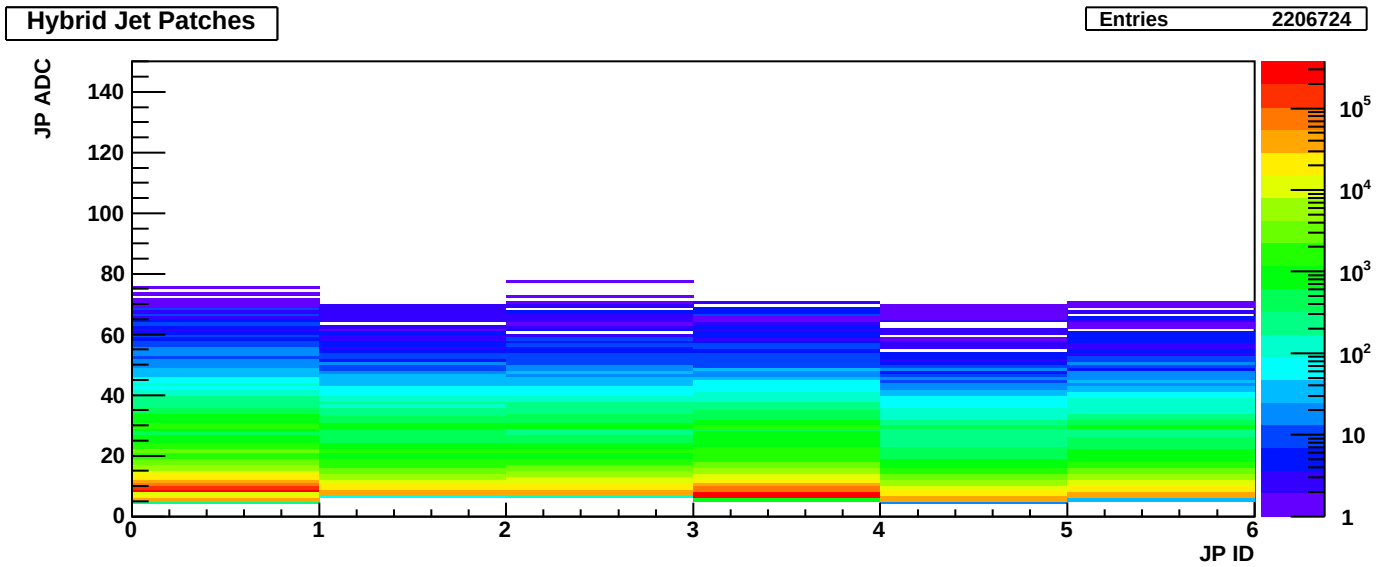

Barrel EMC L0 Input - Patch Sum
Entries 3.310086e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

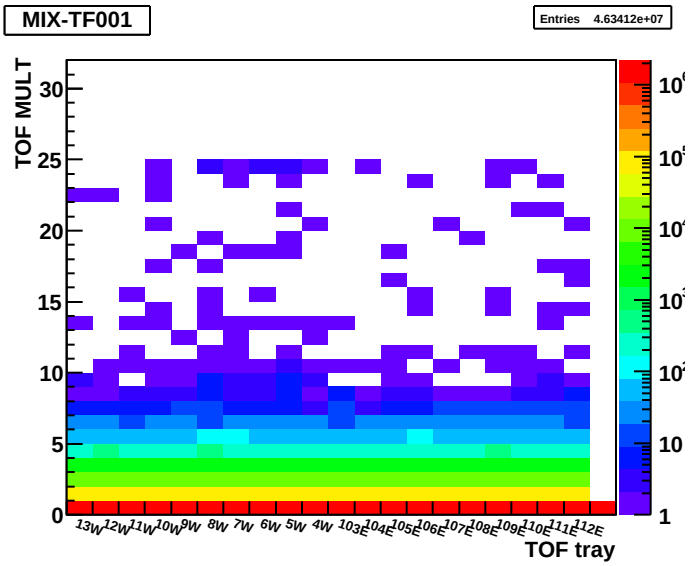

Endcap EMC L0 Input - High Tower
Entries 9.930258e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

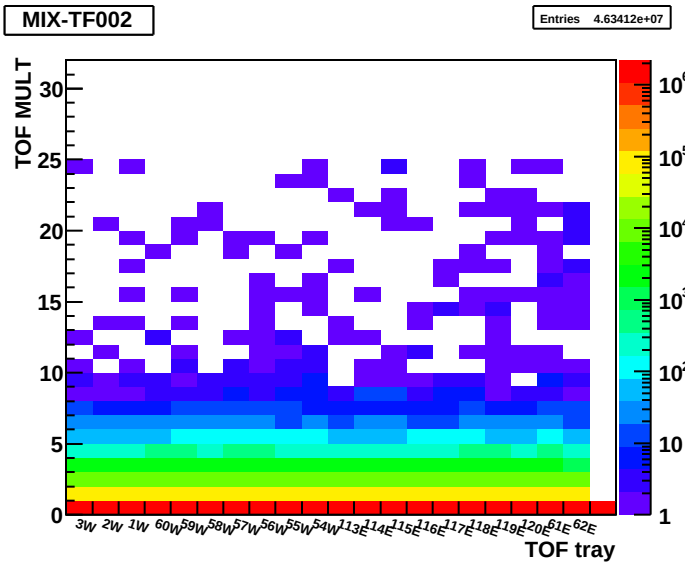

Endcap EMC L0 Input - Patch Sum
Entries 9.930258e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

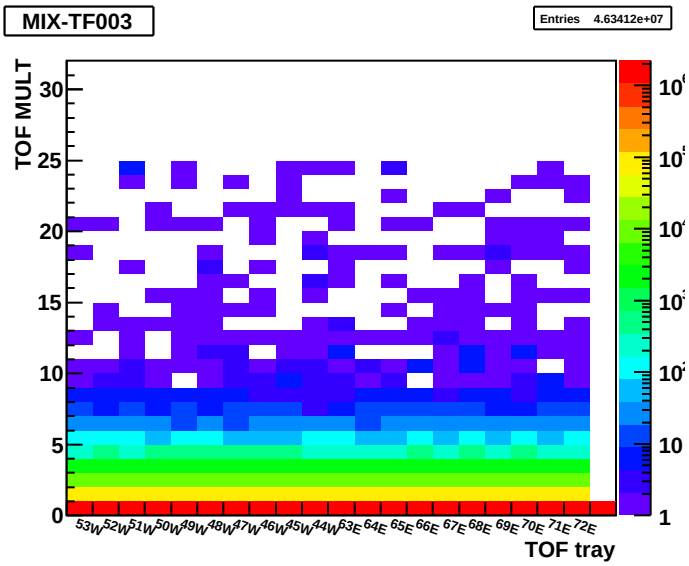

Barrel Jet Patches
Entries 1.986052e+07
JP ADC
JP ID

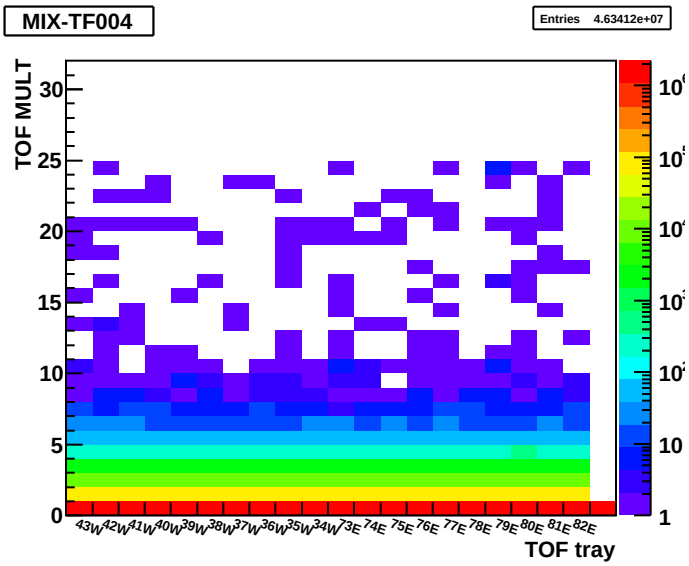

Endcap Jet Patches
Entries 6620172
JP ADC
JP ID

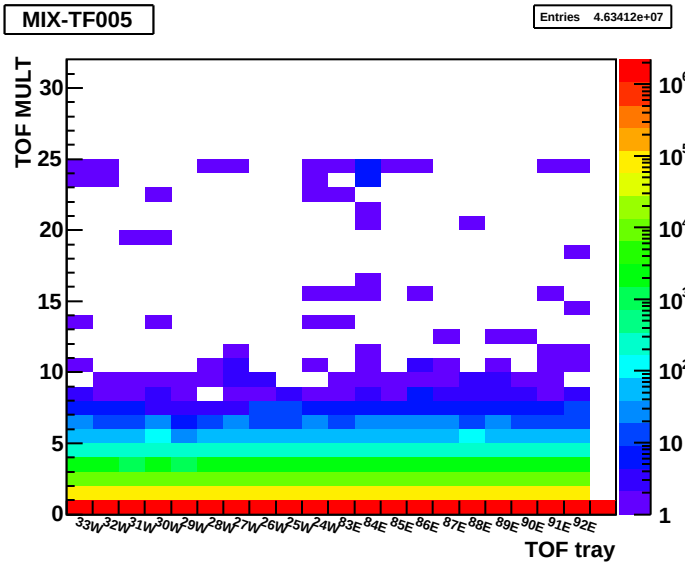

Hybrid Jet Patches
Entries 2206724
JP ADC
JP ID

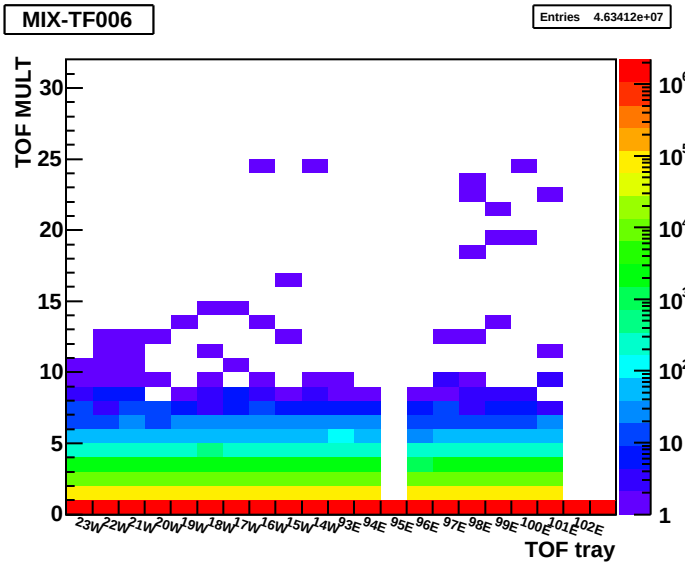

MIX-TF001
Entries 4.63412e+07
TOF MULT
TOF tray

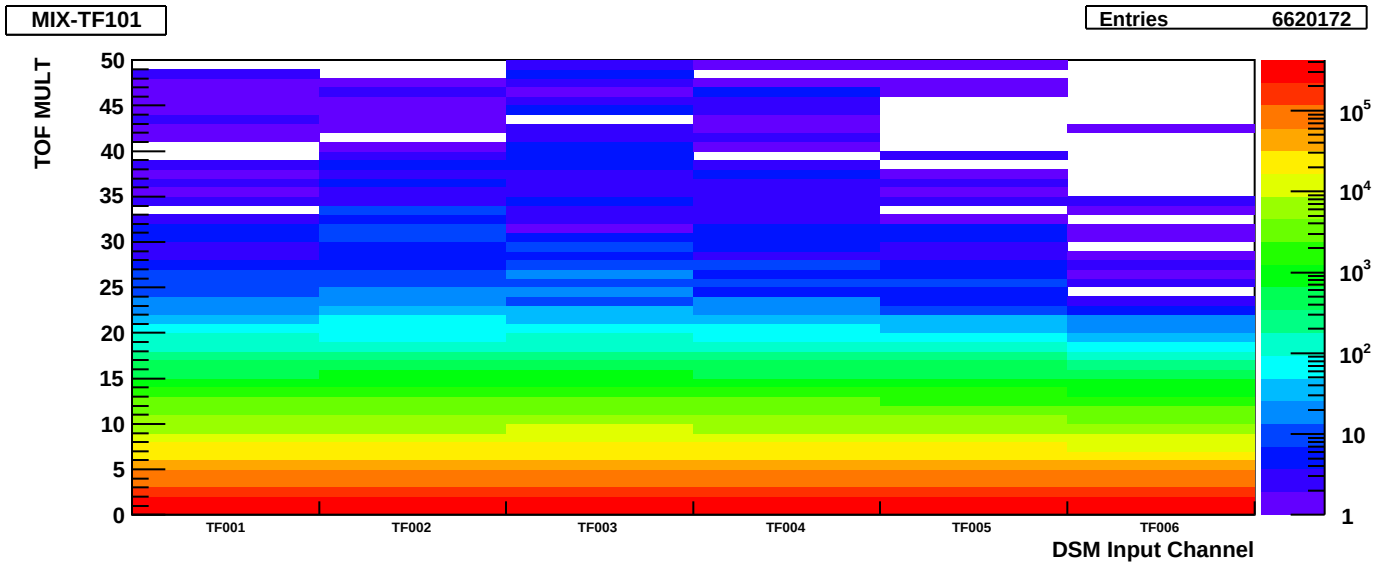

MIX-TF002
Entries 4.63412e+07
TOF MULT
TOF tray

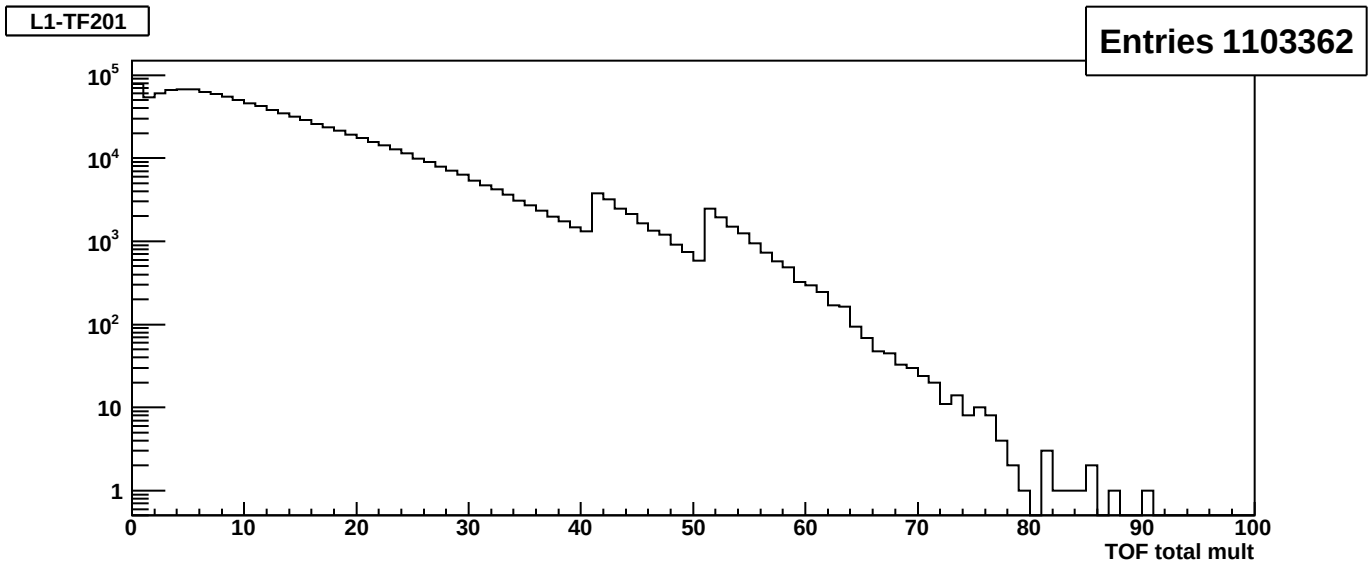

MIX-TF003
Entries 4.63412e+07
TOF MULT
TOF tray

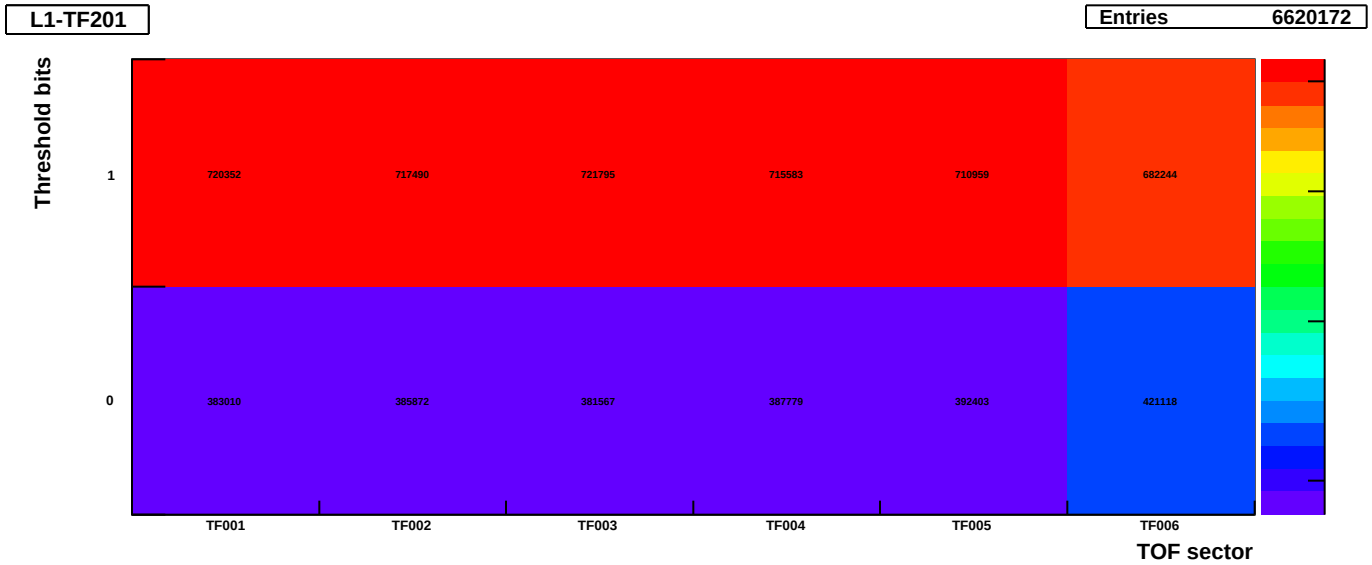

MIX-TF004
Entries 4.63412e+07
TOF MULT
TOF tray

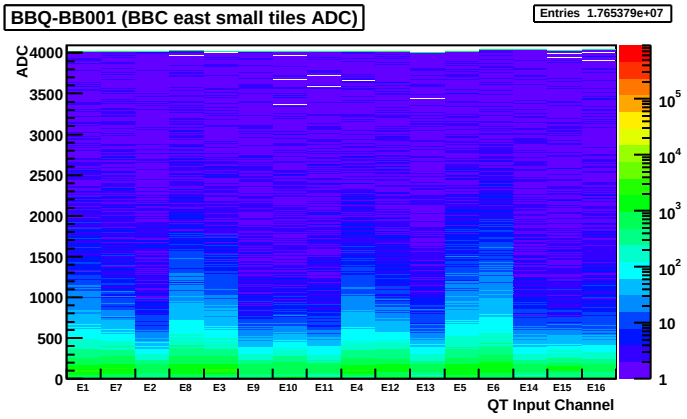

MIX-TF005
Entries 4.63412e+07
TOF MULT
TOF tray

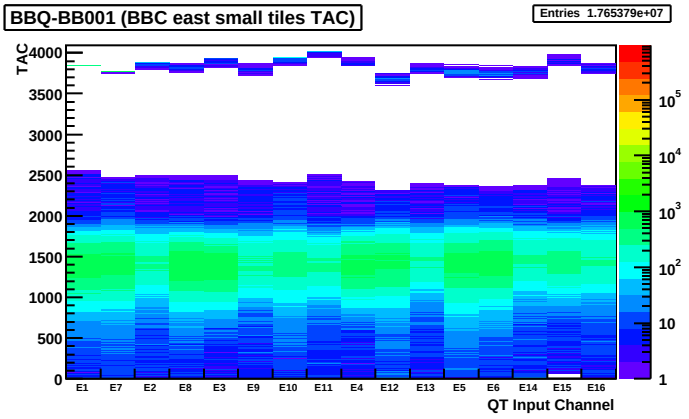

MIX-TF006
Entries 4.63412e+07
TOF MULT
TOF tray

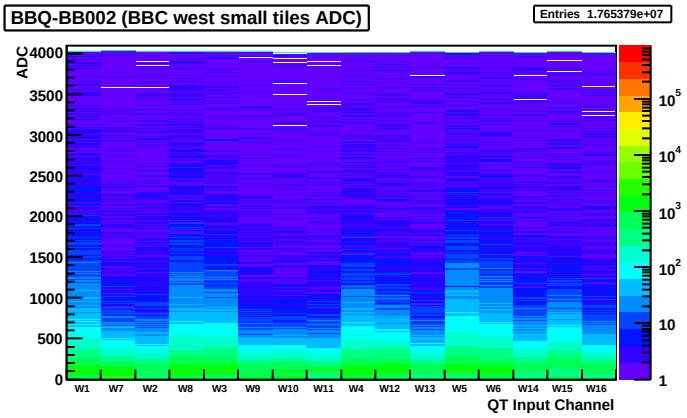

MIX-TF101
Entries 6620172
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

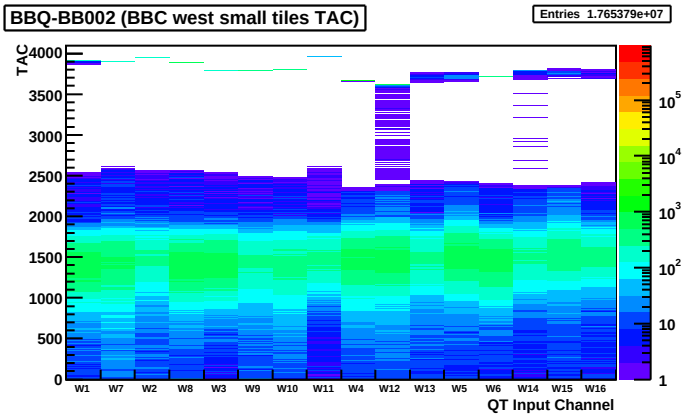

L1-TF201
Entries 1103362
TOF total mult

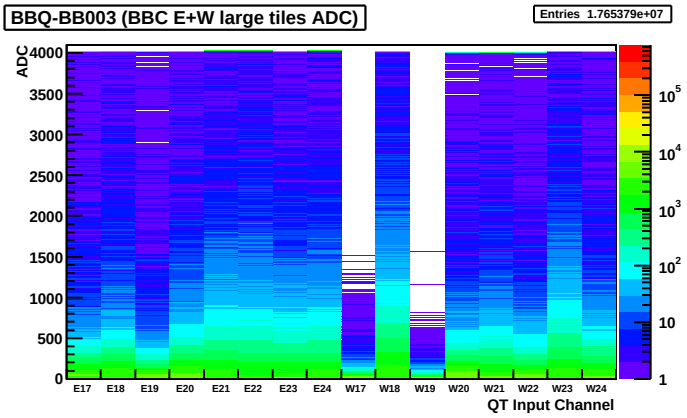

L1-TF201
Entries 6620172
Threshold bits
720352
717490
721795
715583
710959
682244
383010
385872
381567
387779
392403
421118
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

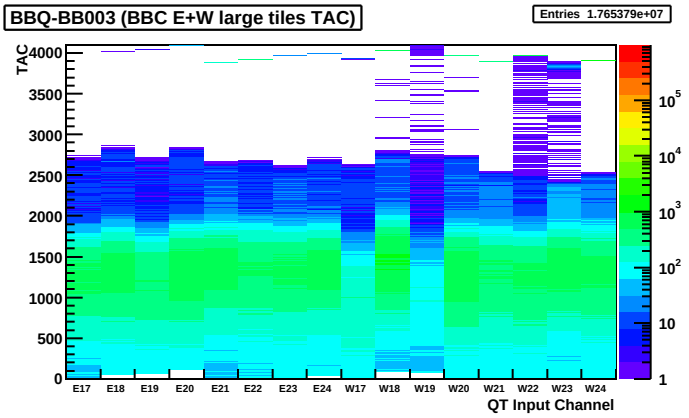

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.765379e+07
ADC
QT Input Channel

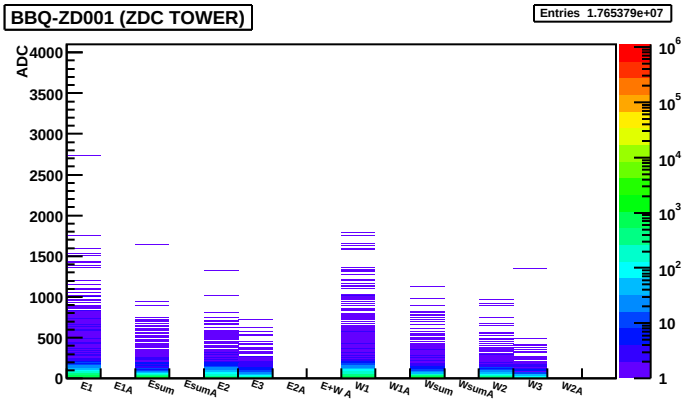

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.765379e+07
TAC
QT Input Channel

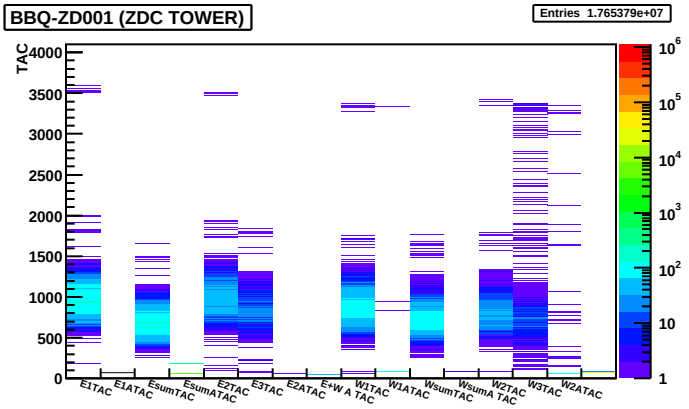

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.765379e+07
ADC
QT Input Channel

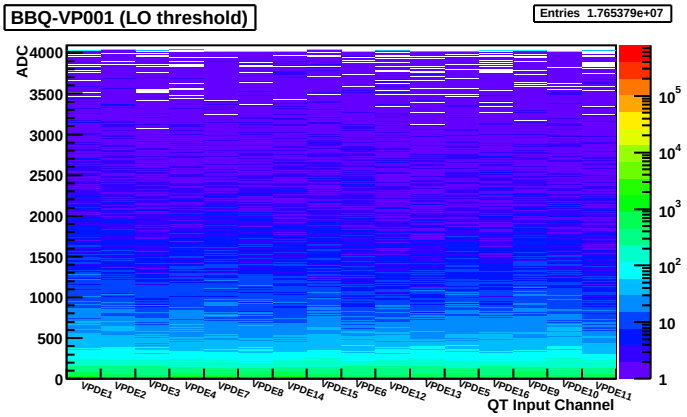

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.765379e+07
TAC
QT Input Channel

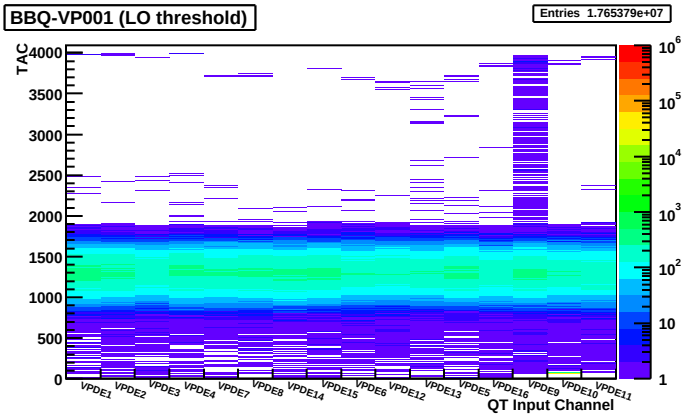

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.765379e+07
ADC
QT Input Channel

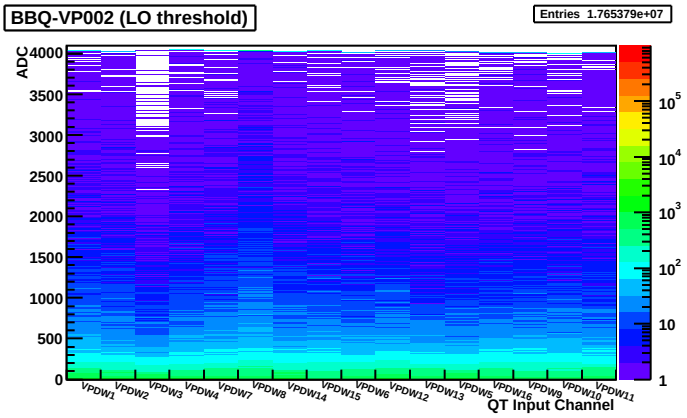

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.765379e+07
TAC
QT Input Channel

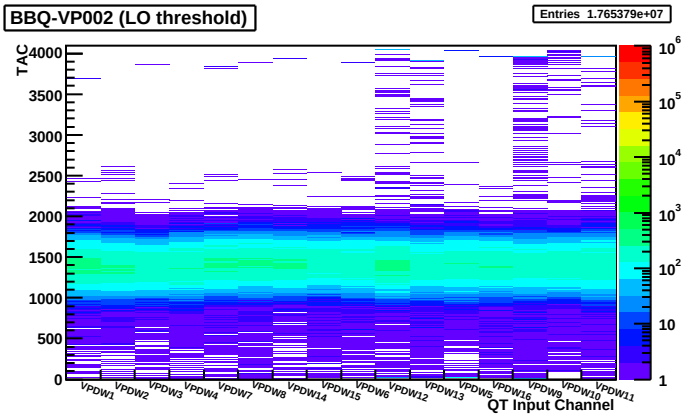

BBQ-ZD001 (ZDC TOWER)
Entries 1.765379e+07
ADC

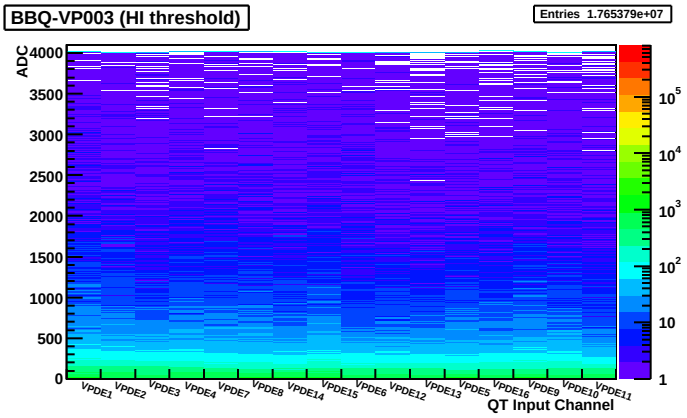

BBQ-ZD001 (ZDC TOWER)
Entries 1.765379e+07
TAC

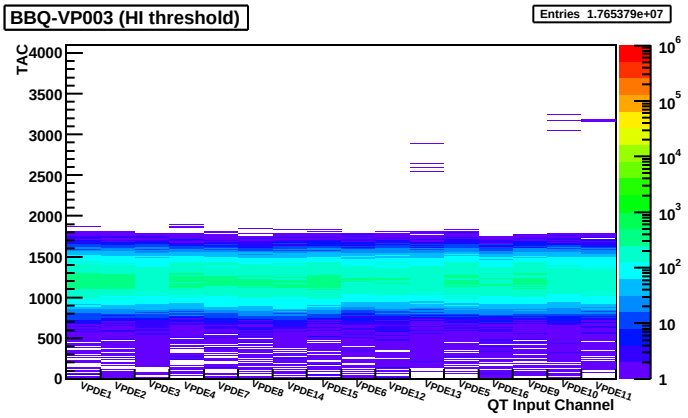

BBQ-VP001 (LO threshold)
Entries 1.765379e+07
ADC
QT Input Channel

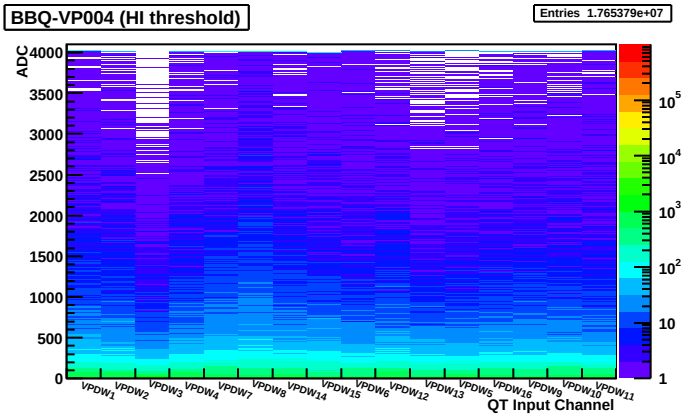

BBQ-VP001 (LO threshold)
Entries 1.765379e+07
TAC
QT Input Channel

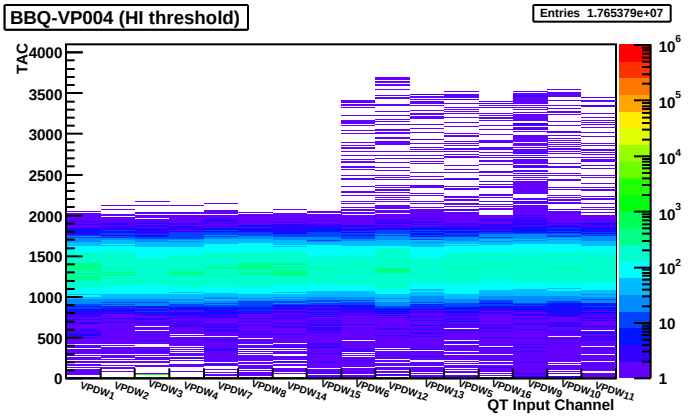

BBQ-VP002 (LO threshold)
Entries 1.765379e+07
ADC
QT Input Channel

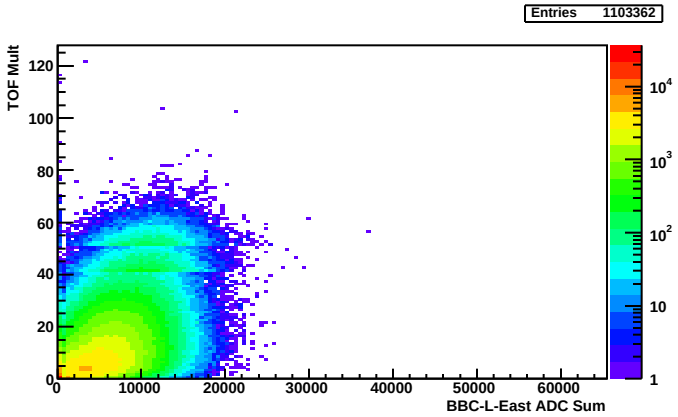

BBQ-VP002 (LO threshold)
Entries 1.765379e+07
TAC
QT Input Channel

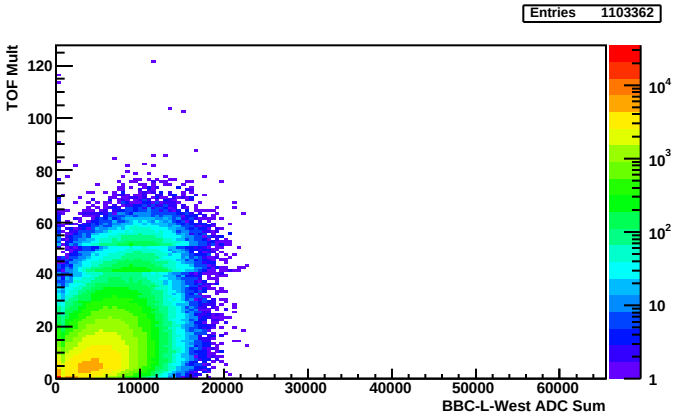

BBQ-VP003 (HI threshold)
Entries 1.765379e+07
ADC
QT Input Channel

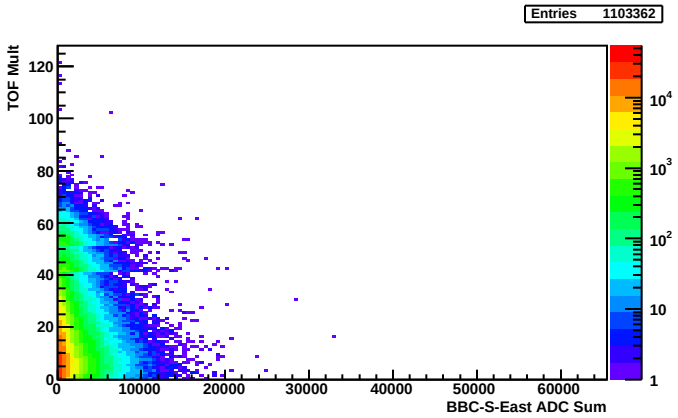

BBQ-VP003 (HI threshold)
Entries 1.765379e+07
TAC
QT Input Channel

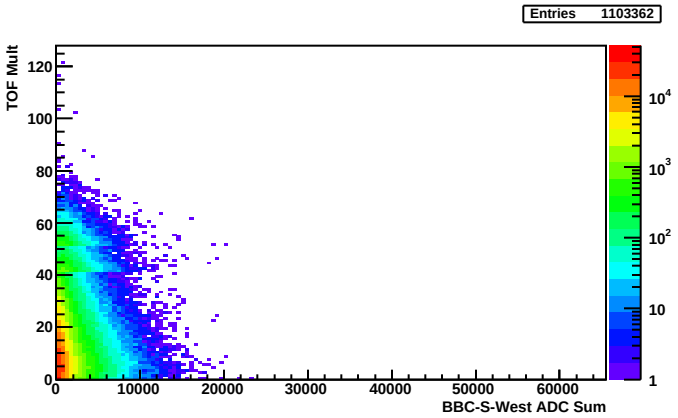

BBQ-VP004 (HI threshold)
Entries 1.765379e+07
ADC
QT Input Channel

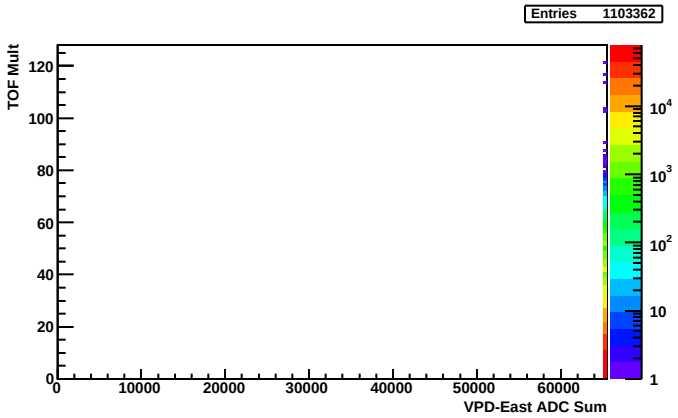

BBQ-VP004 (HI threshold)
Entries 1.765379e+07
TAC
QT Input Channel

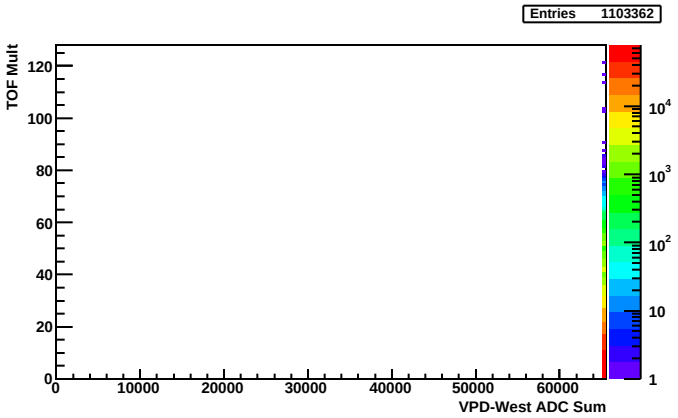

Entries 1103362
TOF Mult
BBC-L-East ADC Sum

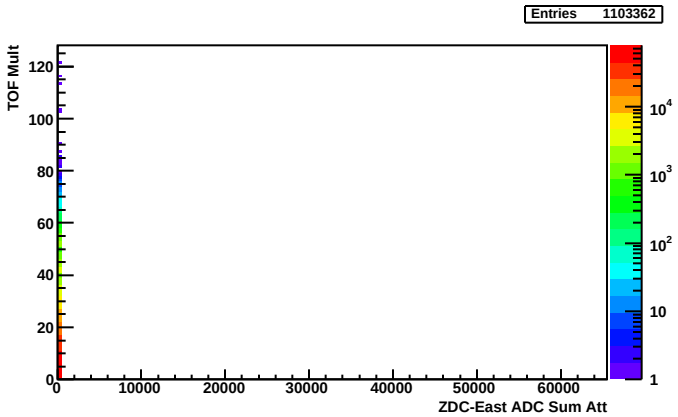

Entries 1103362
TOF Mult
BBC-L-West ADC Sum

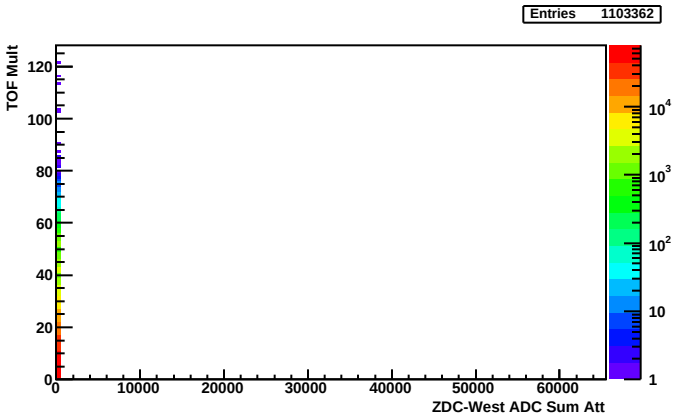

Entries 1103362
TOF Mult
BBC-S-East ADC Sum

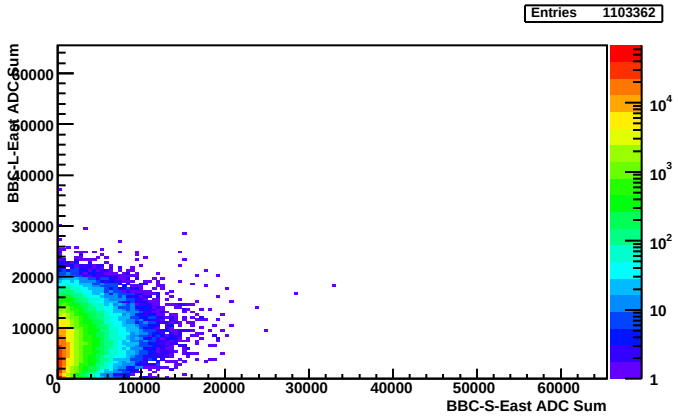

Entries 1103362
TOF Mult
BBC-S-West ADC Sum

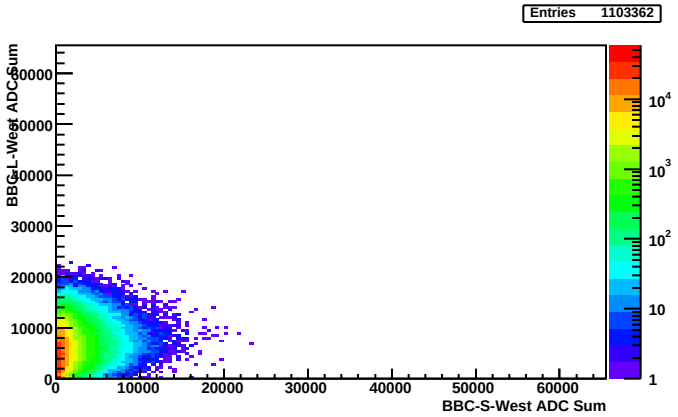

Entries 1103362
TOF Mult
VPD-East ADC Sum

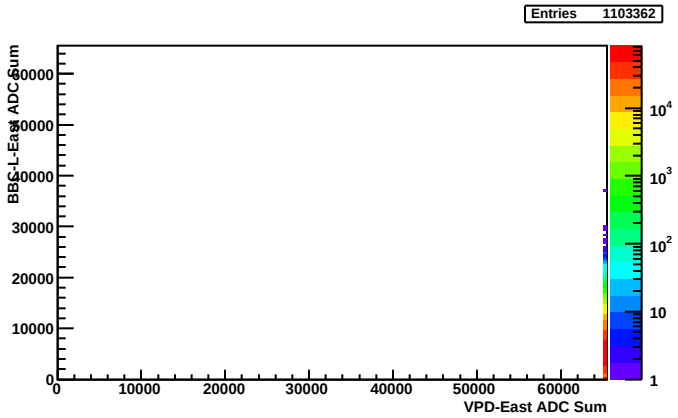

Entries 1103362
TOF Mult
VPD-West ADC Sum

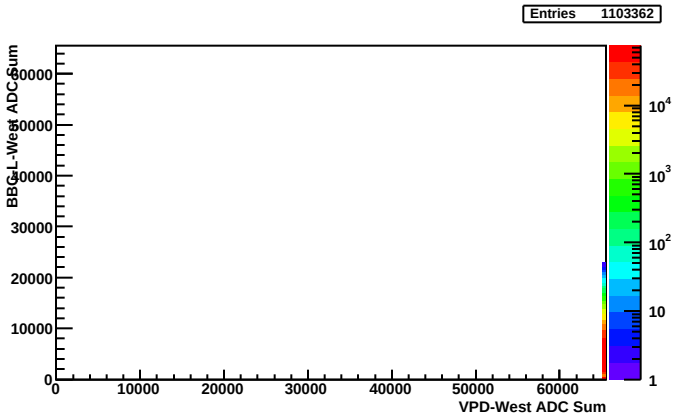

Entries 1103362
TOF Mult
ZDC-East ADC Sum Att

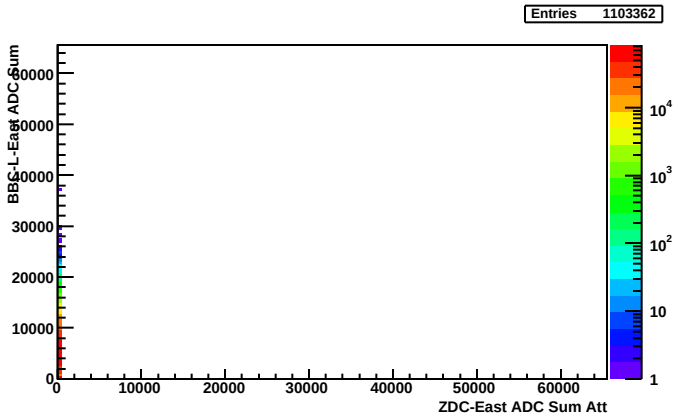

Entries 1103362
TOF Mult
ZDC-West ADC Sum Att

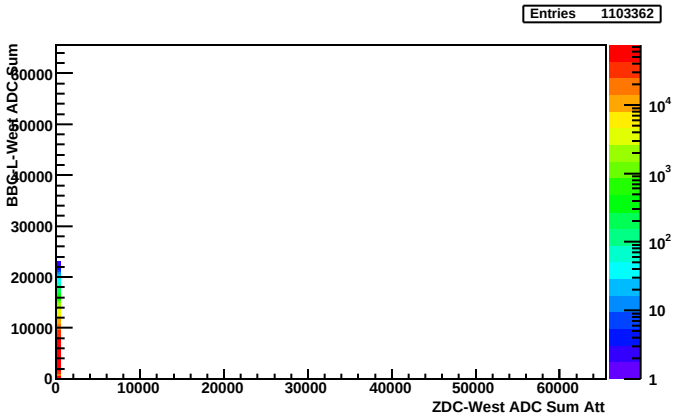

Entries 1103362
BBC-L-East ADC Sum
BBC-S-East ADC Sum

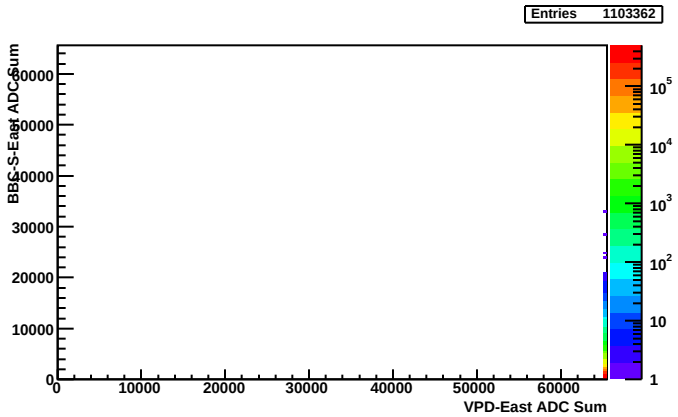

Entries 1103362
BBC-L-West ADC Sum
BBC-S-West ADC Sum

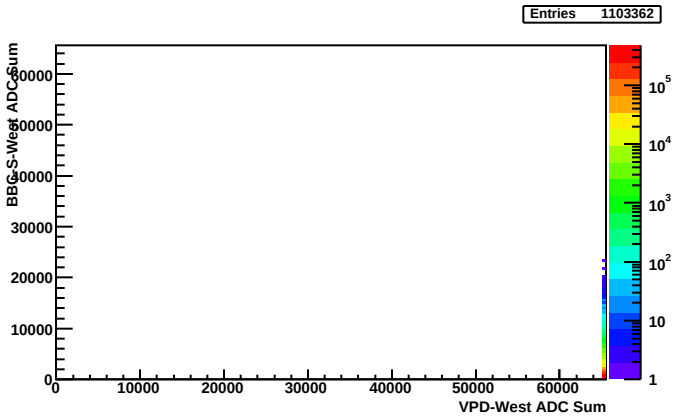

Entries 1103362
BBC-L-East ADC Sum
VPD-East ADC Sum

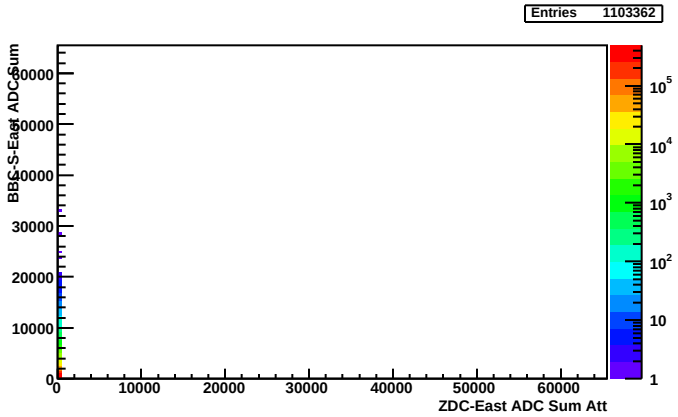

Entries 1103362
BBC-L-West ADC Sum
VPD-West ADC Sum

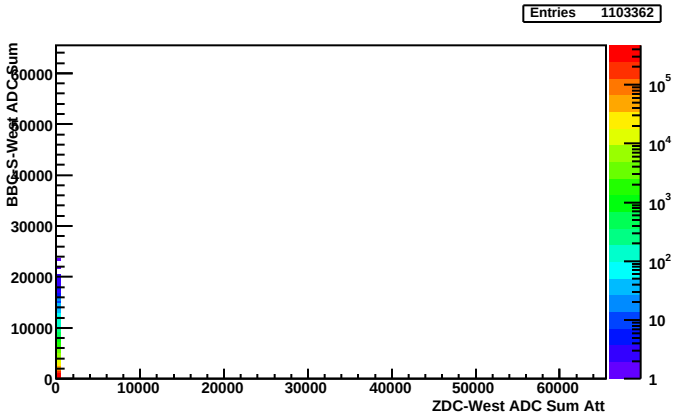

Entries 1103362
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

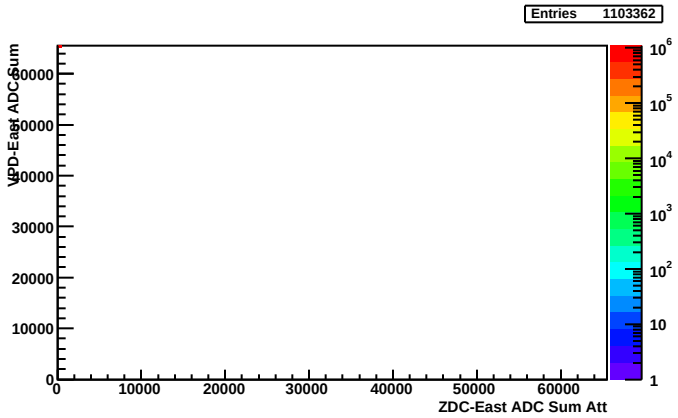

Entries 1103362
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

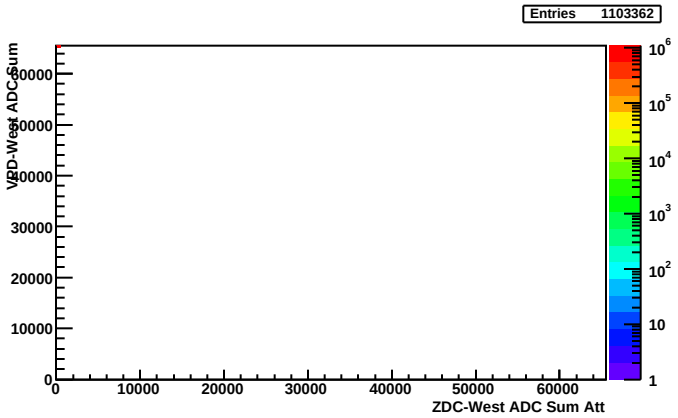

Entries 1103362
BBC-S-East ADC Sum
VPD-East ADC Sum

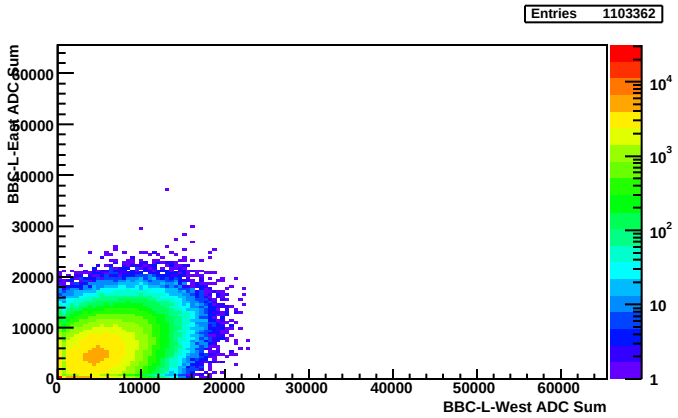

Entries 1103362
BBC-S-West ADC Sum
VPD-West ADC Sum

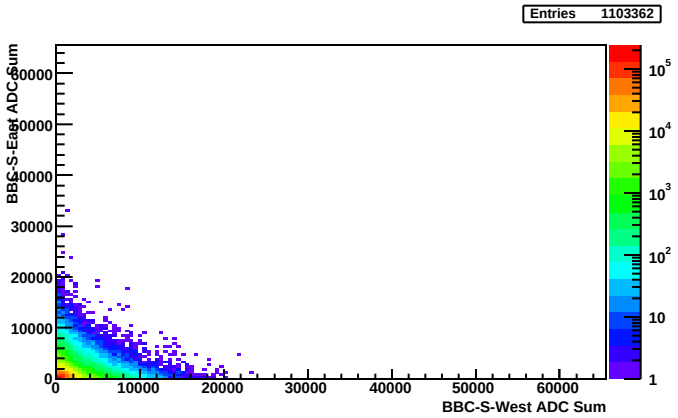

Entries 1103362
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

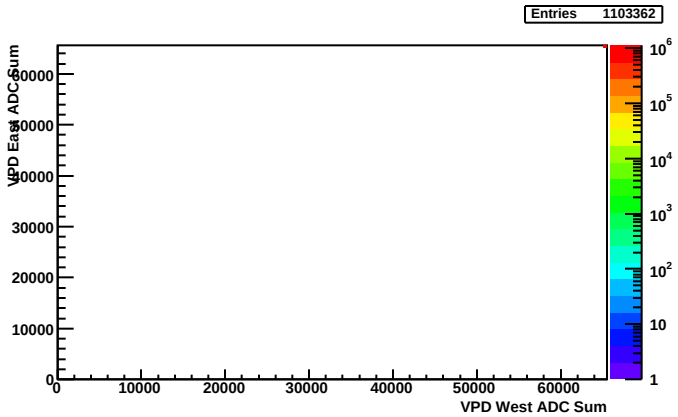

Entries 1103362
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

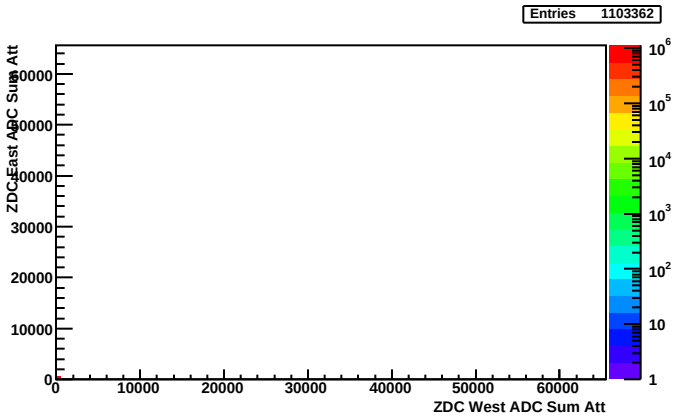

Entries 1103362
VPD-East ADC Sum
ZDC-East ADC Sum Att

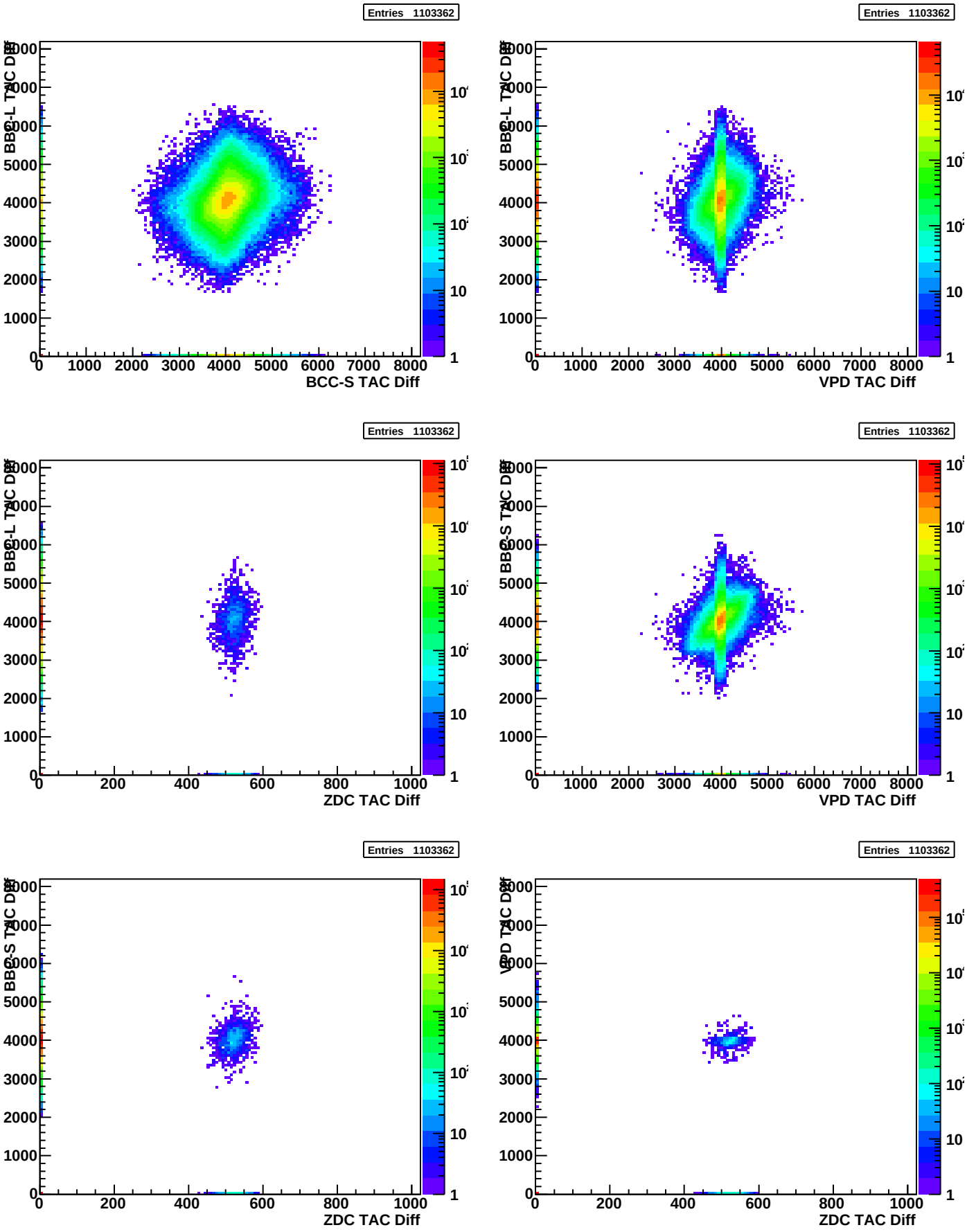

Entries 1103362
VPD-West ADC Sum
ZDC-West ADC Sum Att

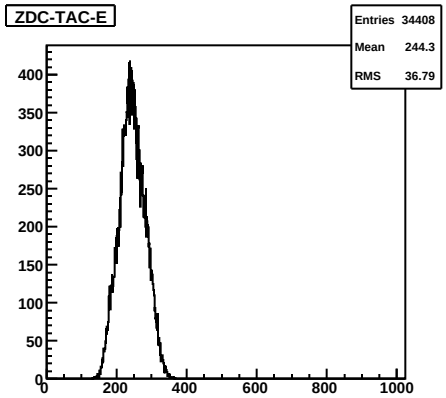

Entries 1103362
BBC-L-East ADC Sum
BBC-L-West ADC Sum

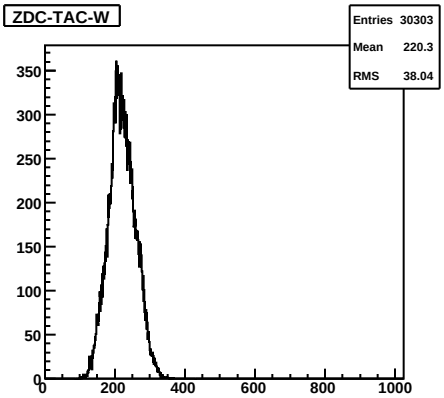

Entries 1103362
BBC-S-East ADC Sum
BBC-S-West ADC Sum

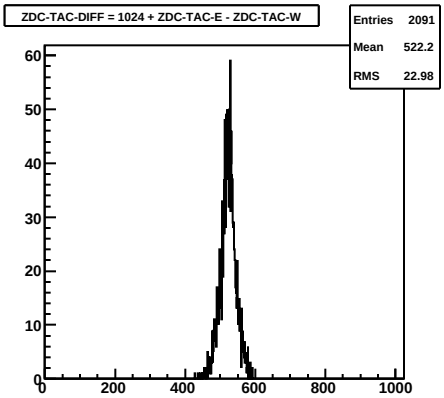

Entries 1103362
VPD East ADC Sum
VPD West ADC Sum

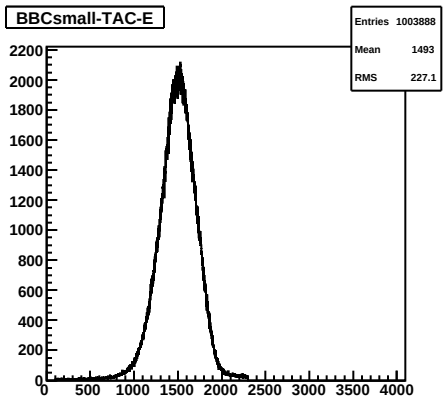

Entries 1103362
ZDC East ADC Sum Att
ZDC West ADC Sum Att

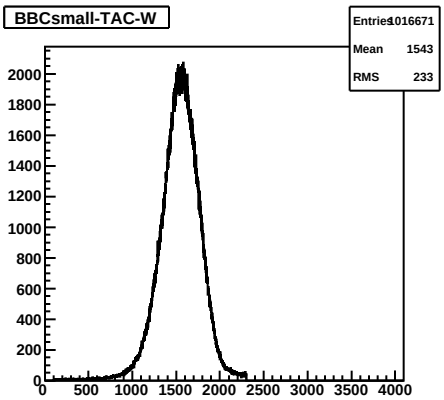

Entries 1103362
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1103362
BBC-L TAC Diff
VPD TAC Diff
Entries 1103362
BBC-L TAC Diff
ZDC TAC Diff
Entries 1103362
BBC-S TAC Diff
VPD TAC Diff
Entries 1103362
BBC-S TAC Diff
ZDC TAC Diff
Entries 1103362
VPD TAC Diff
ZDC TAC Diff

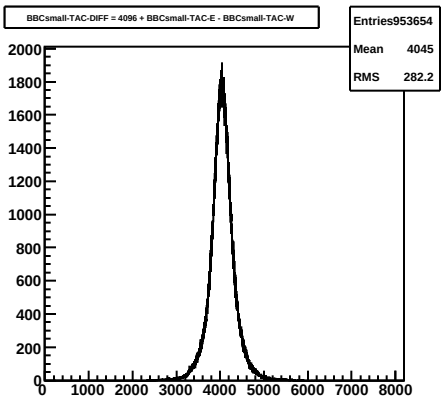

ZDC-TAC-E
Entries 34408
Mean 244.3
RMS 36.79

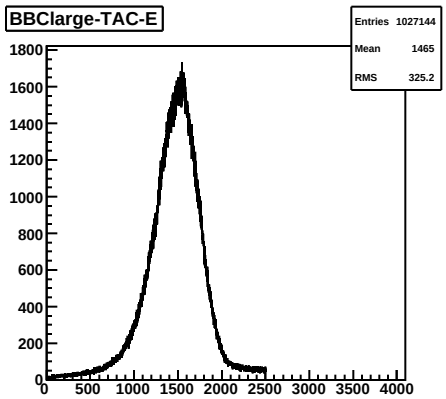

ZDC-TAC-W
Entries 30303
Mean 220.3
RMS 38.04

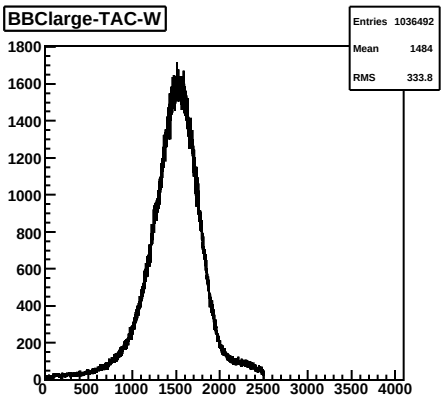

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2091
Mean 522.2
RMS 22.98

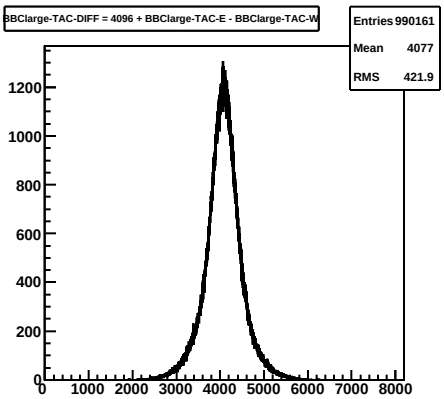

BBCsmall-TAC-E
Entries 1003888
Mean 1493
RMS 227.1

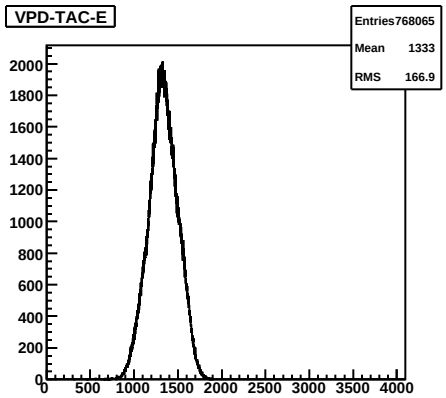

BBCsmall-TAC-W
Entries 1016671
Mean 1543
RMS 233

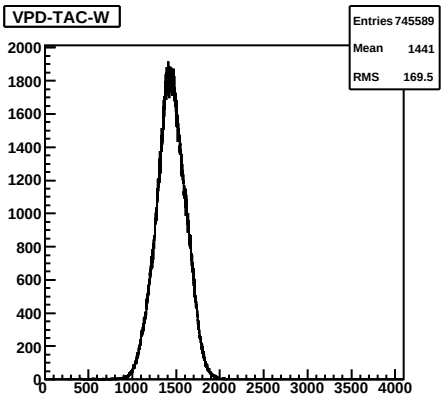

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 953654
Mean 4045
RMS 282.2

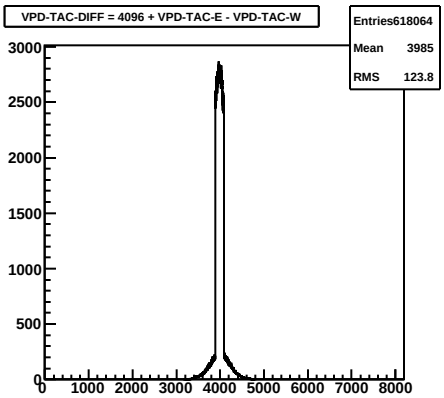

BBClarge-TAC-E
Entries 1027144
Mean 1465
RMS 325.2

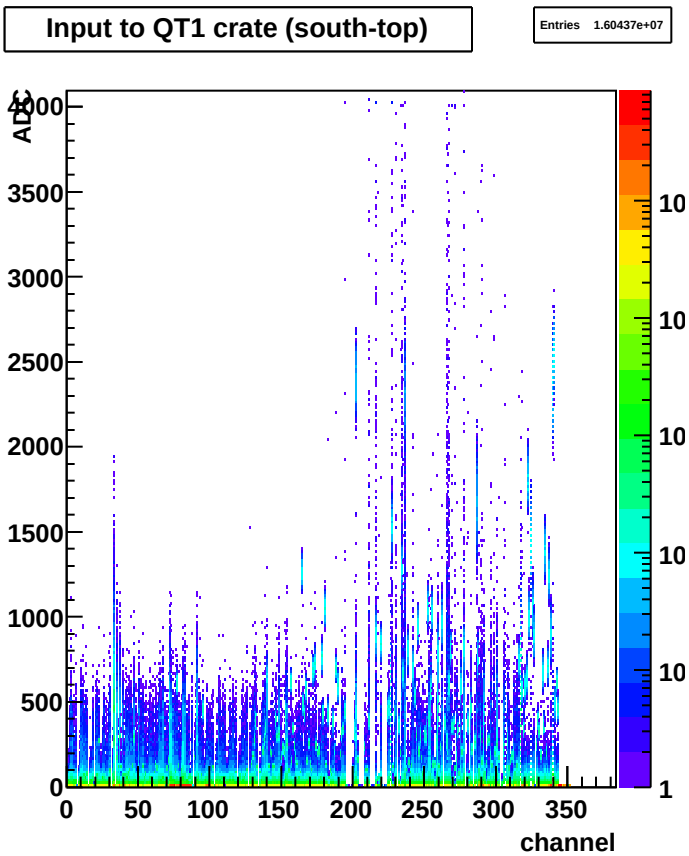

BBClarge-TAC-W
Entries 1036492
Mean 1484
RMS 333.8

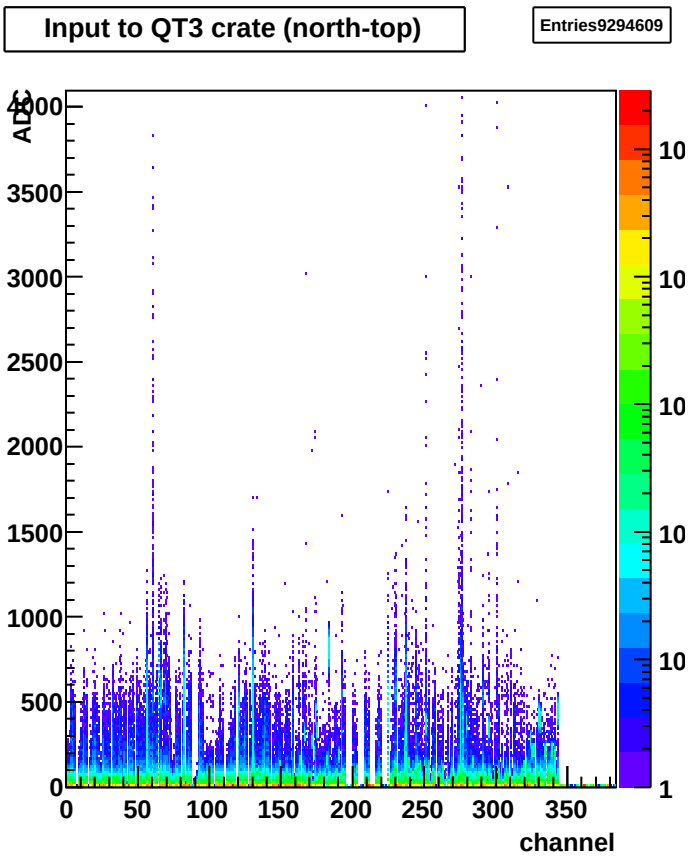

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 990161
Mean 4077
RMS 421.9

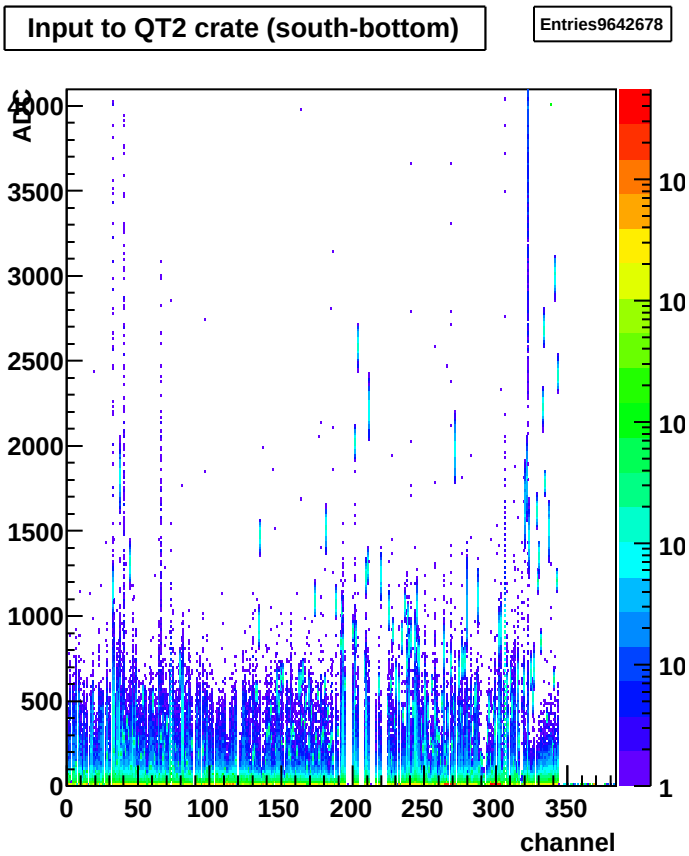

VPD-TAC-E
Entries 768065
Mean 1333
RMS 166.9

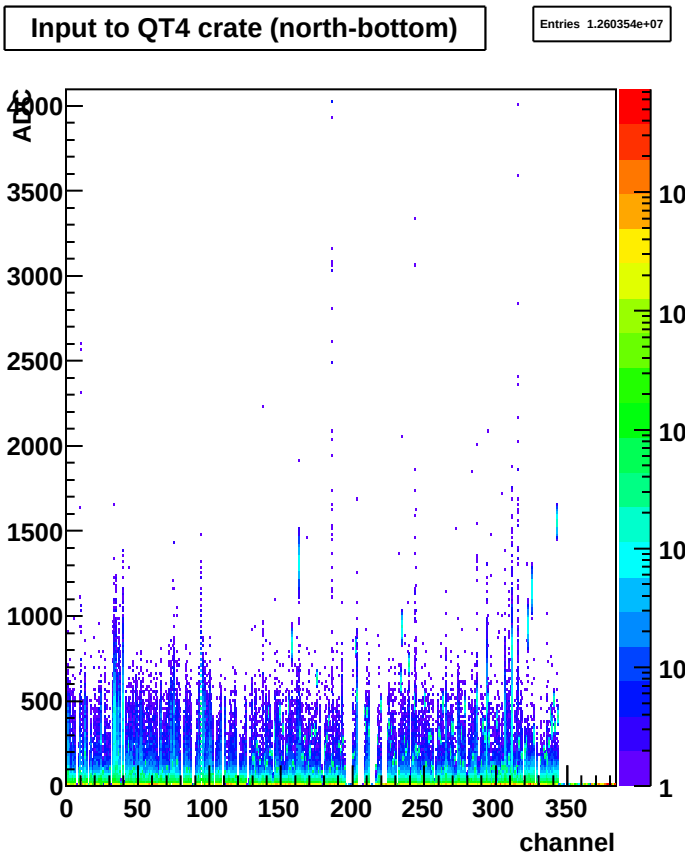

VPD-TAC-W
Entries 745589
Mean 1441
RMS 169.5

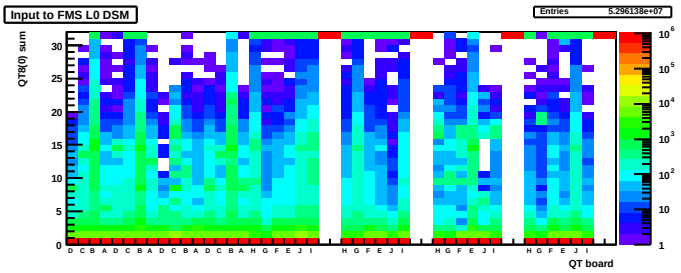

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 618064
Mean 3985
RMS 123.8

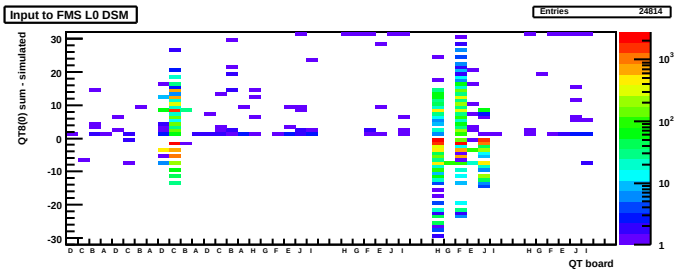

Input to QT1 crate (south-top)
Entries 1.60437e+07
ADC
channel

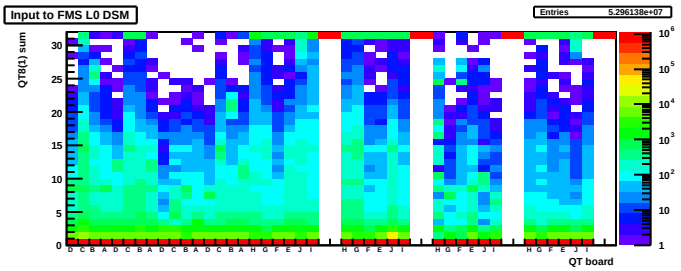

Input to QT3 crate (north-top)
Entries9294609
ADC
channel

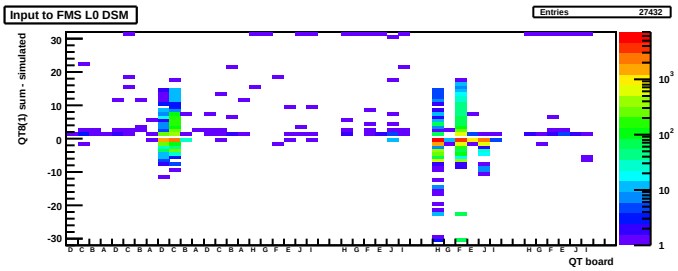

Input to QT2 crate (south-bottom)
Entries9642678
ADC
channel

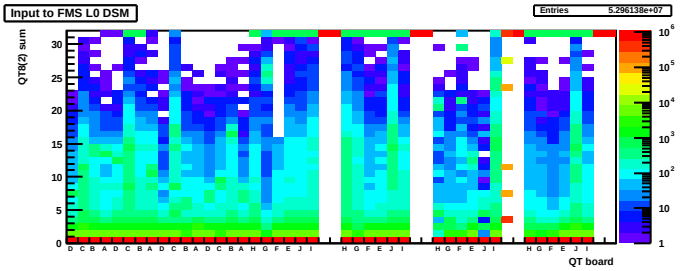

Input to QT4 crate (north-bottom)
Entries 1.260354e+07
ADC
channel

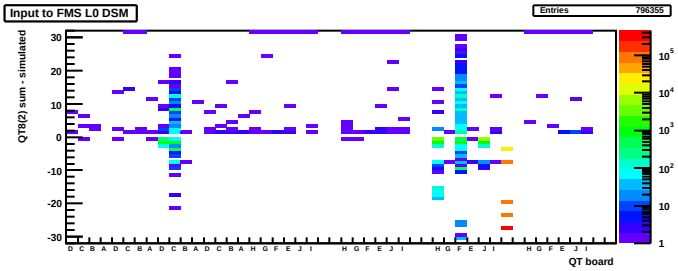

Input to FMS L0 DSM
Entries 5.296138e+07
QT8(0) sum
QT board

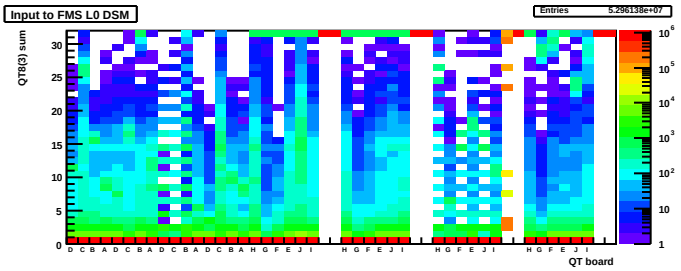

Input to FMS L0 DSM
Entries 24814
QT8(0) sum - simulated
QT board

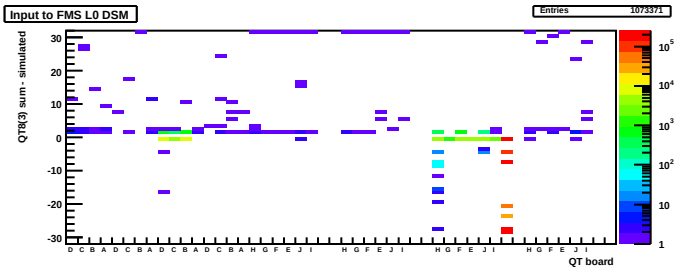

Input to FMS L0 DSM
Entries 5.296138e+07
QT8(1) sum
QT board

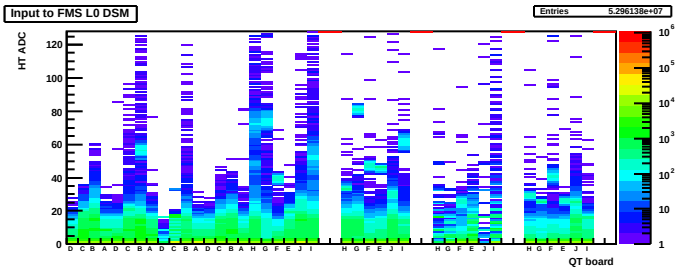

Input to FMS L0 DSM
Entries 27432
QT8(1) sum - simulated
QT board

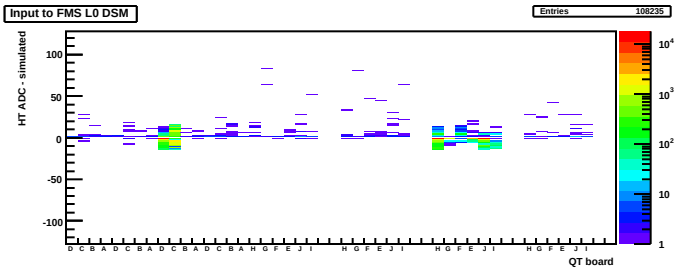

Input to FMS L0 DSM
Entries 5.296138e+07
QT8(2) sum
QT board

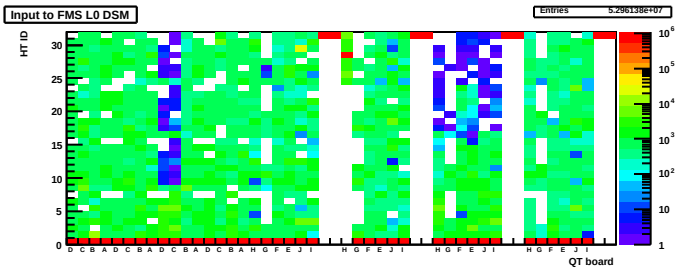

Input to FMS L0 DSM
Entries 796355
QT8(2) sum - simulated
QT board

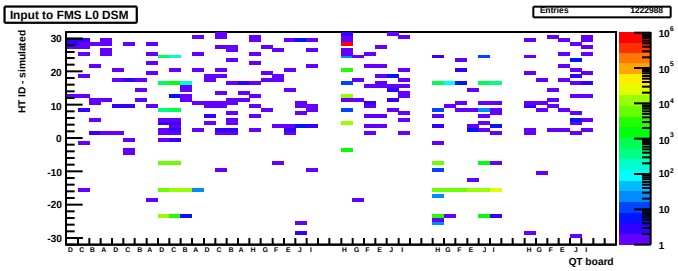

Input to FMS L0 DSM
Entries 5.296138e+07
QT8(3) sum
QT board

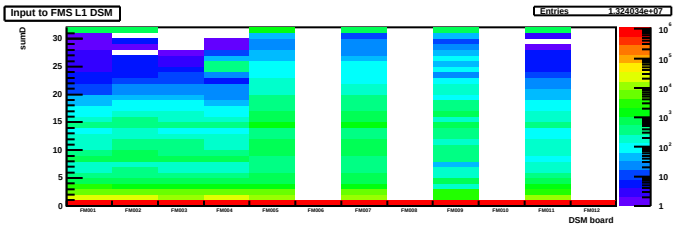

Input to FMS L0 DSM
Entries 1073371
QT8(3) sum - simulated
QT board

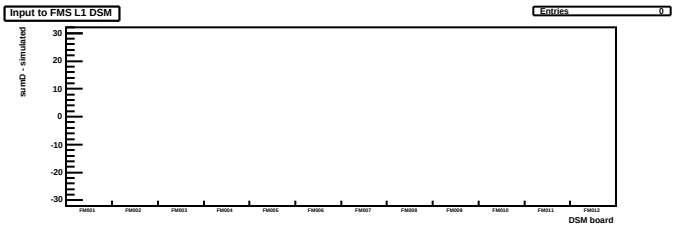

Input to FMS L0 DSM
Entries 5.296138e+07
HT ADC
QT board

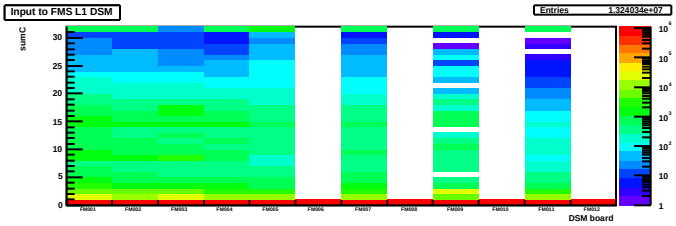

Input to FMS L0 DSM
Entries 108235
HT ADC - simulated
QT board

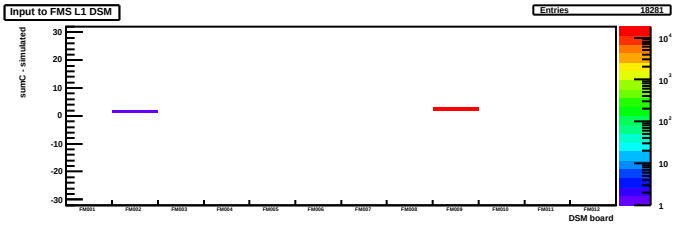

Input to FMS L0 DSM
Entries 5.296138e+07
HT ID
QT board

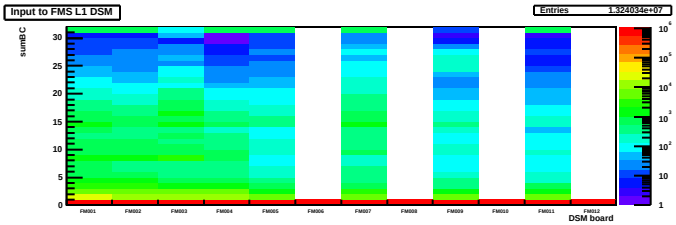

Input to FMS L0 DSM
Entries 1222988
HT ID - simulated
QT board

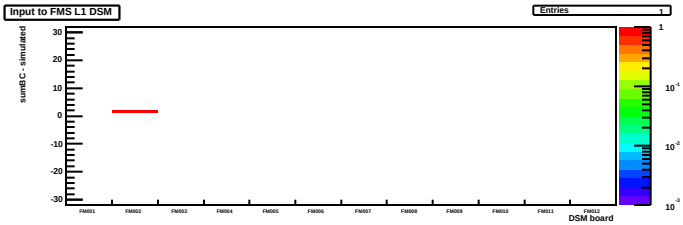

Input to FMS L1 DSM
Entries 1.324034e+07
sumD
DSM board

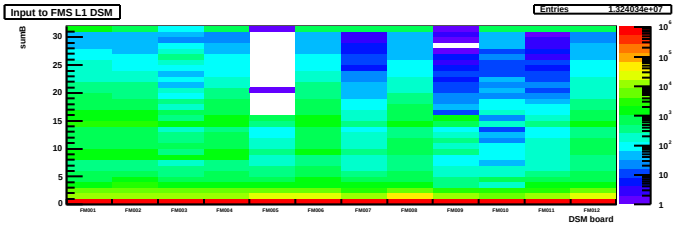

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

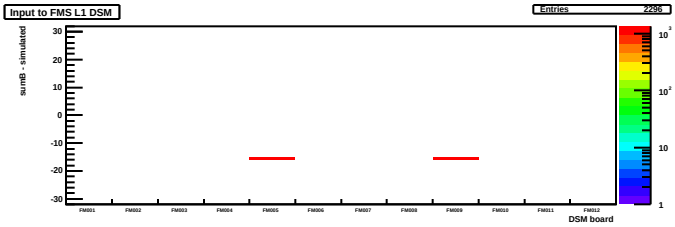

Input to FMS L1 DSM
Entries 1.324034e+07
sumC
DSM board

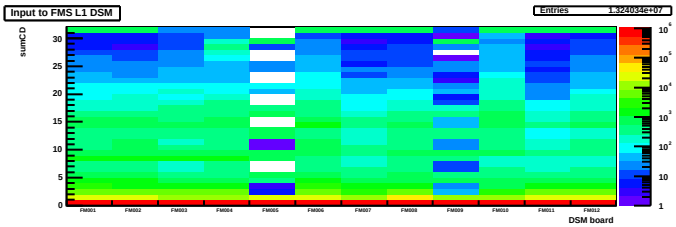

Input to FMS L1 DSM
Entries 18281
sumC - simulated
DSM board

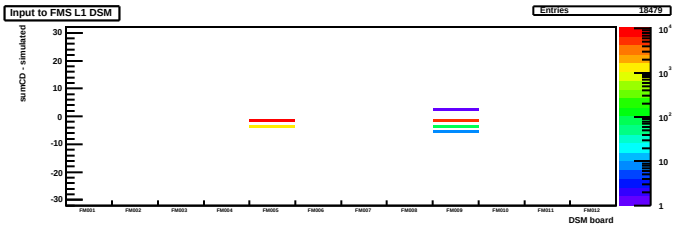

Input to FMS L1 DSM
Entries 1.324034e+07
sumBC
DSM board

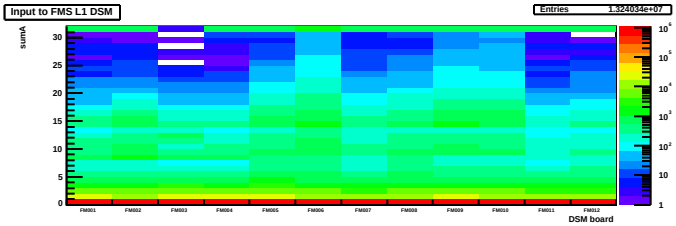

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

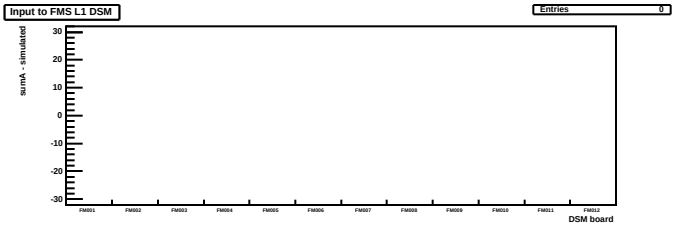

Input to FMS L1 DSM
Entries 1.324034e+07
sumB
DSM board

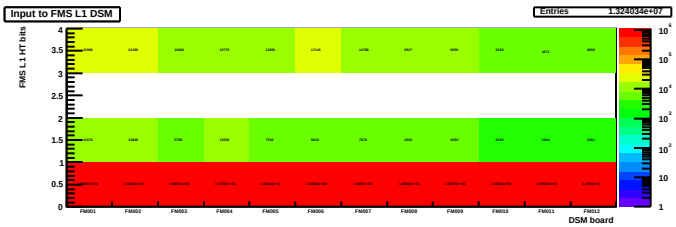

Input to FMS L1 DSM
Entries 2296
sumB - simulated
DSM board

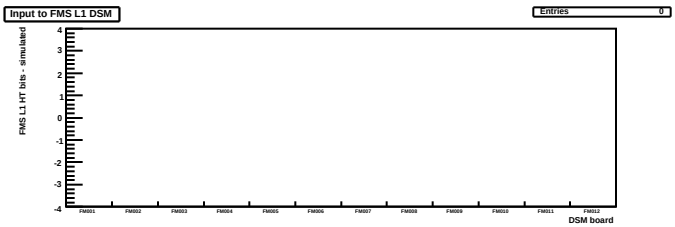

Input to FMS L1 DSM
Entries 1.324034e+07
sumCD
DSM board

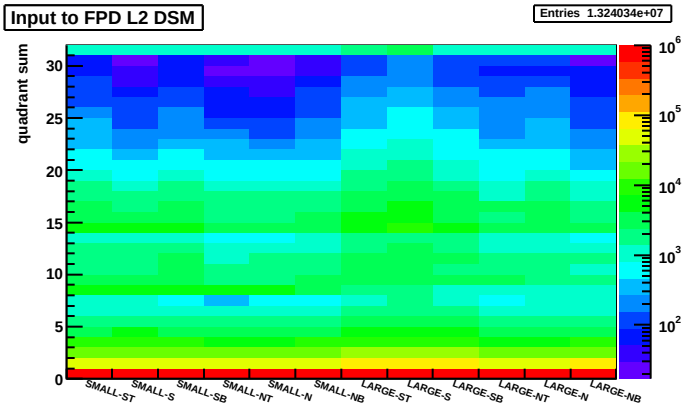

Input to FMS L1 DSM
Entries 18479
sumCD - simulated
DSM board

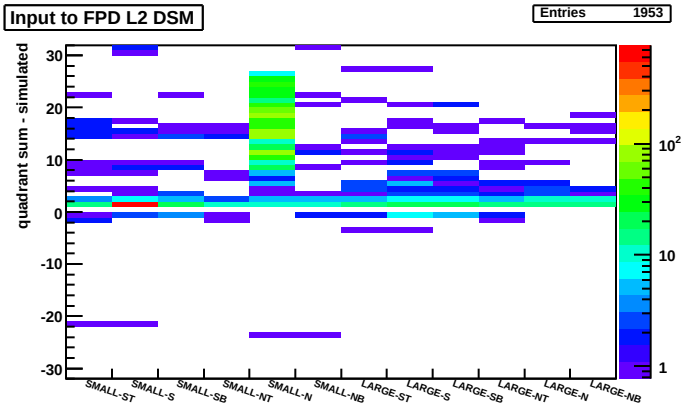

Input to FMS L1 DSM
Entries 1.324034e+07
sumA
DSM board

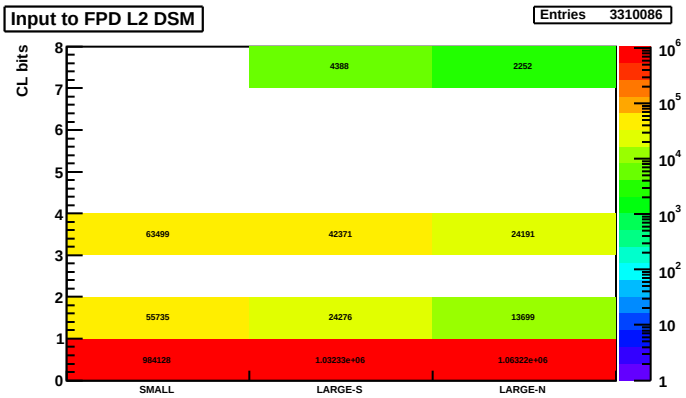

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

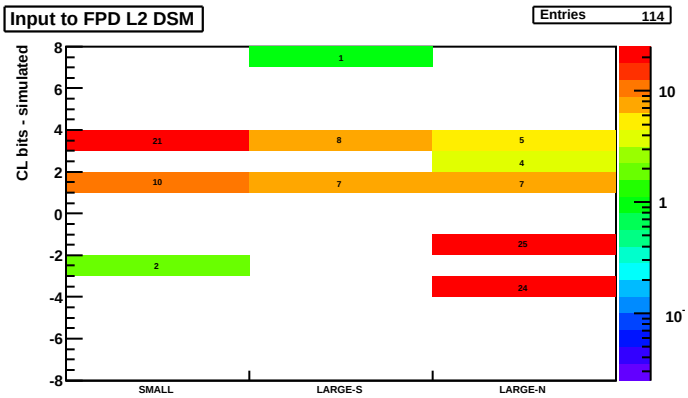

Input to FMS L1 DSM
Entries 1.324034e+07
FMS L1 HT bits
DSM board

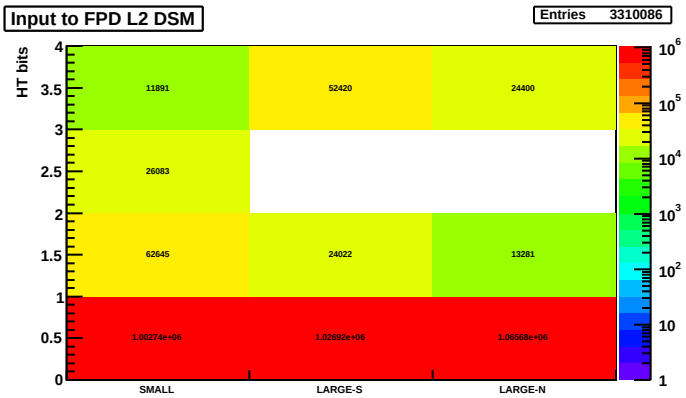

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

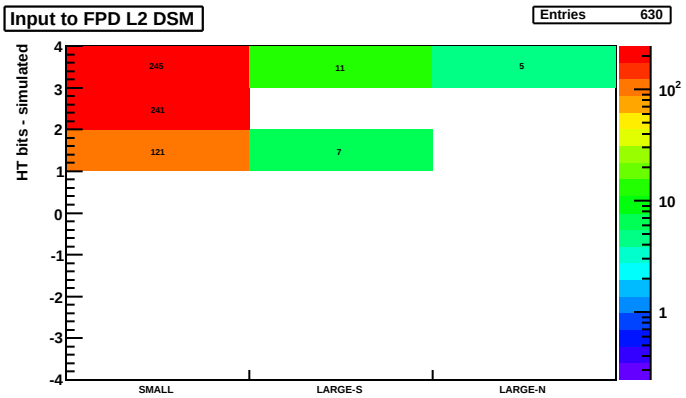

Input to FPD L2 DSM
Entries 1.324034e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

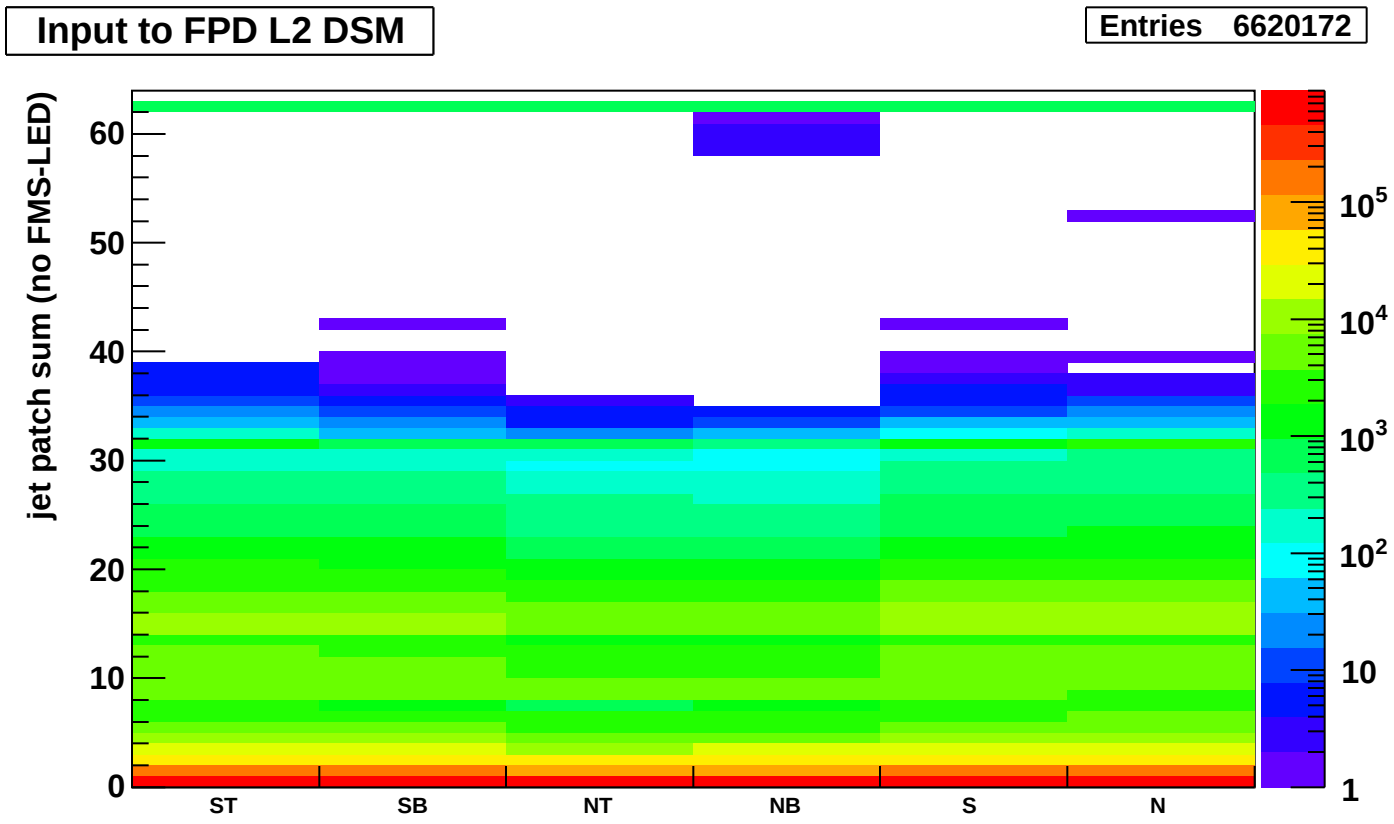

Input to FPD L2 DSM
Entries 1953
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

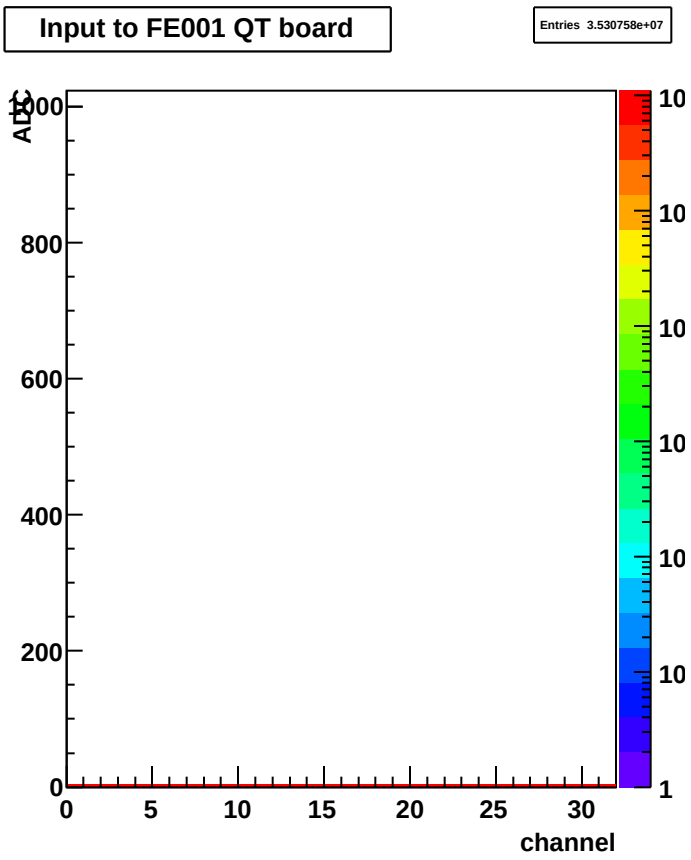

Input to FPD L2 DSM
Entries 3310086
CL bits
4388
2252
63499
42371
24191
55735
24276
13699
984128
1.03233e+06
1.06322e+06
SMALL
LARGE-S
LARGE-N

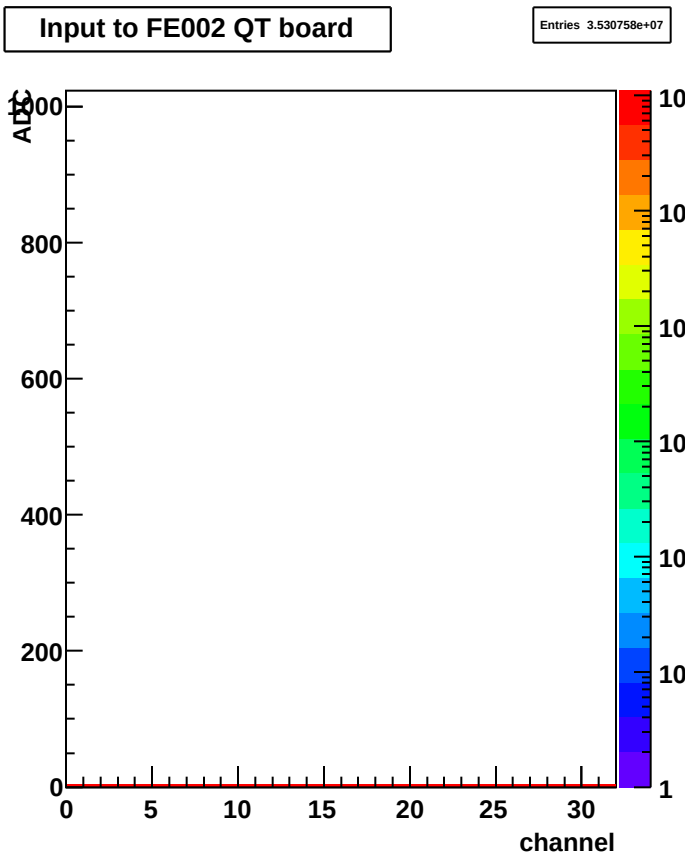

Input to FPD L2 DSM
Entries 114
CL bits - simulated
1
21
8
5
4
10
7
7
25
2
24
SMALL
LARGE-S
LARGE-N

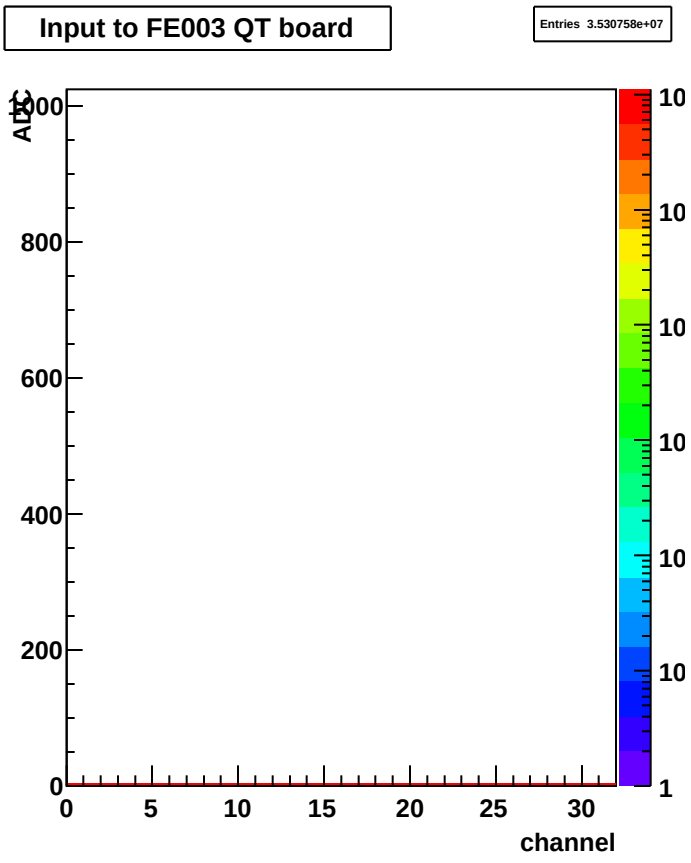

Input to FPD L2 DSM
Entries 3310086
HT bits
11891
52420
24400
26083
62645
24022
13281
1.00274e+06
1.02692e+06
1.06568e+06
SMALL
LARGE-S
LARGE-N

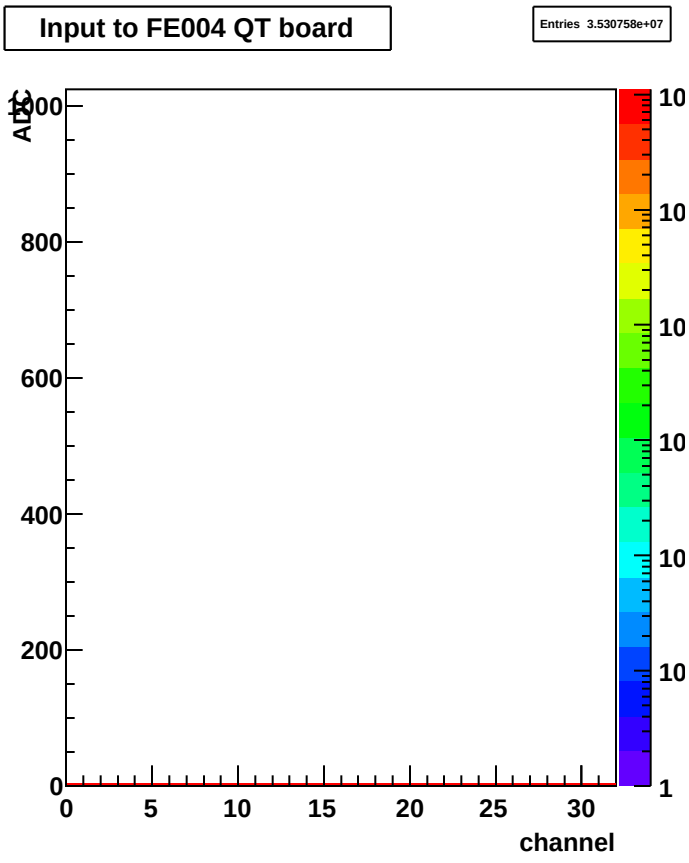

Input to FPD L2 DSM
Entries 630
HT bits - simulated
245
11
5
241
121
7
SMALL
LARGE-S
LARGE-N

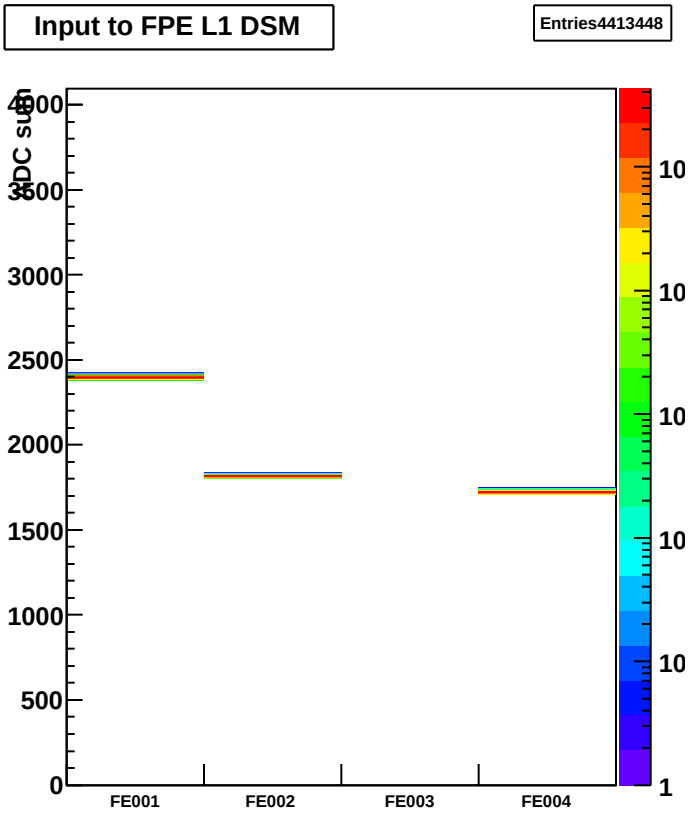

Input to FPD L2 DSM
Entries 6620172
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

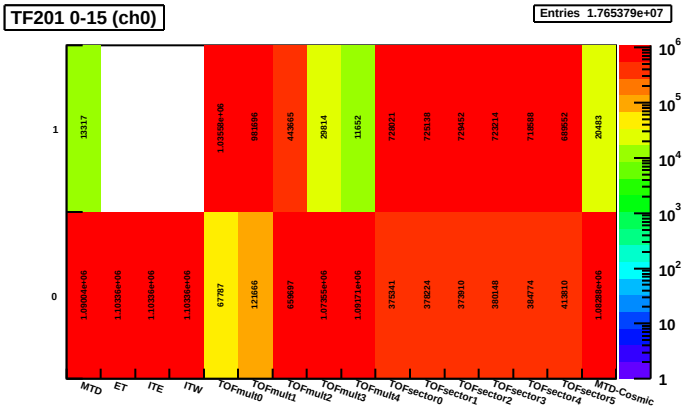

Input to FE001 QT board
Entries 3.530758e+07
ADC
channel

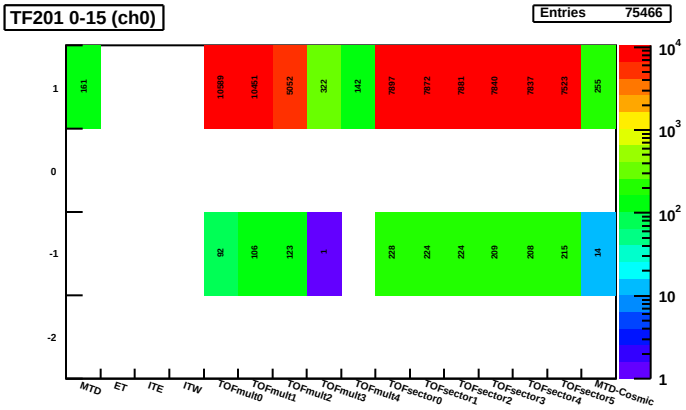

Input to FE002 QT board
Entries 3.530758e+07
ADC
channel

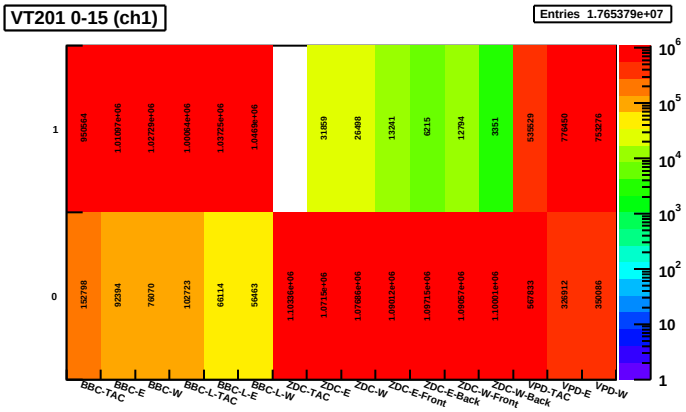

Input to FE003 QT board
Entries 3.530758e+07
ADC
channel

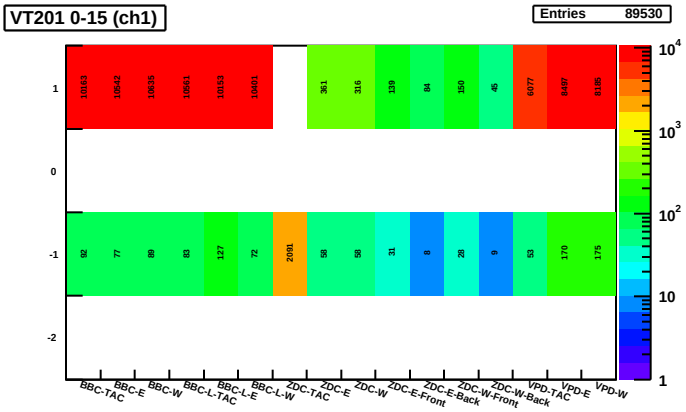

Input to FE004 QT board
Entries 3.530758e+07
ADC
channel

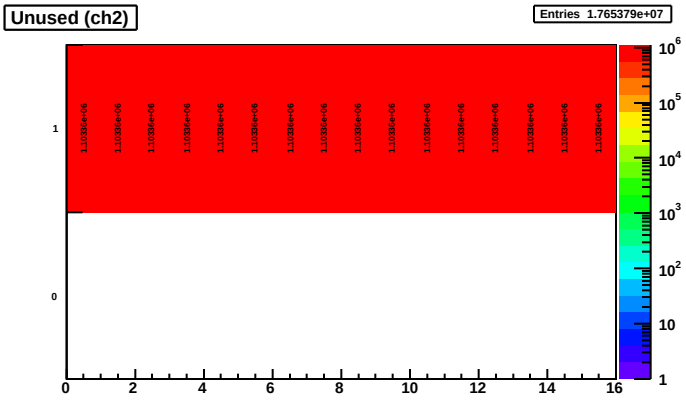

Input to FPE L1 DSM
Entries4413448
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

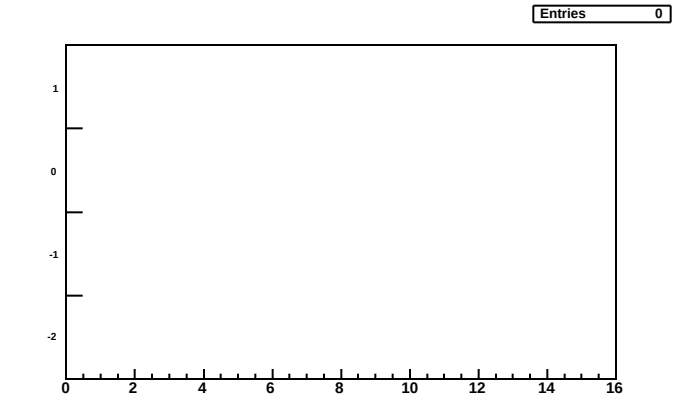

TF201 0-15 (ch0)
Entries 1.765379e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

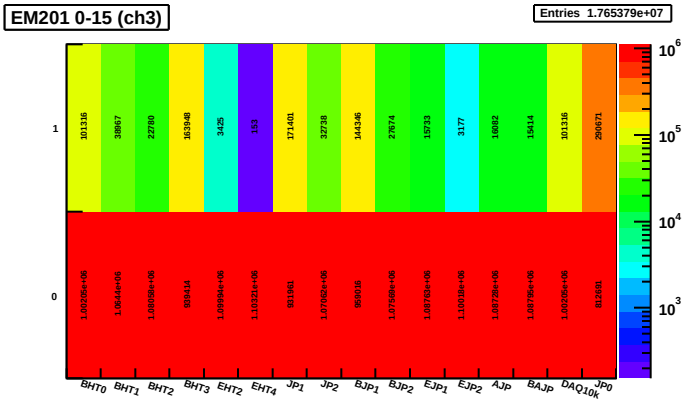

TF201 0-15 (ch0)
Entries 75466
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

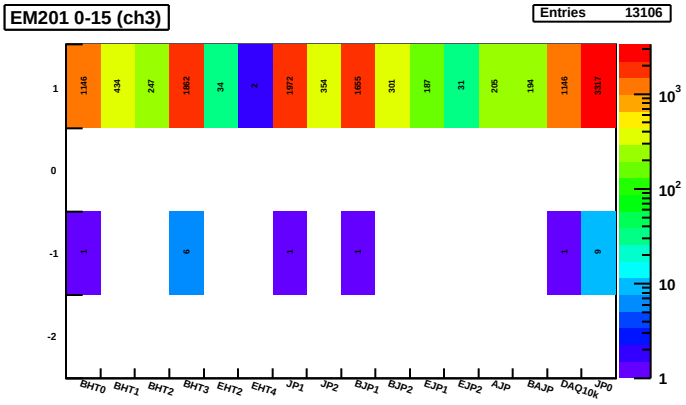

VT201 0-15 (ch1)
Entries 1.765379e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 89530
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

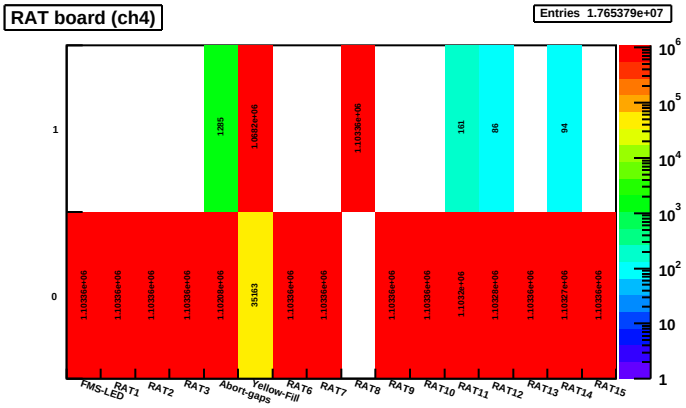

Unused (ch2)
Entries 1.765379e+07

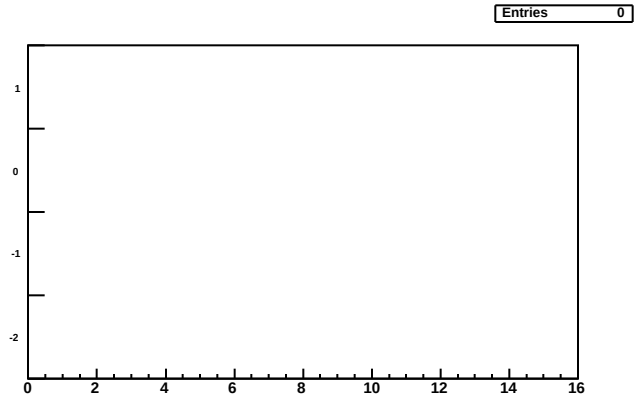

Entries 0


EM201 0-15 (ch3)
Entries 1.765379e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

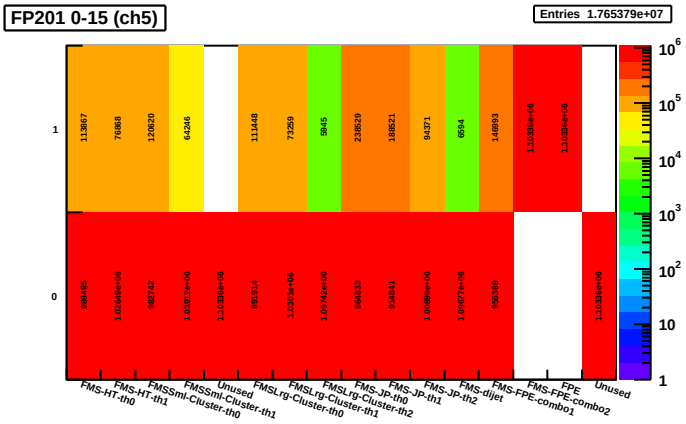

EM201 0-15 (ch3)
Entries 13106
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

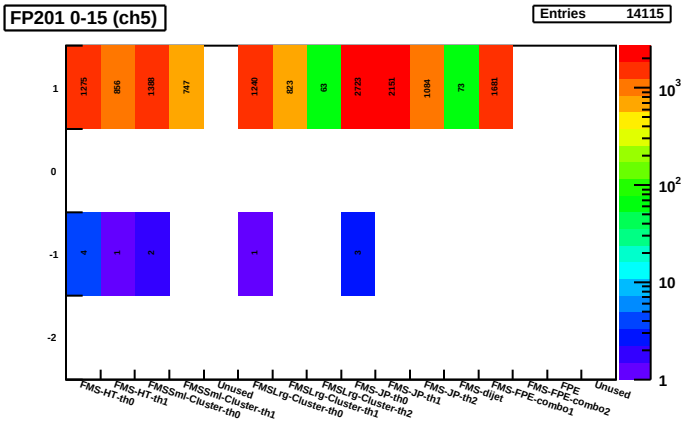




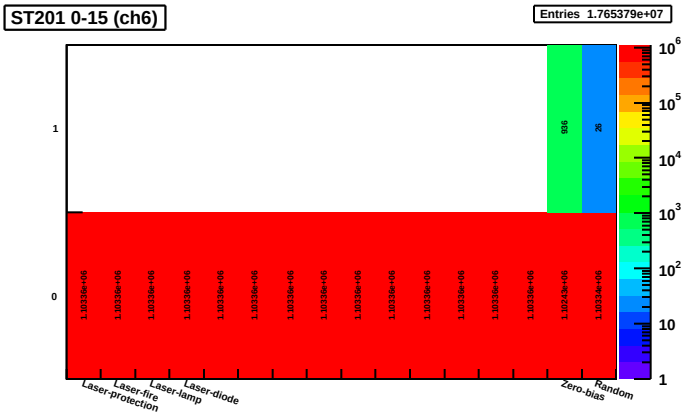
FP201 0-15 (ch5)

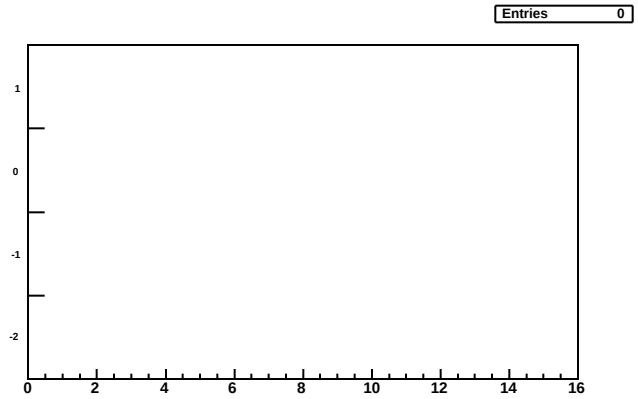
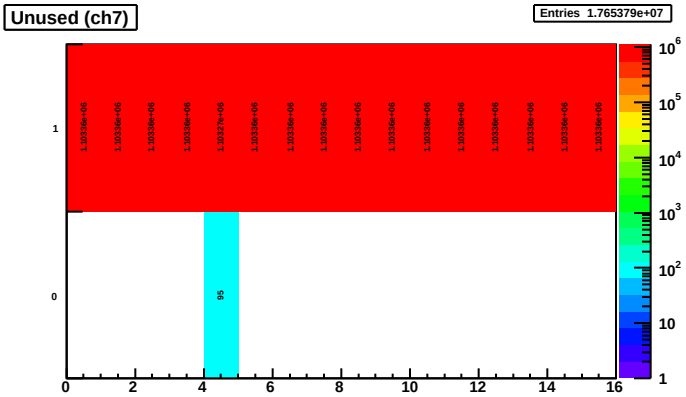
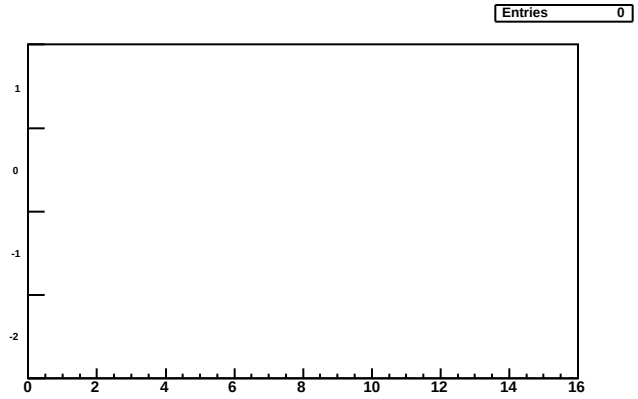


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)