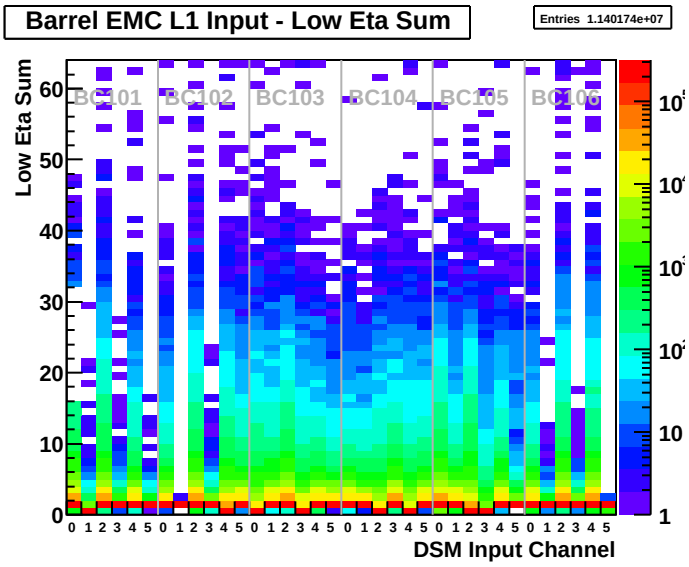
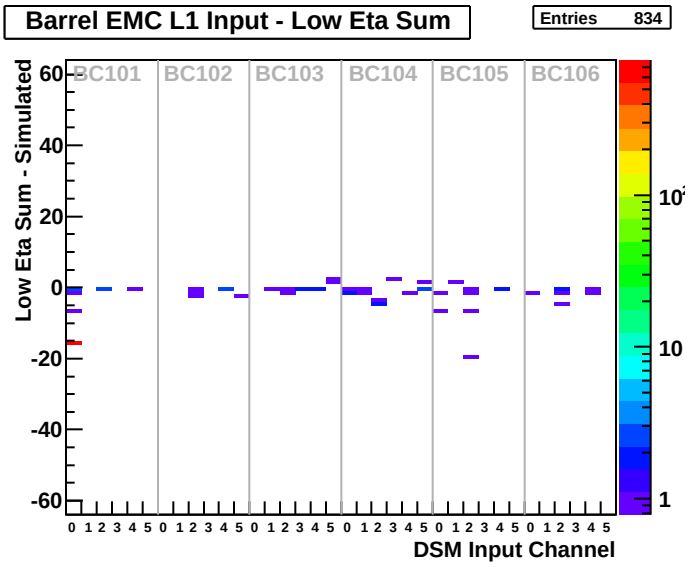
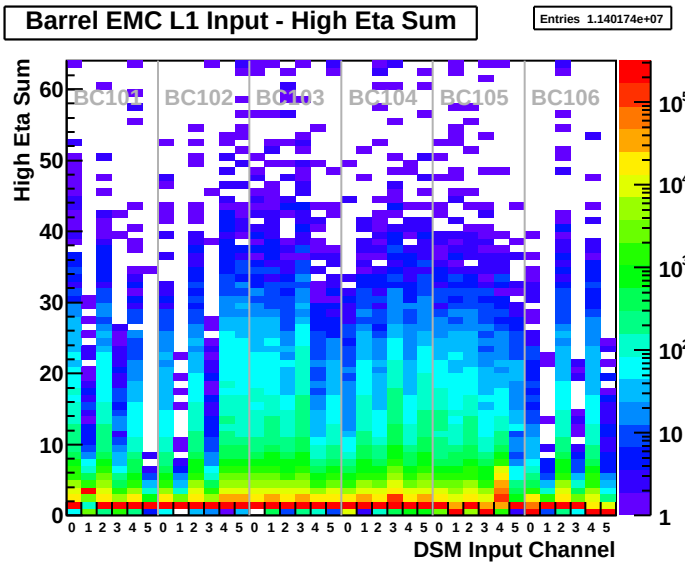
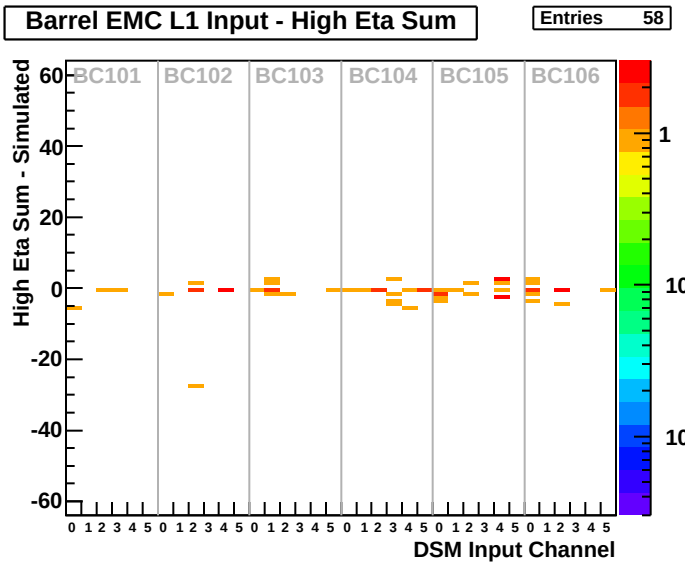
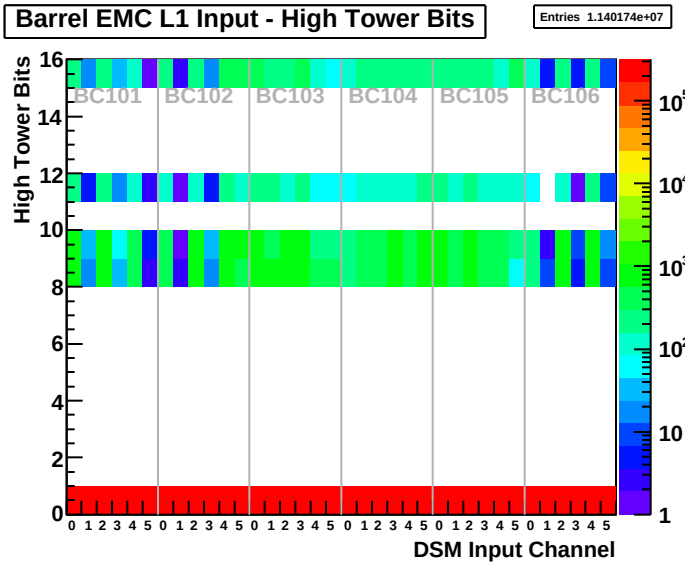
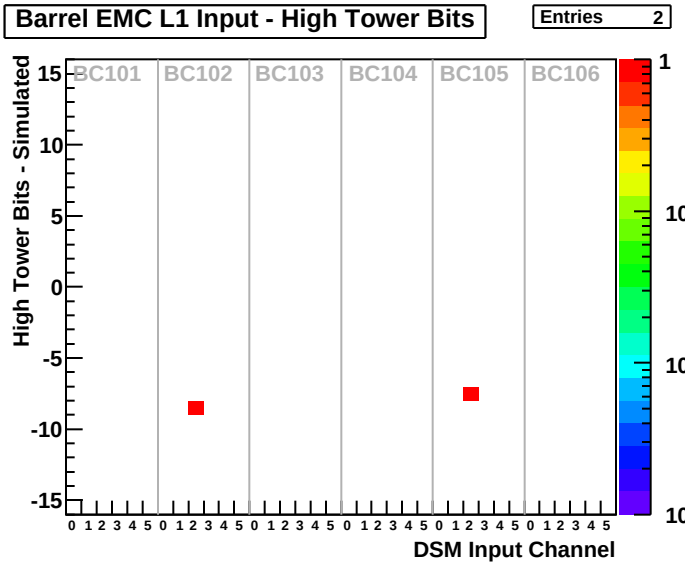

Barrel EMC L1 Input - Low Eta Sum
Entries 1.140174e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

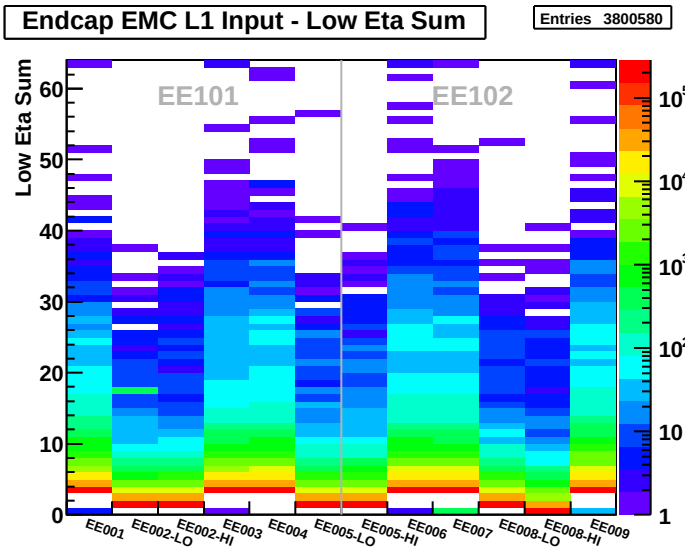

Barrel EMC L1 Input - Low Eta Sum
Entries 834
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

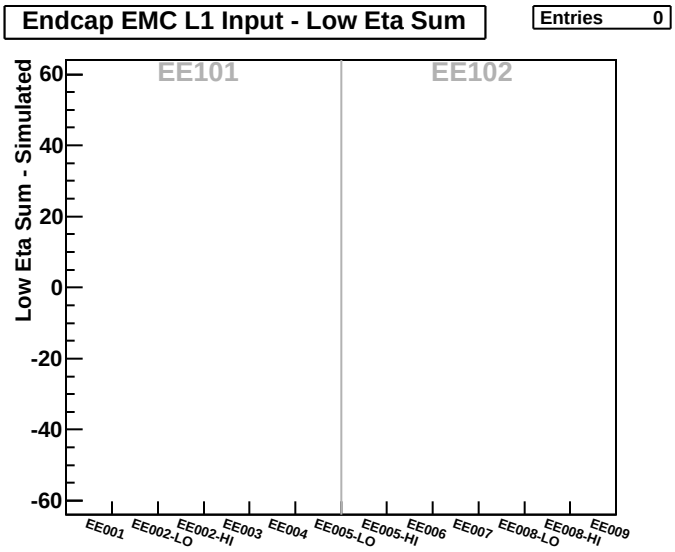

Barrel EMC L1 Input - High Eta Sum
Entries 1.140174e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

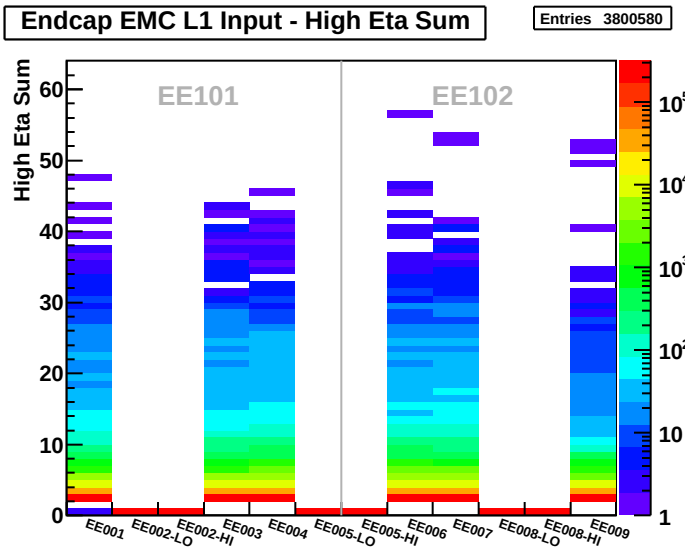

Barrel EMC L1 Input - High Eta Sum
Entries 58
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

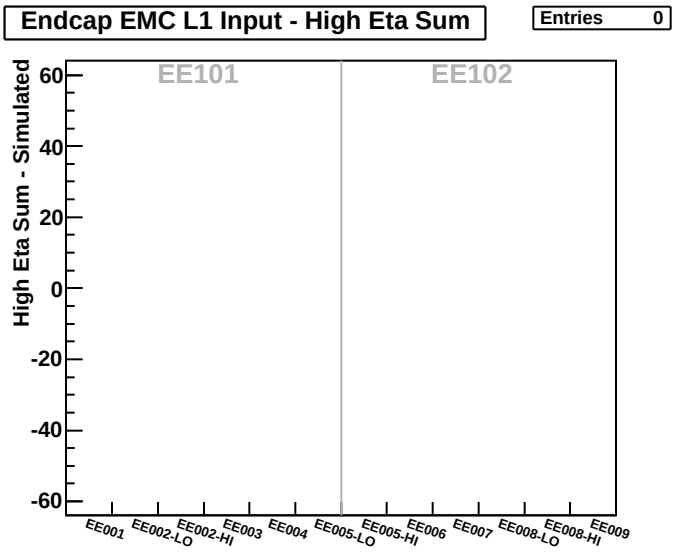

Barrel EMC L1 Input - High Tower Bits
Entries 1.140174e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

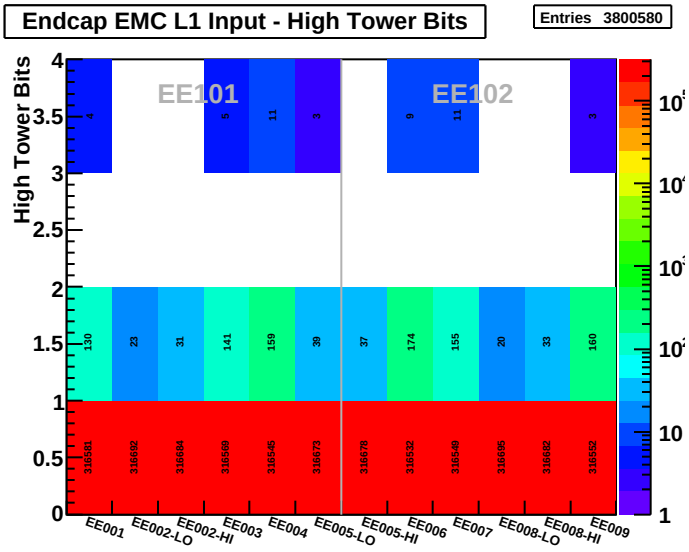

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

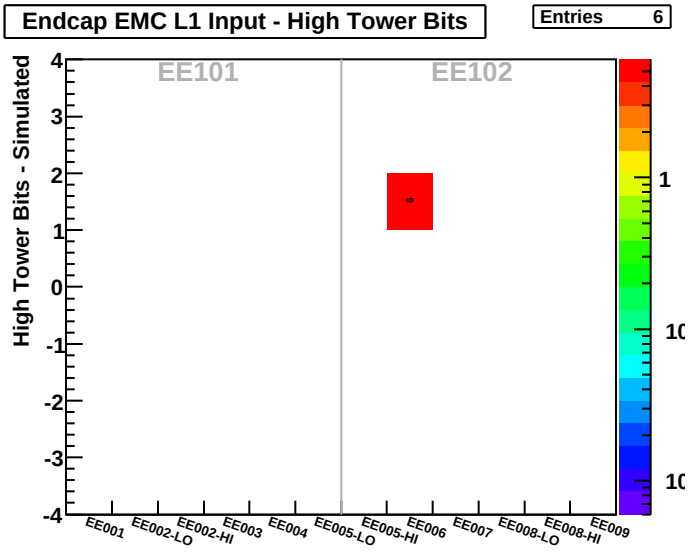

Endcap EMC L1 Input - Low Eta Sum
Entries 3800580
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

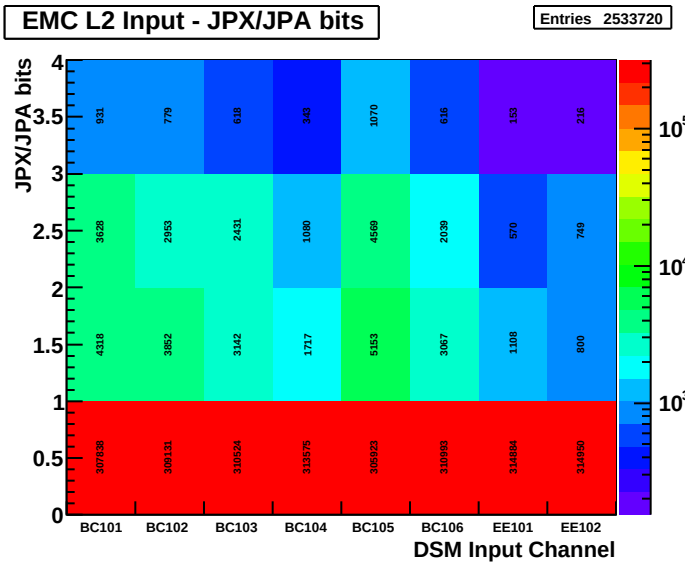

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

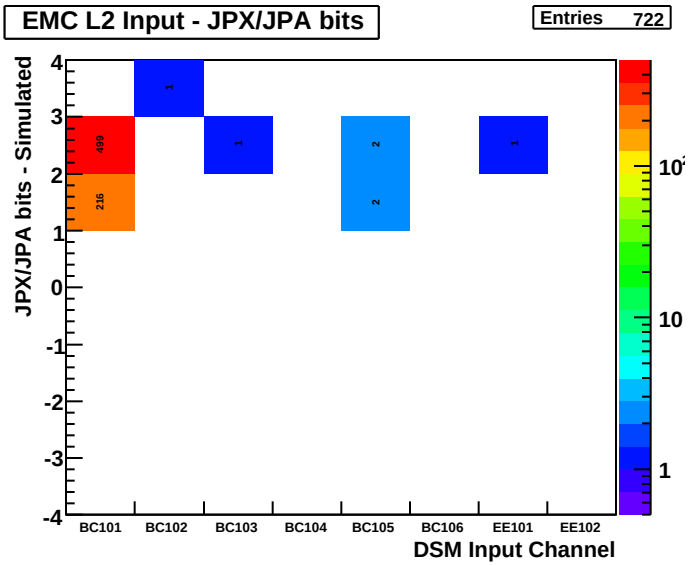

Endcap EMC L1 Input - High Eta Sum
Entries 3800580
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

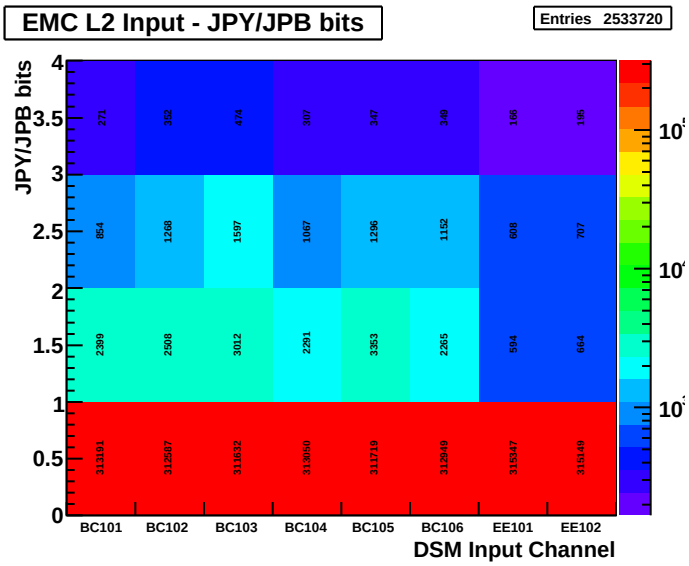

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

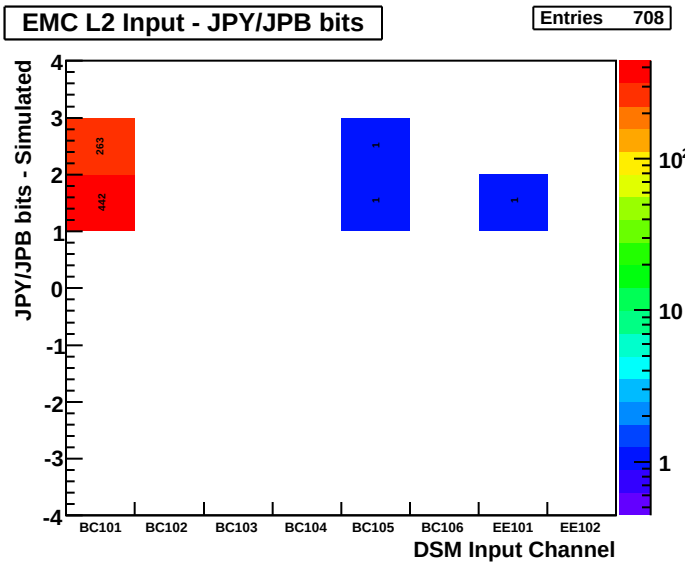

Endcap EMC L1 Input - High Tower Bits
Entries 3800580
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

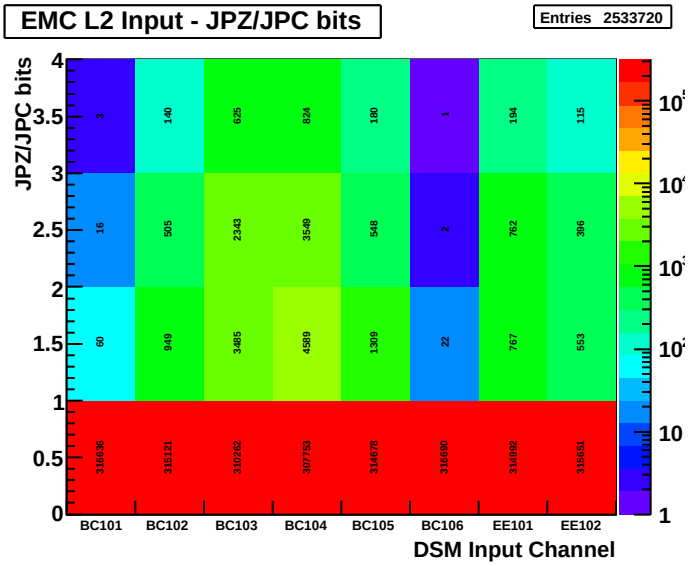

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

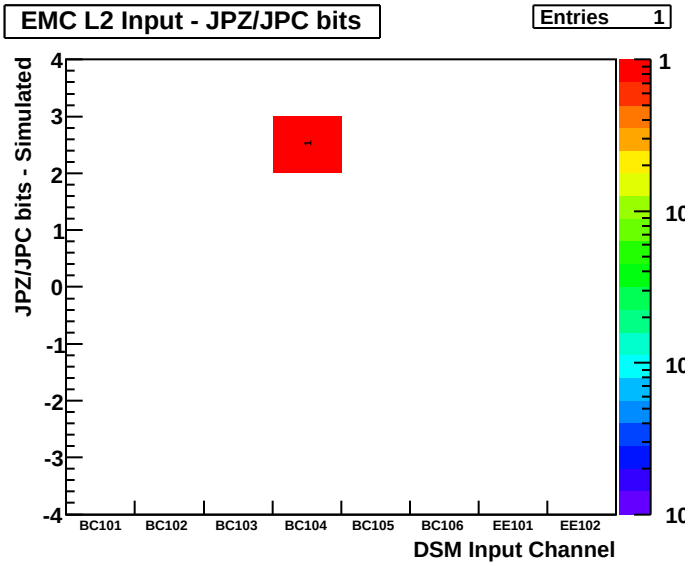

EMC L2 Input - JPX/JPA bits
Entries 2533720
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

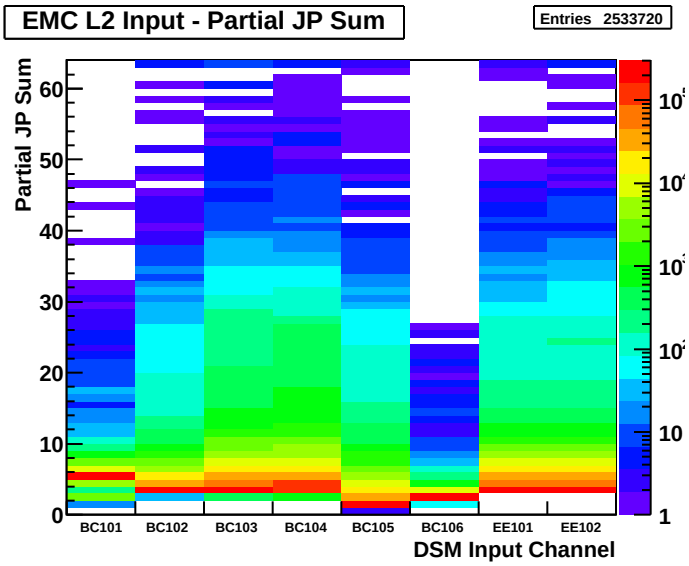

EMC L2 Input - JPX/JPA bits
Entries 722
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

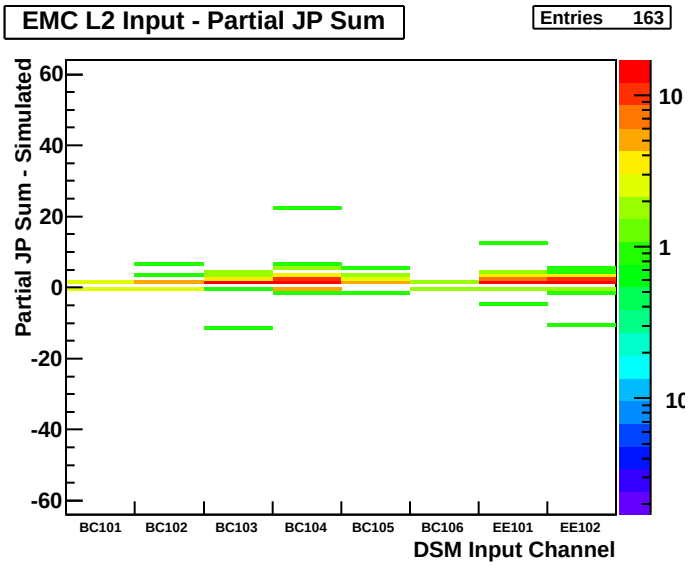

EMC L2 Input - JPY/JPB bits
Entries 2533720
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

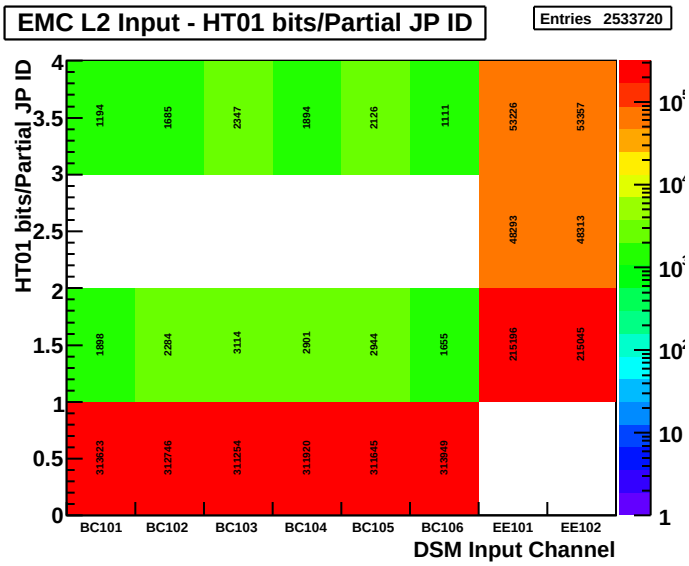

EMC L2 Input - JPY/JPB bits
Entries 708
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

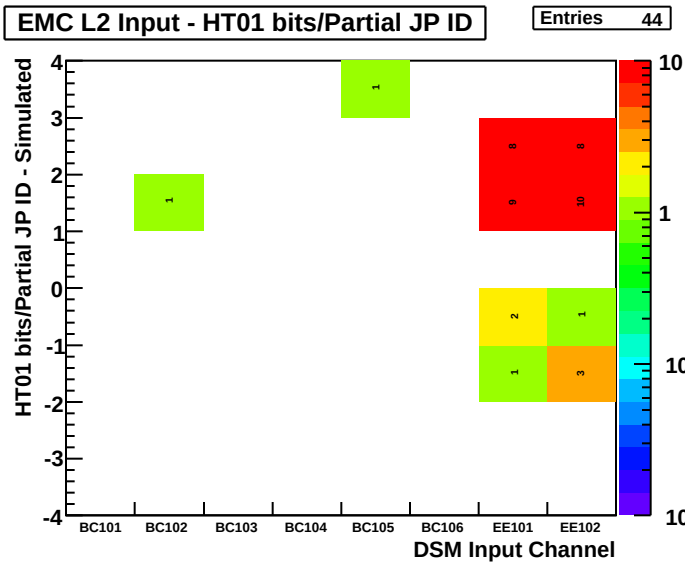

EMC L2 Input - JPZ/JPC bits
Entries 2533720
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

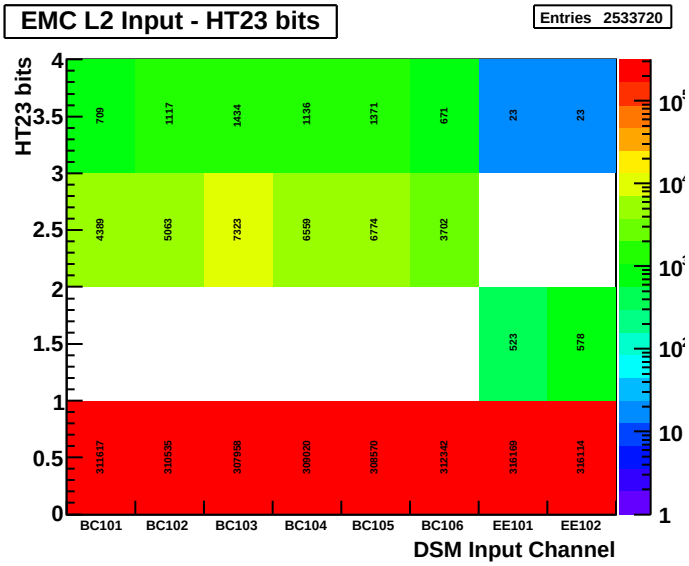

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

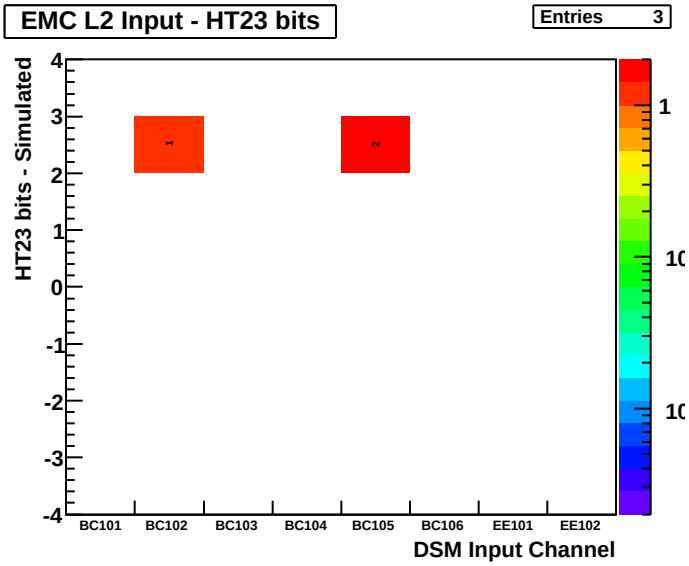

EMC L2 Input - Partial JP Sum
Entries 2533720
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

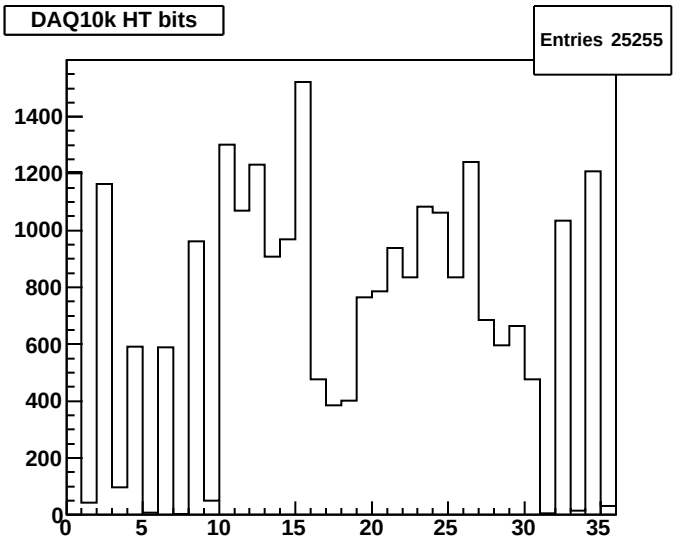

EMC L2 Input - Partial JP Sum
Entries 163
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

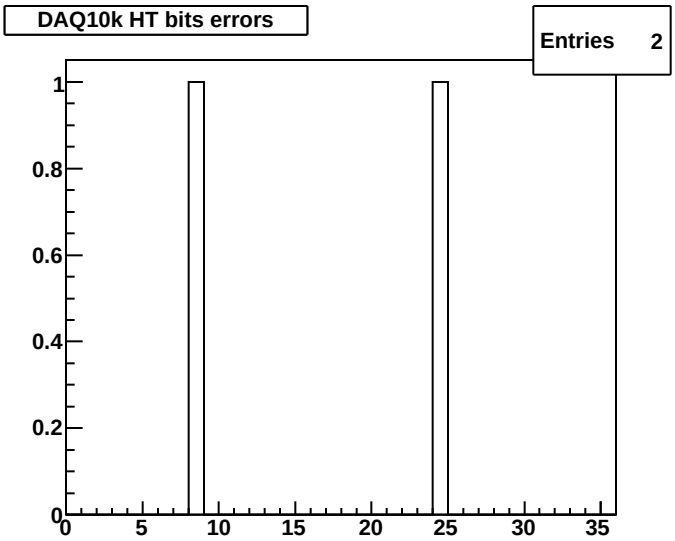

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2533720
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

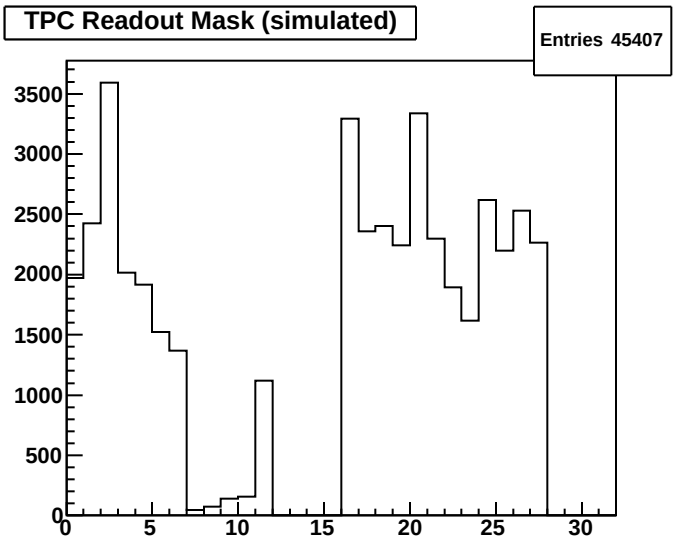

EMC L2 Input - HT01 bits/Partial JP ID
Entries 44
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

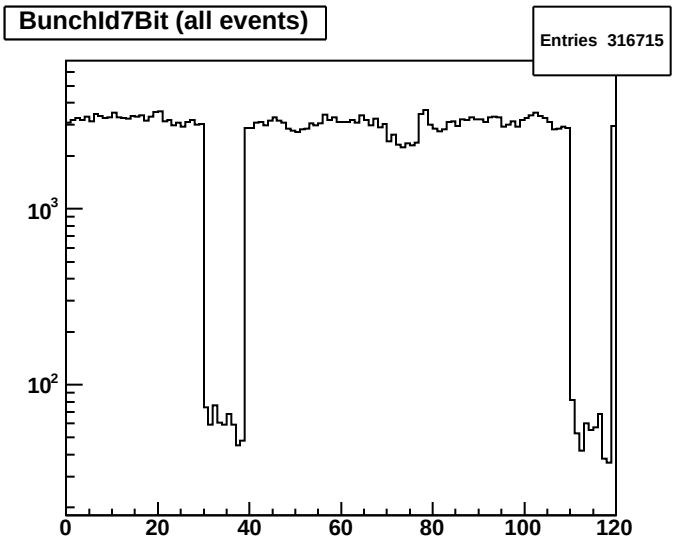

EMC L2 Input - HT23 bits
Entries 2533720
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

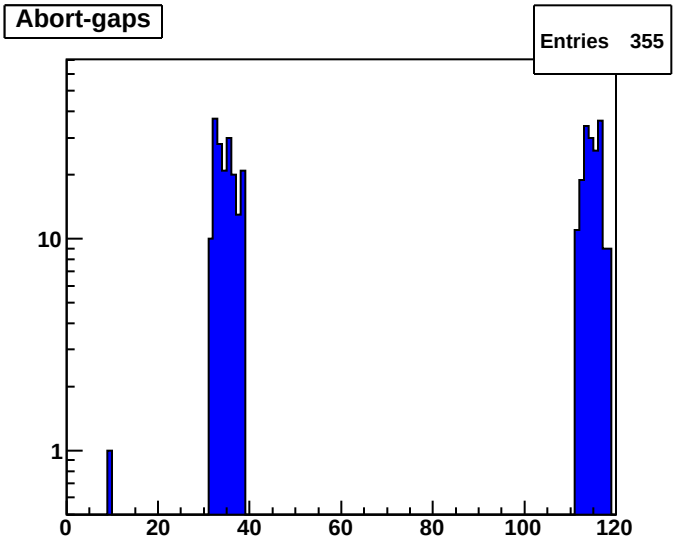

EMC L2 Input - HT23 bits
Entries 3
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

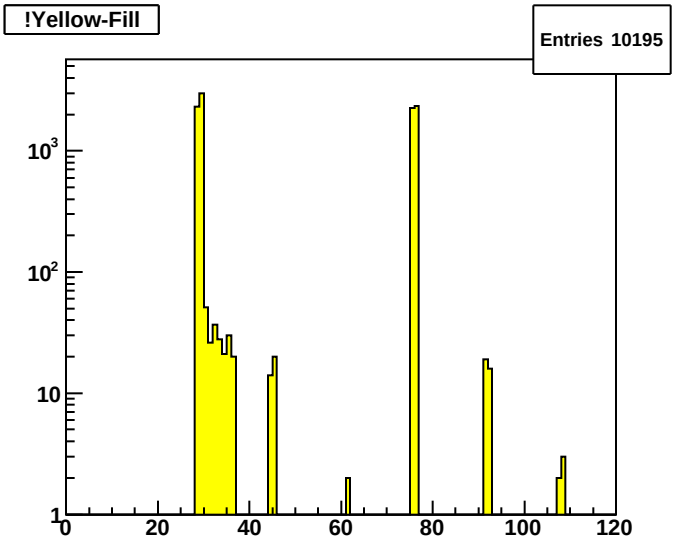

DAQ10k HT bits
Entries 25255

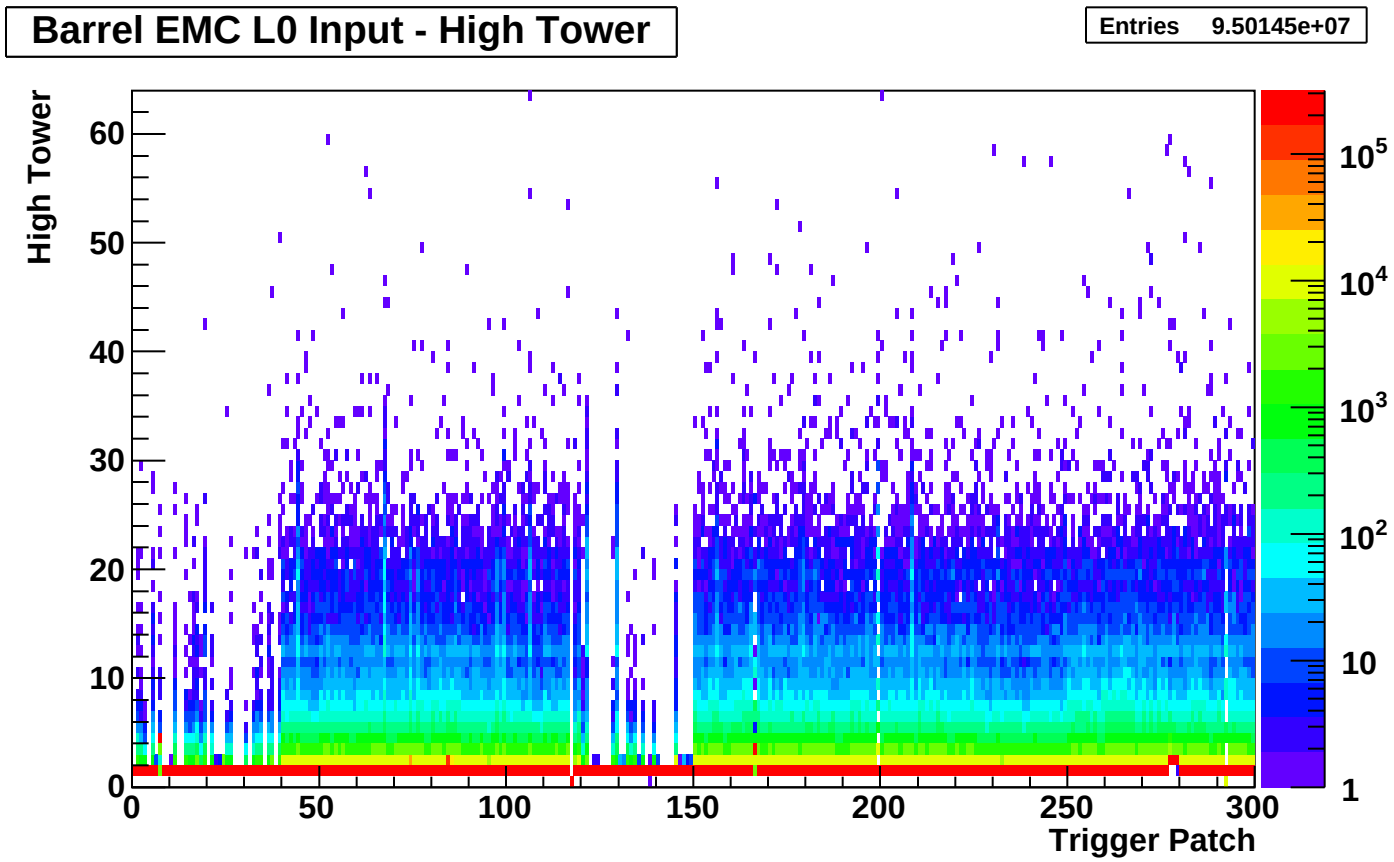

DAQ10k HT bits errors
Entries 2

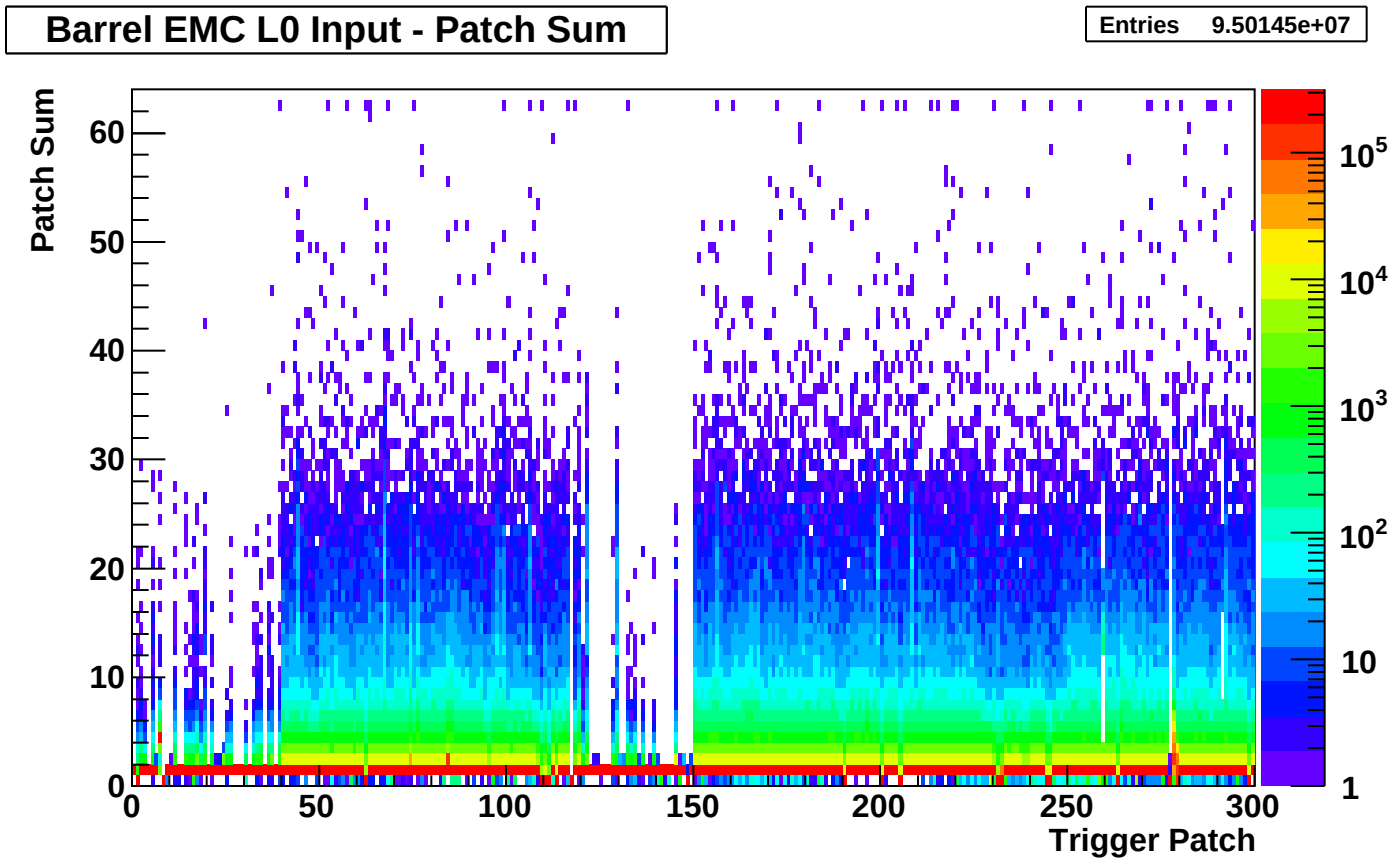

TPC Readout Mask (simulated)
Entries 45407

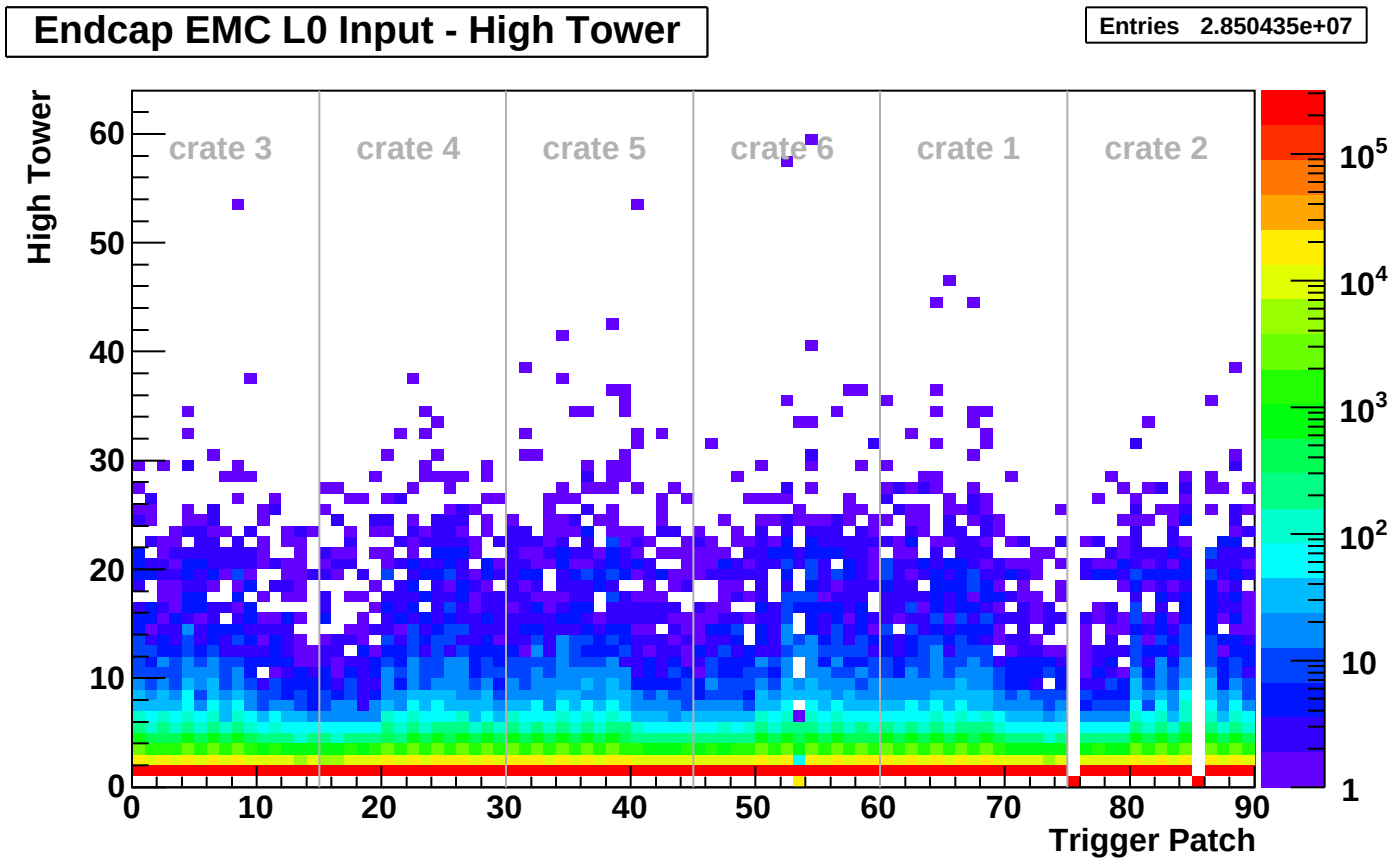

BunchId7Bit (all events)
Entries 316715

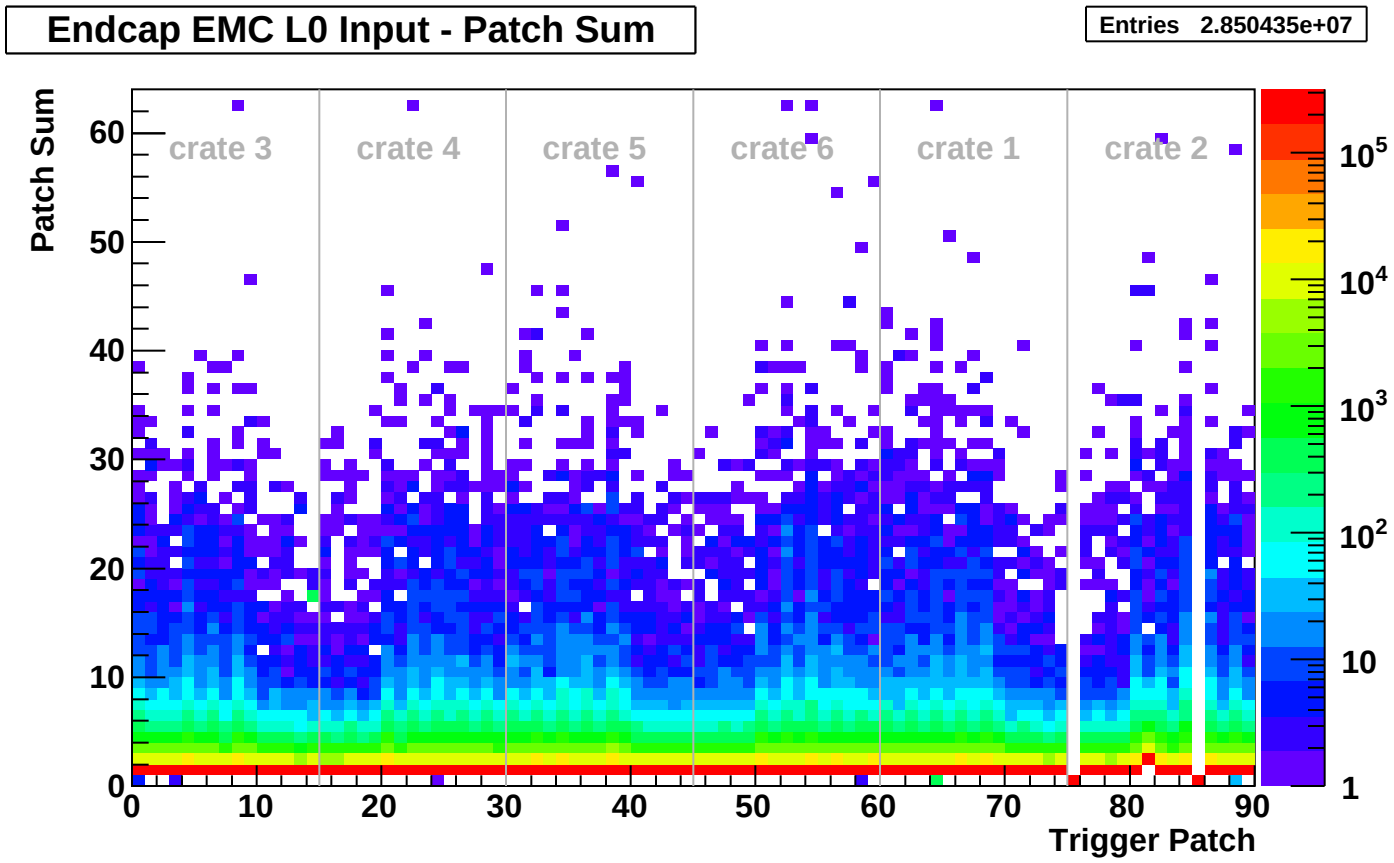

Abort-gaps
Entries 355

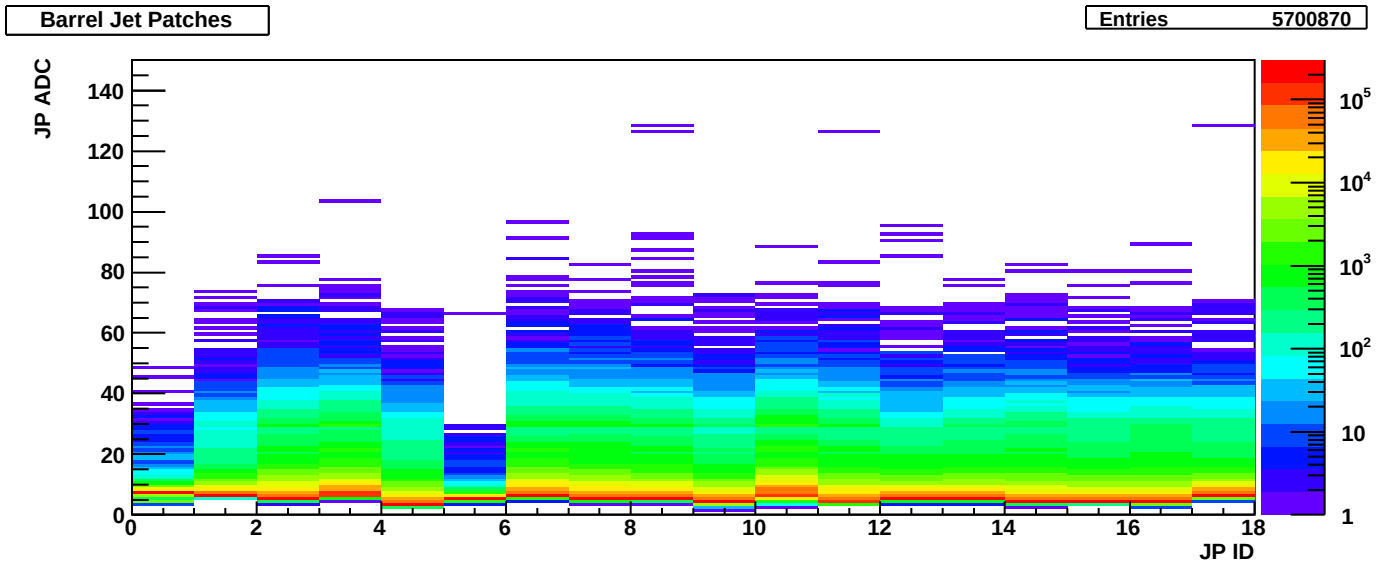

!Yellow-Fill
Entries 10195

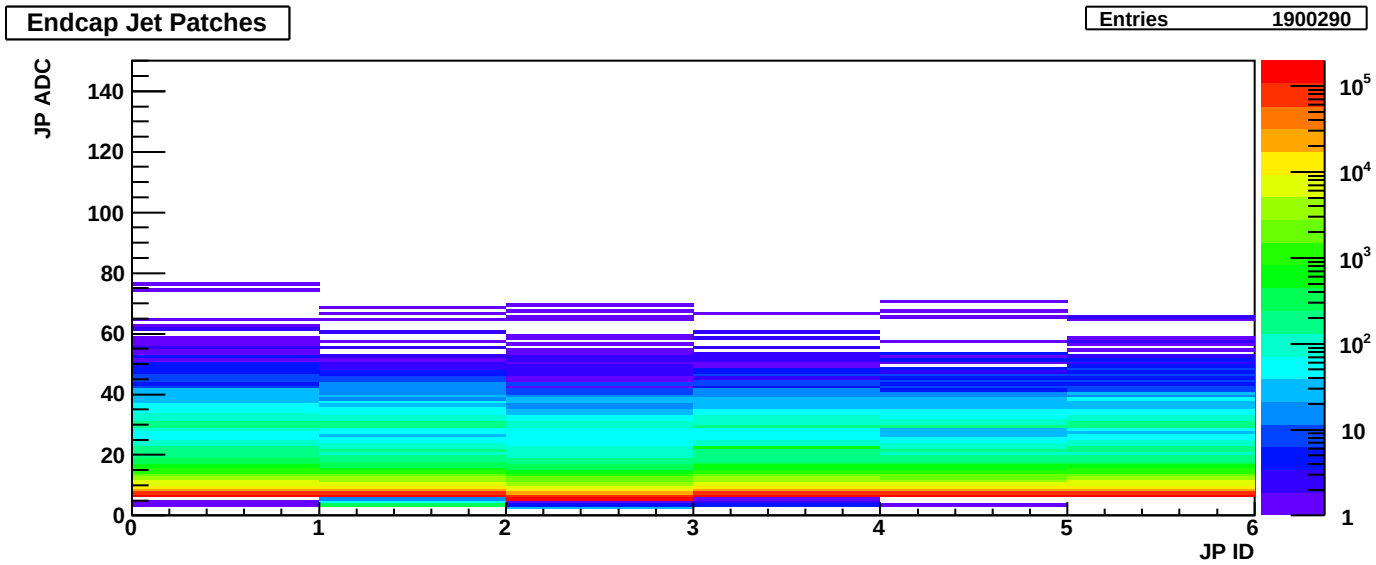

Barrel EMC L0 Input - High Tower
Entries 9.50145e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

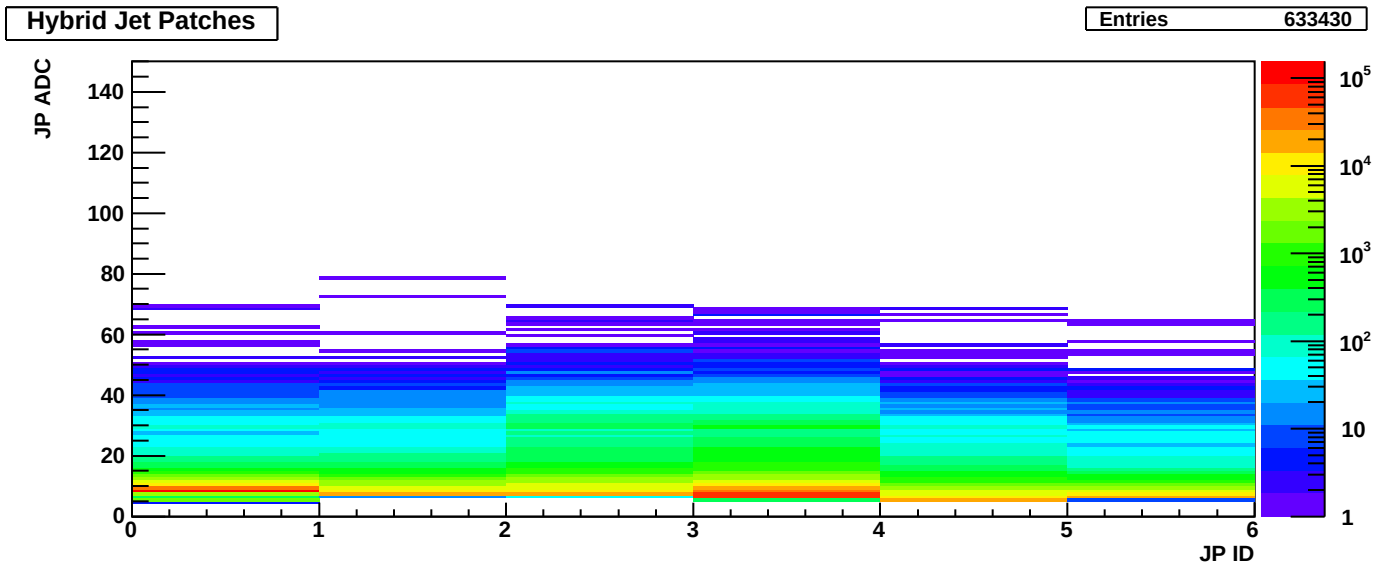

Barrel EMC L0 Input - Patch Sum
Entries 9.50145e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

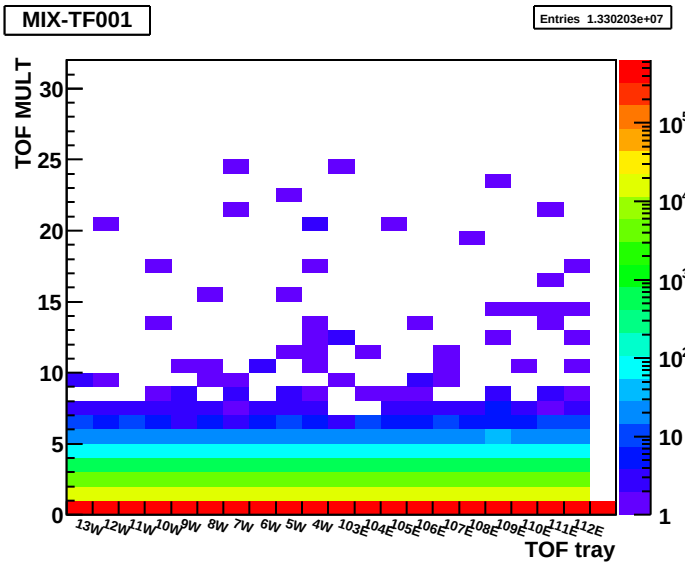

Endcap EMC L0 Input - High Tower
Entries 2.850435e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

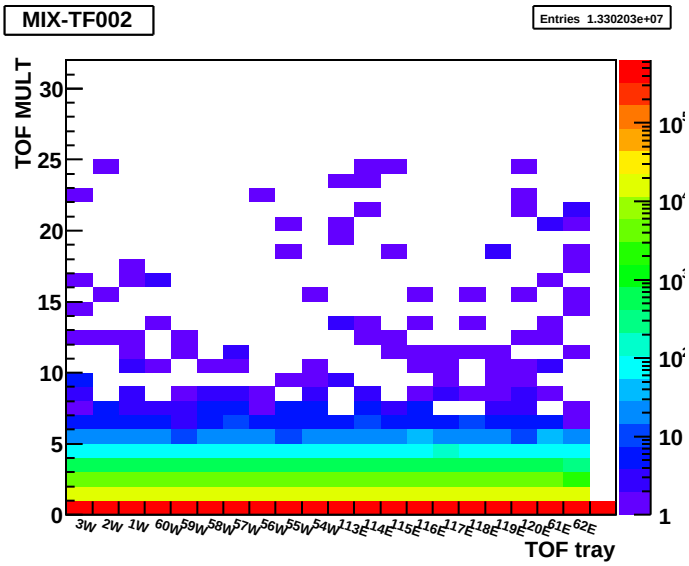

Endcap EMC L0 Input - Patch Sum
Entries 2.850435e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

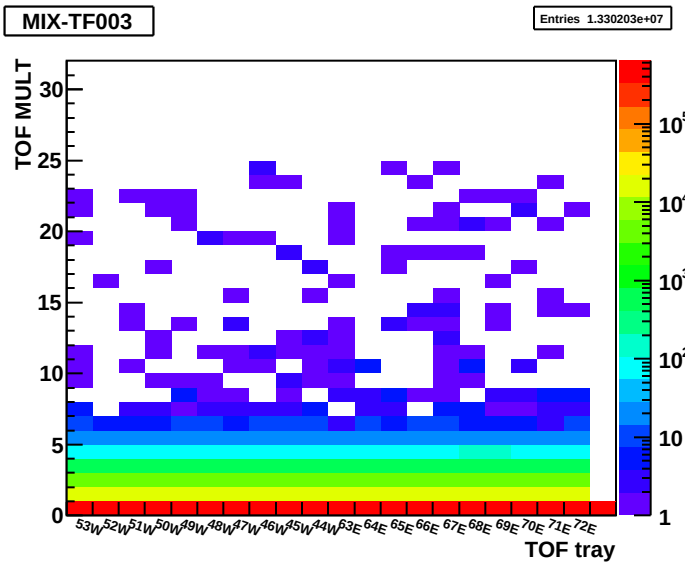

Barrel Jet Patches
Entries 5700870
JP ADC
JP ID

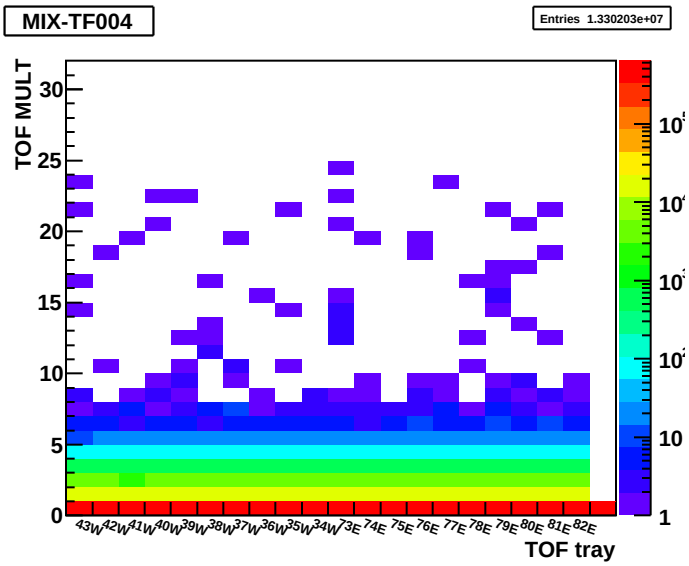

Endcap Jet Patches
Entries 1900290
JP ADC
JP ID

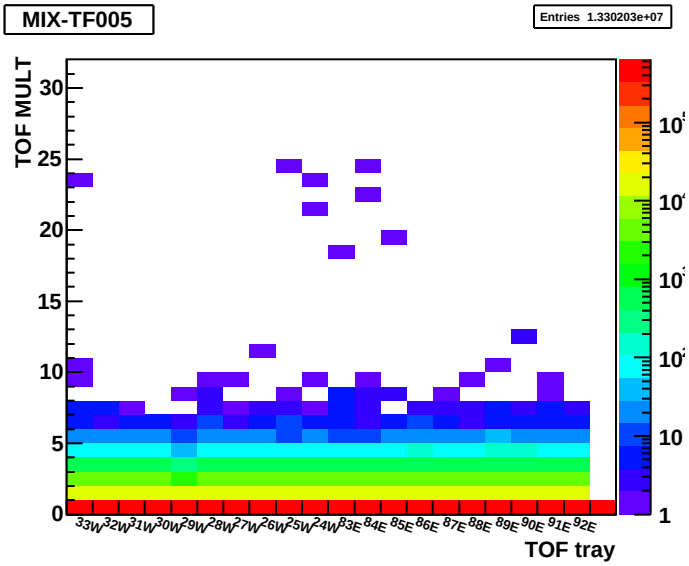

Hybrid Jet Patches
Entries 633430
JP ADC
JP ID

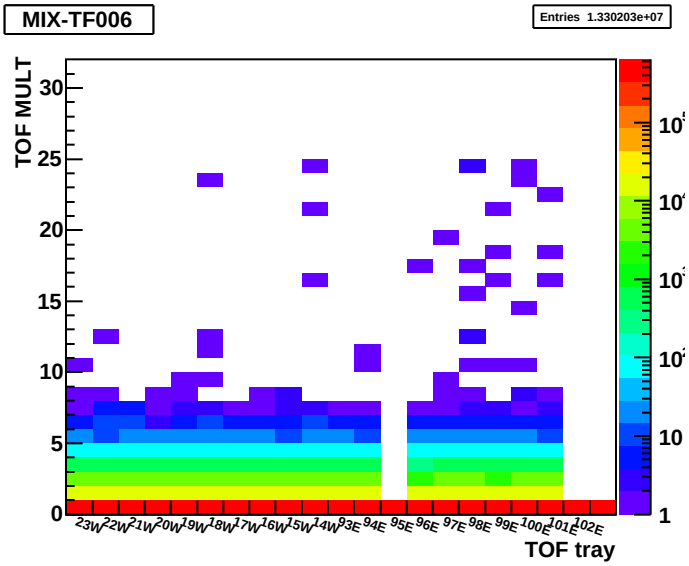

MIX-TF001
Entries 1.330203e+07
TOF MULT
TOF tray

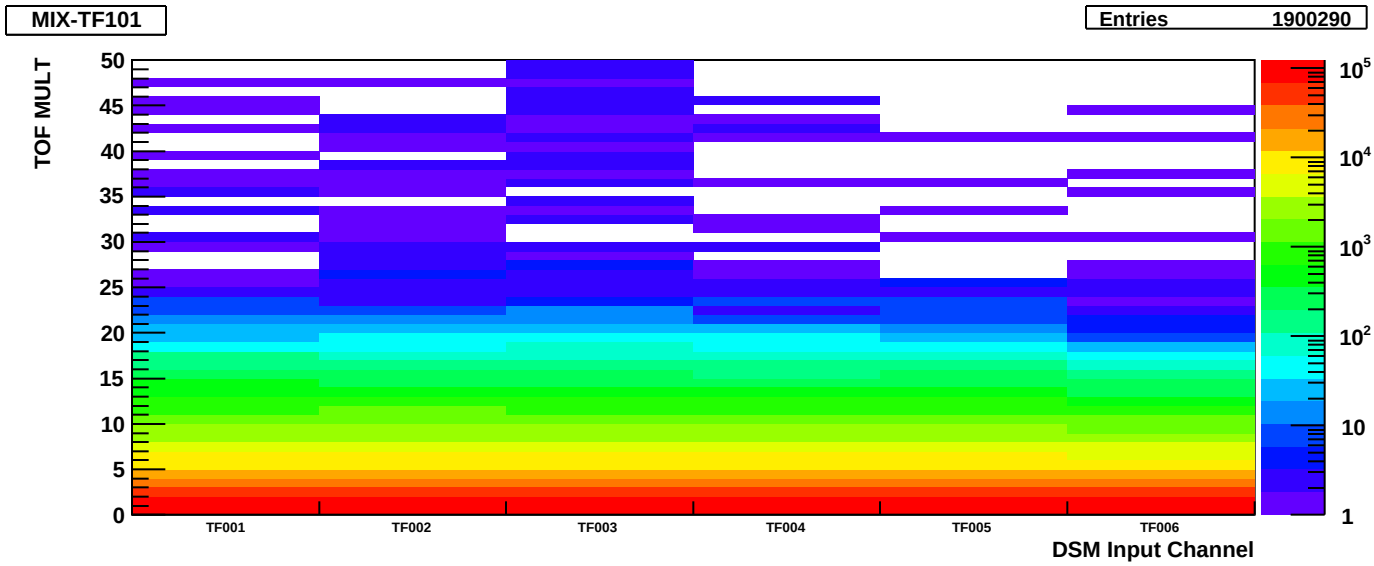

MIX-TF002
Entries 1.330203e+07
TOF MULT
TOF tray

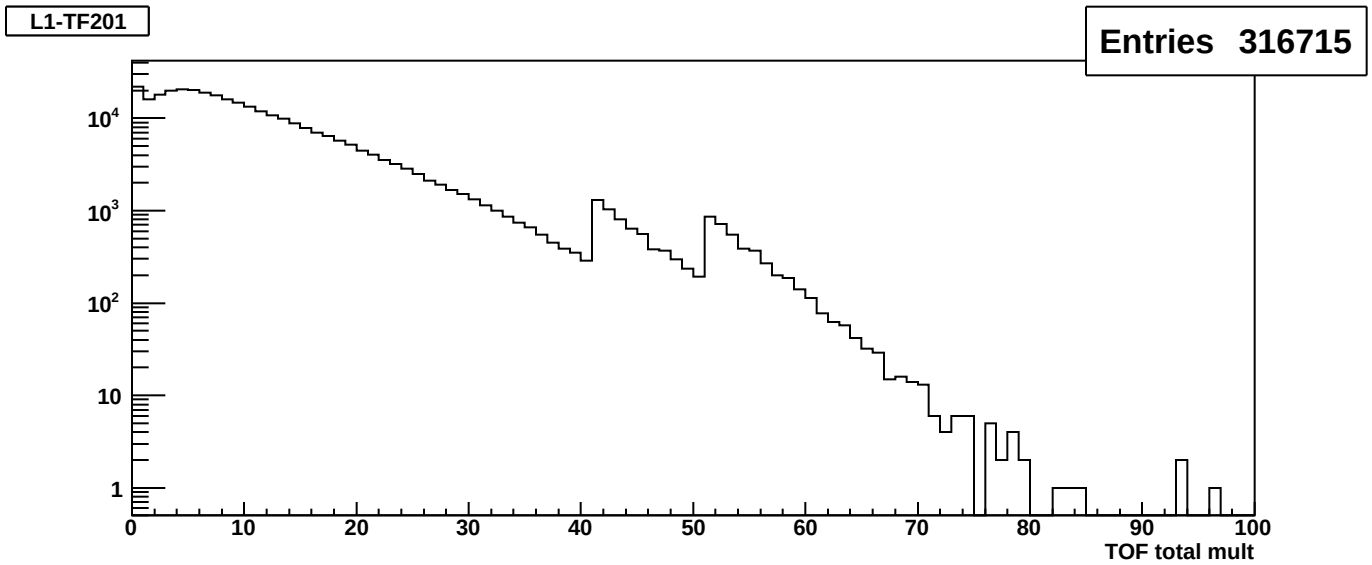

MIX-TF003
Entries 1.330203e+07
TOF MULT
TOF tray

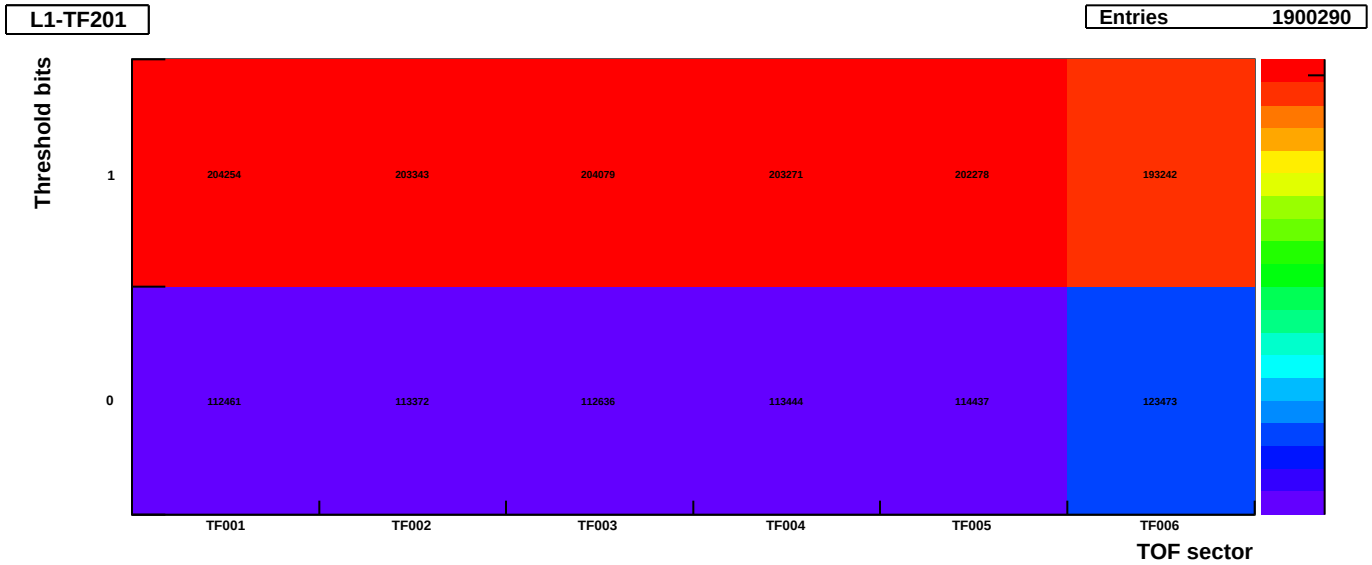

MIX-TF004
Entries 1.330203e+07
TOF MULT
TOF tray

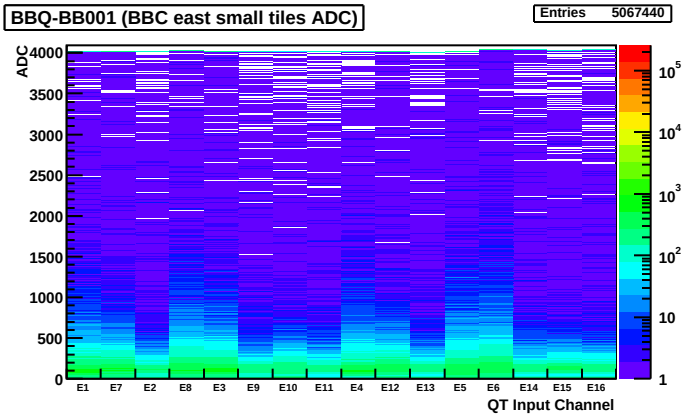

MIX-TF005
Entries 1.330203e+07
TOF MULT
TOF tray

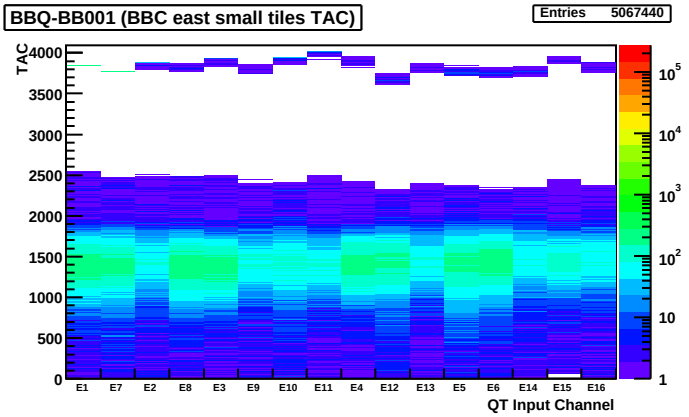

MIX-TF006
Entries 1.330203e+07
TOF MULT
TOF tray

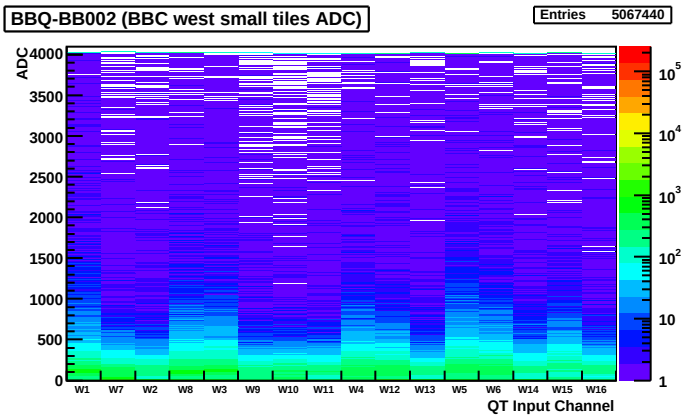

MIX-TF101
Entries 1900290
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

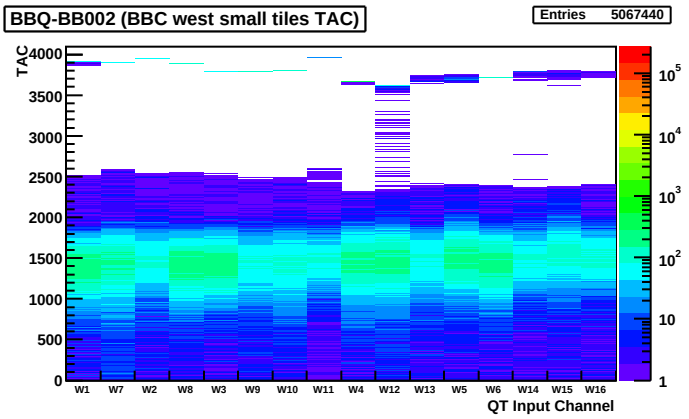

L1-TF201
Entries 316715
TOF total mult

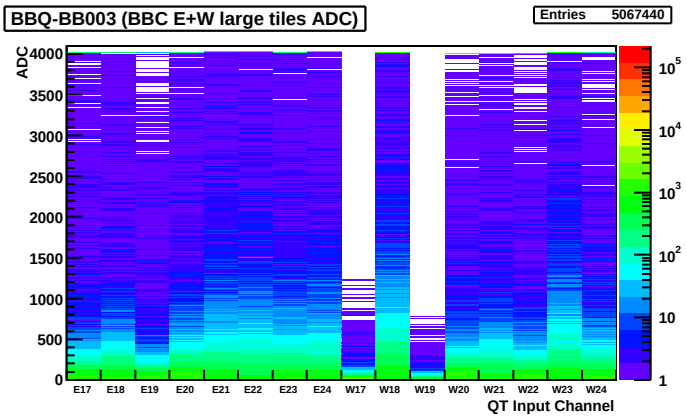

L1-TF201
Entries 1900290
Threshold bits
204254
203343
204079
203271
202278
193242
112461
113372
112636
113444
114437
123473
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

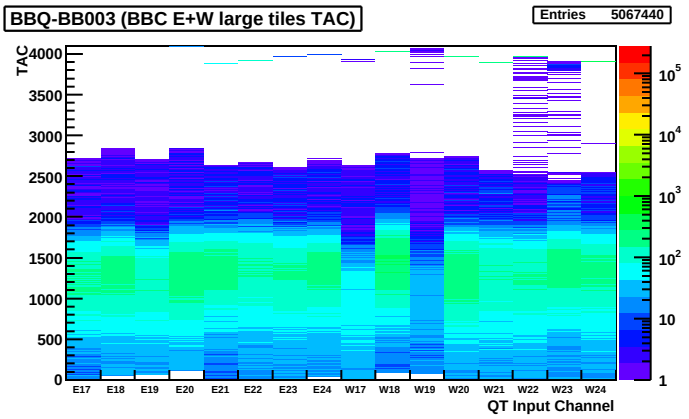

BBQ-BB001 (BBC east small tiles ADC)
Entries 5067440
ADC
QT Input Channel

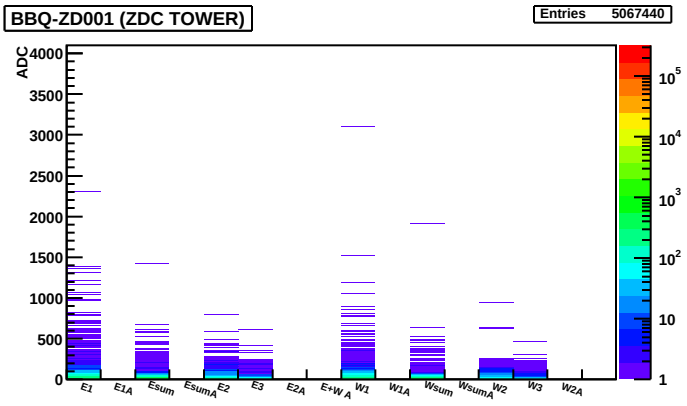

BBQ-BB001 (BBC east small tiles TAC)
Entries 5067440
TAC
QT Input Channel

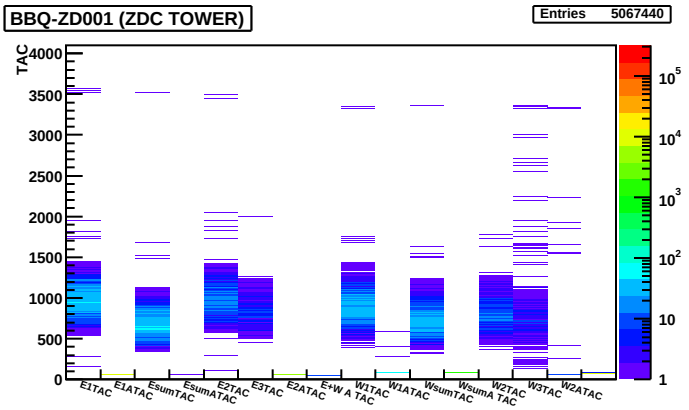

BBQ-BB002 (BBC west small tiles ADC)
Entries 5067440
ADC
QT Input Channel

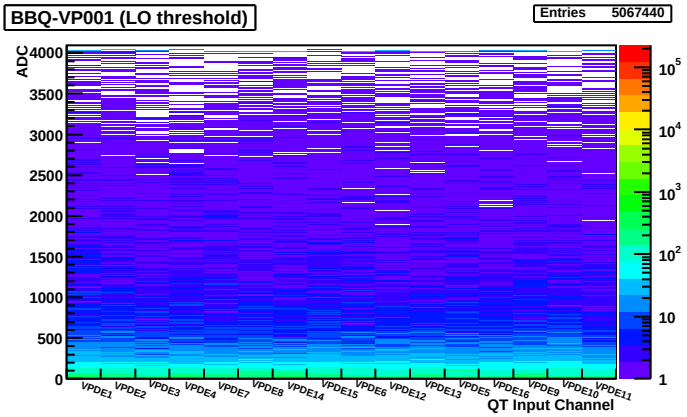

BBQ-BB002 (BBC west small tiles TAC)
Entries 5067440
TAC
QT Input Channel

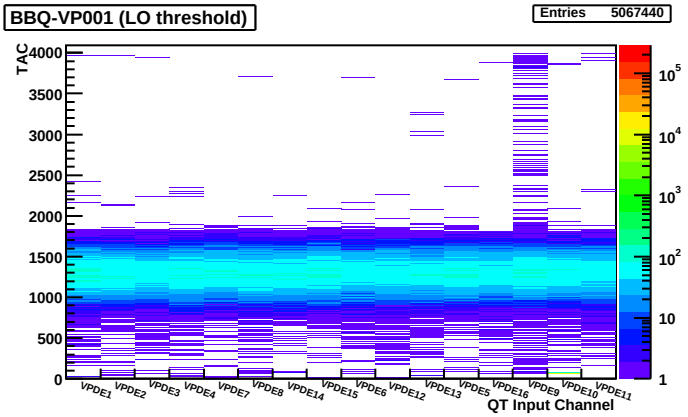

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5067440
ADC
QT Input Channel

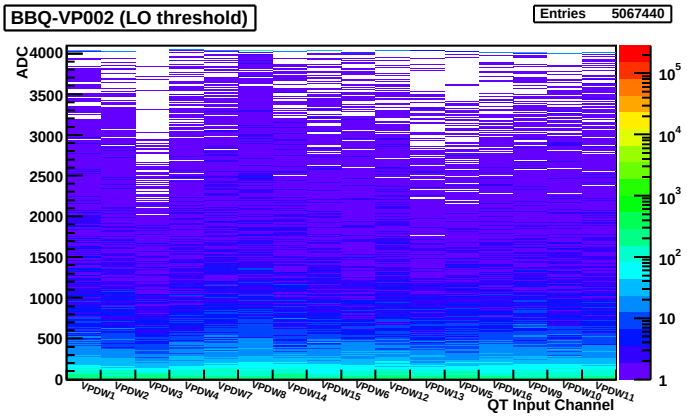

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5067440
TAC
QT Input Channel

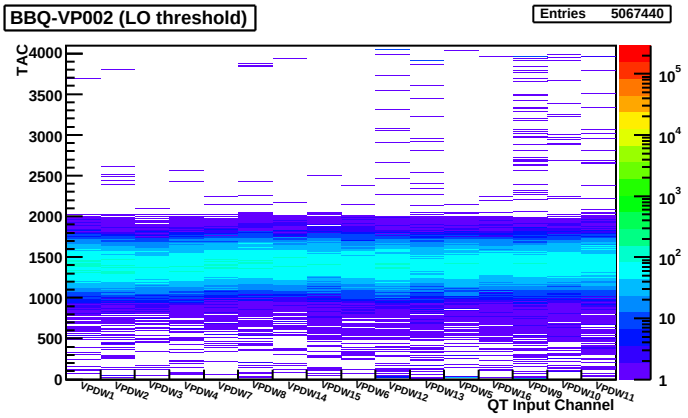

BBQ-ZD001 (ZDC TOWER)
Entries 5067440
ADC

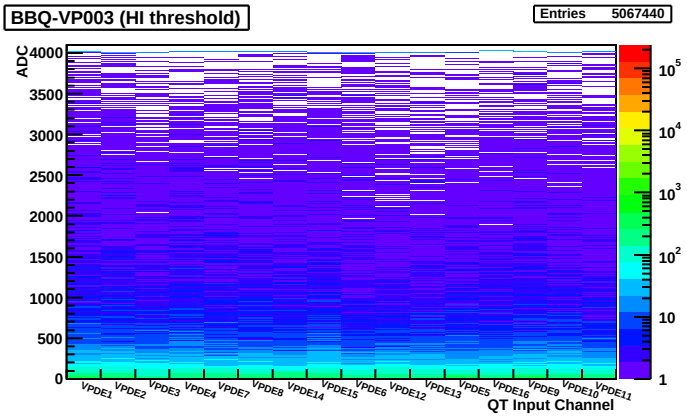

BBQ-ZD001 (ZDC TOWER)
Entries 5067440
TAC

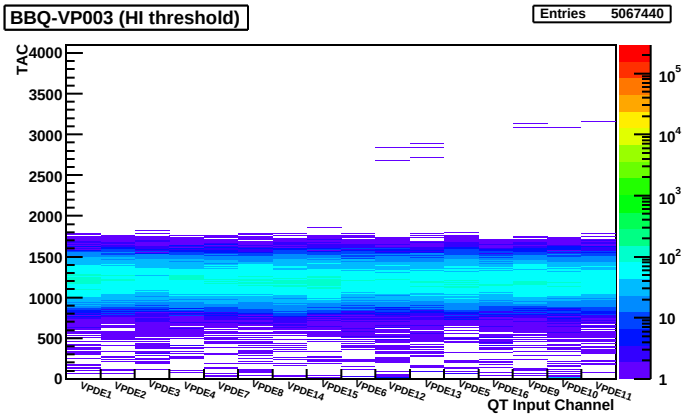

BBQ-VP001 (LO threshold)
Entries 5067440
ADC
QT Input Channel

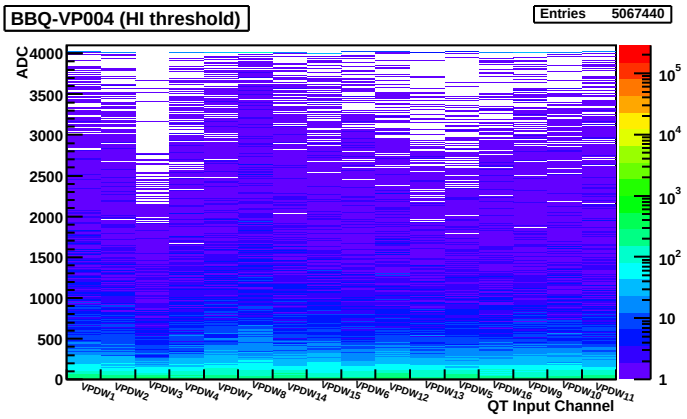

BBQ-VP001 (LO threshold)
Entries 5067440
TAC
QT Input Channel

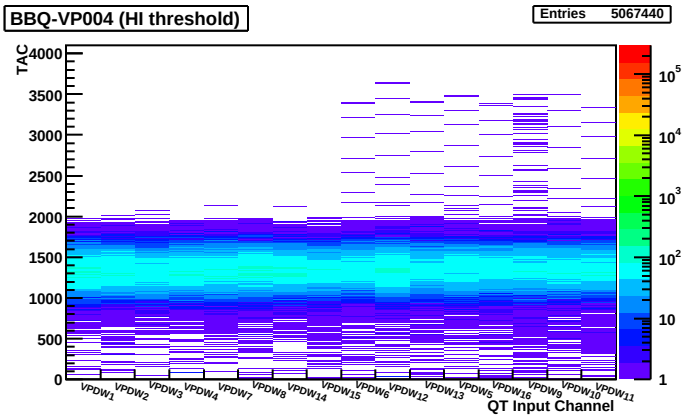

BBQ-VP002 (LO threshold)
Entries 5067440
ADC
QT Input Channel

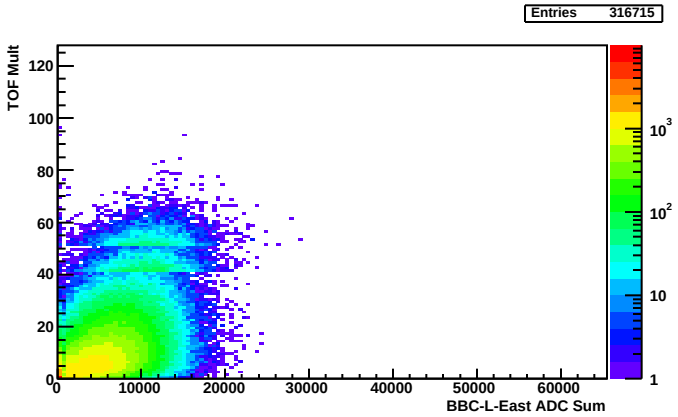

BBQ-VP002 (LO threshold)
Entries 5067440
TAC
QT Input Channel

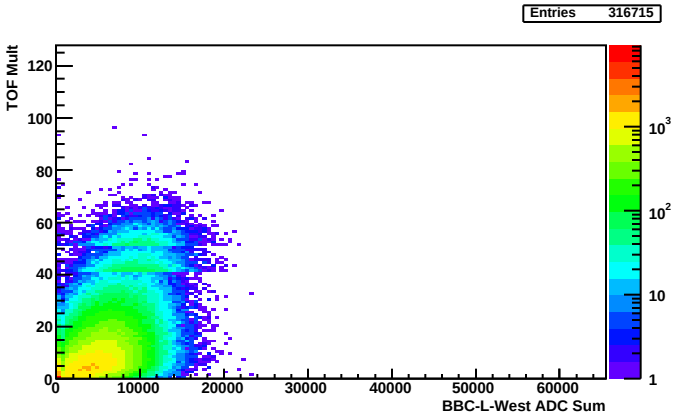

BBQ-VP003 (HI threshold)
Entries 5067440
ADC
QT Input Channel

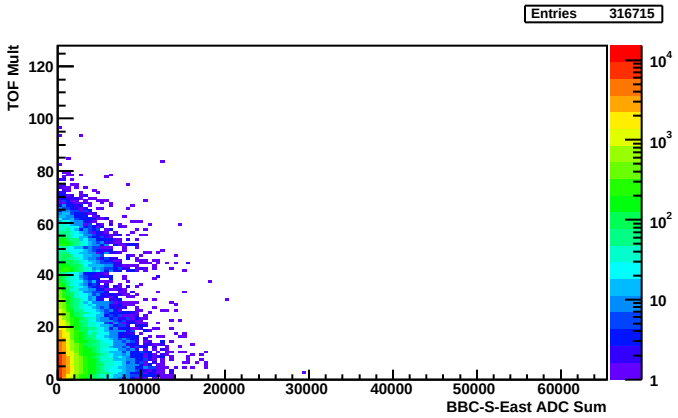

BBQ-VP003 (HI threshold)
Entries 5067440
TAC
QT Input Channel

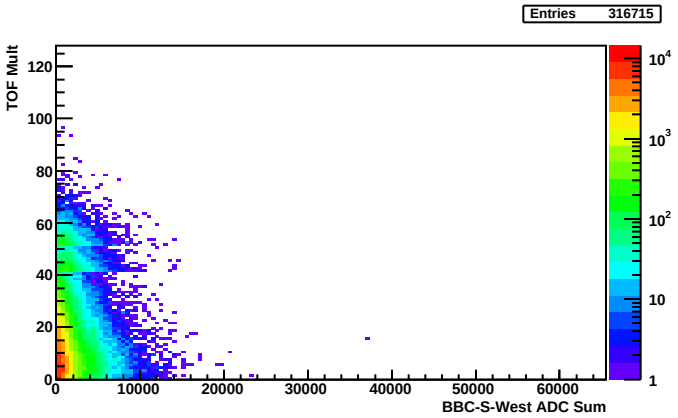

BBQ-VP004 (HI threshold)
Entries 5067440
ADC
QT Input Channel

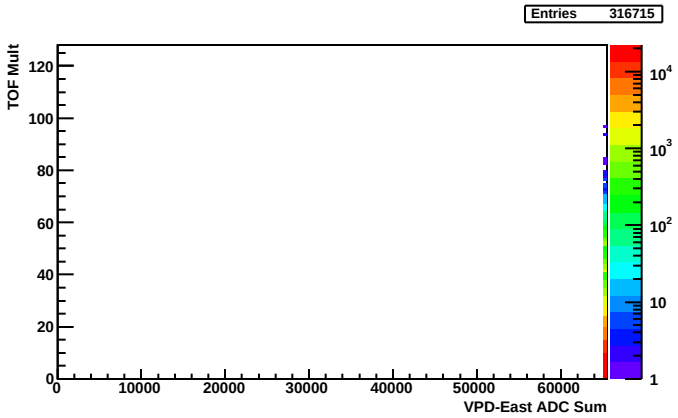

BBQ-VP004 (HI threshold)
Entries 5067440
TAC
QT Input Channel

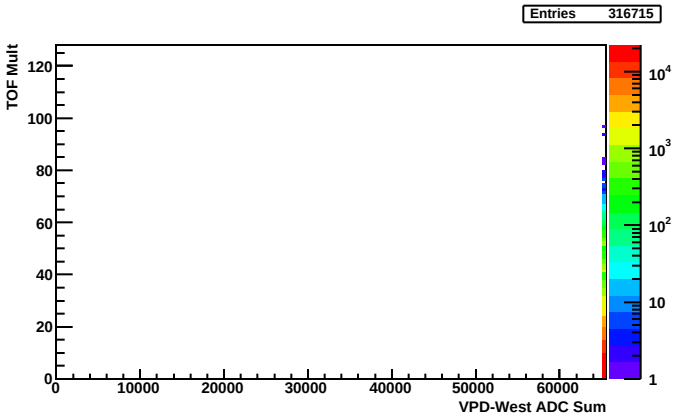

Entries 316715
TOF Mult
BBC-L-East ADC Sum

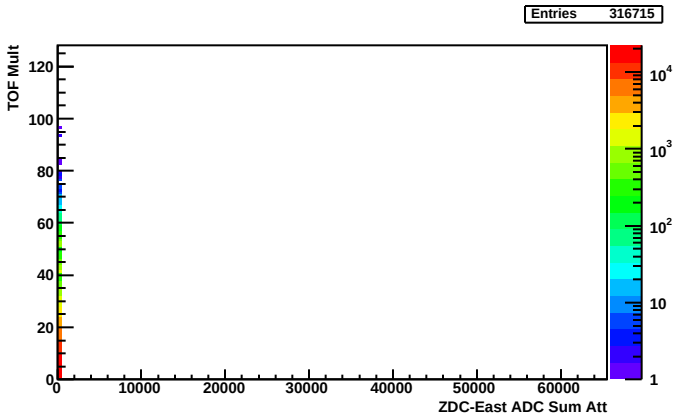

Entries 316715
TOF Mult
BBC-L-West ADC Sum

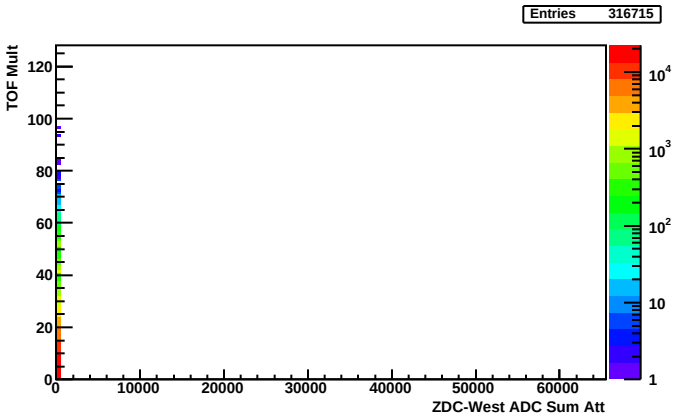

Entries 316715
TOF Mult
BBC-S-East ADC Sum

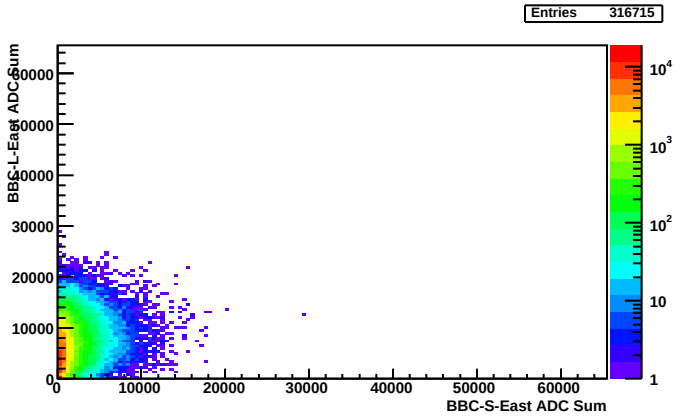

Entries 316715
TOF Mult
BBC-S-West ADC Sum

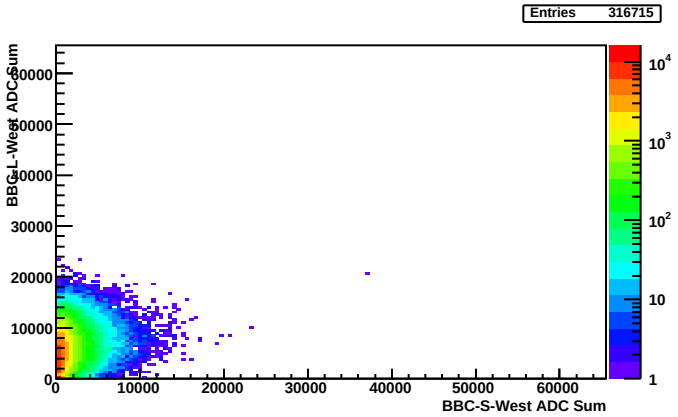

Entries 316715
TOF Mult
VPD-East ADC Sum

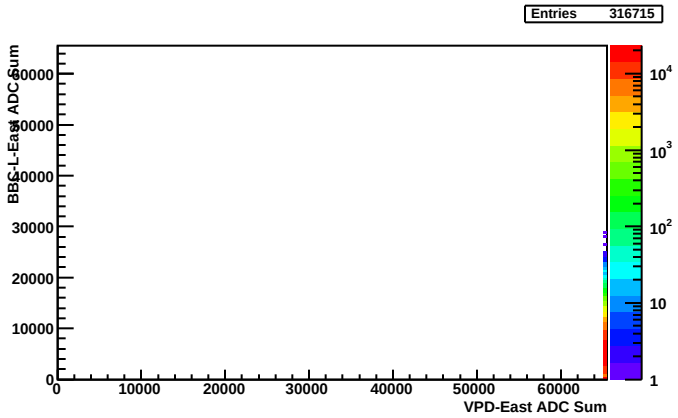

Entries 316715
TOF Mult
VPD-West ADC Sum

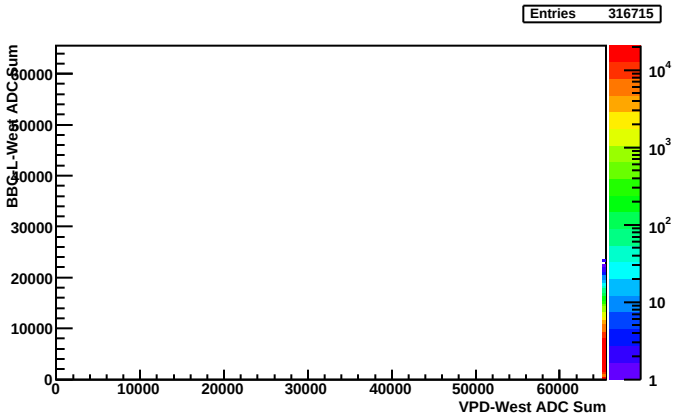

Entries 316715
TOF Mult
ZDC-East ADC Sum Att

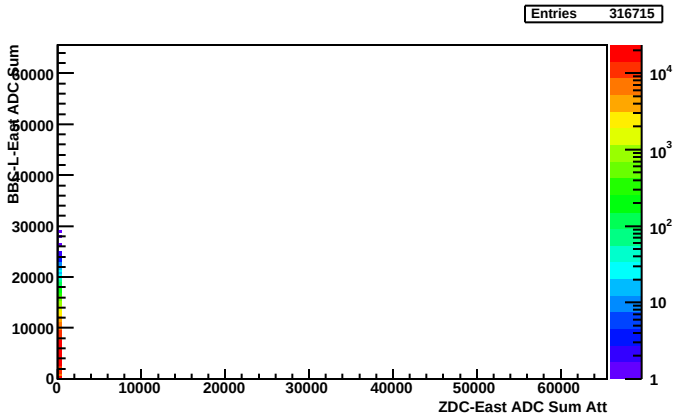

Entries 316715
TOF Mult
ZDC-West ADC Sum Att

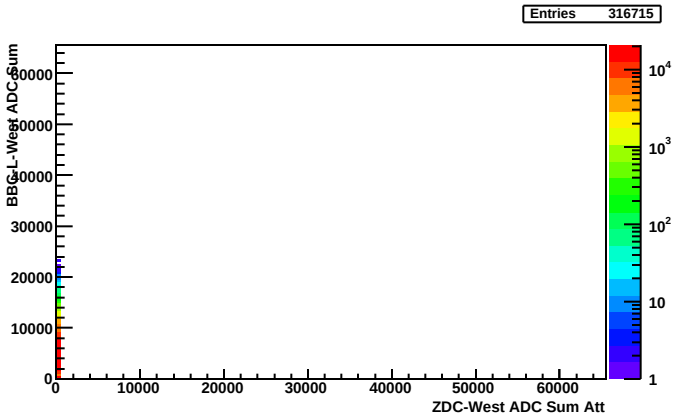

Entries 316715
BBC-L-East ADC Sum
BBC-S-East ADC Sum

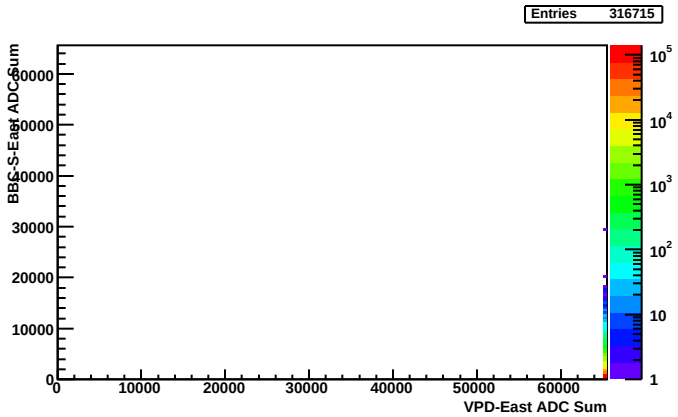

Entries 316715
BBC-L-West ADC Sum
BBC-S-West ADC Sum

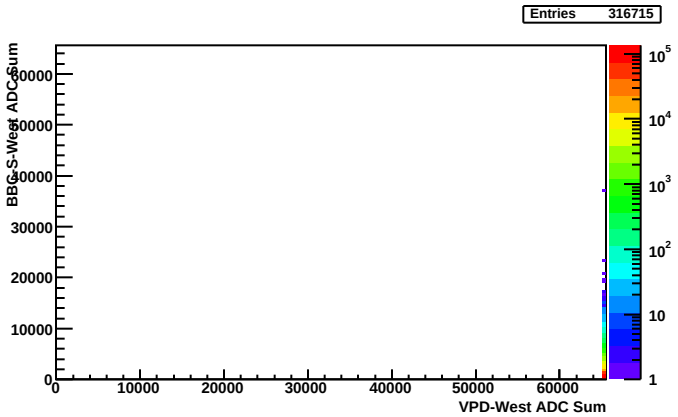

Entries 316715
BBC-L-East ADC Sum
VPD-East ADC Sum

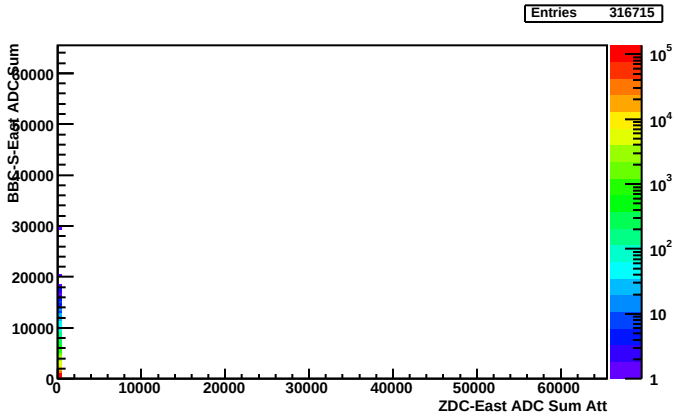

Entries 316715
BBC-L-West ADC Sum
VPD-West ADC Sum

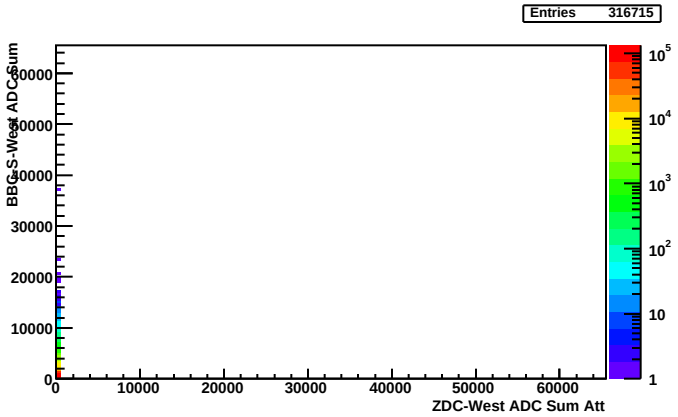

Entries 316715
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

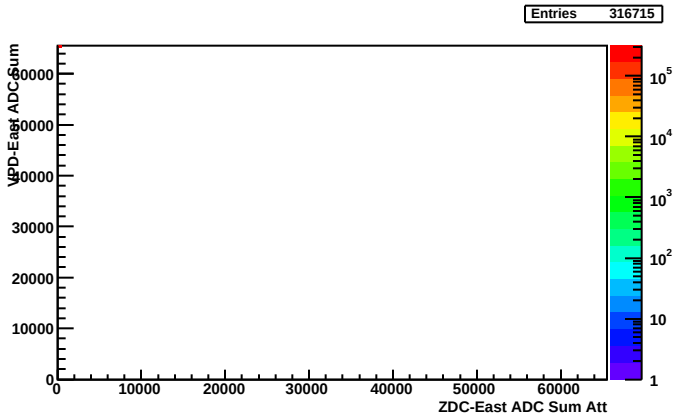

Entries 316715
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

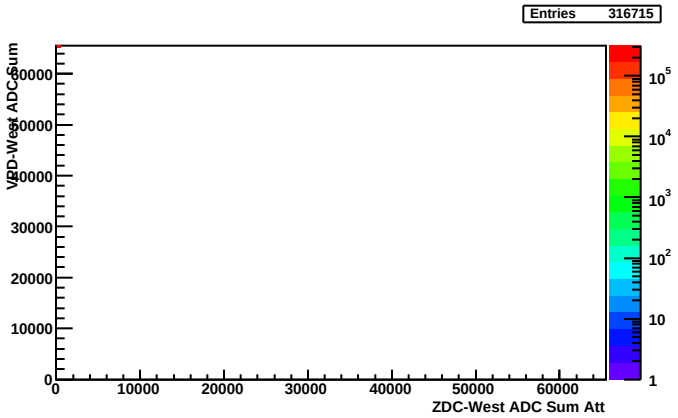

Entries 316715
BBC-S-East ADC Sum
VPD-East ADC Sum

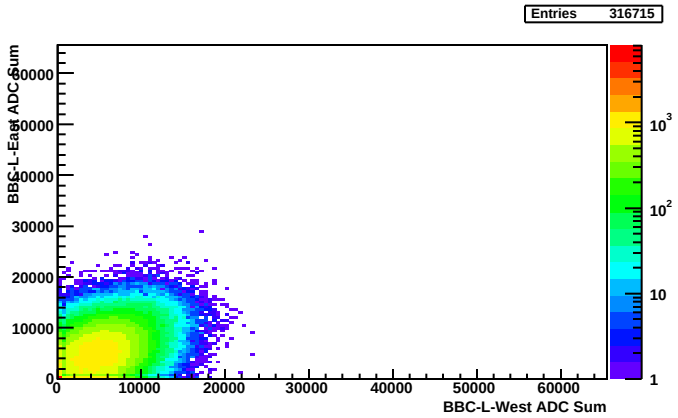

Entries 316715
BBC-S-West ADC Sum
VPD-West ADC Sum

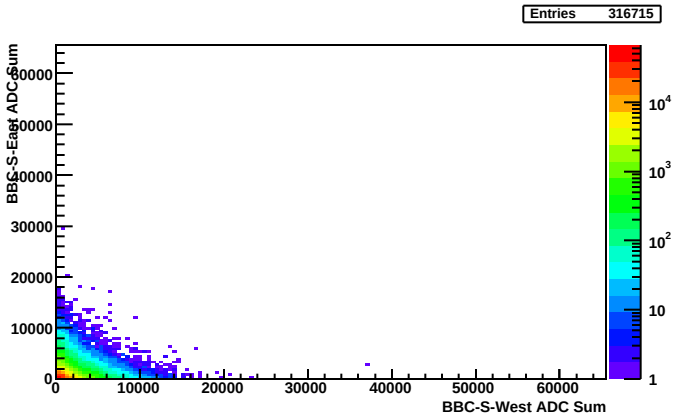

Entries 316715
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

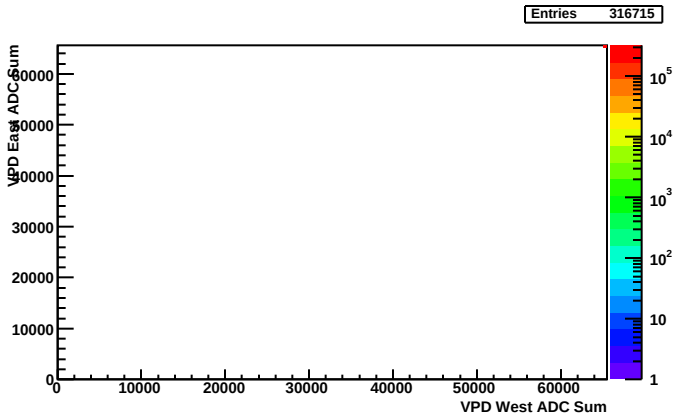

Entries 316715
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

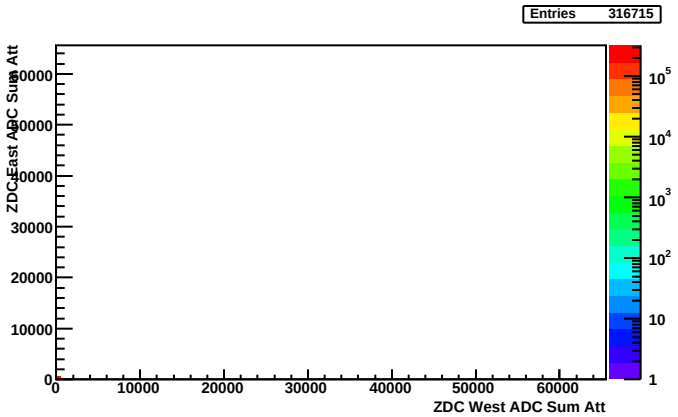

Entries 316715
VPD-East ADC Sum
ZDC-East ADC Sum Att

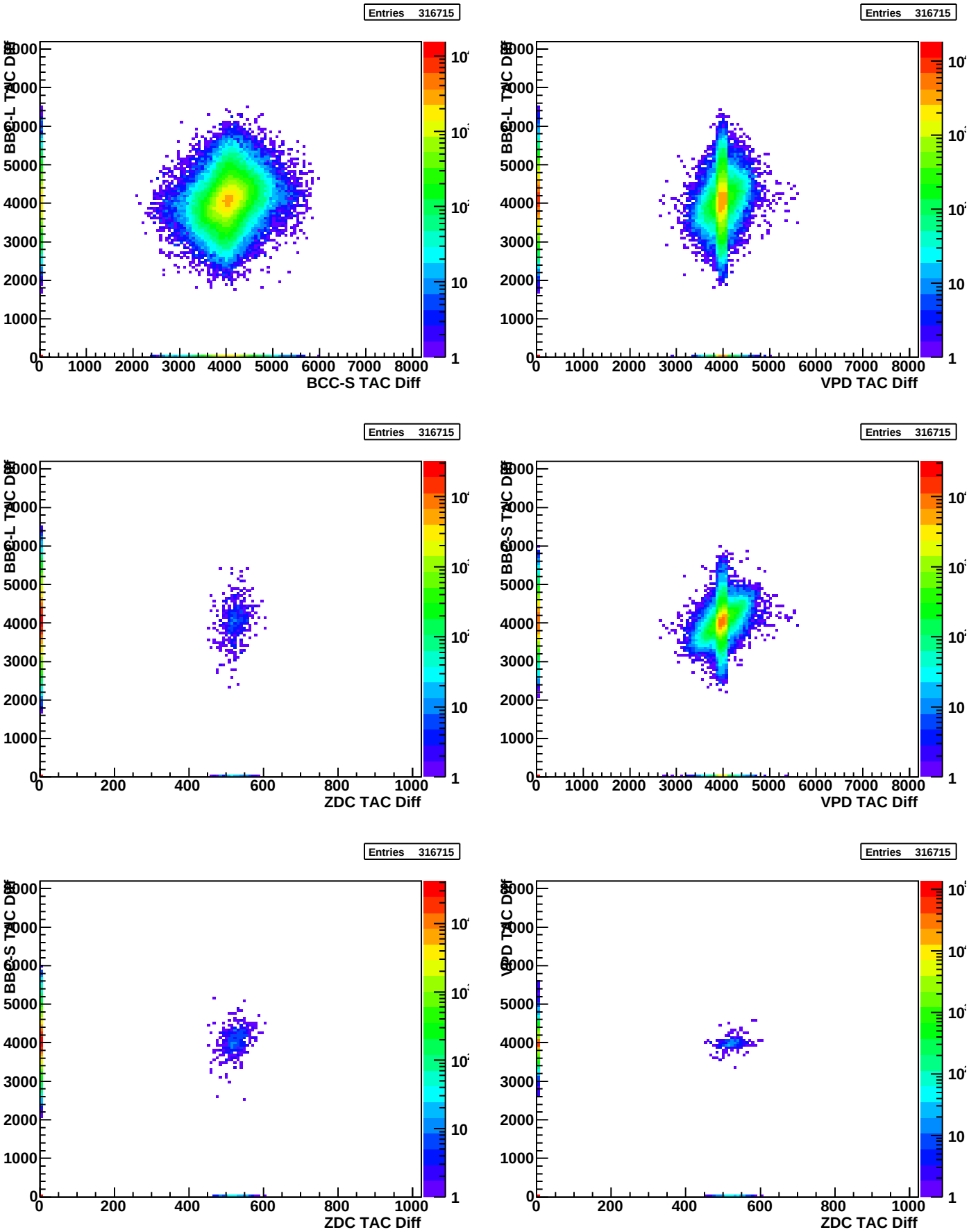

Entries 316715
VPD-West ADC Sum
ZDC-West ADC Sum Att

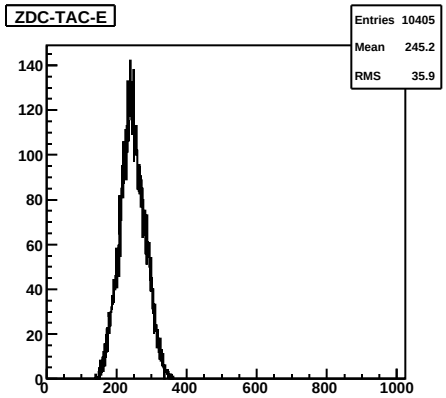

Entries 316715
BBC-L-East ADC Sum
BBC-L-West ADC Sum

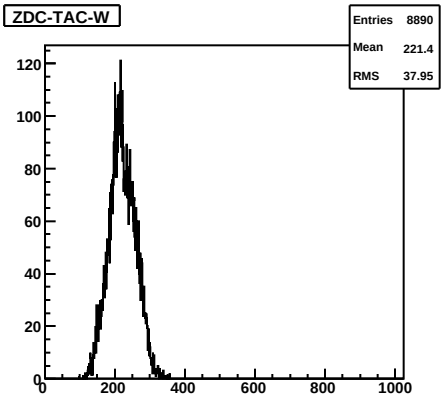

Entries 316715
BBC-S-East ADC Sum
BBC-S-West ADC Sum

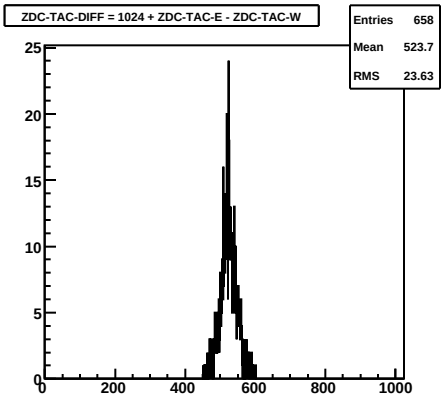

Entries 316715
VPD East ADC Sum
VPD West ADC Sum

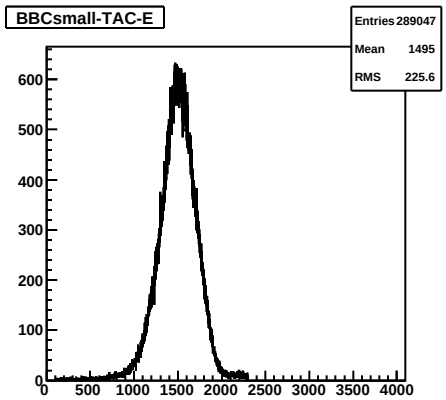

Entries 316715
ZDC East ADC Sum Att
ZDC West ADC Sum Att

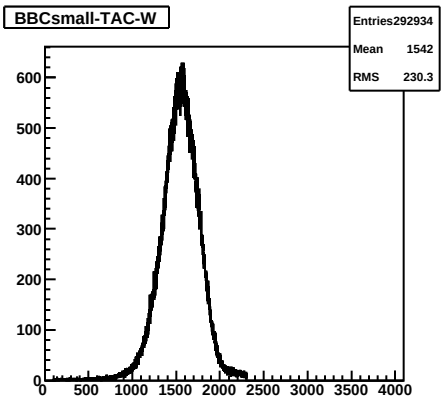

Entries 316715
BBC-L TAC Diff
BCC-S TAC Diff
Entries 316715
BBC-L TAC Diff
VPD TAC Diff
Entries 316715
BBC-L TAC Diff
ZDC TAC Diff
Entries 316715
BBC-S TAC Diff
VPD TAC Diff
Entries 316715
BBC-S TAC Diff
ZDC TAC Diff
Entries 316715
VPD TAC Diff
ZDC TAC Diff

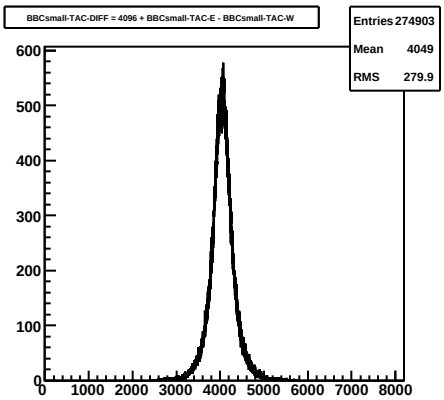

ZDC-TAC-E
Entries 10405
Mean 245.2
RMS 35.9

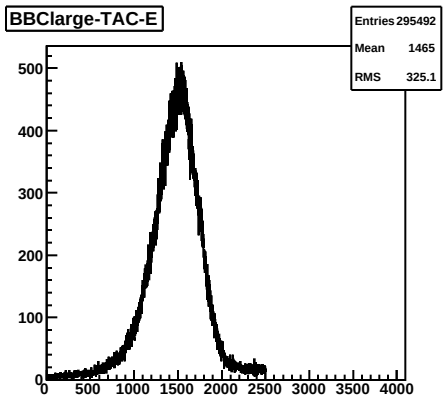

ZDC-TAC-W
Entries 8890
Mean 221.4
RMS 37.95

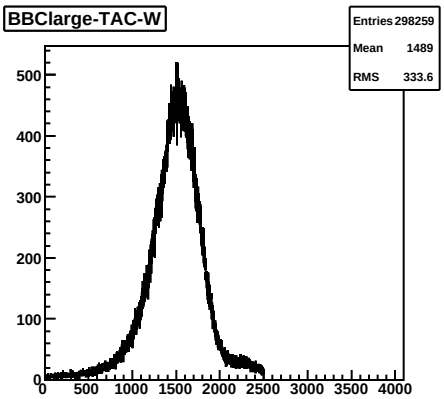

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 658
Mean 523.7
RMS 23.63

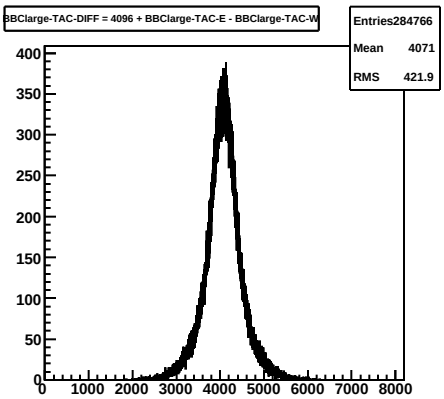

BBCsmall-TAC-E
Entries 289047
Mean 1495
RMS 225.6

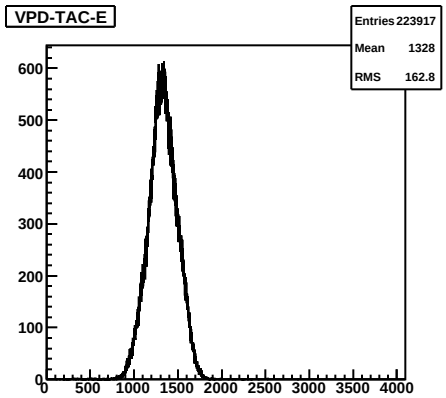

BBCsmall-TAC-W
Entries 292934
Mean 1542
RMS 230.3

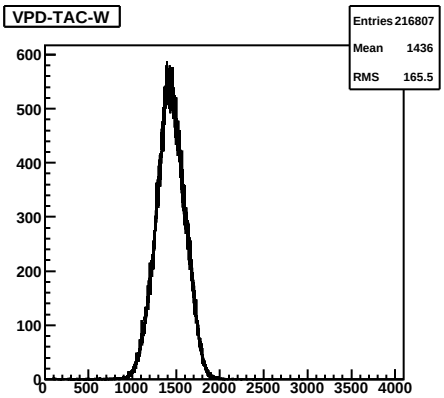

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 274903
Mean 4049
RMS 279.9

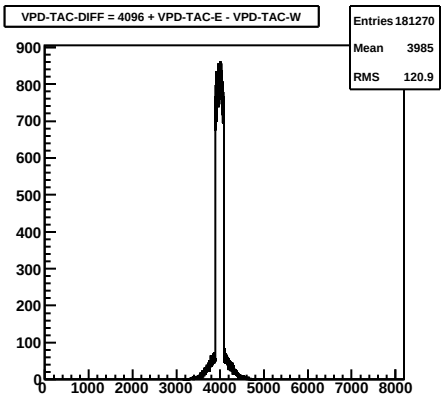

BBClarge-TAC-E
Entries 295492
Mean 1465
RMS 325.1

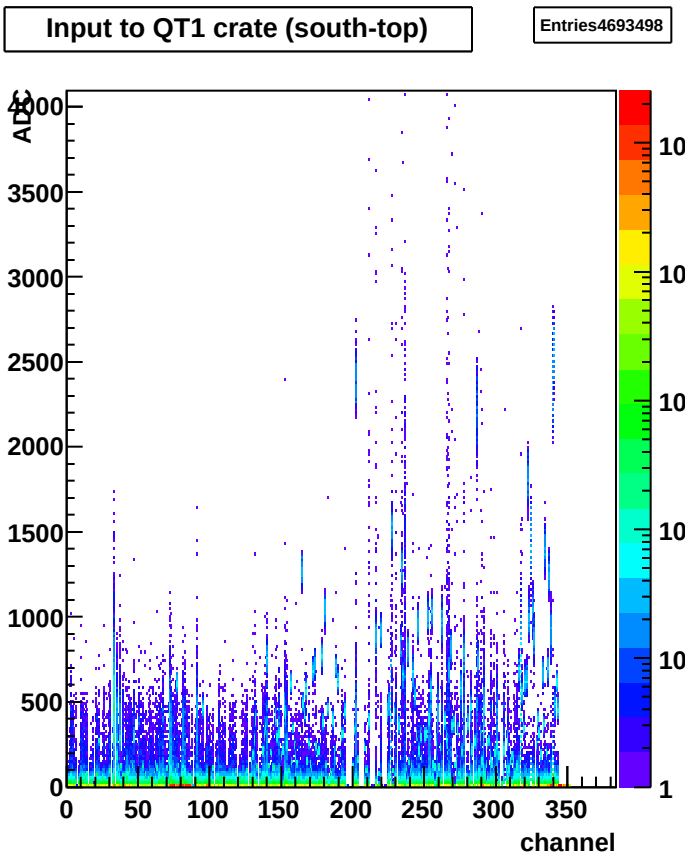

BBClarge-TAC-W
Entries 298259
Mean 1489
RMS 333.6

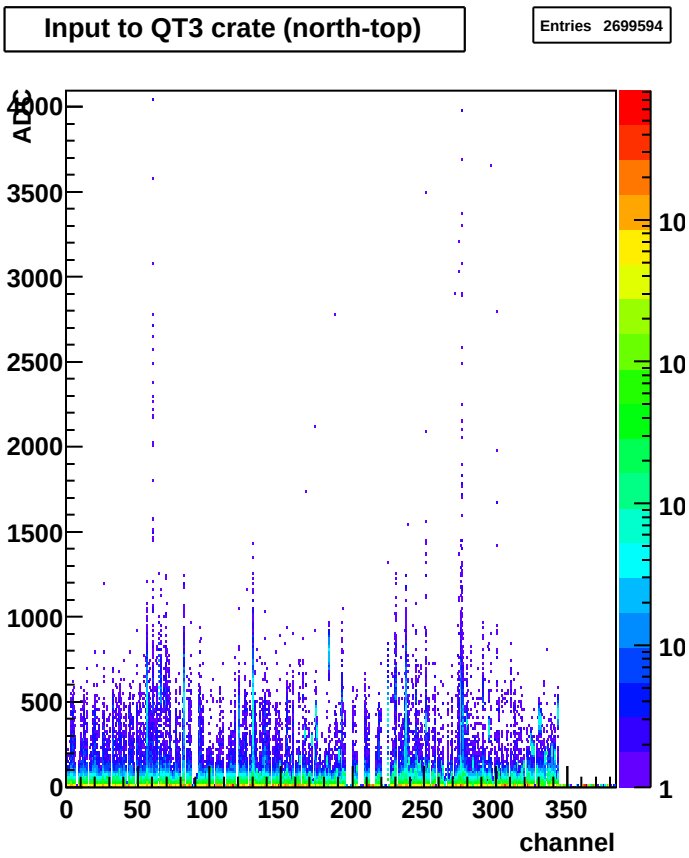

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 284766
Mean 4071
RMS 421.9

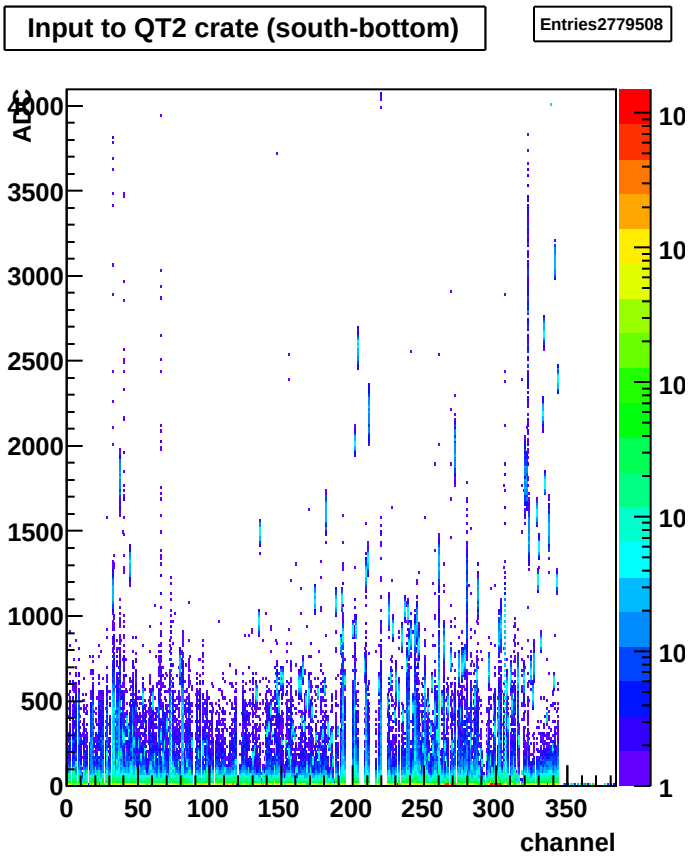

VPD-TAC-E
Entries 223917
Mean 1328
RMS 162.8

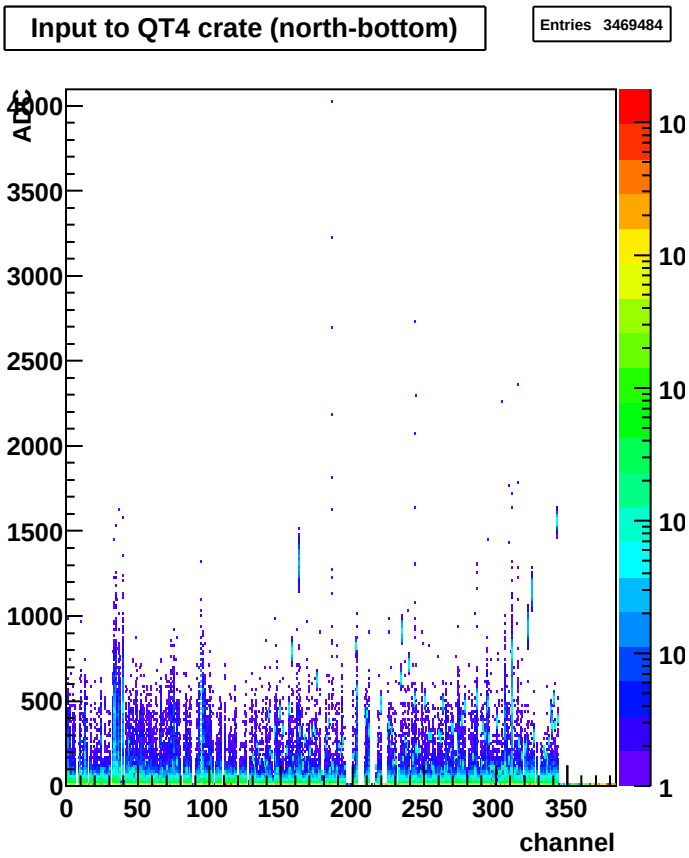

VPD-TAC-W
Entries 216807
Mean 1436
RMS 165.5

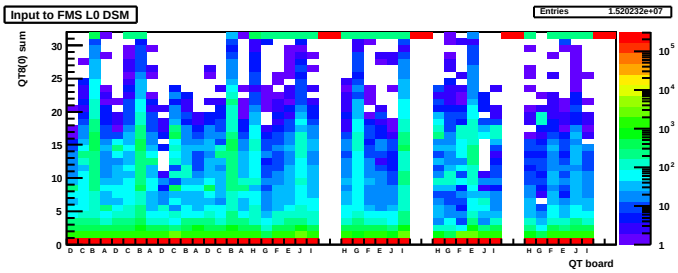

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 181270
Mean 3985
RMS 120.9

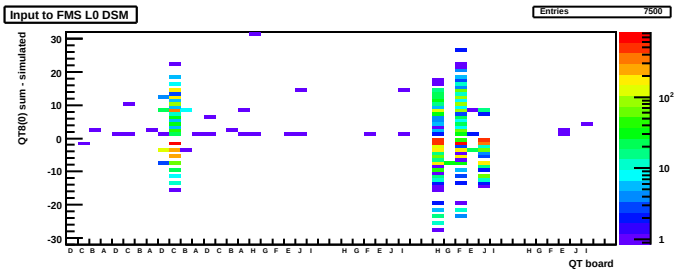

Input to QT1 crate (south-top)
Entries4693498
ADC
channel

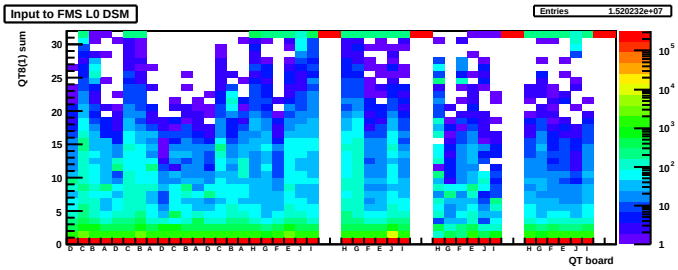

Input to QT3 crate (north-top)
Entries 2699594
ADC
channel

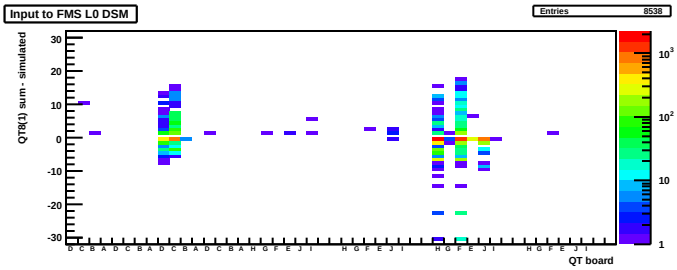

Input to QT2 crate (south-bottom)
Entries2779508
ADC
channel

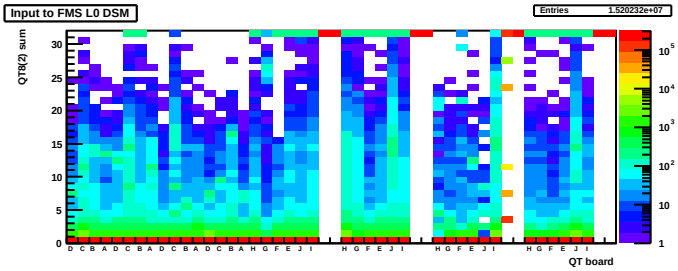

Input to QT4 crate (north-bottom)
Entries 3469484
ADC
channel

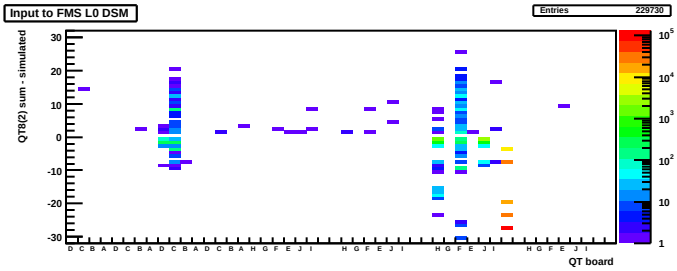

Input to FMS L0 DSM
Entries 1.520232e+07
QT8(0) sum
QT board

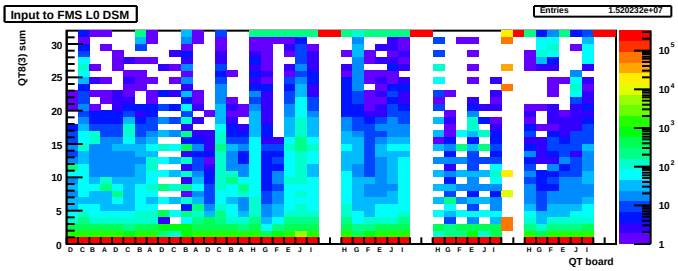

Input to FMS L0 DSM
Entries 7500
QT8(0) sum - simulated
QT board

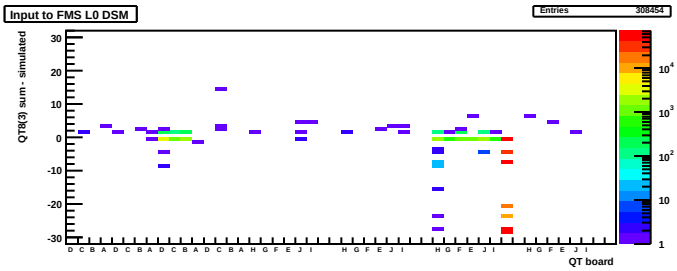

Input to FMS L0 DSM
Entries 1.520232e+07
QT8(1) sum
QT board

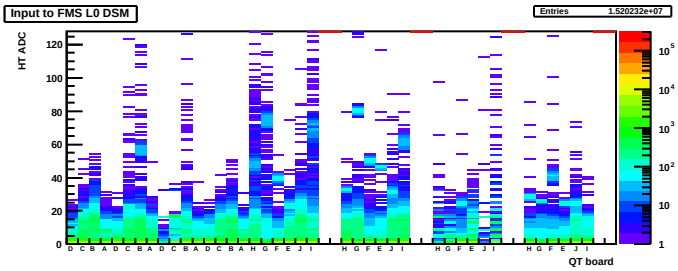

Input to FMS L0 DSM
Entries 8538
QT8(1) sum - simulated
QT board

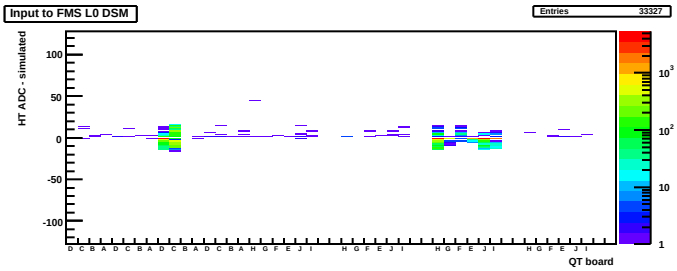

Input to FMS L0 DSM
Entries 1.520232e+07
QT8(2) sum
QT board

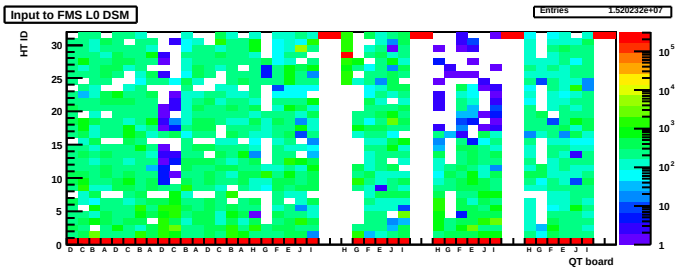

Input to FMS L0 DSM
Entries 229730
QT8(2) sum - simulated
QT board

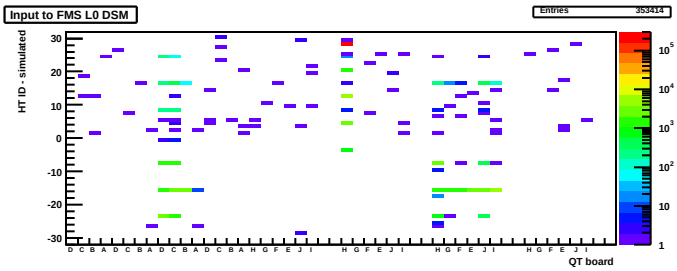

Input to FMS L0 DSM
Entries 1.520232e+07
QT8(3) sum
QT board

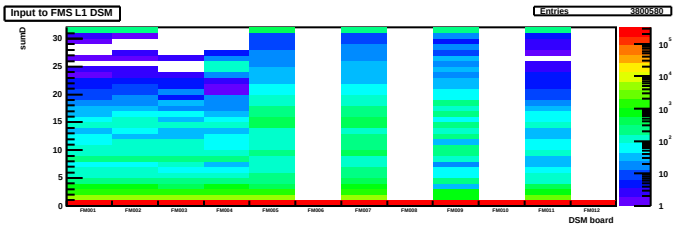

Input to FMS L0 DSM
Entries 308454
QT8(3) sum - simulated
QT board

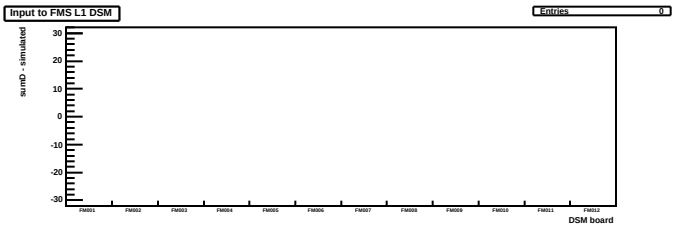

Input to FMS L0 DSM
Entries 1.520232e+07
HT ADC
QT board

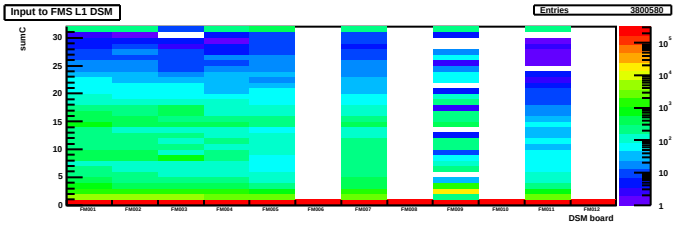

Input to FMS L0 DSM
Entries 33327
HT ADC - simulated
QT board

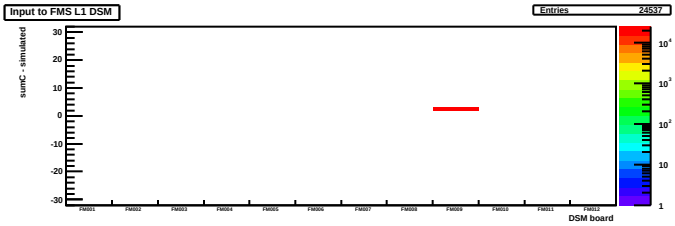

Input to FMS L0 DSM
Entries 1.520232e+07
HT ID
QT board

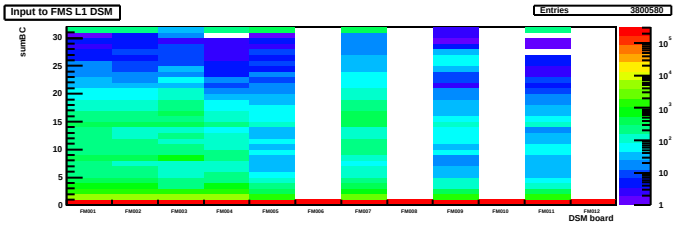

Input to FMS L0 DSM
Entries 353414
HT ID - simulated
QT board

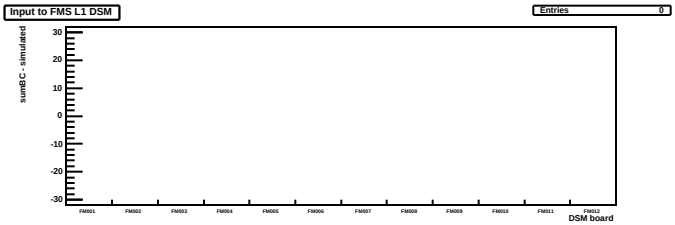

Input to FMS L1 DSM
Entries 3800580
sumD
DSM board

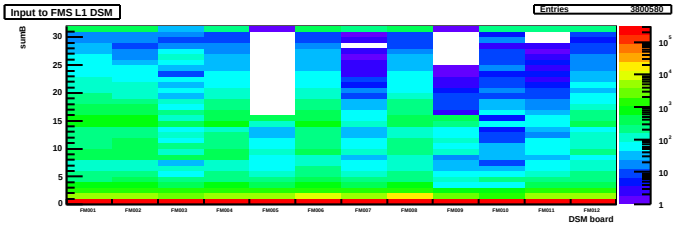

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

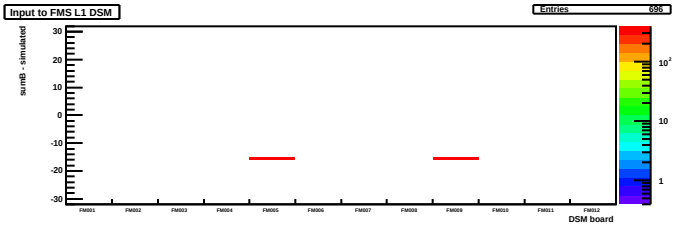

Input to FMS L1 DSM
Entries 3800580
sumC
DSM board

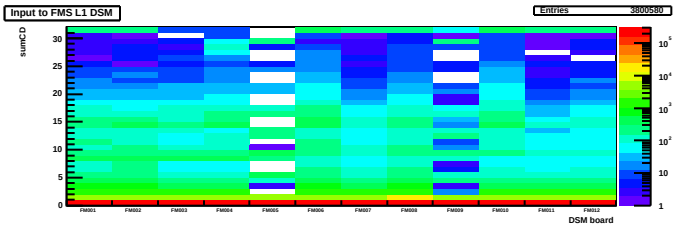

Input to FMS L1 DSM
Entries 24537
sumC - simulated
DSM board

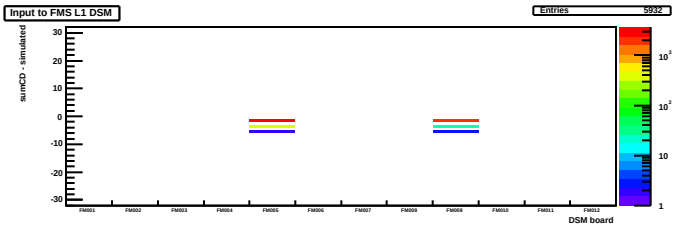

Input to FMS L1 DSM
Entries 3800580
sumBC
DSM board

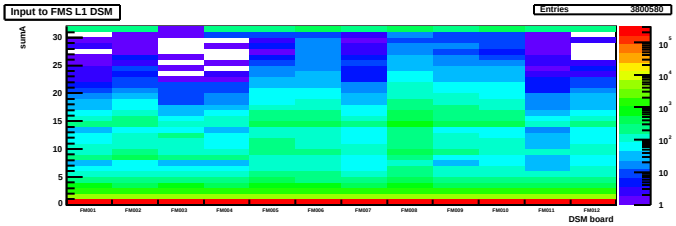

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

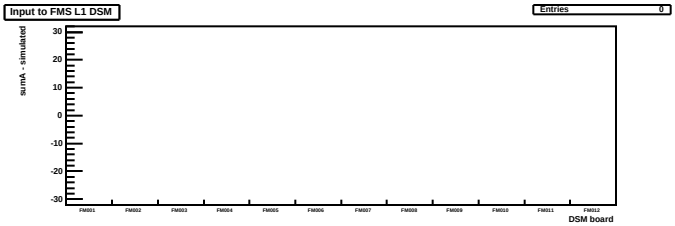

Input to FMS L1 DSM
Entries 3800580
sumB
DSM board

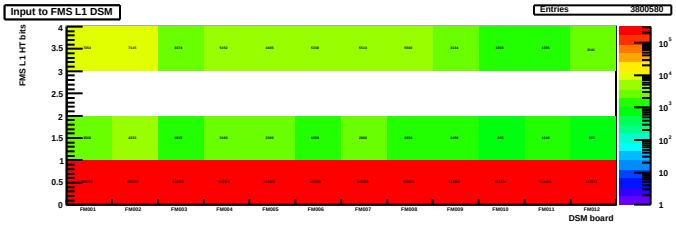

Input to FMS L1 DSM
Entries 696
sumB - simulated
DSM board

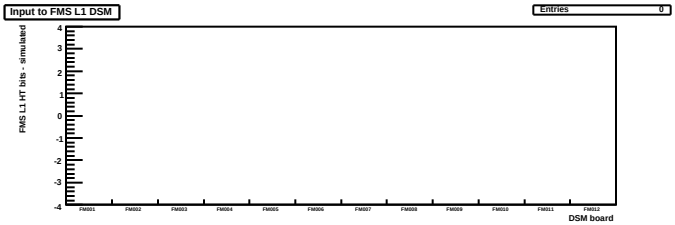

Input to FMS L1 DSM
Entries 3800580
sumCD
DSM board

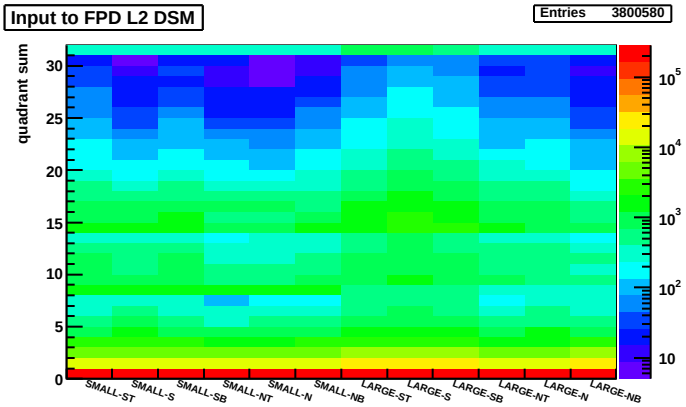

Input to FMS L1 DSM
Entries 5932
sumCD - simulated
DSM board

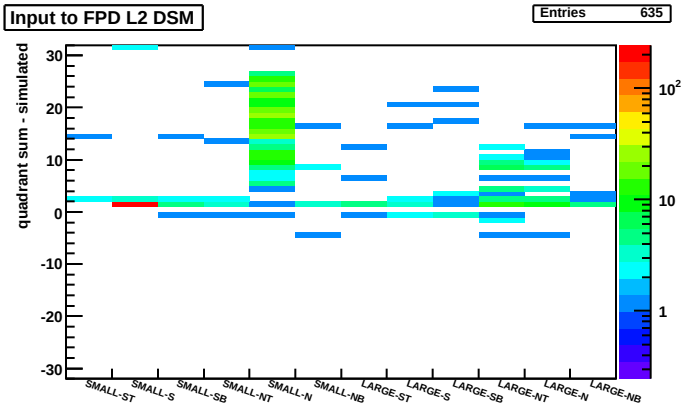

Input to FMS L1 DSM
Entries 3800580
sumA
DSM board

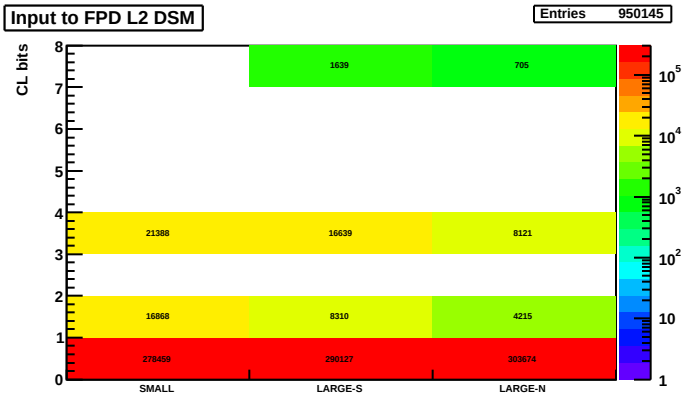

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

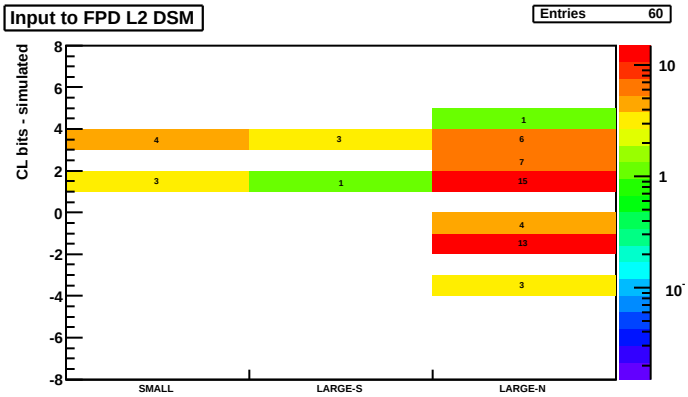

Input to FMS L1 DSM
Entries 3800580
FMS L1 HT bits
DSM board

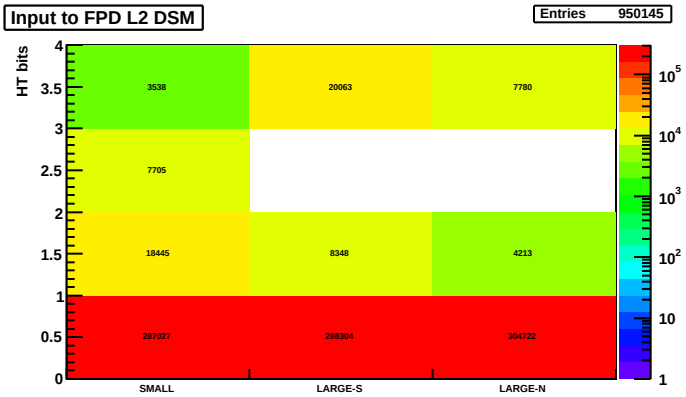

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

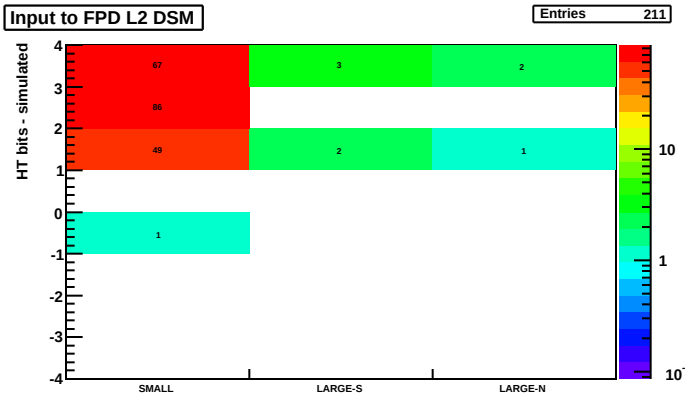

Input to FPD L2 DSM
Entries 3800580
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

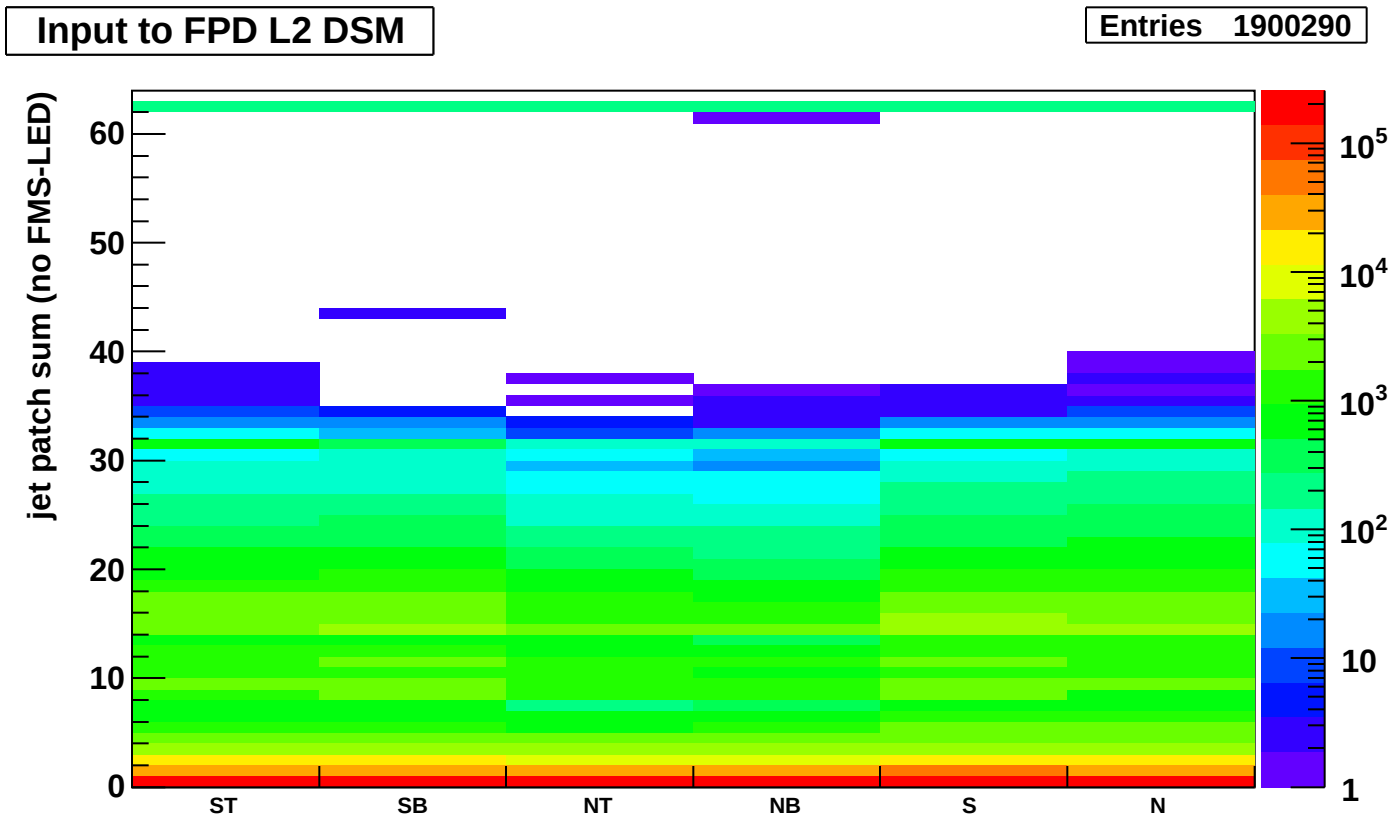

Input to FPD L2 DSM
Entries 635
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

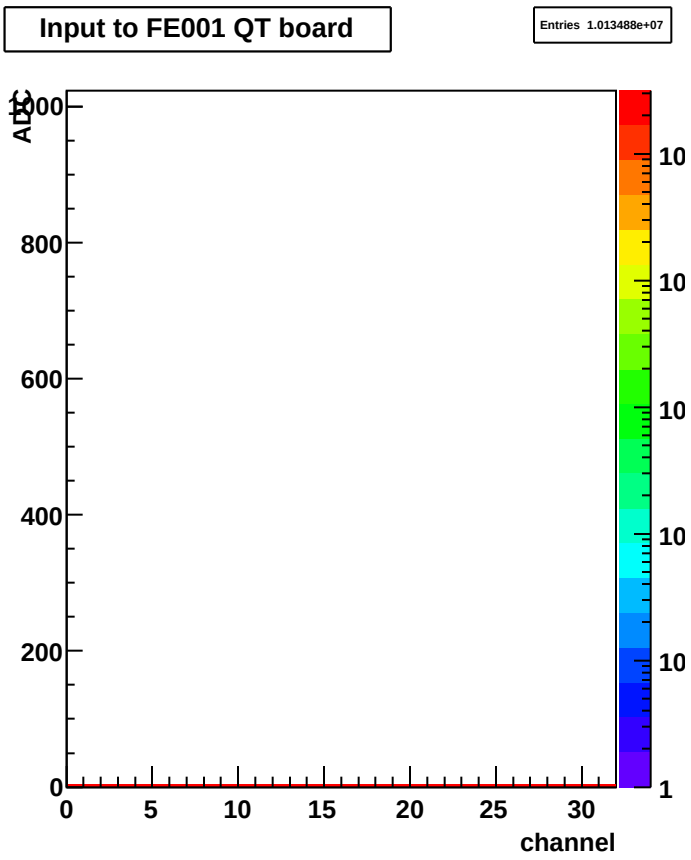

Input to FPD L2 DSM
Entries 950145
CL bits
1639
705
21388
16639
8121
16868
8310
4215
278459
290127
303674
SMALL
LARGE-S
LARGE-N

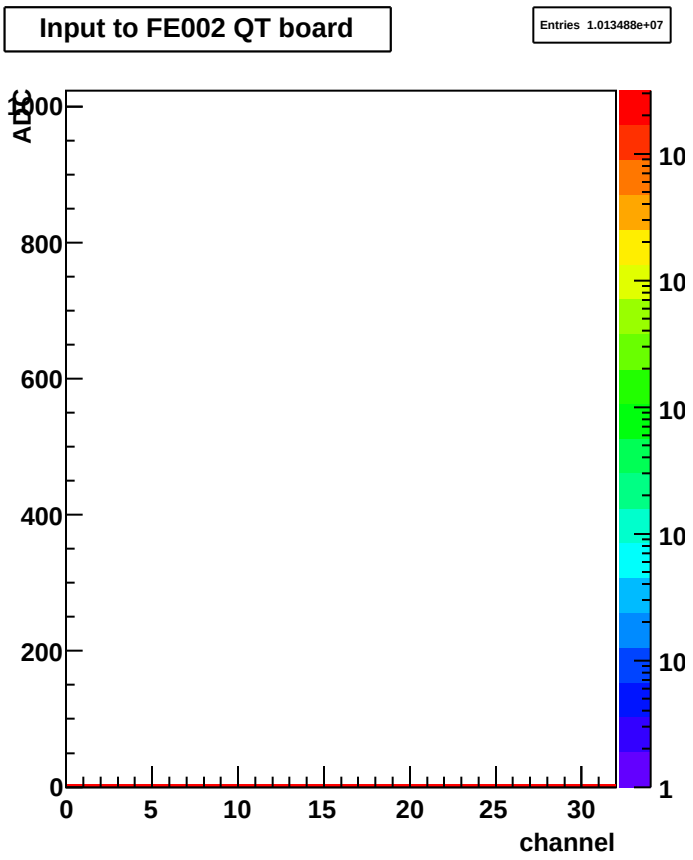

Input to FPD L2 DSM
Entries 60
CL bits - simulated
SMALL
LARGE-S
LARGE-N

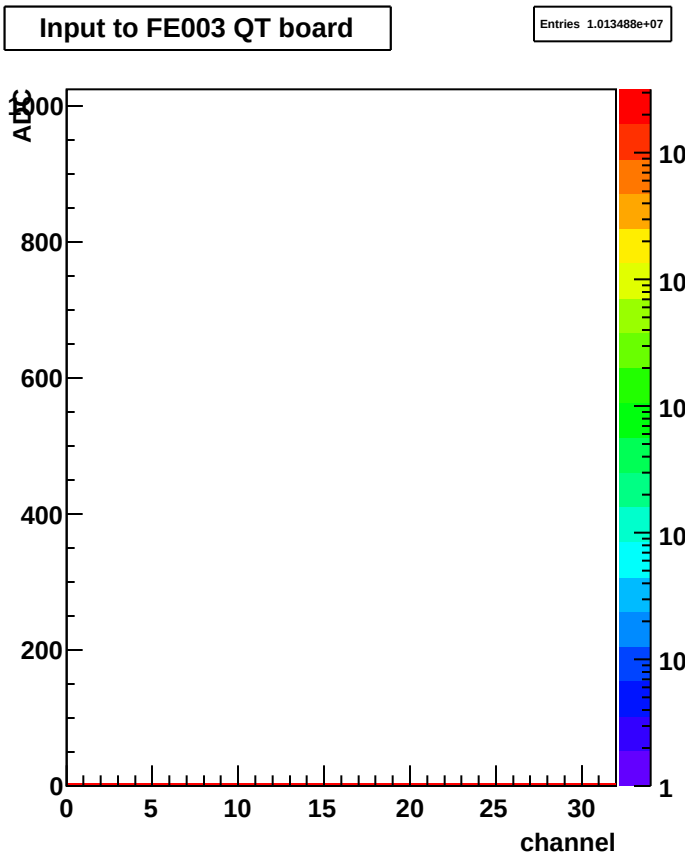

Input to FPD L2 DSM
Entries 950145
HT bits
3538
20063
7780
7705
18445
8348
4213
287027
288304
304722
SMALL
LARGE-S
LARGE-N

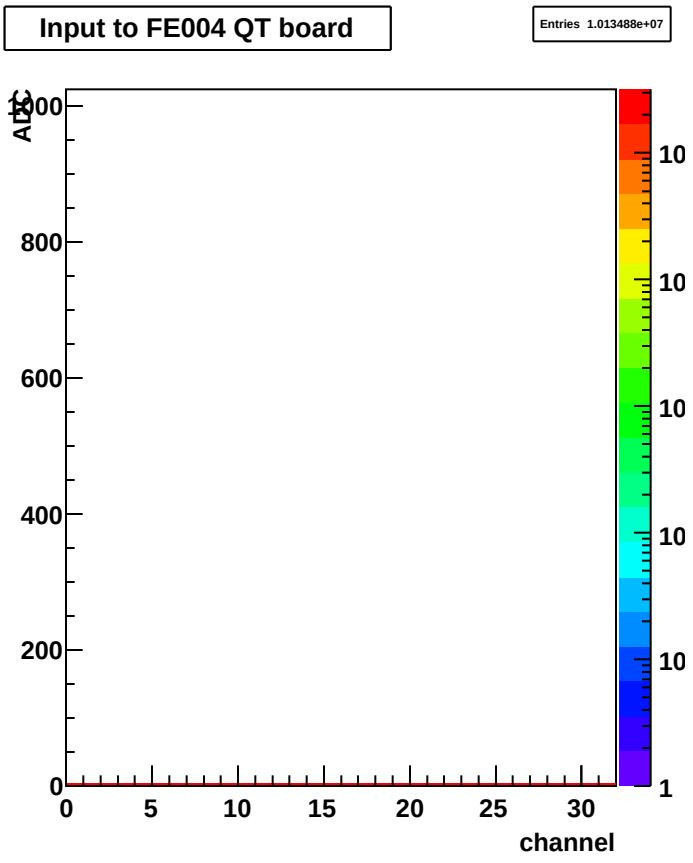

Input to FPD L2 DSM
Entries 211
HT bits - simulated
SMALL
LARGE-S
LARGE-N

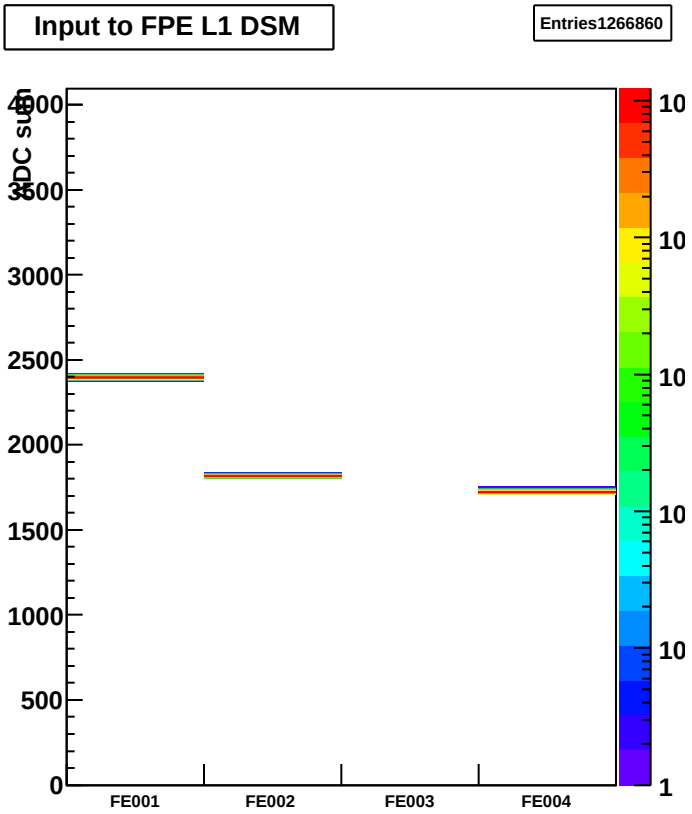

Input to FPD L2 DSM
Entries 1900290
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

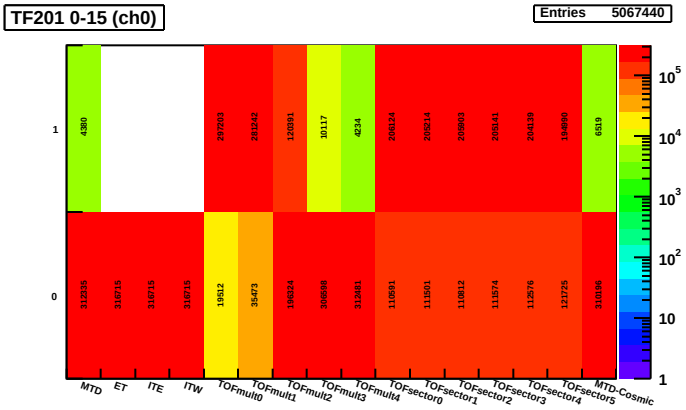

Input to FE001 QT board
Entries 1.013488e+07
ADC
channel

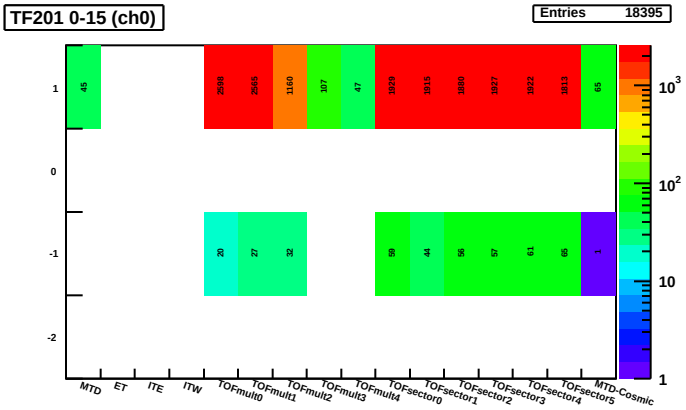

Input to FE002 QT board
Entries 1.013488e+07
ADC
channel

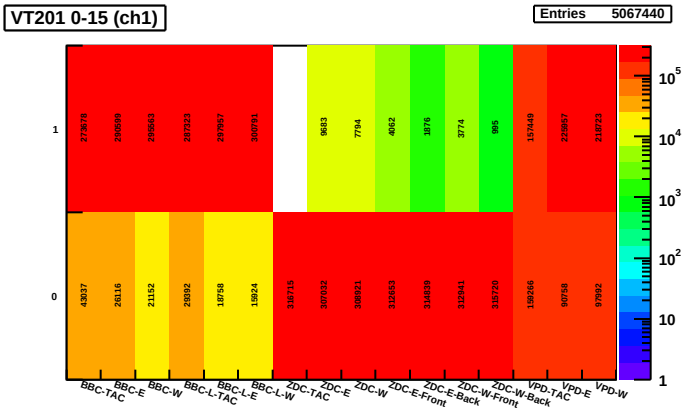

Input to FE003 QT board
Entries 1.013488e+07
ADC
channel

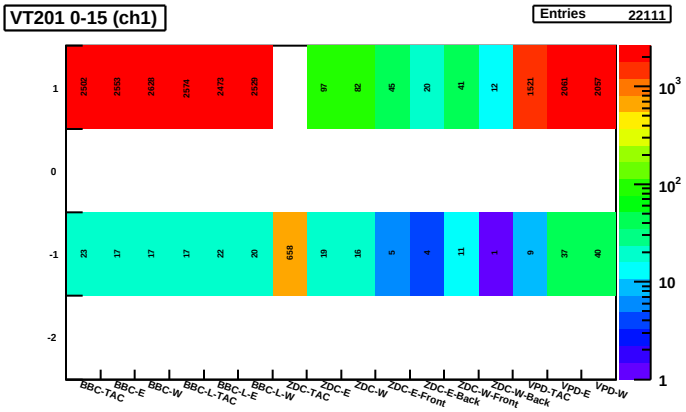

Input to FE004 QT board
Entries 1.013488e+07
ADC
channel

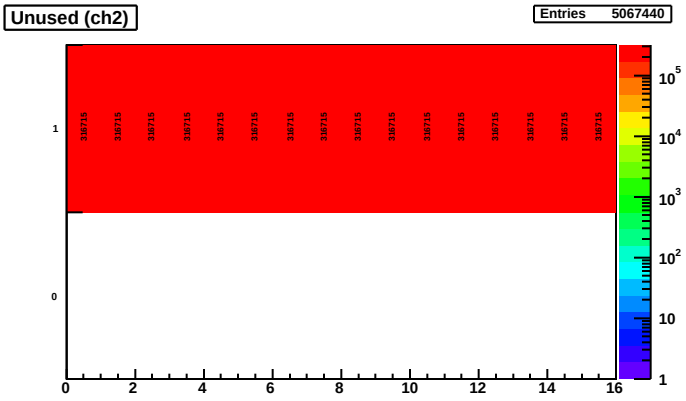

Input to FPE L1 DSM
Entries1266860
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

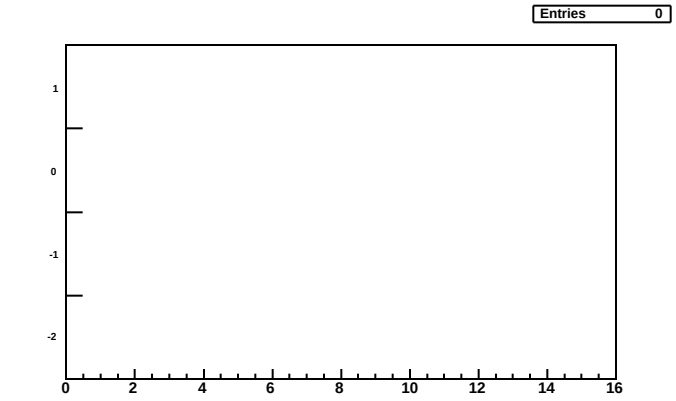

TF201 0-15 (ch0)
Entries 5067440
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

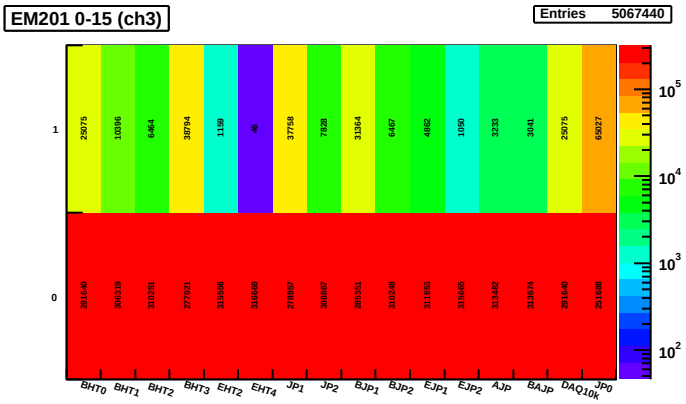

TF201 0-15 (ch0)
Entries 18395
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

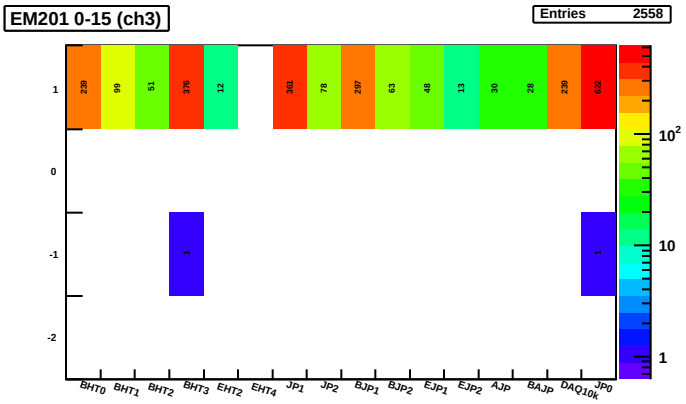

VT201 0-15 (ch1)
Entries 5067440
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 22111
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

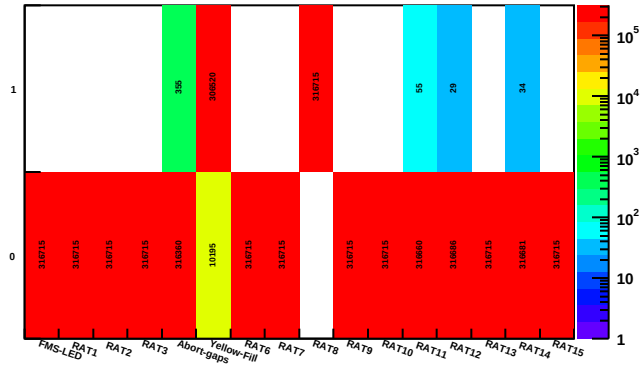

Unused (ch2)
Entries 5067440

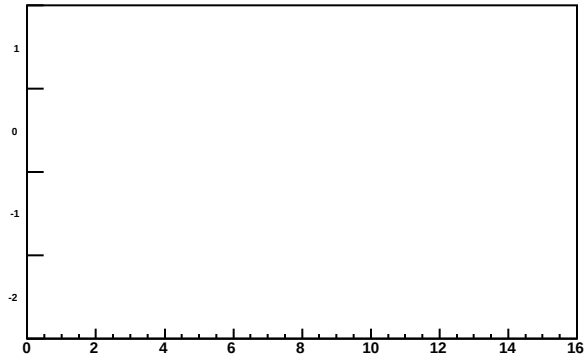

Entries 0


EM201 0-15 (ch3)
Entries 5067440
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

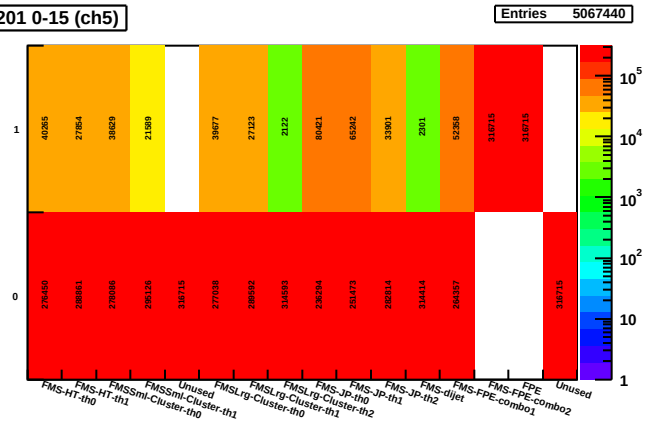

EM201 0-15 (ch3)
Entries 2558
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

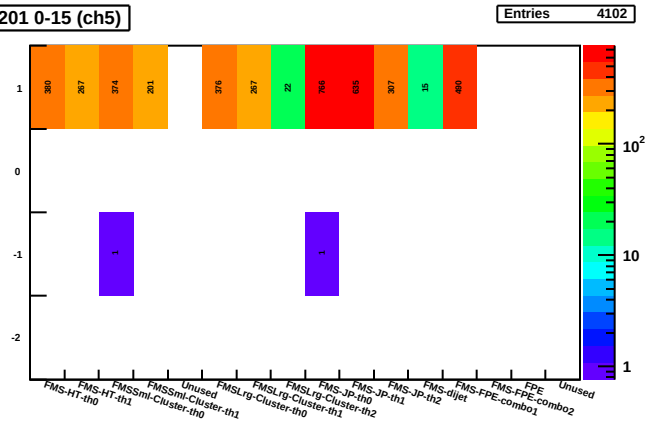




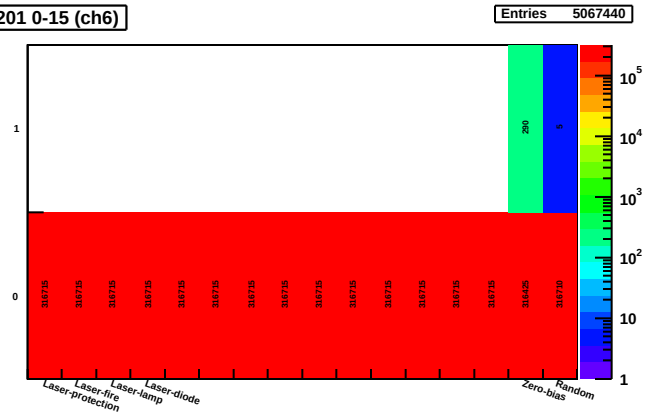
FP201 0-15 (ch5)

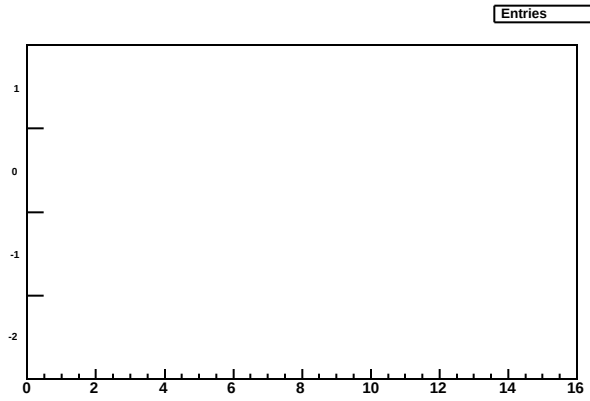
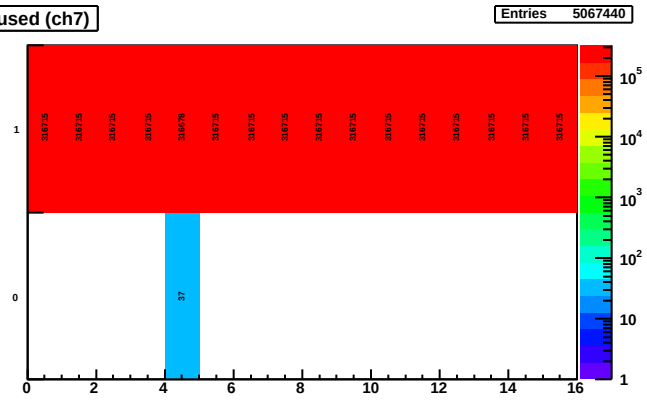
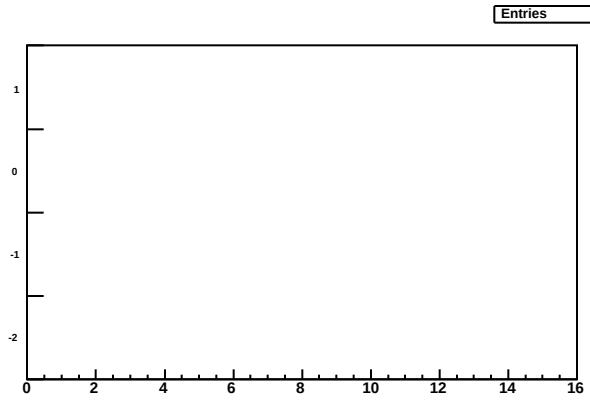


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)