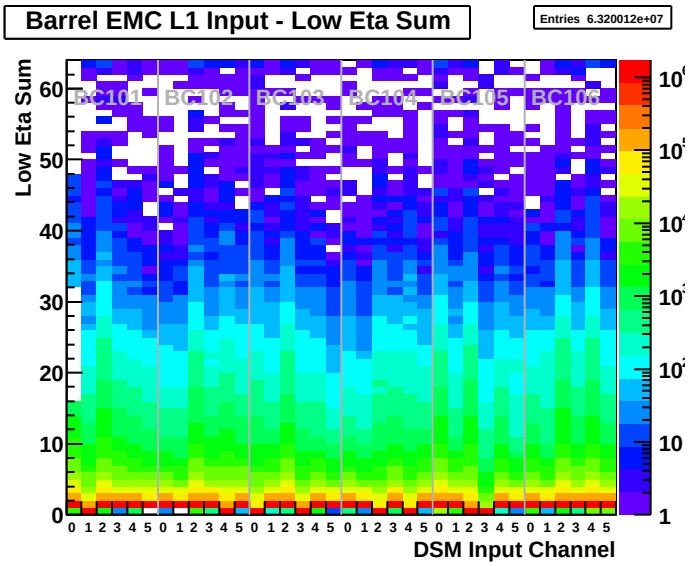
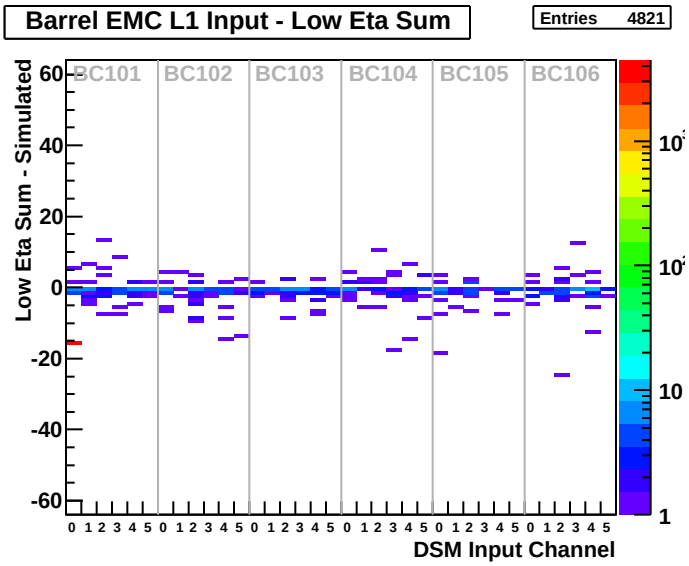
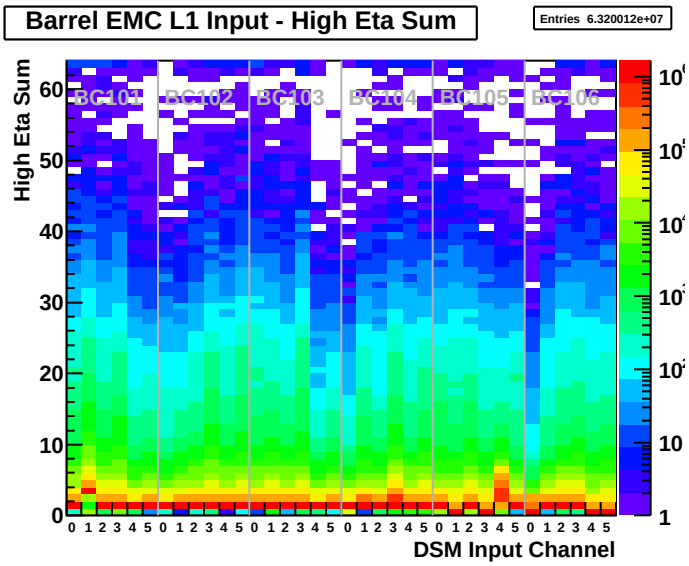
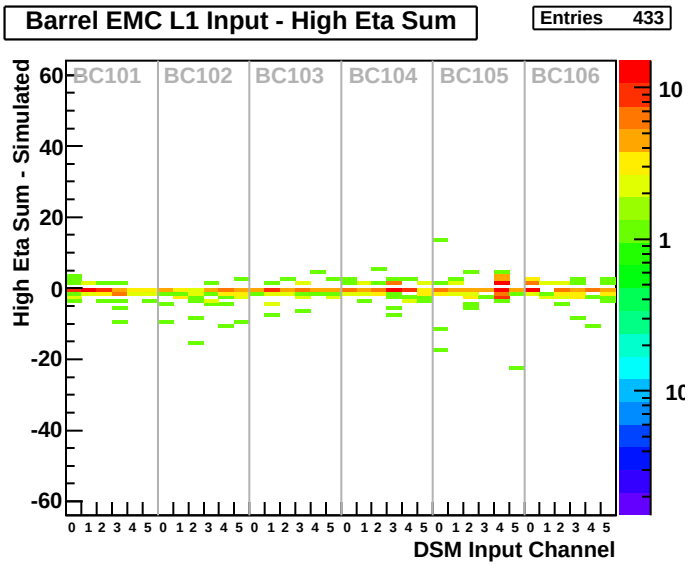
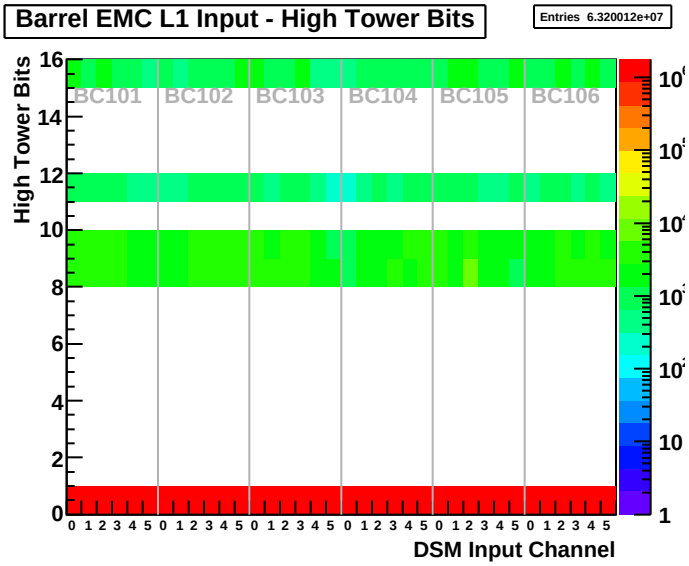
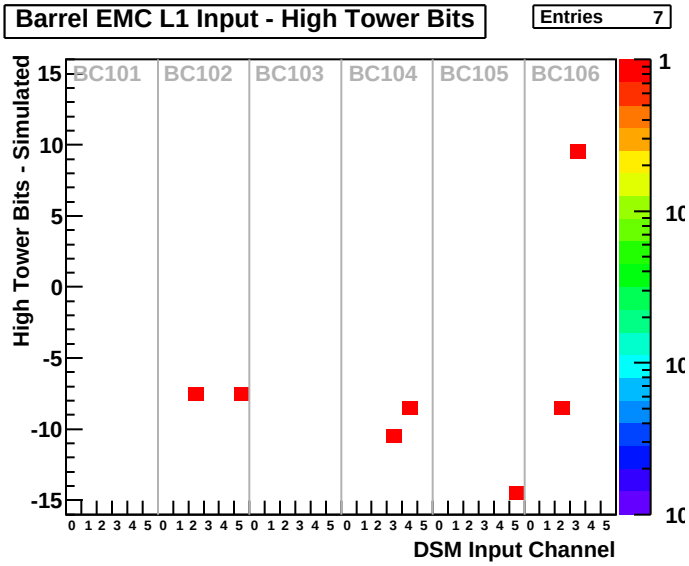

Barrel EMC L1 Input - Low Eta Sum
Entries 6.320012e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

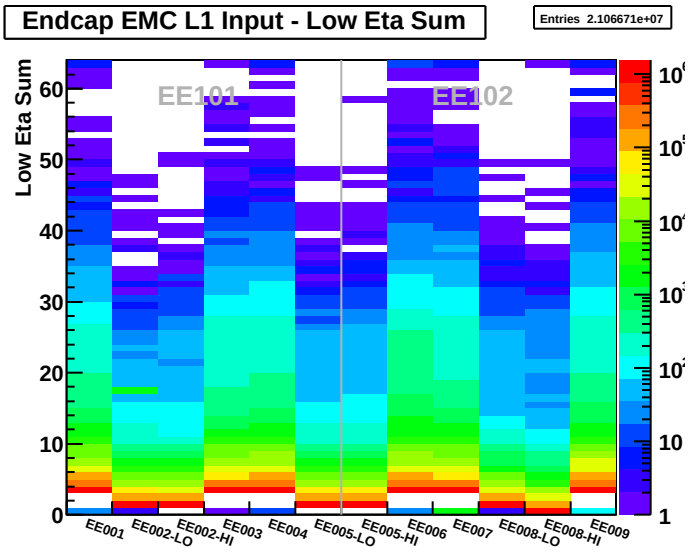

Barrel EMC L1 Input - Low Eta Sum
Entries 4821
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

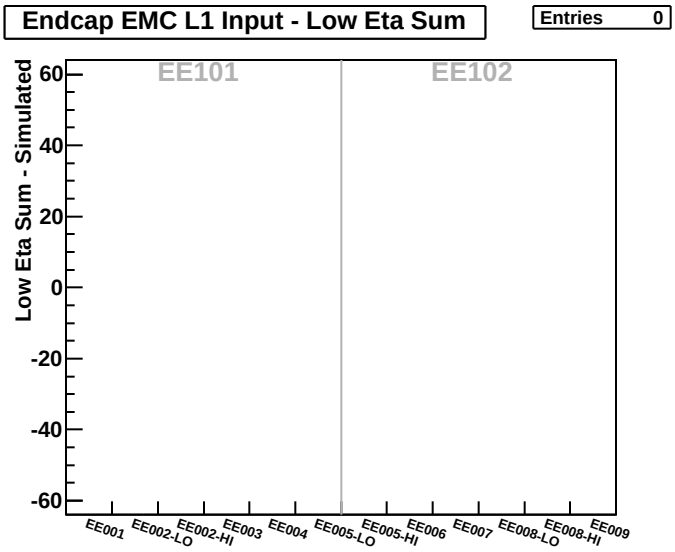

Barrel EMC L1 Input - High Eta Sum
Entries 6.320012e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

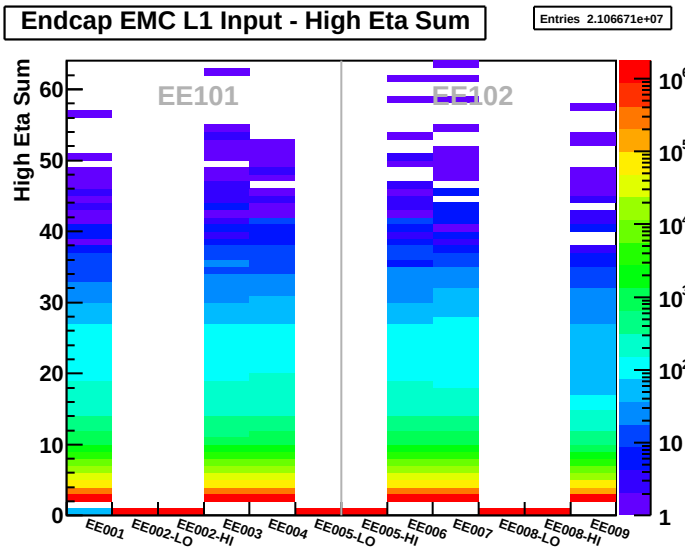

Barrel EMC L1 Input - High Eta Sum
Entries 433
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

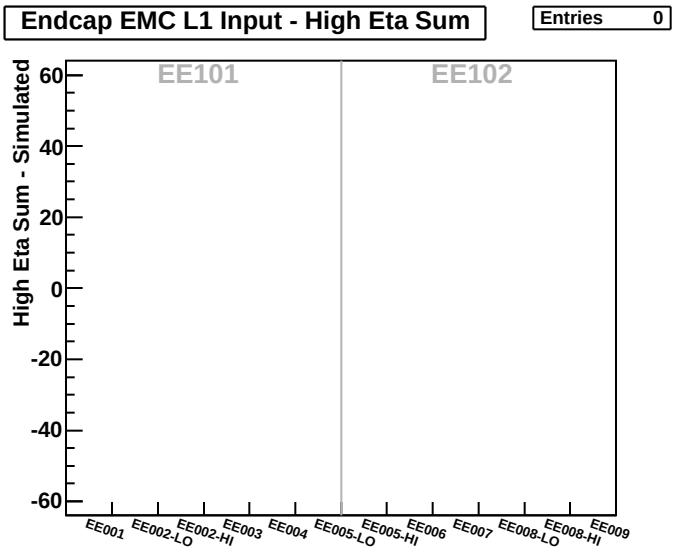

Barrel EMC L1 Input - High Tower Bits
Entries 6.320012e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

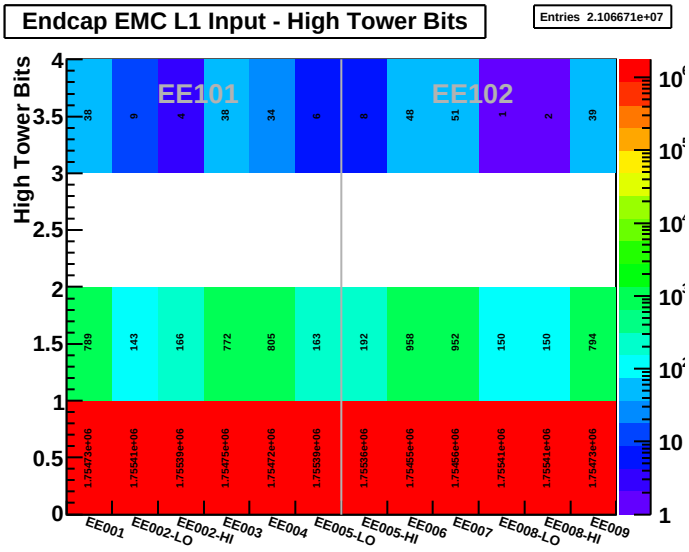

Barrel EMC L1 Input - High Tower Bits
Entries 7
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

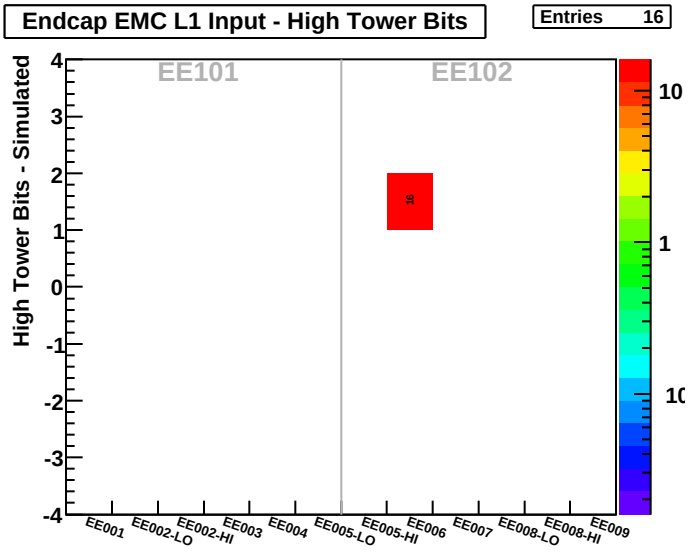

Endcap EMC L1 Input - Low Eta Sum
Entries 2.106671e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

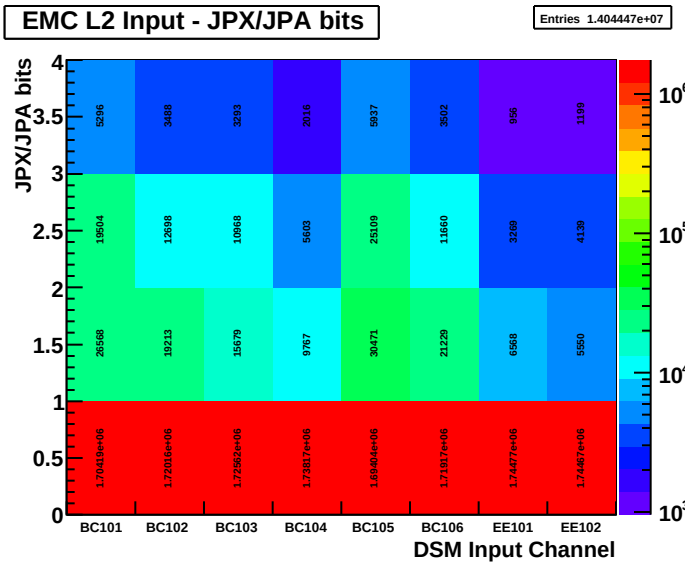

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

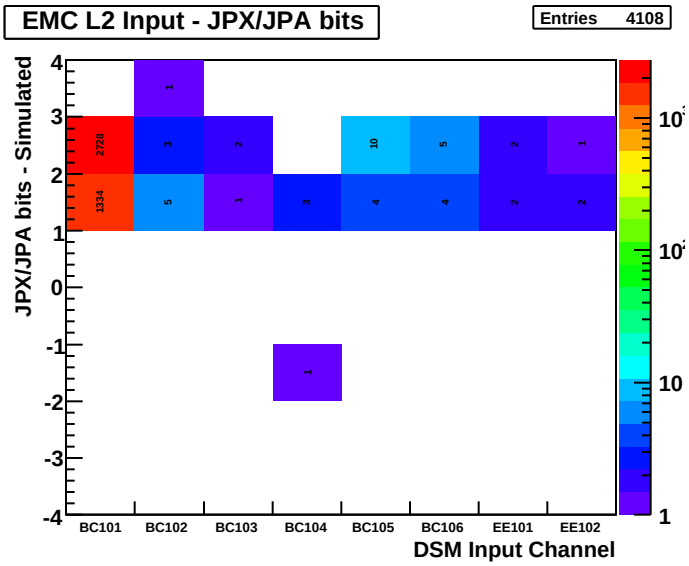

Endcap EMC L1 Input - High Eta Sum
Entries 2.106671e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

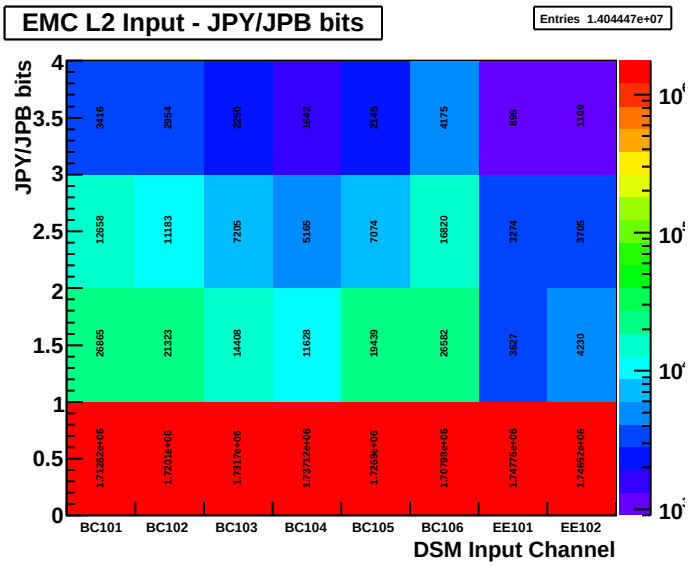

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

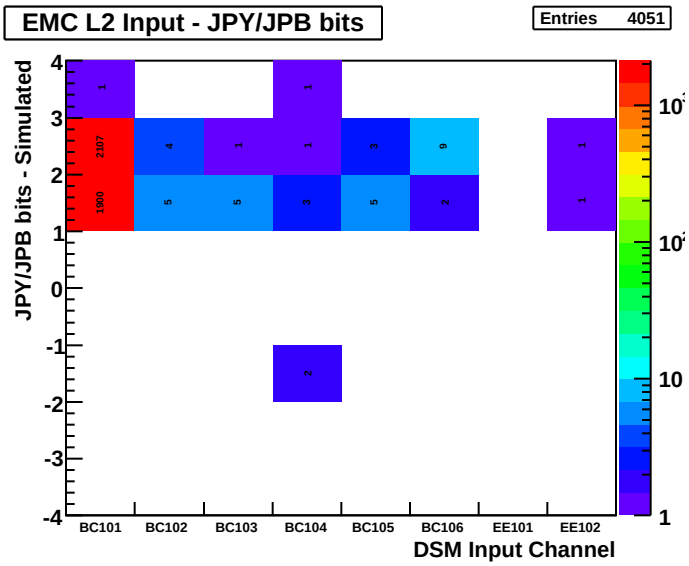

Endcap EMC L1 Input - High Tower Bits
Entries 2.106671e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

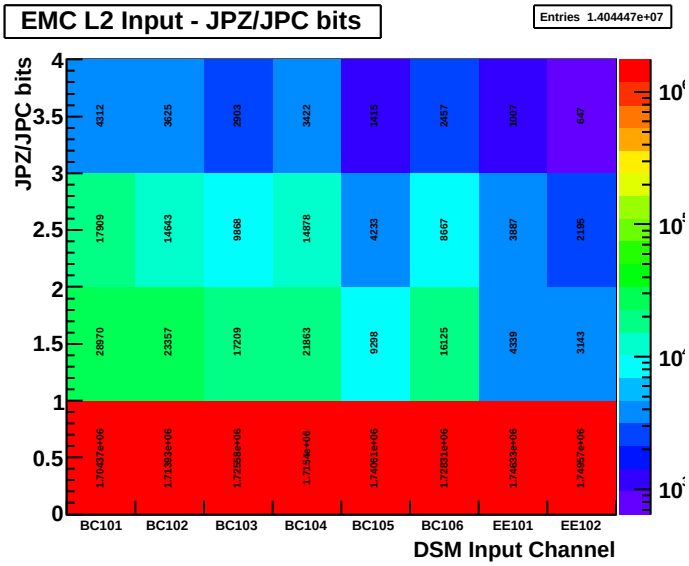

Endcap EMC L1 Input - High Tower Bits
Entries 16
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

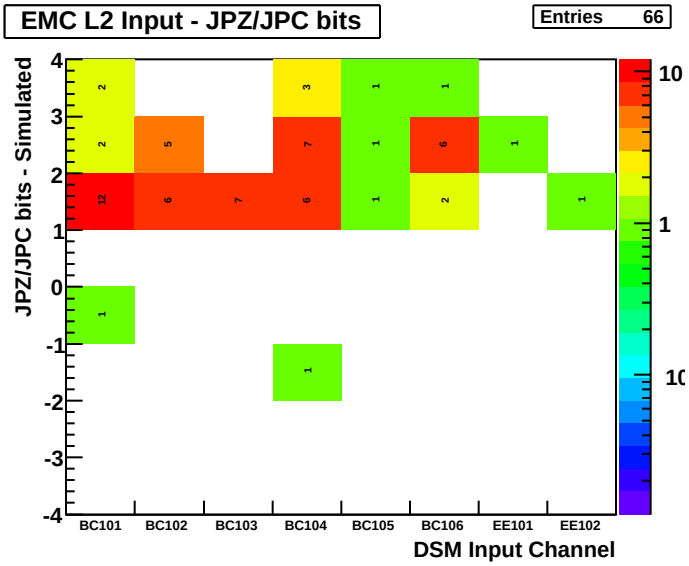

EMC L2 Input - JPX/JPA bits
Entries 1.404447e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

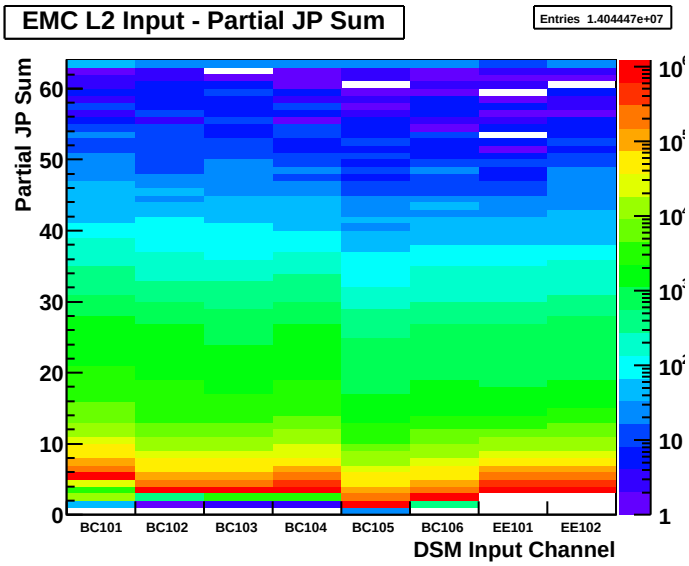

EMC L2 Input - JPX/JPA bits
Entries 4108
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

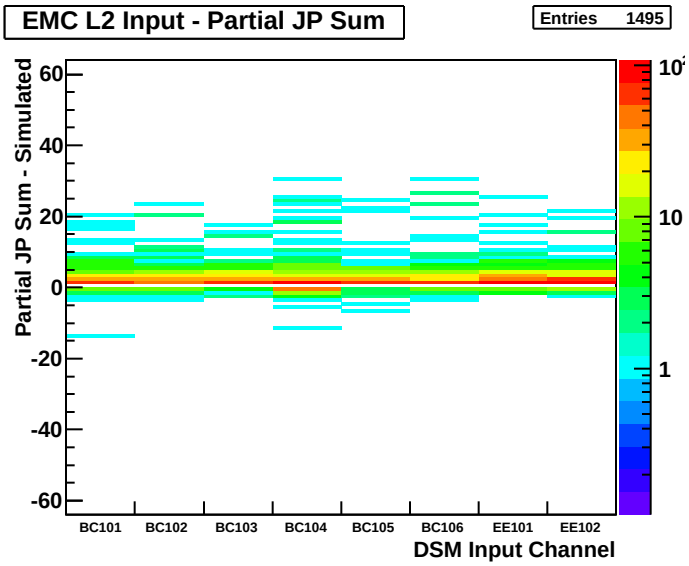

EMC L2 Input - JPY/JPB bits
Entries 1.404447e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

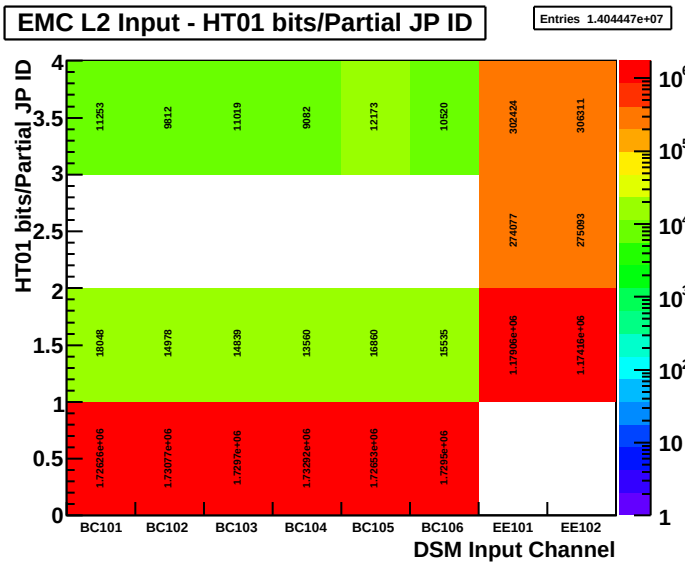

EMC L2 Input - JPY/JPB bits
Entries 4051
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

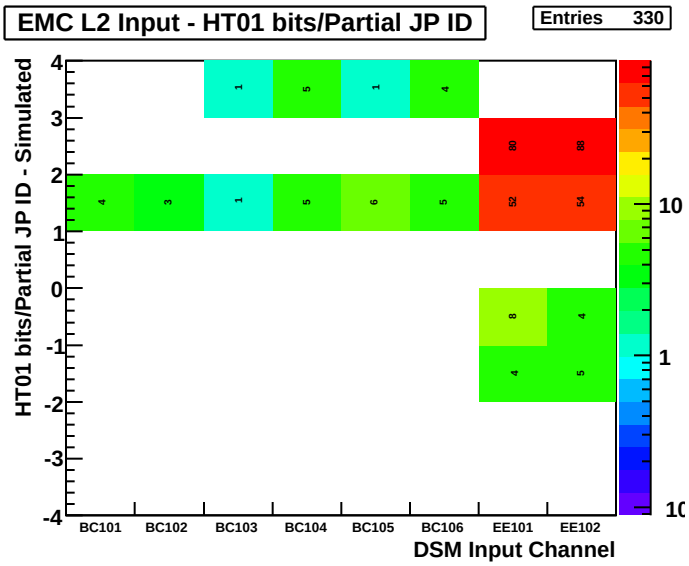

EMC L2 Input - JPZ/JPC bits
Entries 1.404447e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

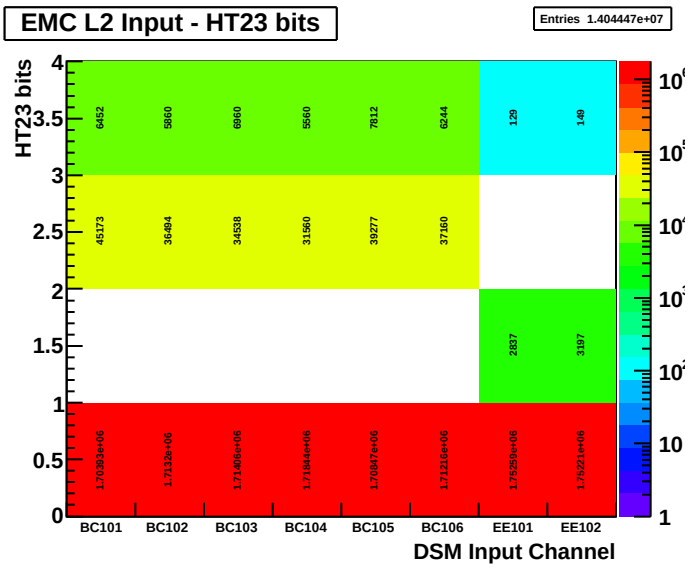

EMC L2 Input - JPZ/JPC bits
Entries 66
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

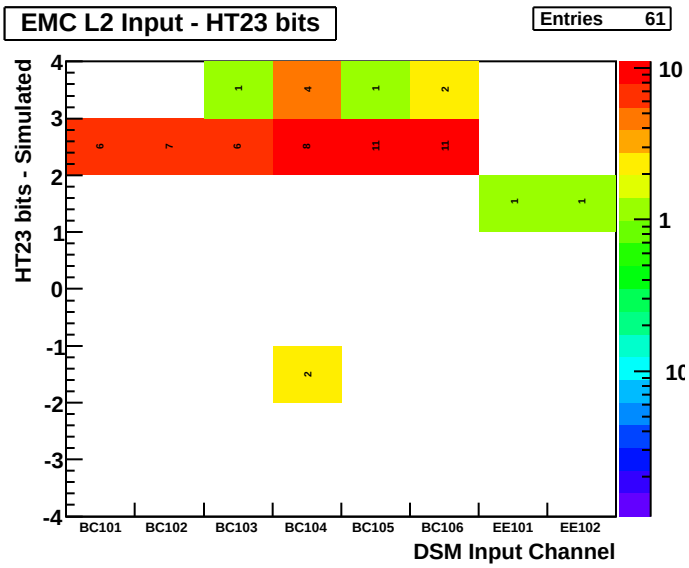

EMC L2 Input - Partial JP Sum
Entries 1.404447e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

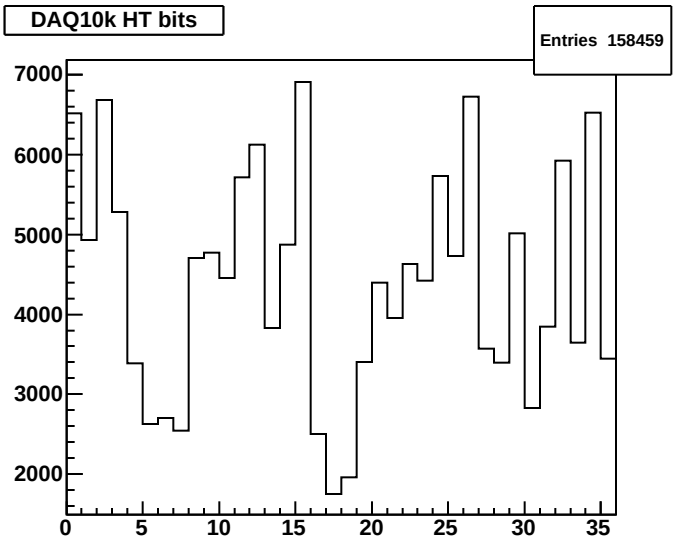

EMC L2 Input - Partial JP Sum
Entries 1495
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

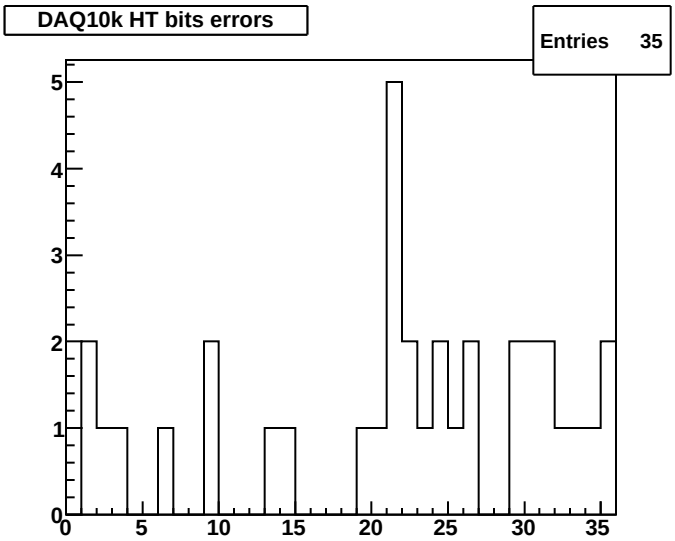

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.404447e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

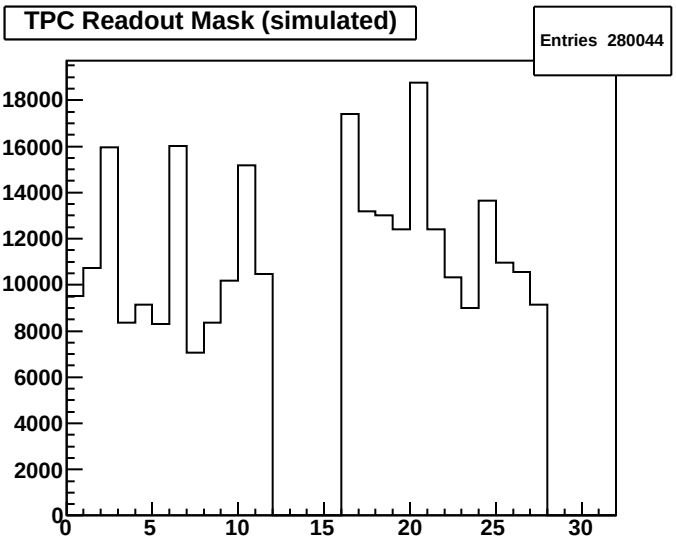

EMC L2 Input - HT01 bits/Partial JP ID
Entries 330
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

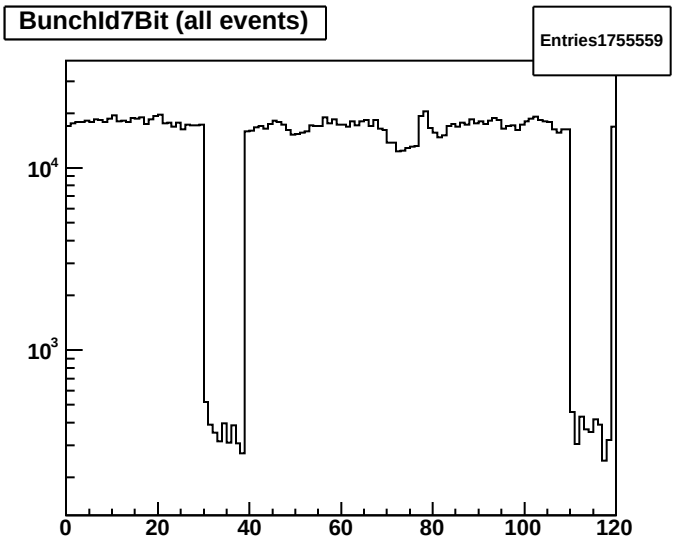

EMC L2 Input - HT23 bits
Entries 1.404447e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

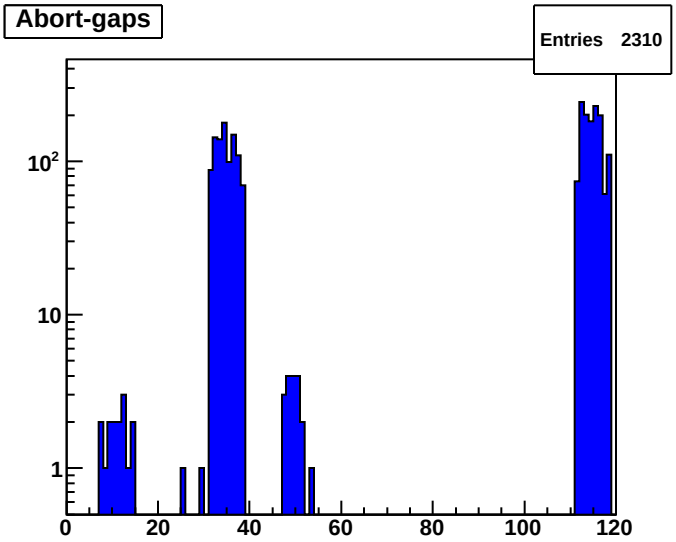

EMC L2 Input - HT23 bits
Entries 61
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

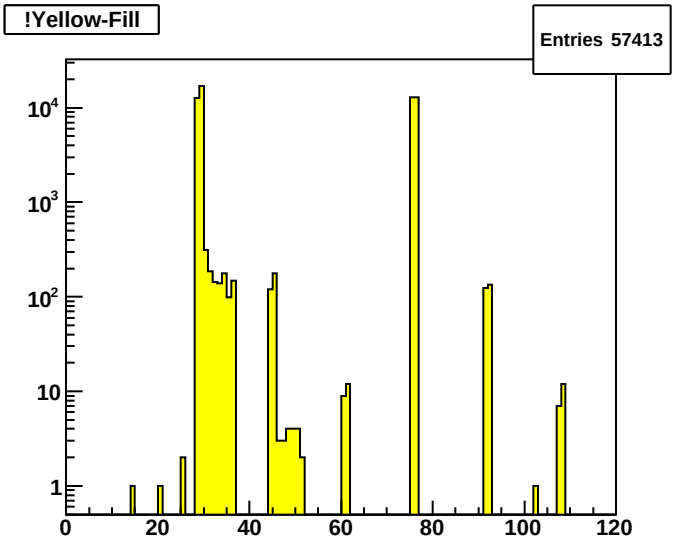

DAQ10k HT bits
Entries 158459

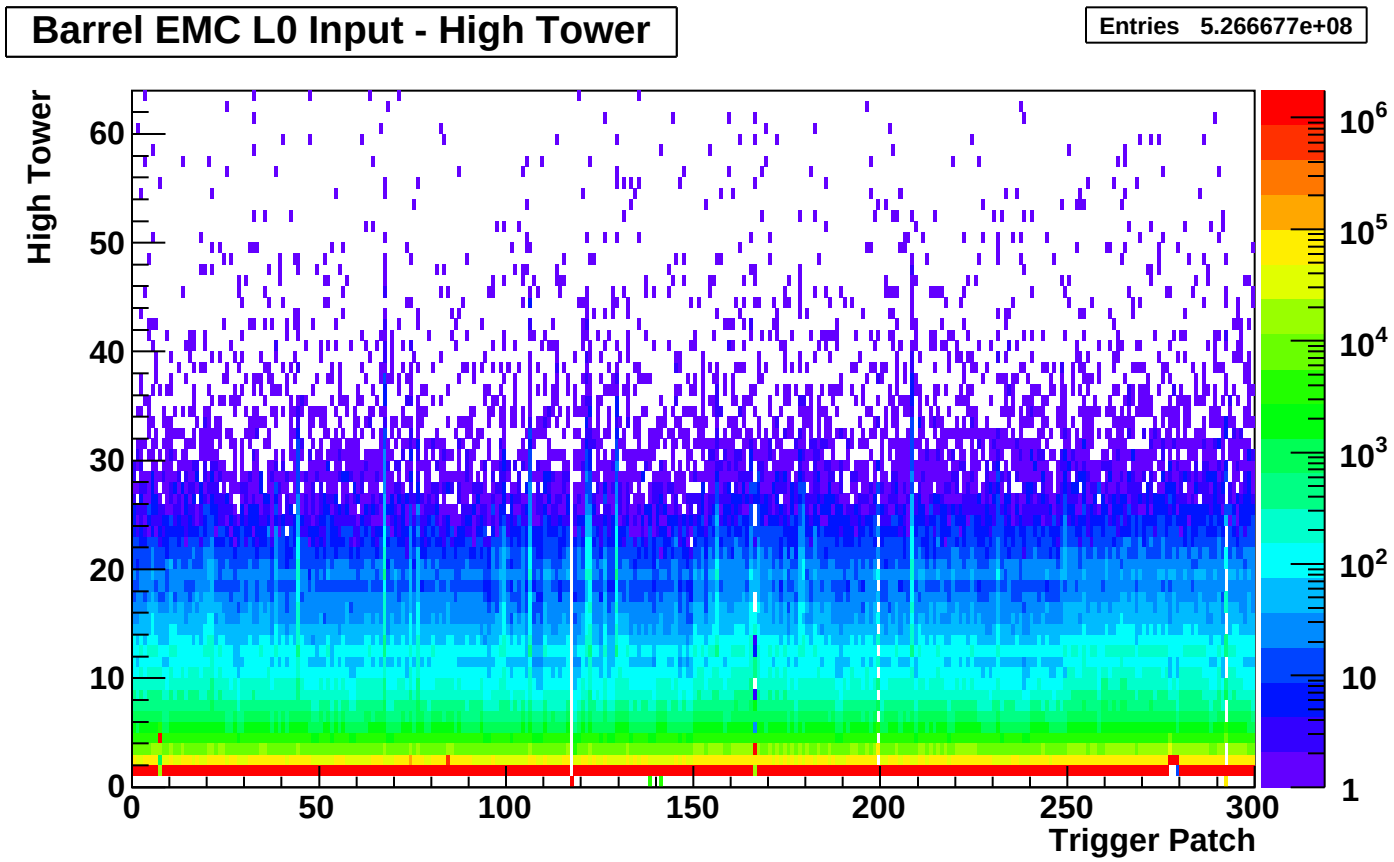

DAQ10k HT bits errors
Entries 35

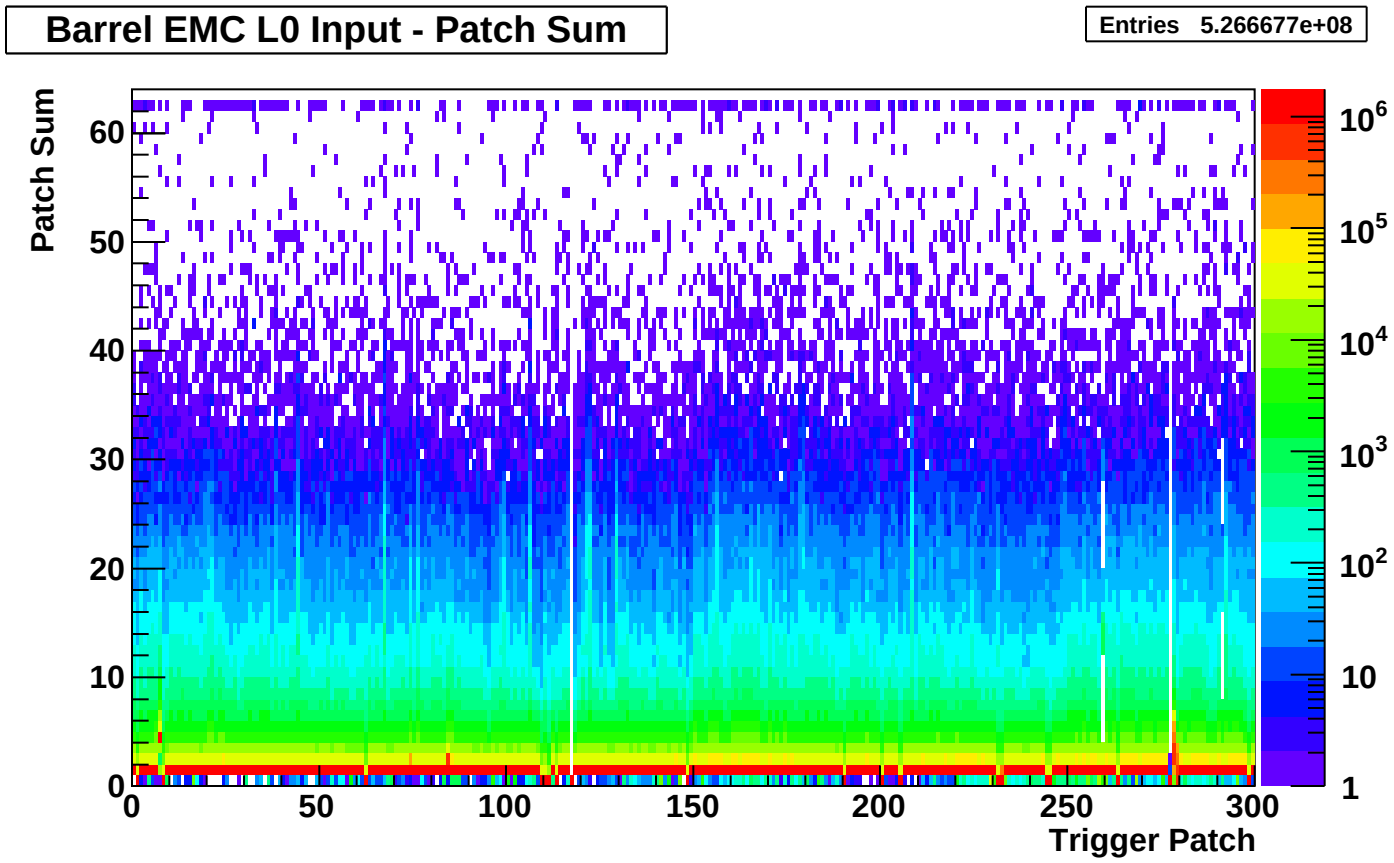

TPC Readout Mask (simulated)
Entries 280044

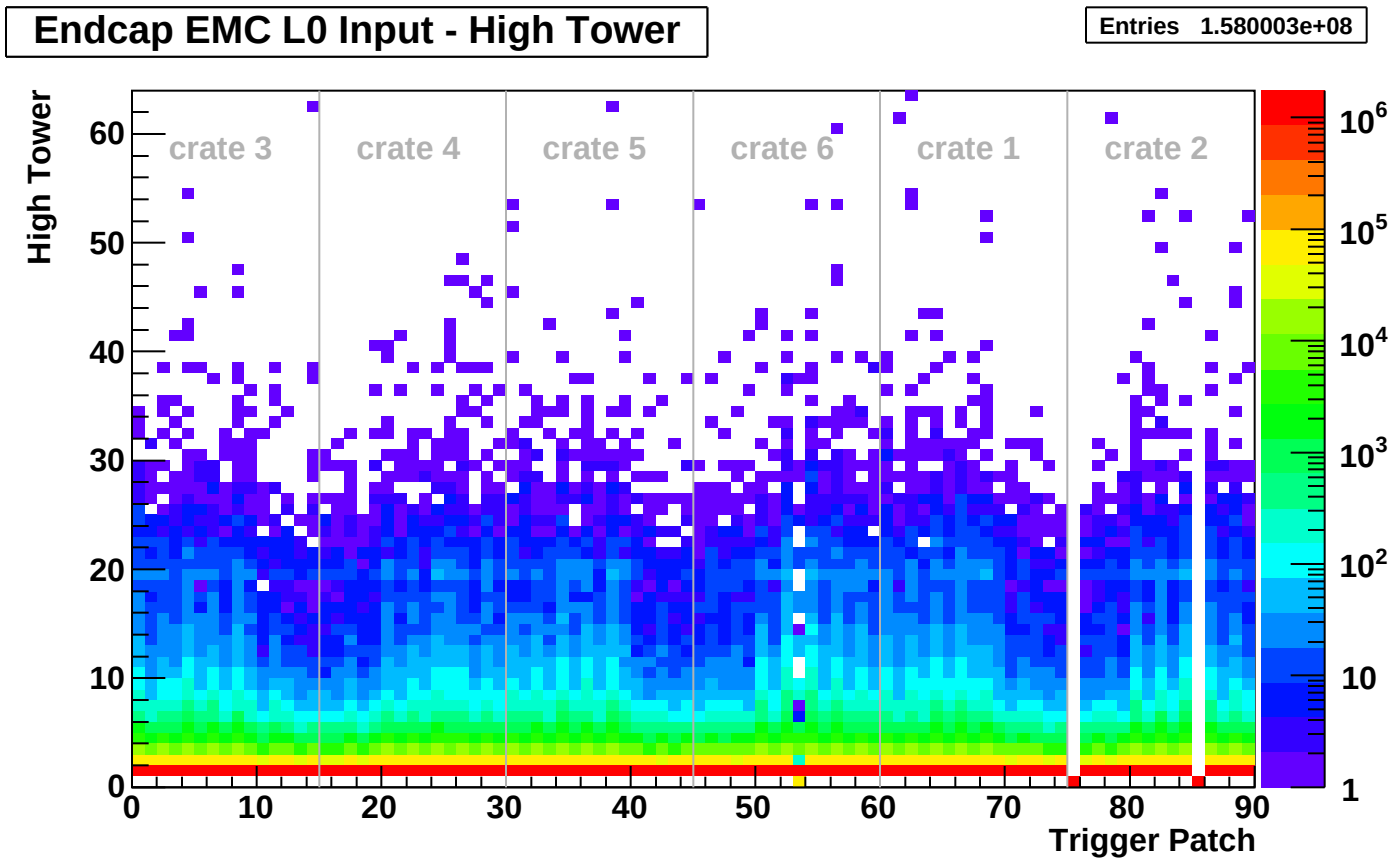

BunchId7Bit (all events)
Entries1755559

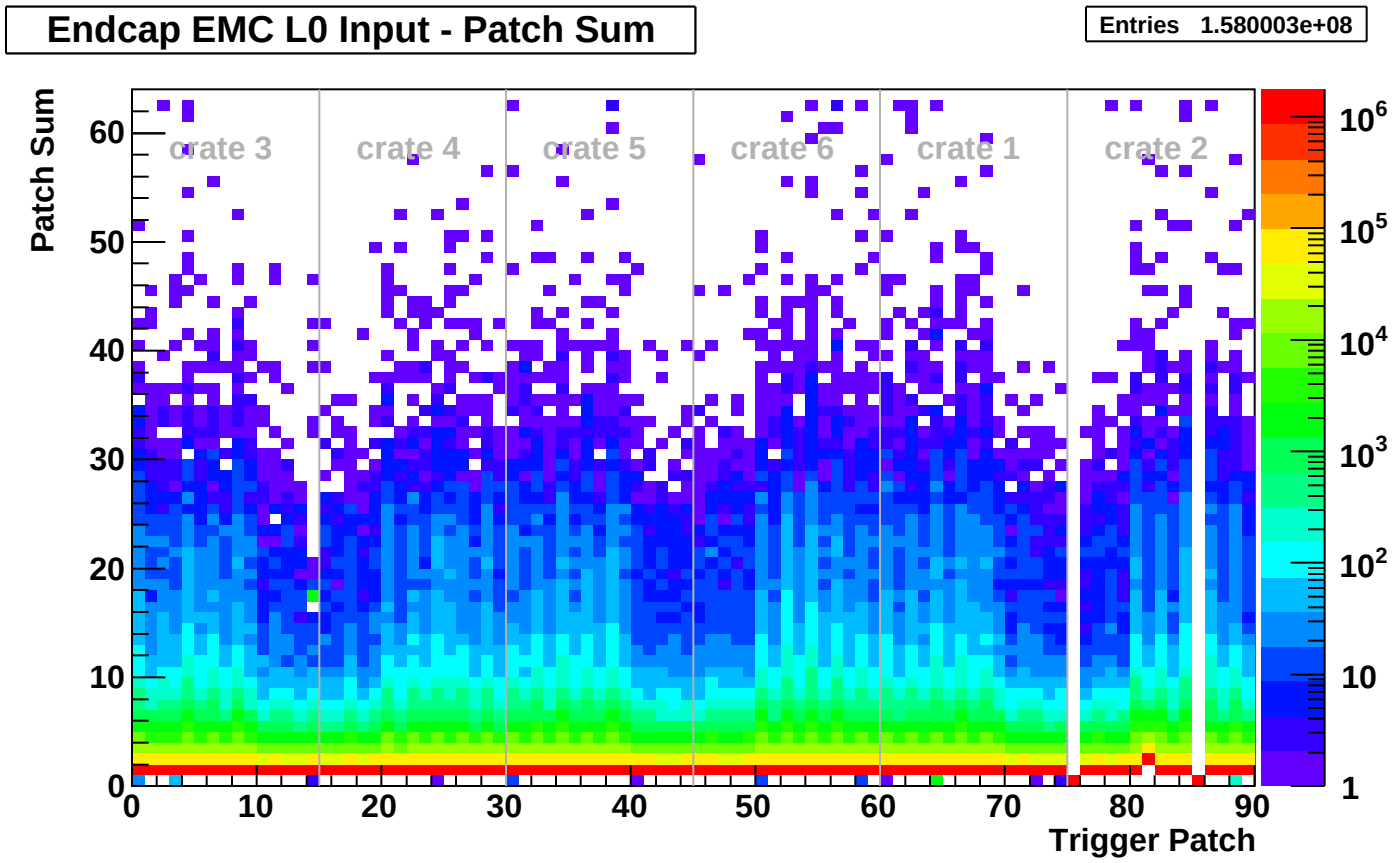

Abort-gaps
Entries 2310

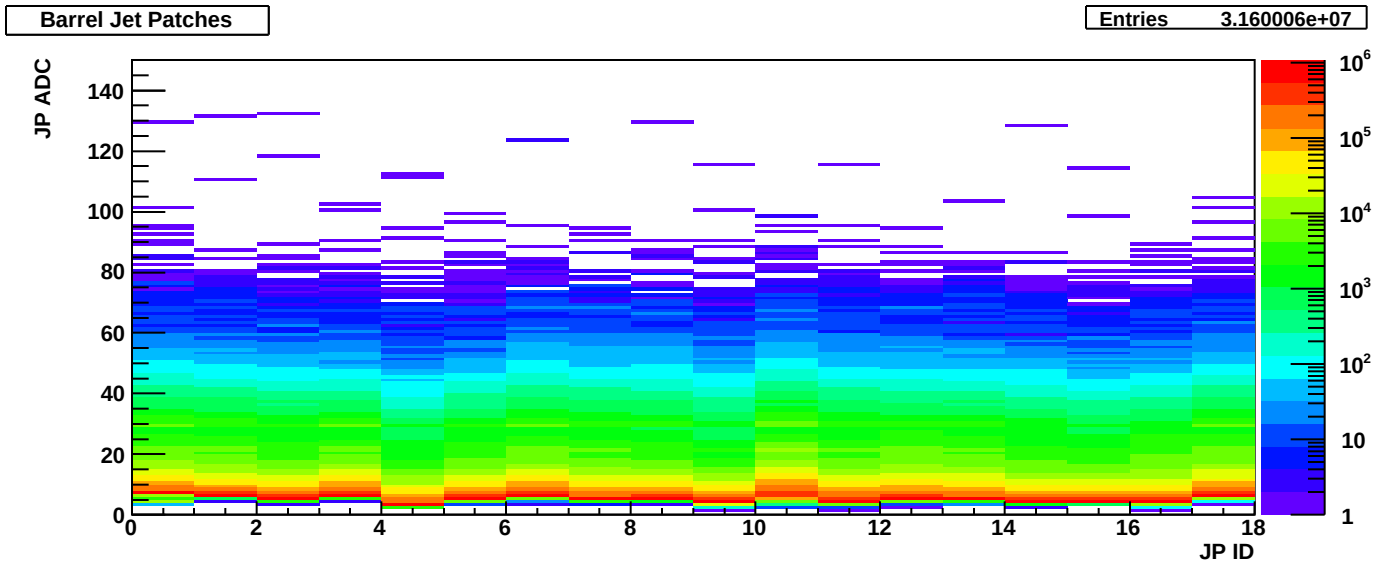

!Yellow-Fill
Entries 57413

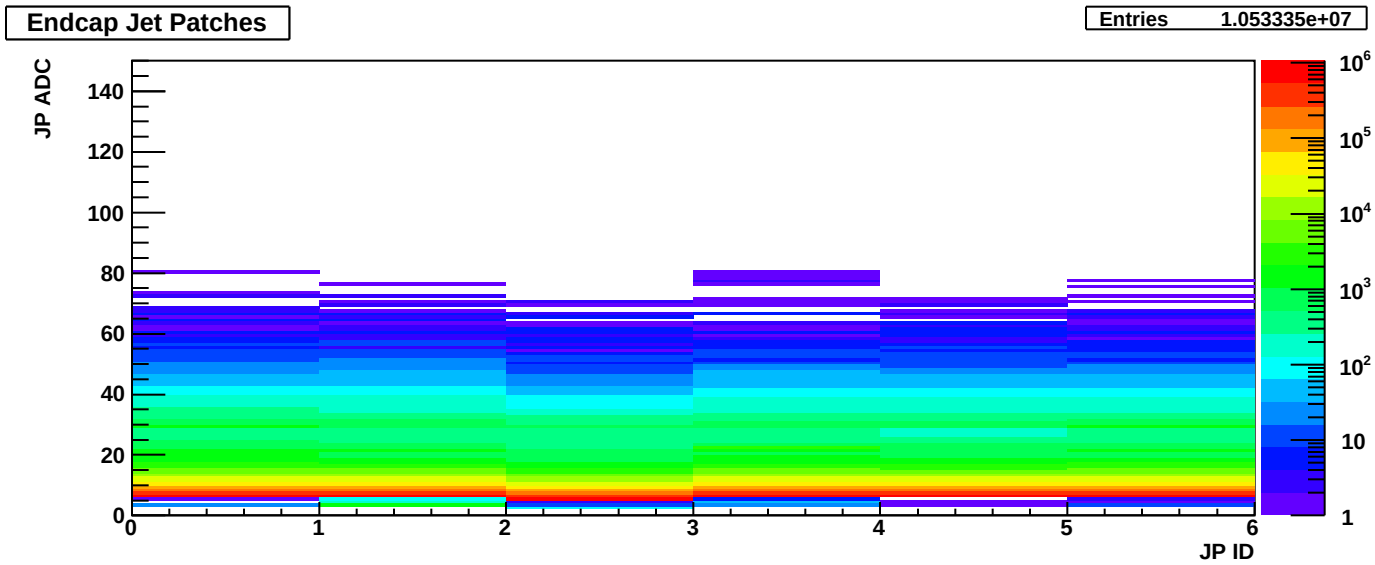

Barrel EMC L0 Input - High Tower
Entries 5.266677e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

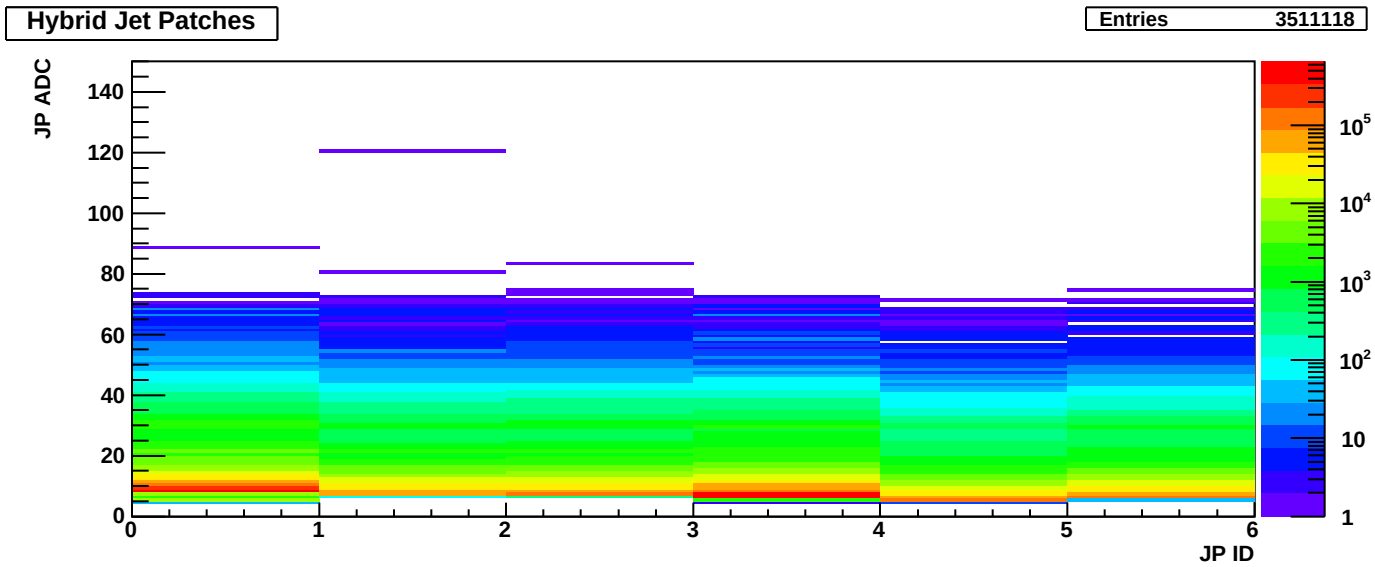

Barrel EMC L0 Input - Patch Sum
Entries 5.266677e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

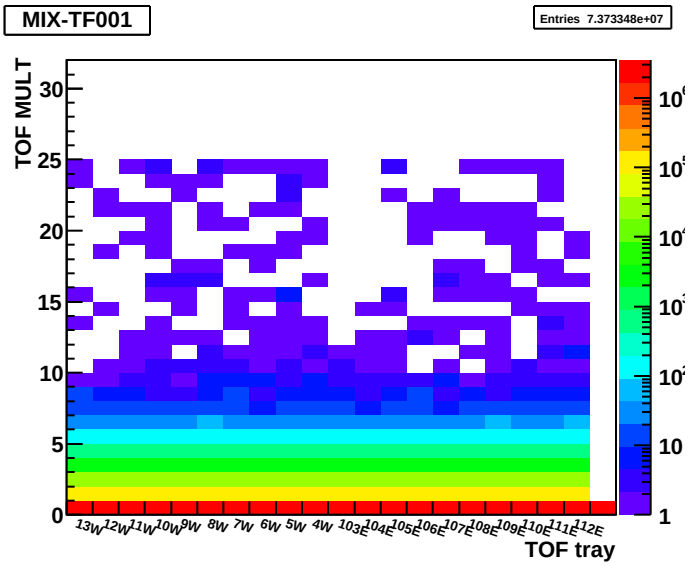

Endcap EMC L0 Input - High Tower
Entries 1.580003e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

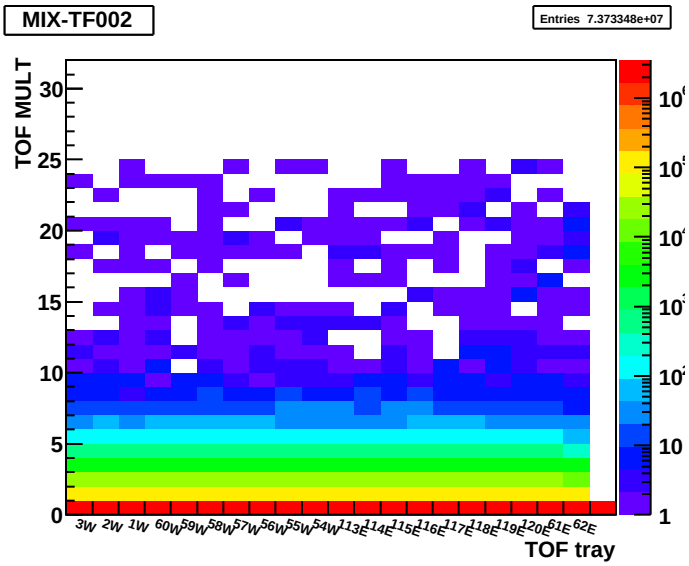

Endcap EMC L0 Input - Patch Sum
Entries 1.580003e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

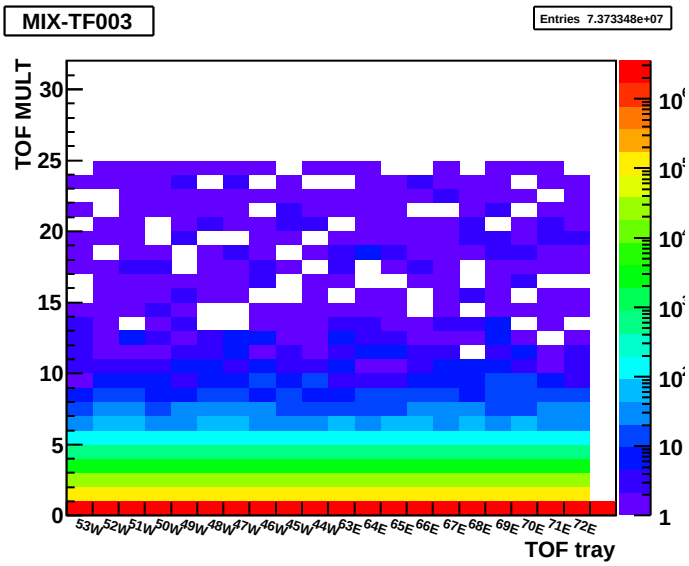

Barrel Jet Patches
Entries 3.160006e+07
JP ADC
JP ID

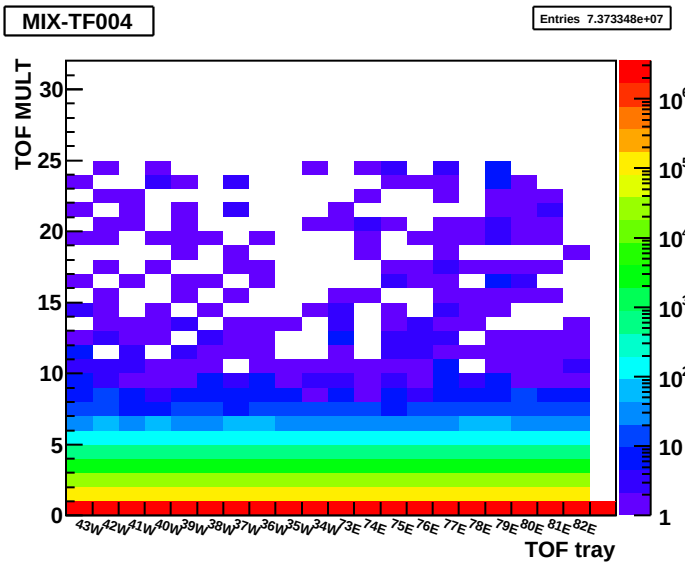

Endcap Jet Patches
Entries 1.053335e+07
JP ADC
JP ID

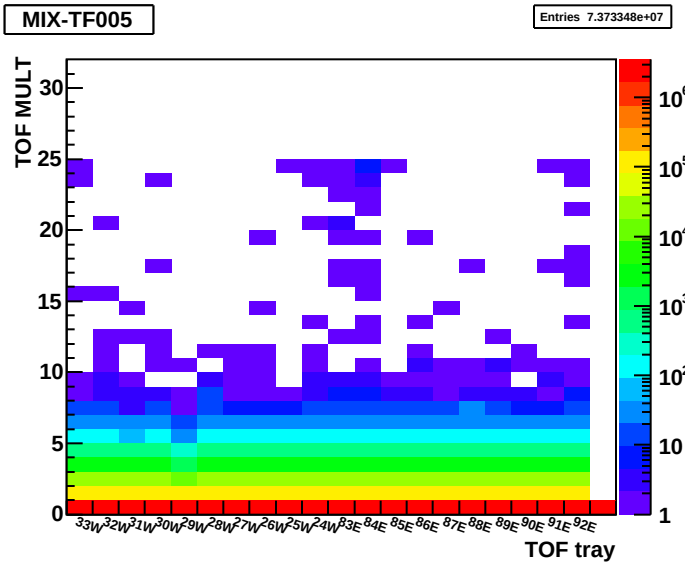

Hybrid Jet Patches
Entries 3511118
JP ADC
JP ID

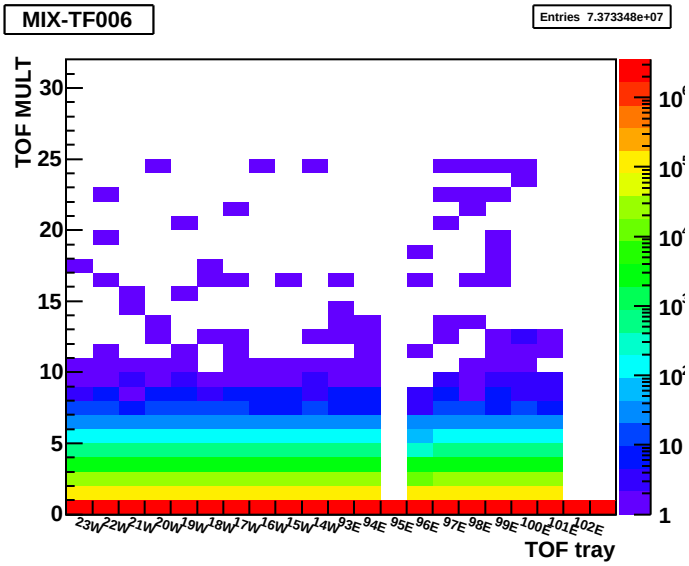

MIX-TF001
Entries 7.373348e+07
TOF MULT
TOF tray

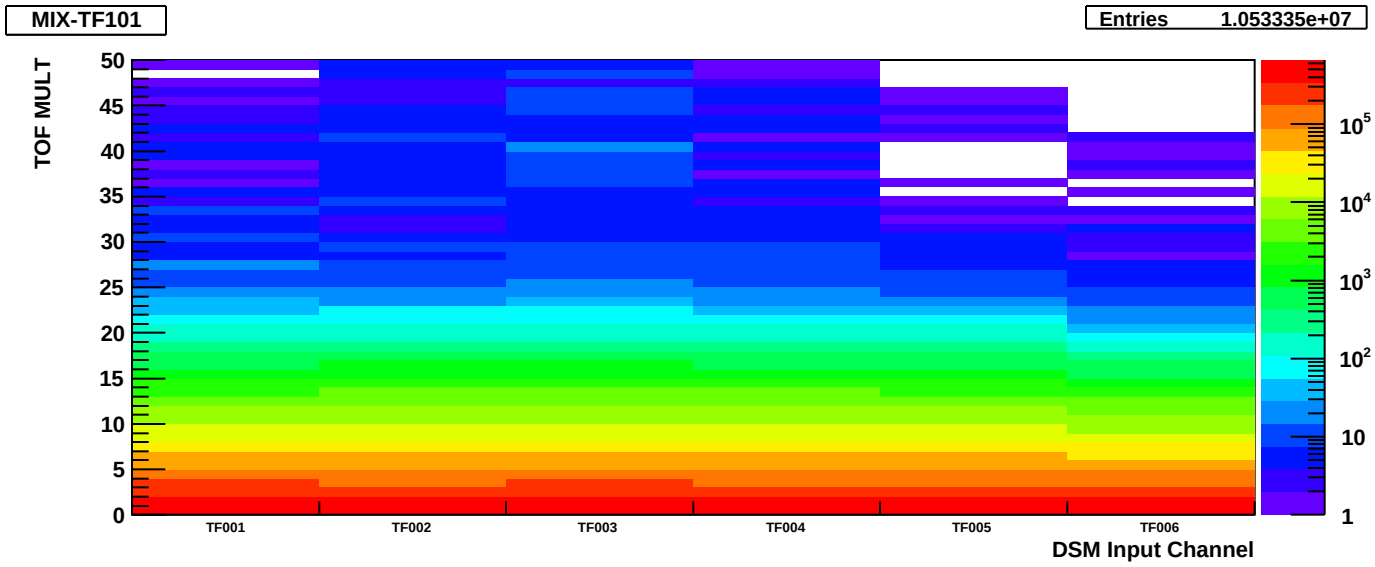

MIX-TF002
Entries 7.373348e+07
TOF MULT
TOF tray

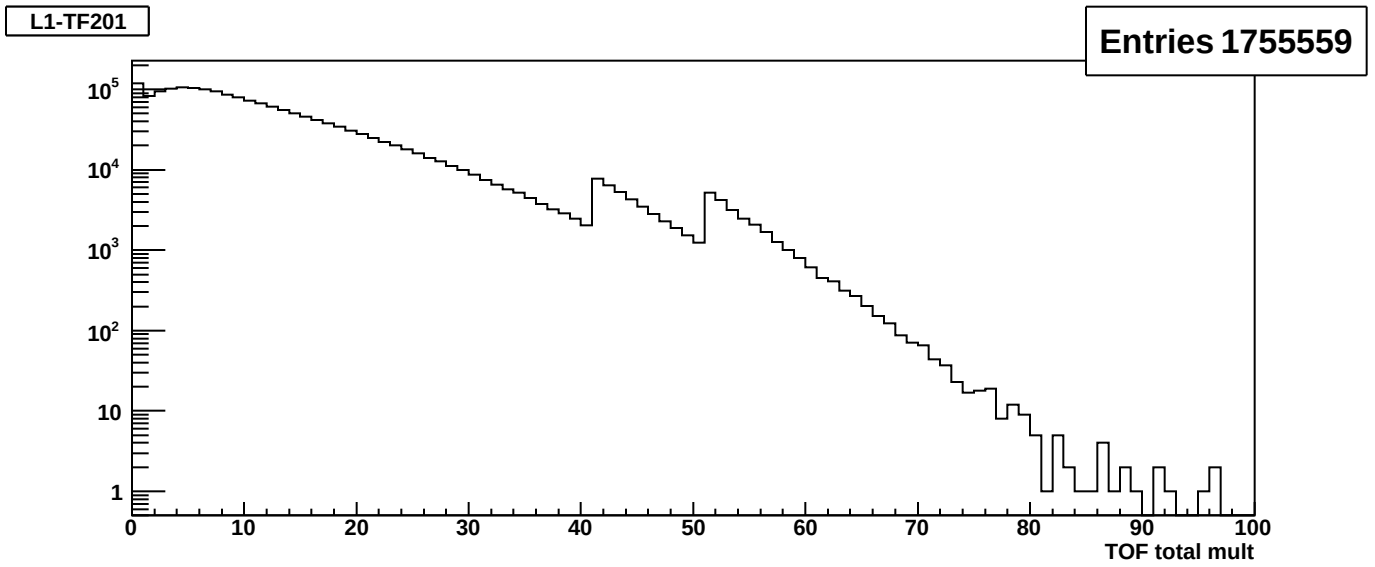

MIX-TF003
Entries 7.373348e+07
TOF MULT
TOF tray

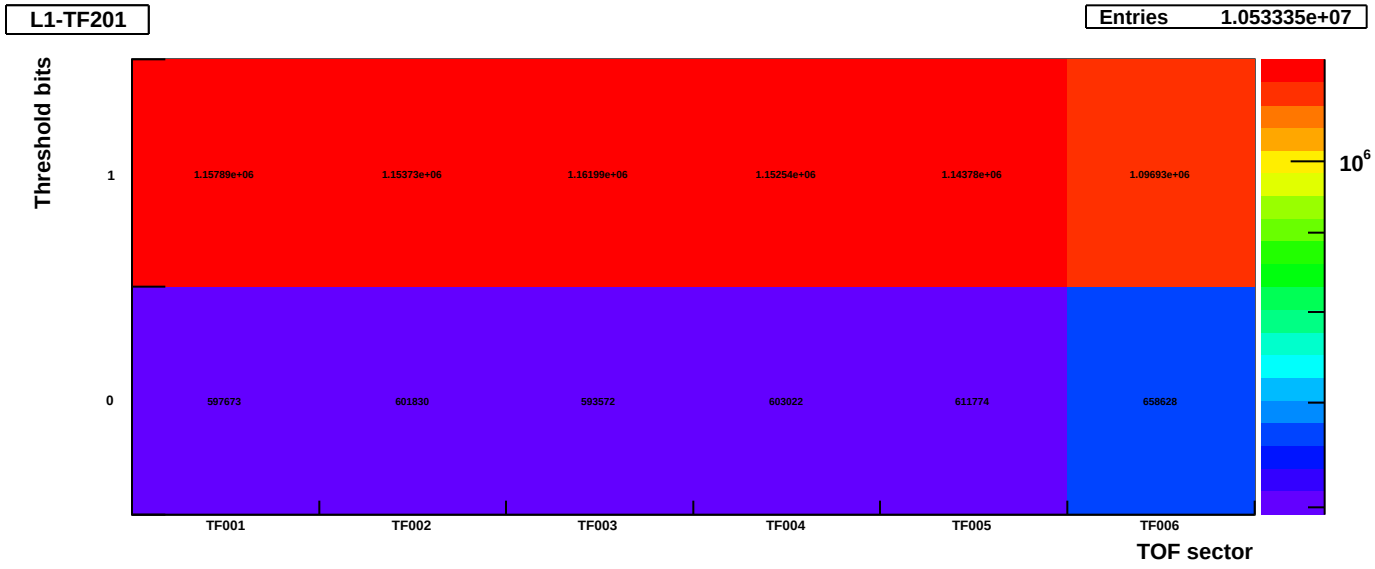

MIX-TF004
Entries 7.373348e+07
TOF MULT
TOF tray

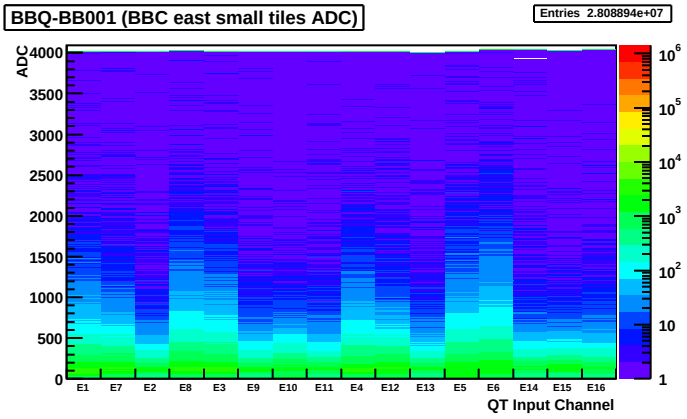

MIX-TF005
Entries 7.373348e+07
TOF MULT
TOF tray

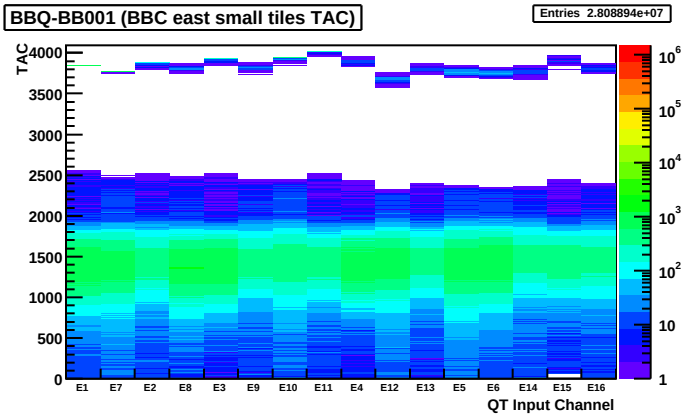

MIX-TF006
Entries 7.373348e+07
TOF MULT
TOF tray

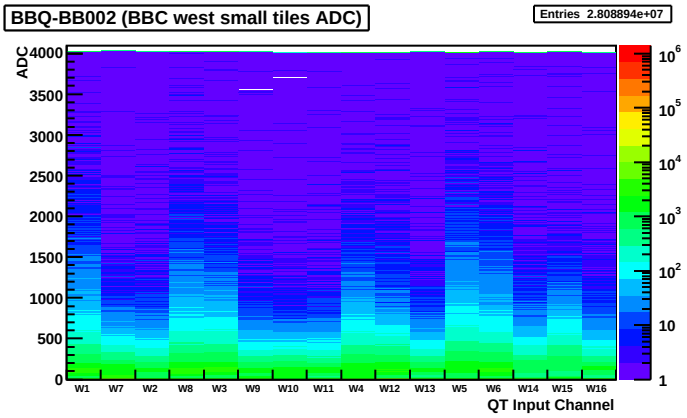

MIX-TF101
Entries 1.053335e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

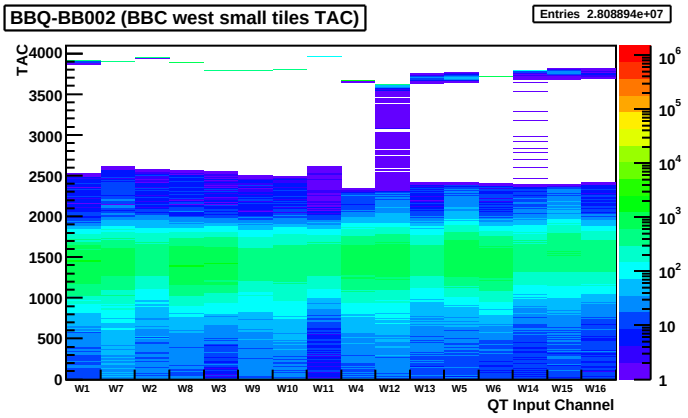

L1-TF201
Entries 1755559
TOF total mult

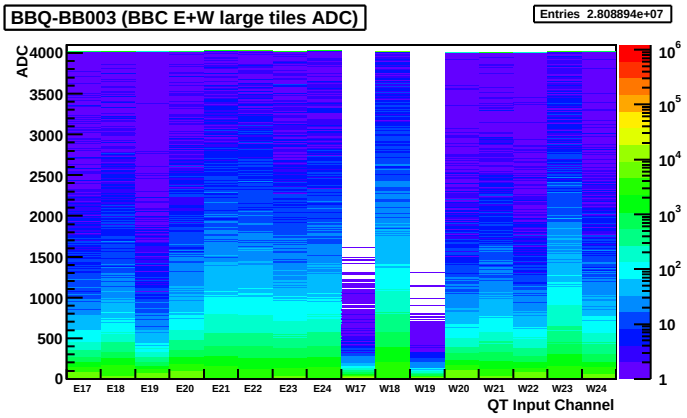

L1-TF201
Entries 1.053335e+07
Threshold bits
1.15789e+06
1.15373e+06
1.16199e+06
1.15254e+06
1.14378e+06
1.09693e+06
597673
601830
593572
603022
611774
658628
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

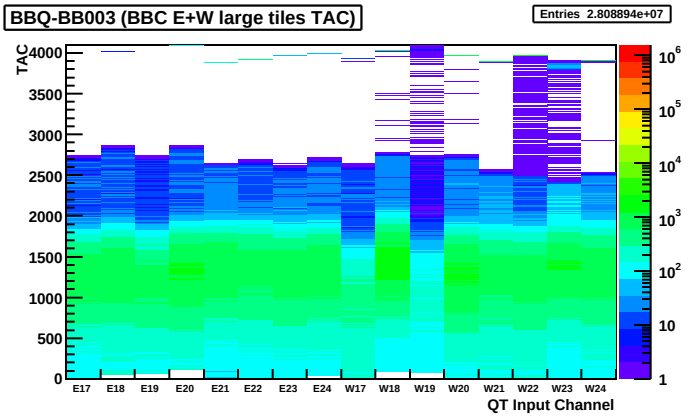

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.808894e+07
ADC
QT Input Channel

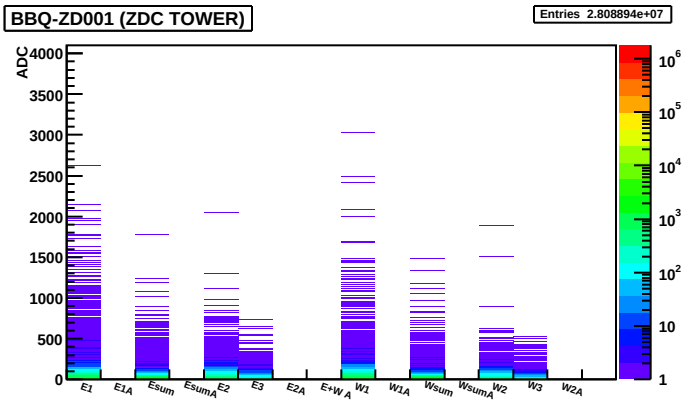

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.808894e+07
TAC
QT Input Channel

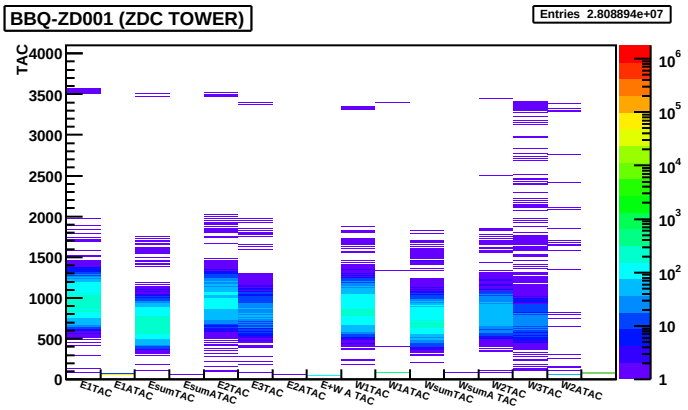

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.808894e+07
ADC
QT Input Channel

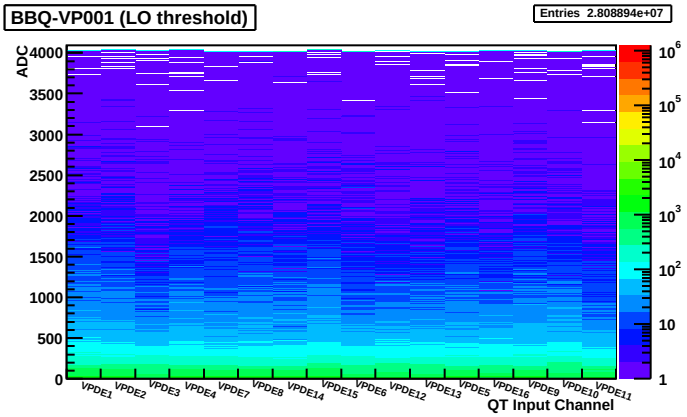

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.808894e+07
TAC
QT Input Channel

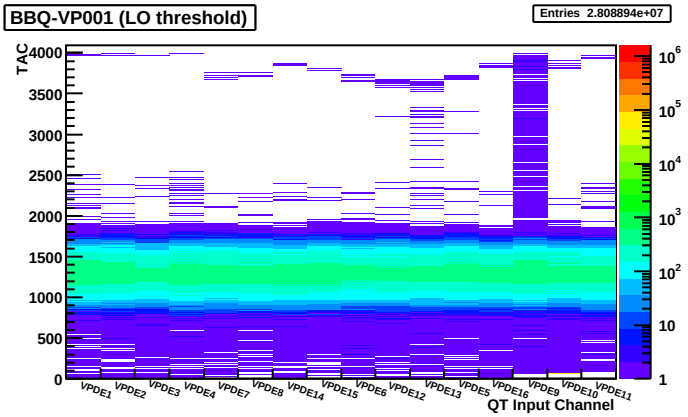

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.808894e+07
ADC
QT Input Channel

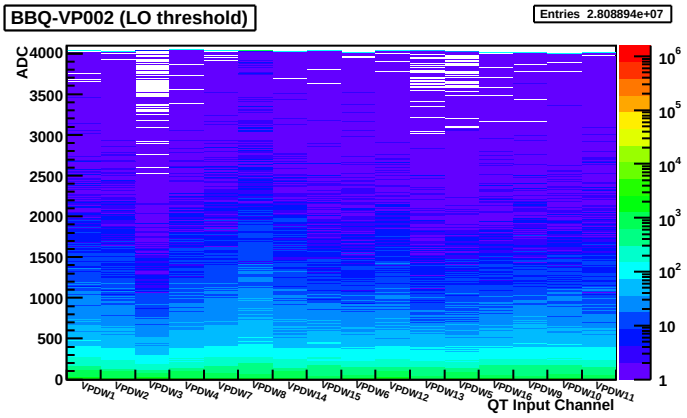

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.808894e+07
TAC
QT Input Channel

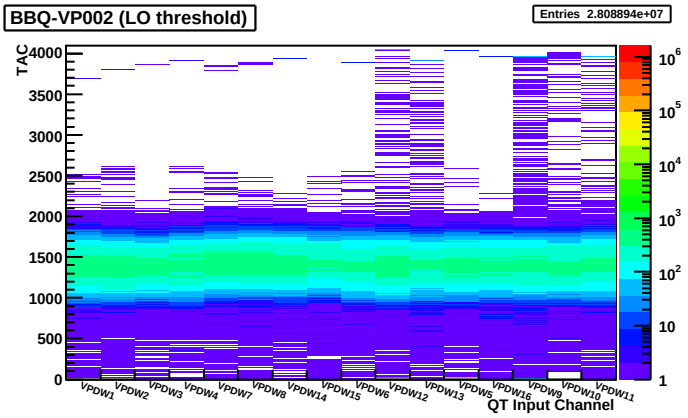

BBQ-ZD001 (ZDC TOWER)
Entries 2.808894e+07
ADC

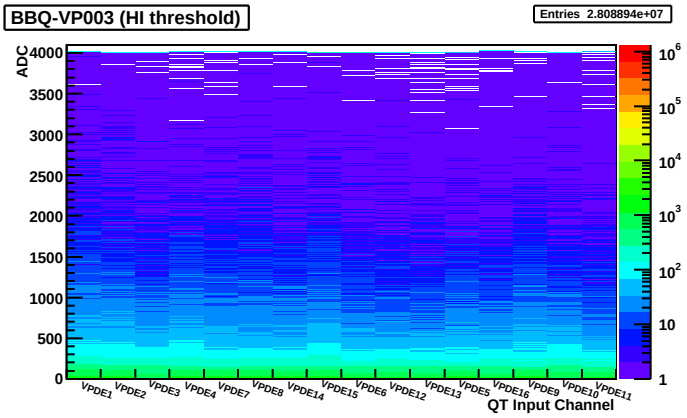

BBQ-ZD001 (ZDC TOWER)
Entries 2.808894e+07
TAC

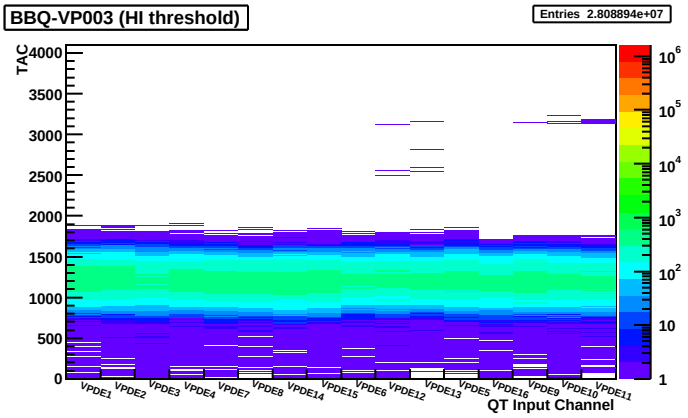

BBQ-VP001 (LO threshold)
Entries 2.808894e+07
ADC
QT Input Channel

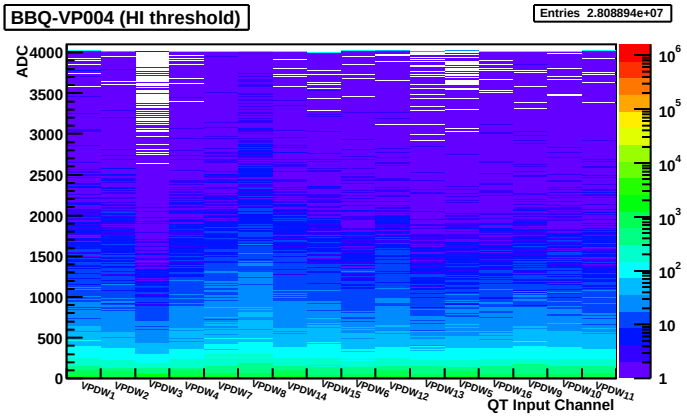

BBQ-VP001 (LO threshold)
Entries 2.808894e+07
TAC
QT Input Channel

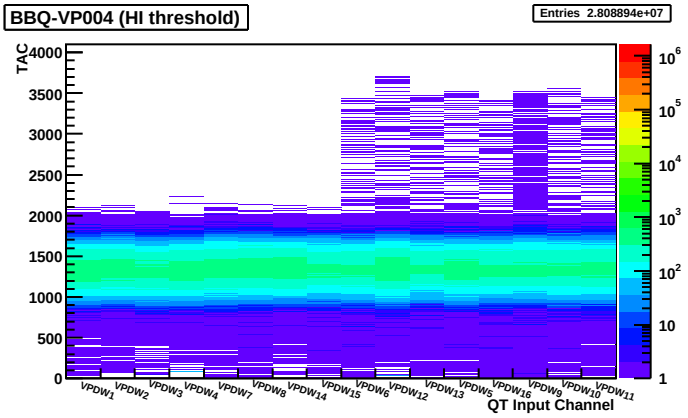

BBQ-VP002 (LO threshold)
Entries 2.808894e+07
ADC
QT Input Channel

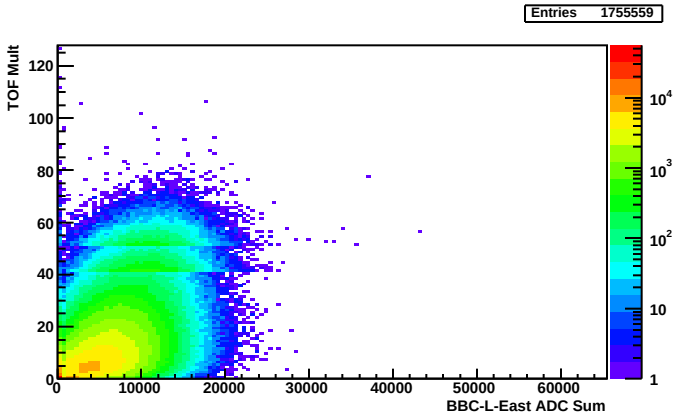

BBQ-VP002 (LO threshold)
Entries 2.808894e+07
TAC
QT Input Channel

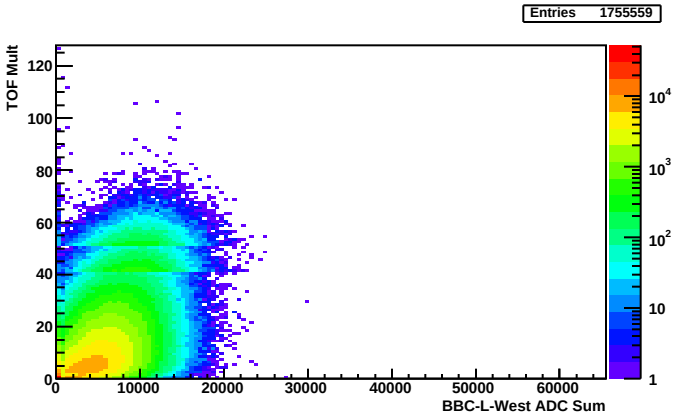

BBQ-VP003 (HI threshold)
Entries 2.808894e+07
ADC
QT Input Channel

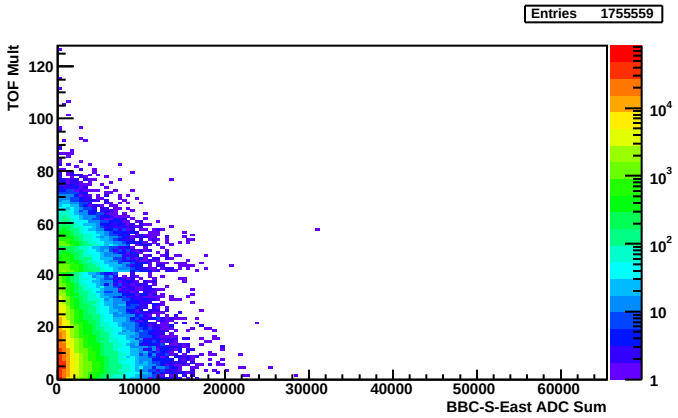

BBQ-VP003 (HI threshold)
Entries 2.808894e+07
TAC
QT Input Channel

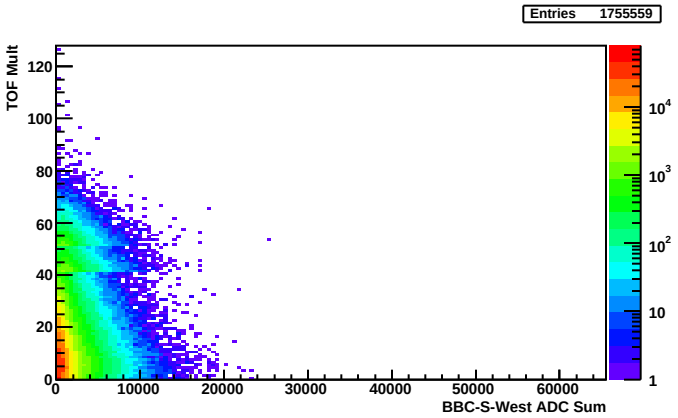

BBQ-VP004 (HI threshold)
Entries 2.808894e+07
ADC
QT Input Channel

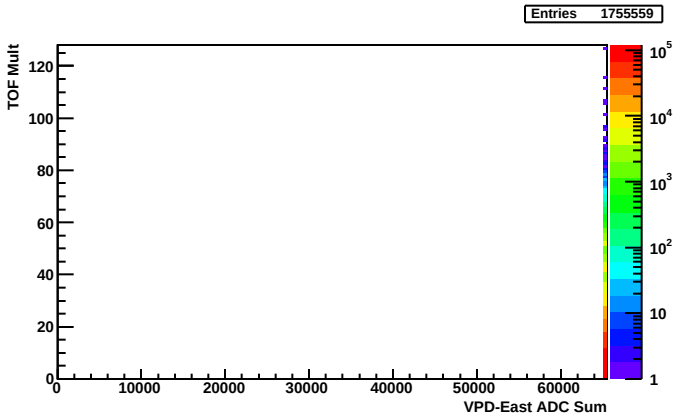

BBQ-VP004 (HI threshold)
Entries 2.808894e+07
TAC
QT Input Channel

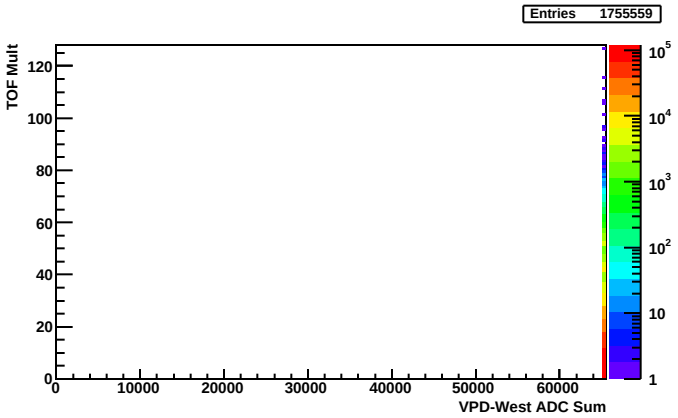

Entries 1755559
TOF Mult
BBC-L-East ADC Sum

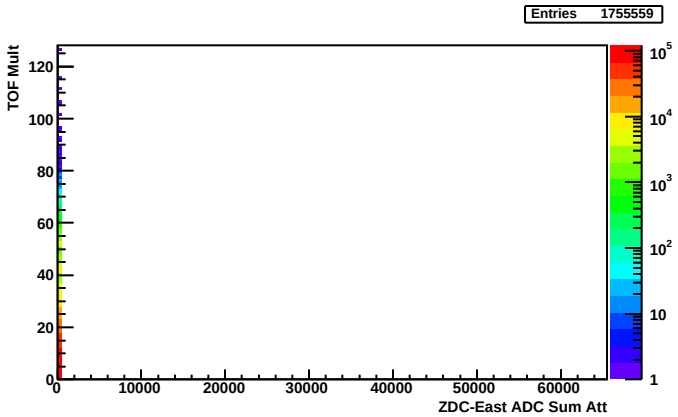

Entries 1755559
TOF Mult
BBC-L-West ADC Sum

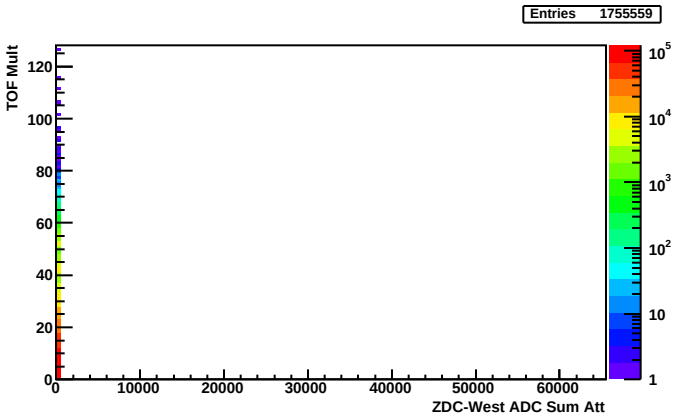

Entries 1755559
TOF Mult
BBC-S-East ADC Sum

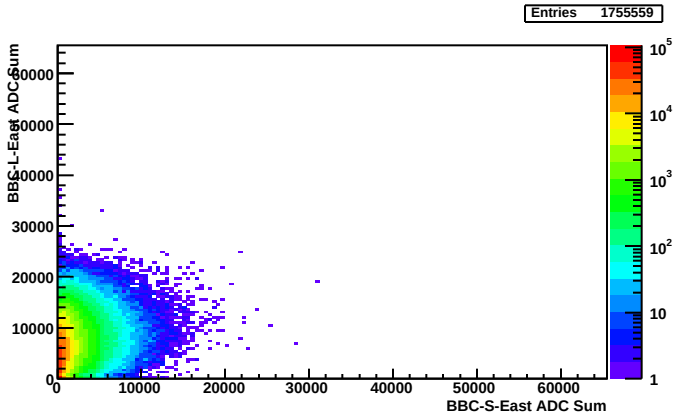

Entries 1755559
TOF Mult
BBC-S-West ADC Sum

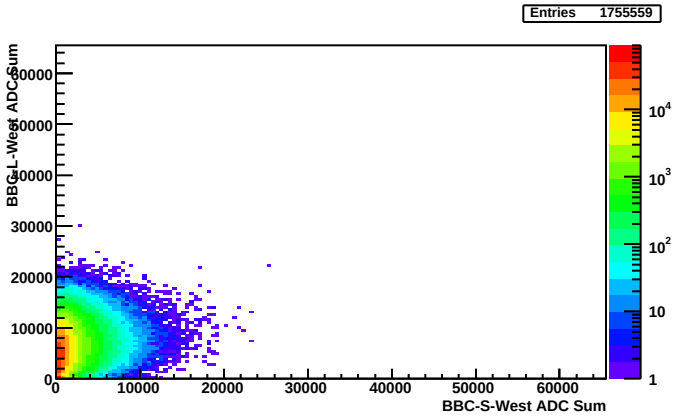

Entries 1755559
TOF Mult
VPD-East ADC Sum

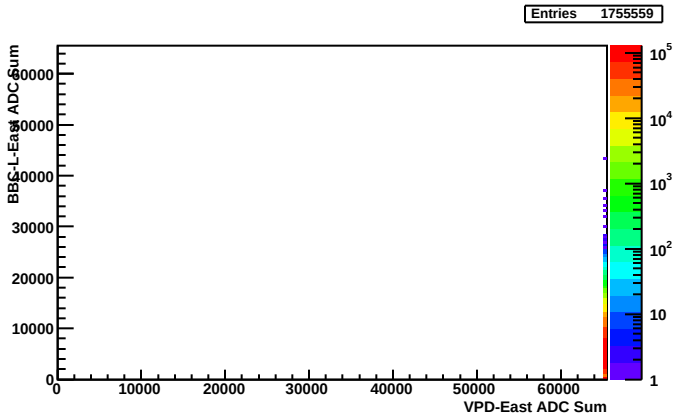

Entries 1755559
TOF Mult
VPD-West ADC Sum

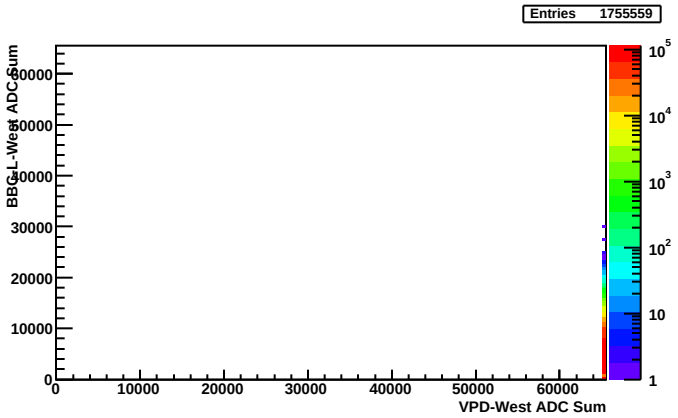

Entries 1755559
TOF Mult
ZDC-East ADC Sum Att

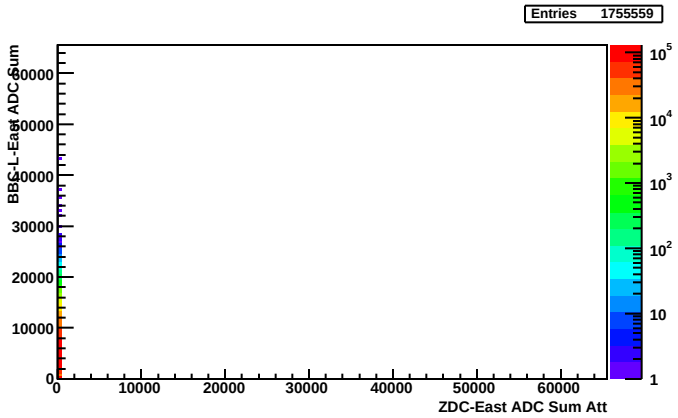

Entries 1755559
TOF Mult
ZDC-West ADC Sum Att

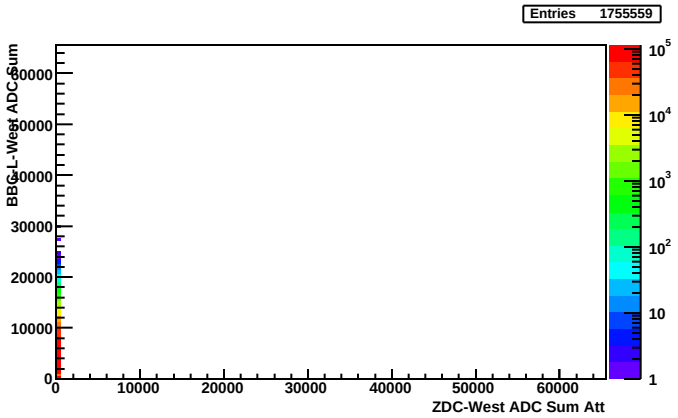

Entries 1755559
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1755559
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1755559
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 1755559
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 1755559
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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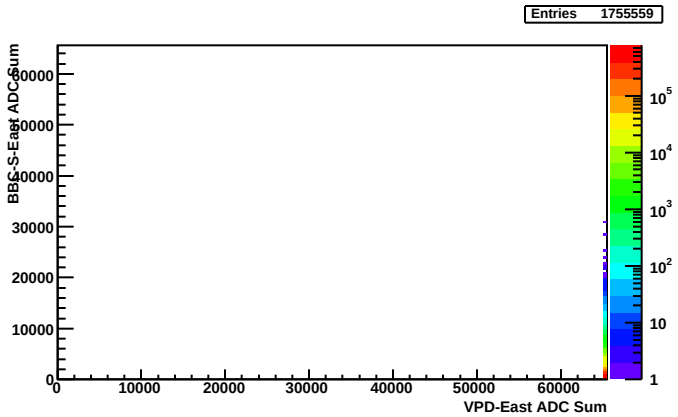
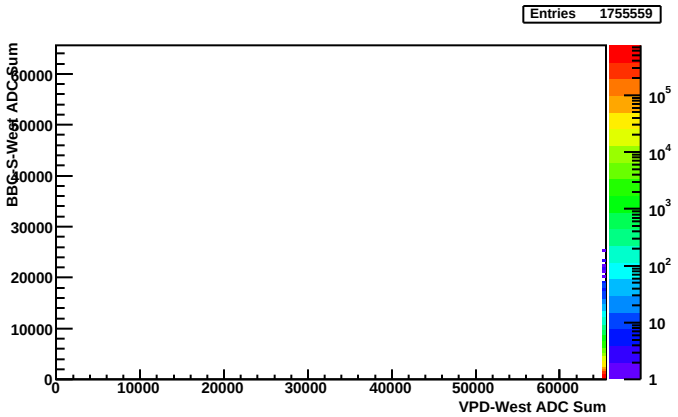
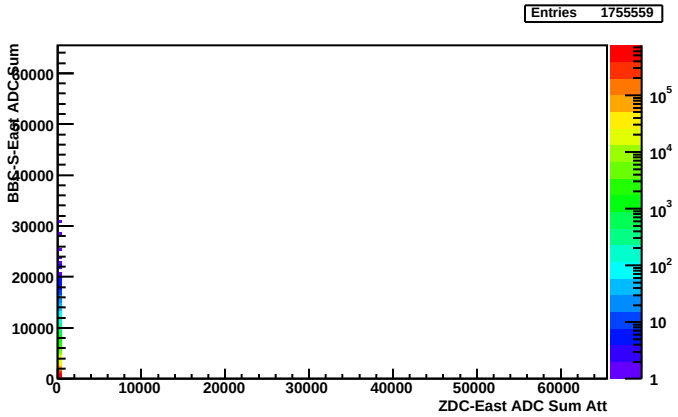
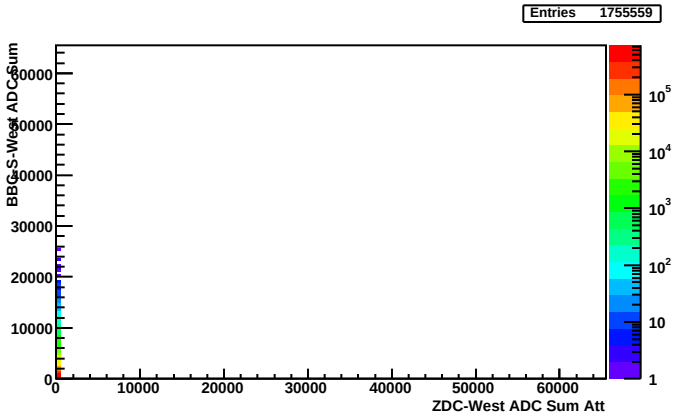
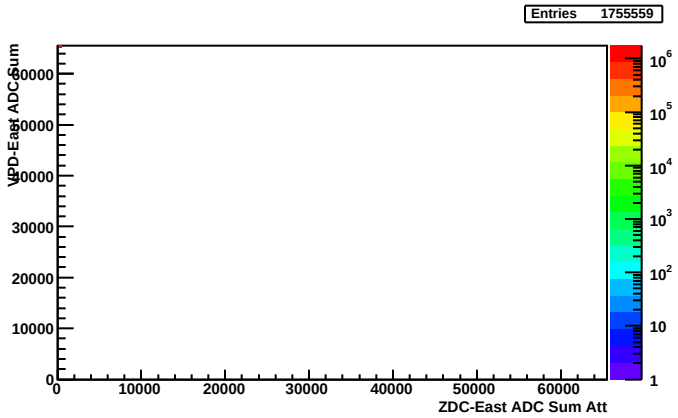
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

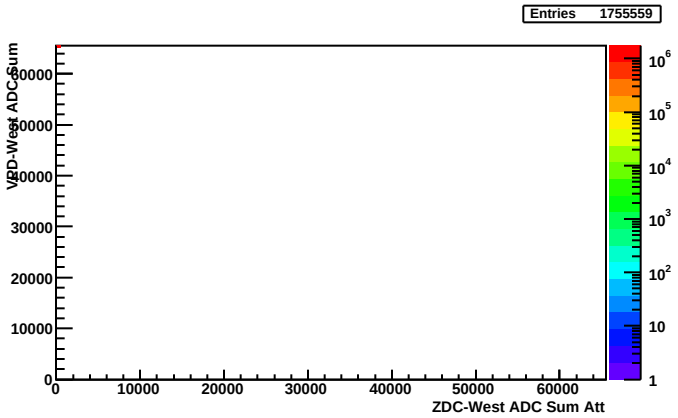

Entries 1755559
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1755559
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1755559
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1755559
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1755559
VPD-East ADC Sum
ZDC-East ADC Sum Att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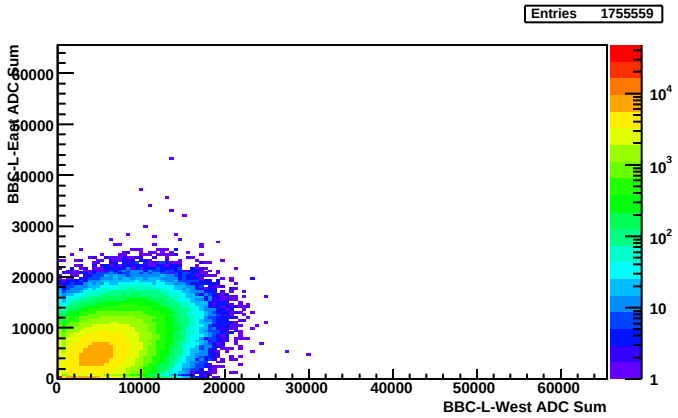
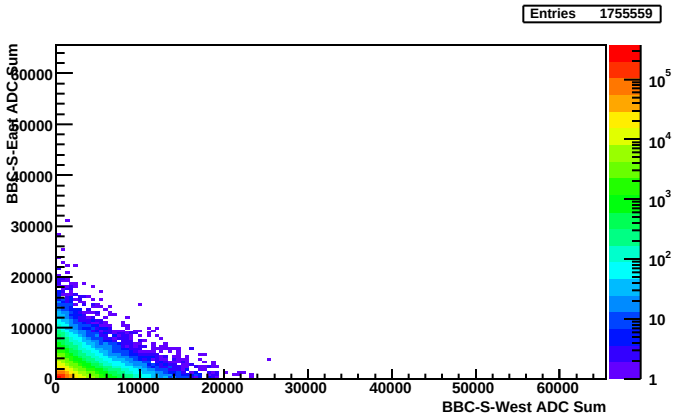
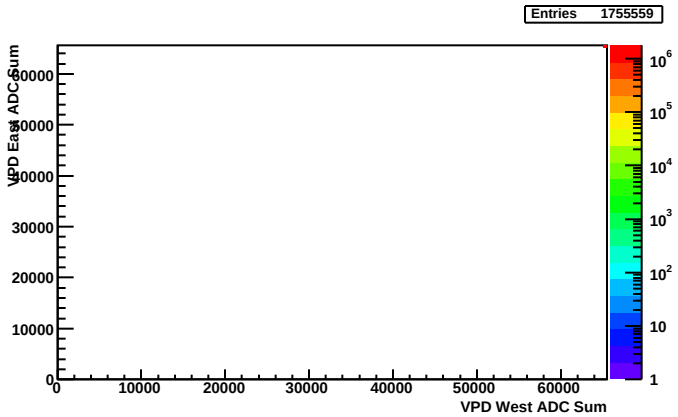
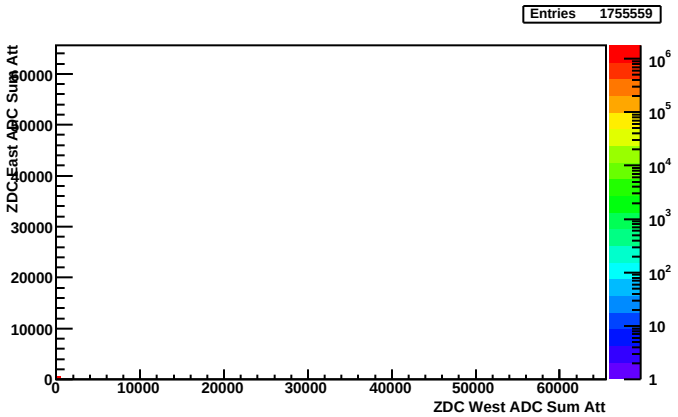
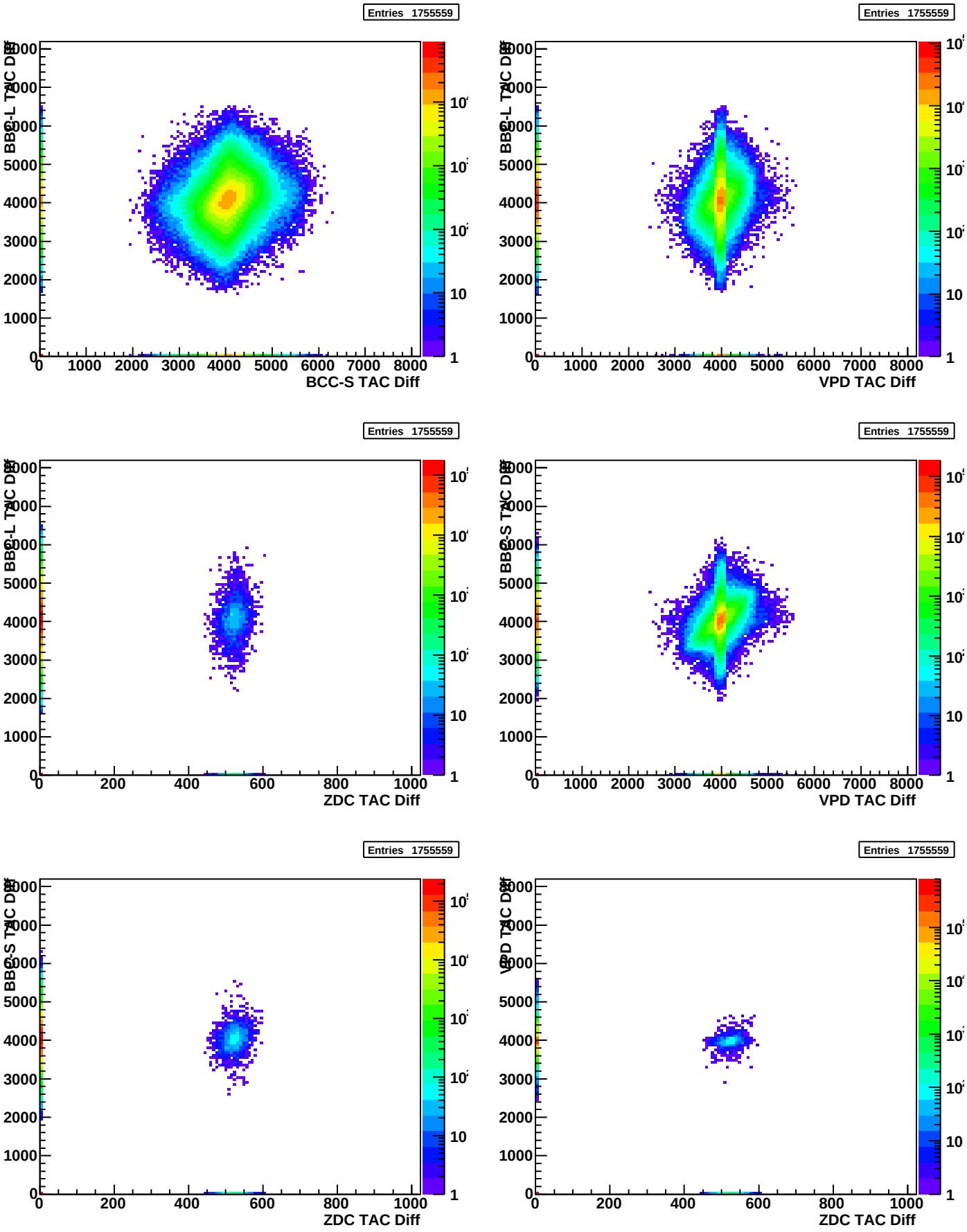
VPD-West ADC Sum
ZDC-West ADC Sum Att

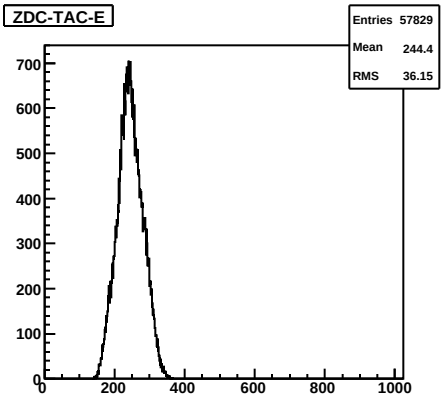

Entries 1755559
BBC-L-East ADC Sum
BBC-L-West ADC Sum

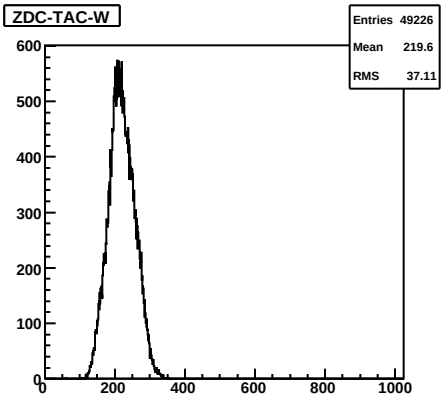

Entries 1755559
BBC-S-East ADC Sum
BBC-S-West ADC Sum

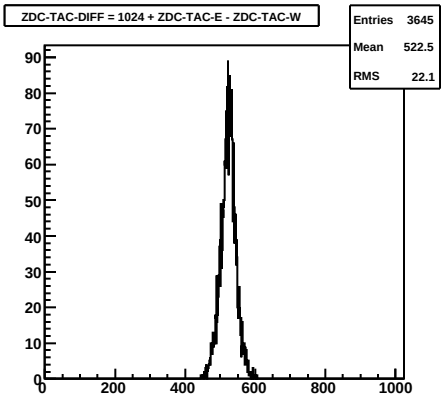

Entries 1755559
VPD East ADC Sum
VPD West ADC Sum

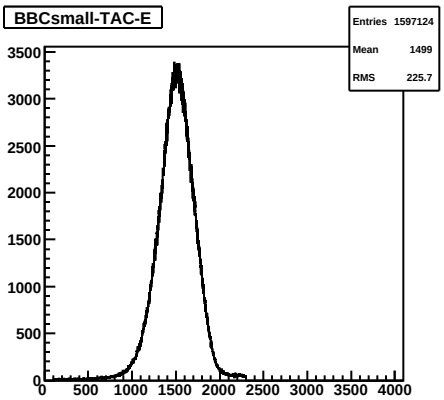

Entries 1755559
ZDC East ADC Sum Att
ZDC West ADC Sum Att

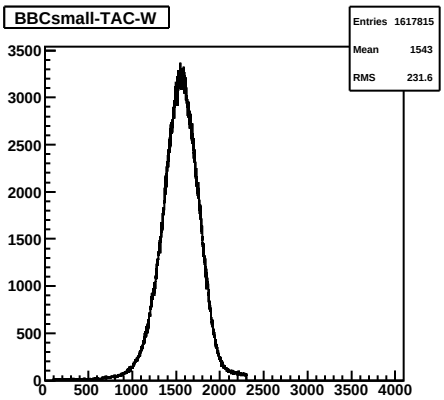

Entries 1755559
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1755559
BBC-L TAC Diff
VPD TAC Diff
Entries 1755559
BBC-L TAC Diff
ZDC TAC Diff
Entries 1755559
BBC-S TAC Diff
VPD TAC Diff
Entries 1755559
BBC-S TAC Diff
ZDC TAC Diff
Entries 1755559
VPD TAC Diff
ZDC TAC Diff

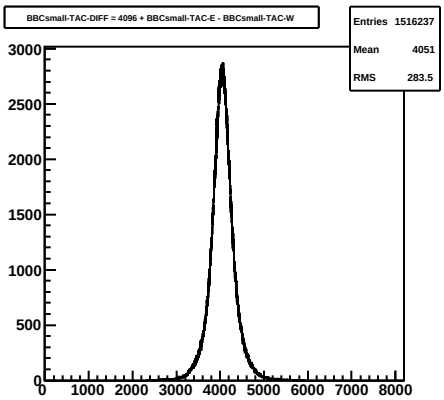

ZDC-TAC-E
Entries 57829
Mean 244.4
RMS 36.15

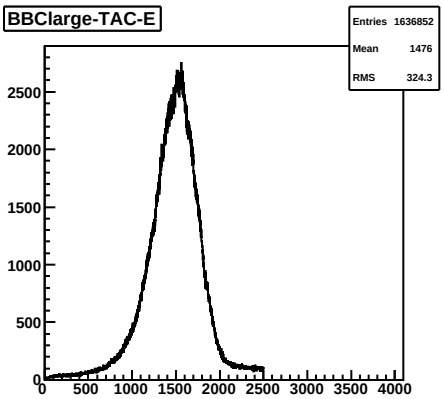

ZDC-TAC-W
Entries 49226
Mean 219.6
RMS 37.11

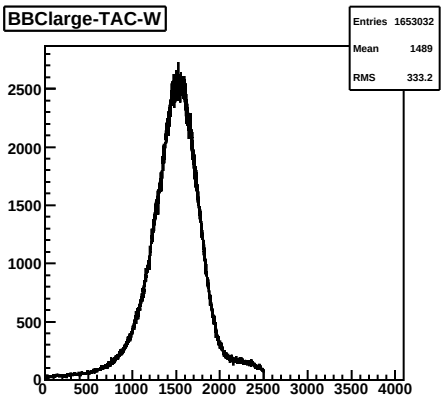

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3645
Mean 522.5
RMS 22.1

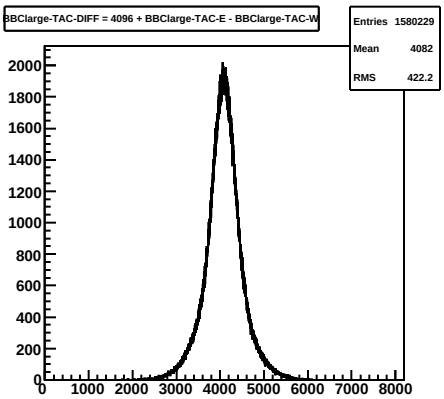

BBCsmall-TAC-E
Entries 1597124
Mean 1499
RMS 225.7

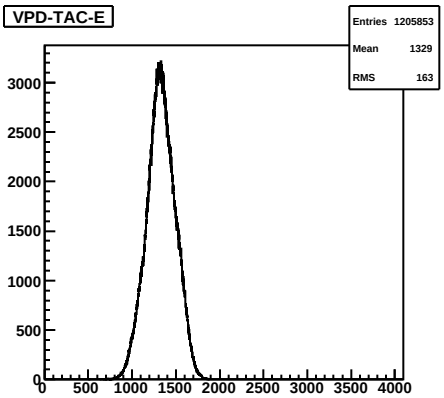

BBCsmall-TAC-W
Entries 1617815
Mean 1543
RMS 231.6

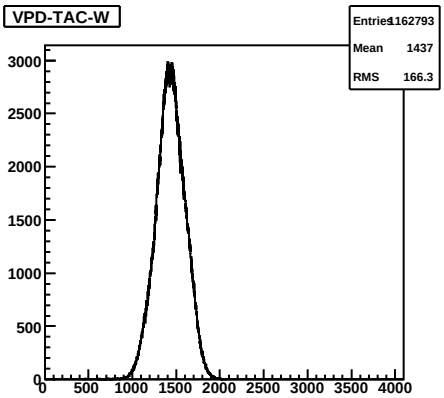

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1516237
Mean 4051
RMS 283.5

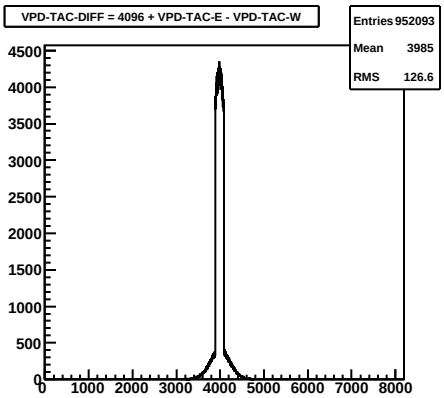

BBClarge-TAC-E
Entries 1636852
Mean 1476
RMS 324.3

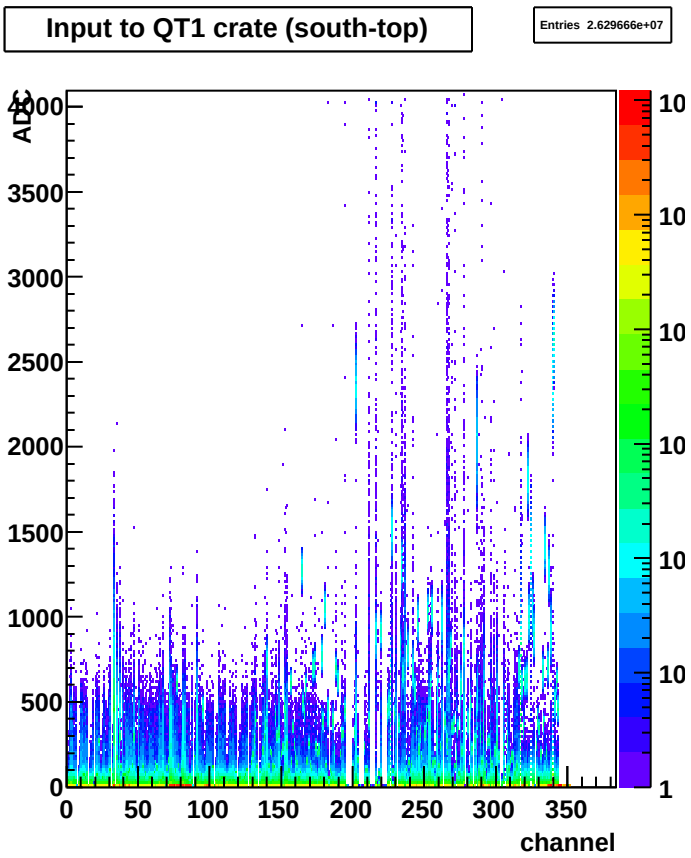

BBClarge-TAC-W
Entries 1653032
Mean 1489
RMS 333.2

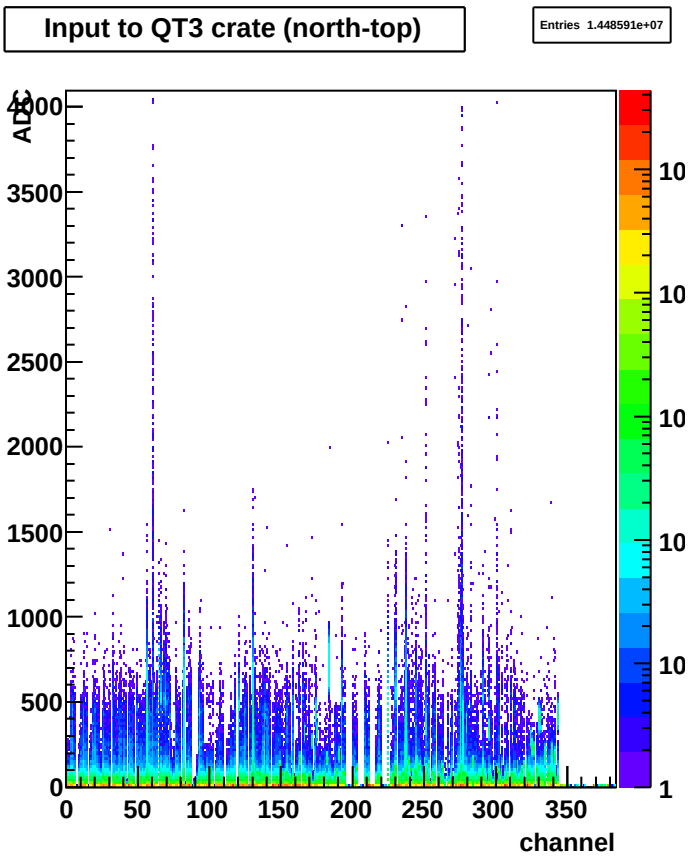

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1580229
Mean 4082
RMS 422.2

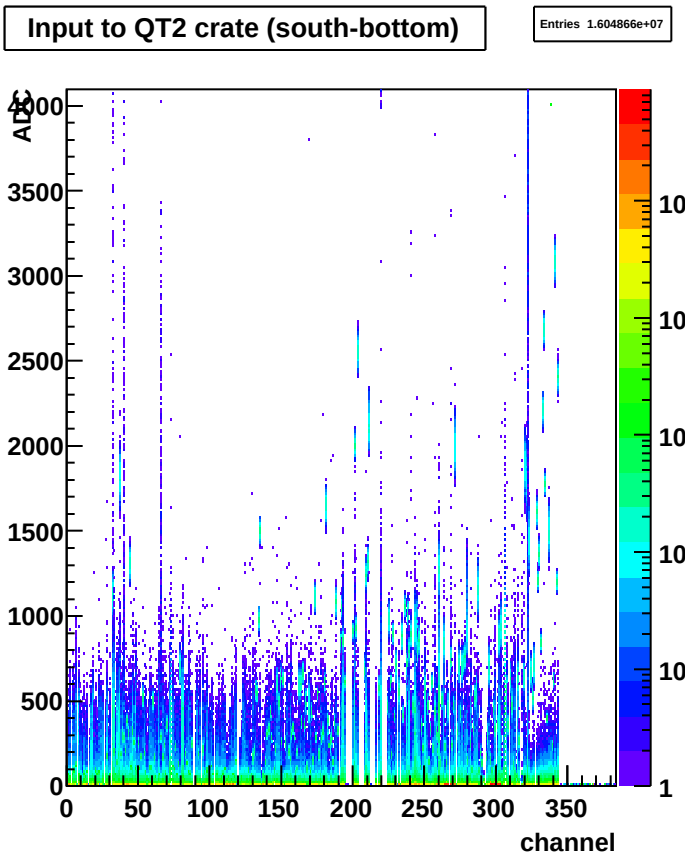

VPD-TAC-E
Entries 1205853
Mean 1329
RMS 163

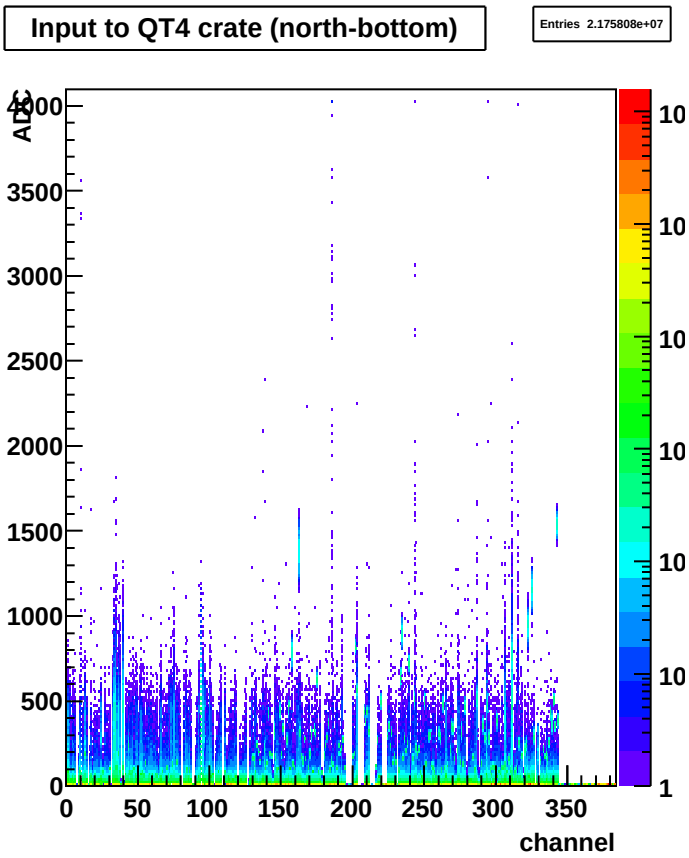

VPD-TAC-W
Entries 1162793
Mean 1437
RMS 166.3

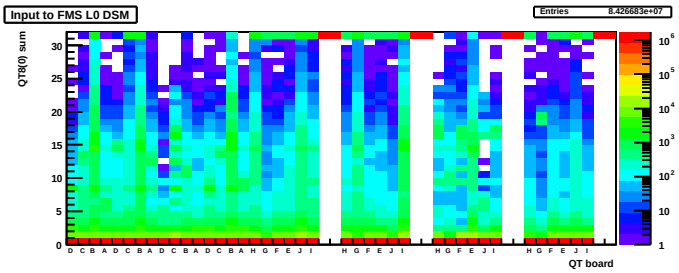

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 952093
Mean 3985
RMS 126.6

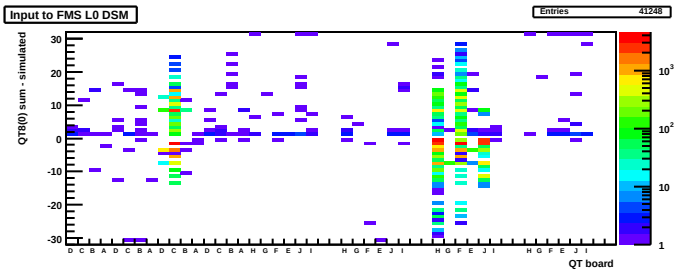

Input to QT1 crate (south-top)
Entries 2.629666e+07
ADC
channel

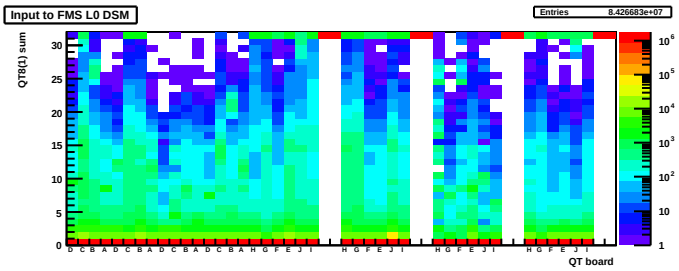

Input to QT3 crate (north-top)
Entries 1.448591e+07
ADC
channel

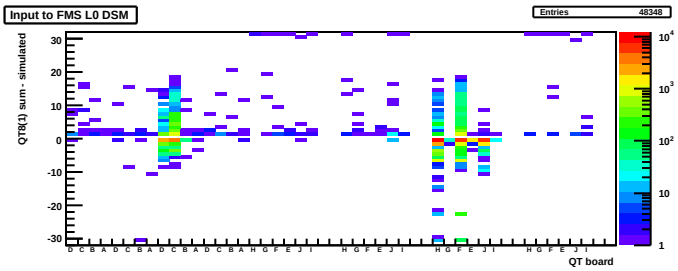

Input to QT2 crate (south-bottom)
Entries 1.604866e+07
ADC
channel

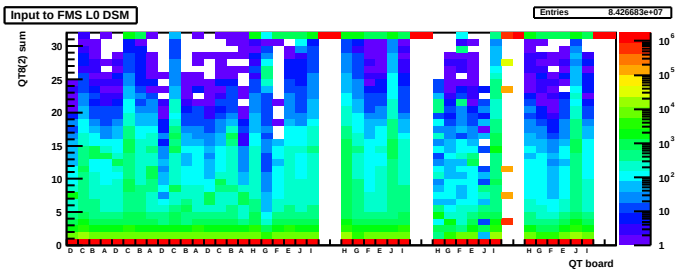

Input to QT4 crate (north-bottom)
Entries 2.175808e+07
ADC
channel

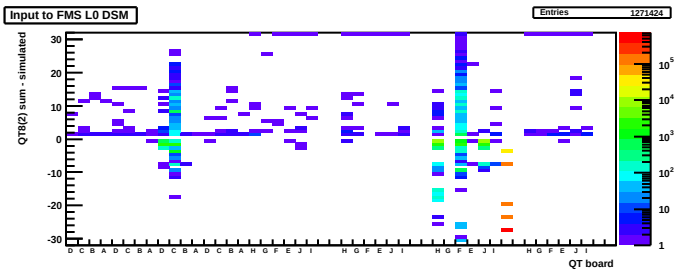

Input to FMS L0 DSM
Entries 8.426683e+07
QT8(0) sum
QT board

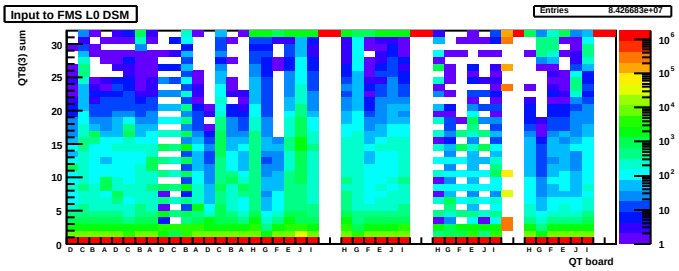

Input to FMS L0 DSM
Entries 41248
QT8(0) sum - simulated
QT board

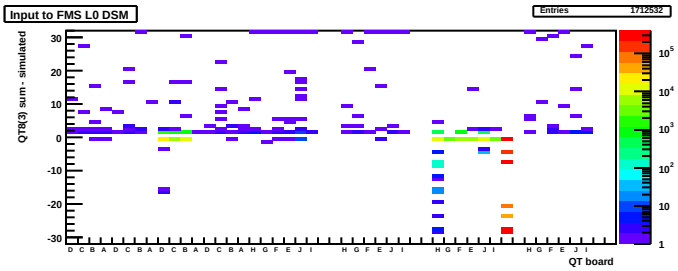

Input to FMS L0 DSM
Entries 8.426683e+07
QT8(1) sum
QT board

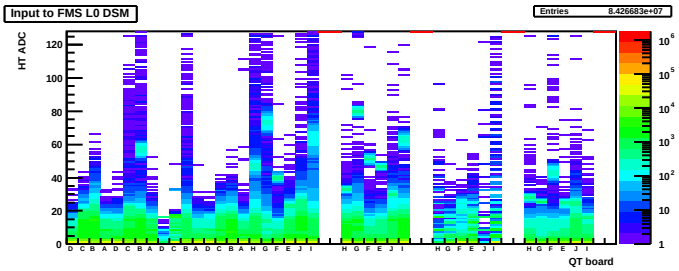

Input to FMS L0 DSM
Entries 48348
QT8(1) sum - simulated
QT board

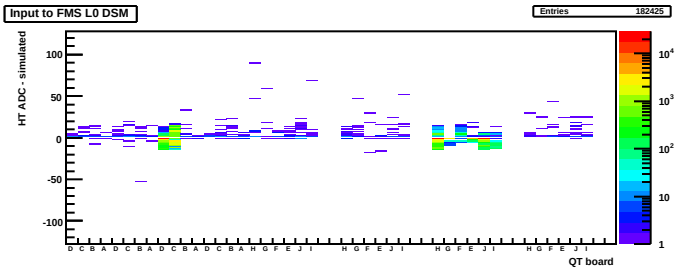

Input to FMS L0 DSM
Entries 8.426683e+07
QT8(2) sum
QT board

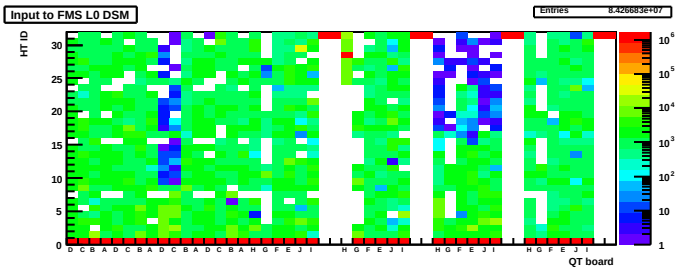

Input to FMS L0 DSM
Entries 1271424
QT8(2) sum - simulated
QT board

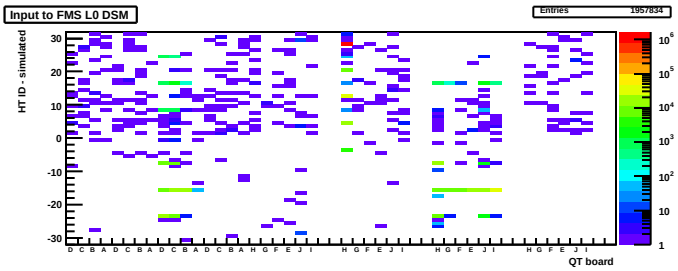

Input to FMS L0 DSM
Entries 8.426683e+07
QT8(3) sum
QT board

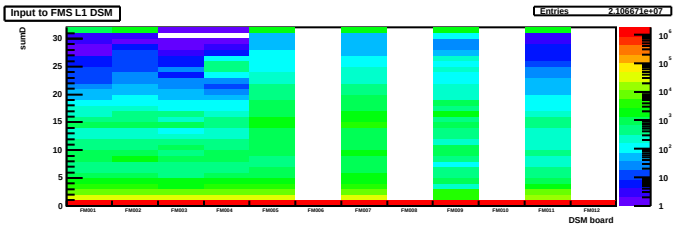

Input to FMS L0 DSM
Entries 1712532
QT8(3) sum - simulated
QT board

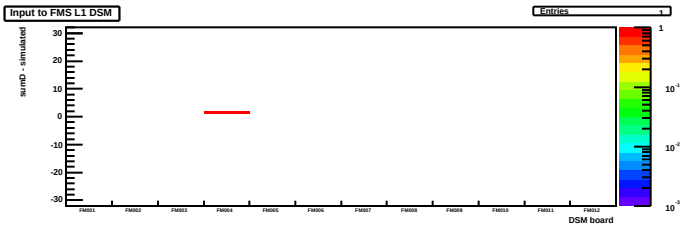

Input to FMS L0 DSM
Entries 8.426683e+07
HT ADC
QT board

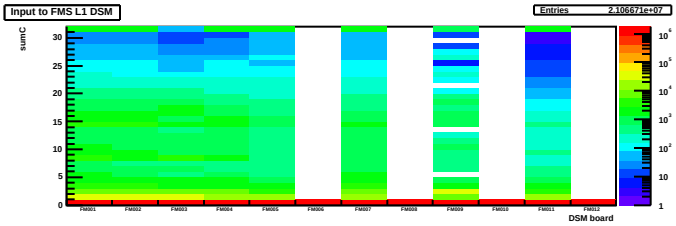

Input to FMS L0 DSM
Entries 182425
HT ADC - simulated
QT board

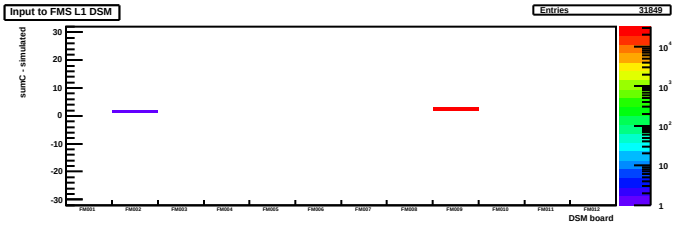

Input to FMS L0 DSM
Entries 8.426683e+07
HT ID
QT board

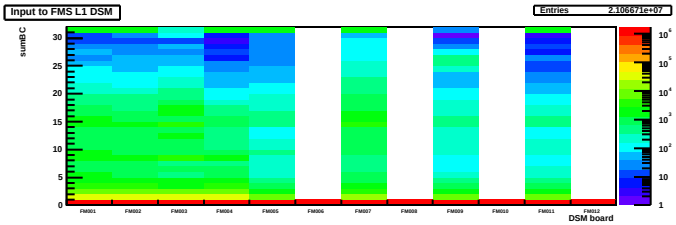

Input to FMS L0 DSM
Entries 1957834
HT ID - simulated
QT board

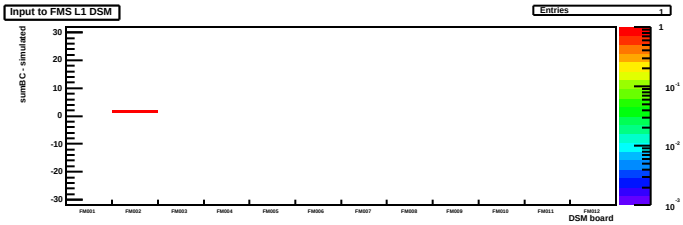

Input to FMS L1 DSM
Entries 2.106671e+07
sumD
DSM board

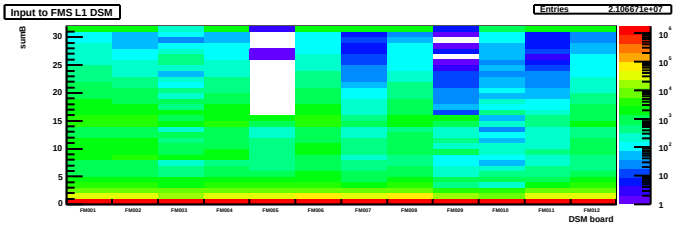

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

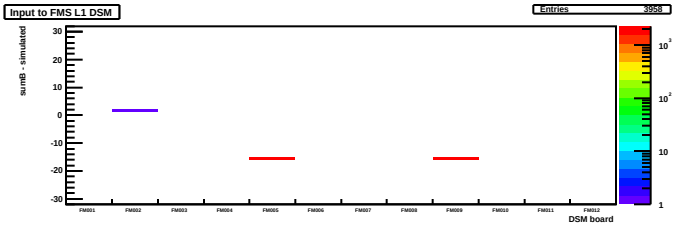

Input to FMS L1 DSM
Entries 2.106671e+07
sumC
DSM board

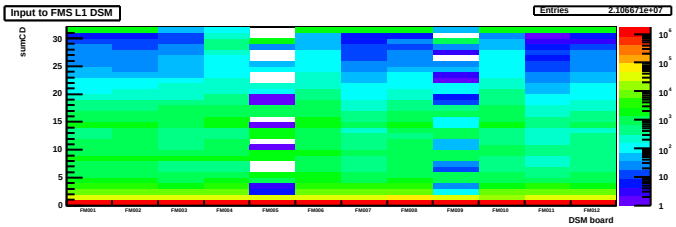

Input to FMS L1 DSM
Entries 31849
sumC - simulated
DSM board

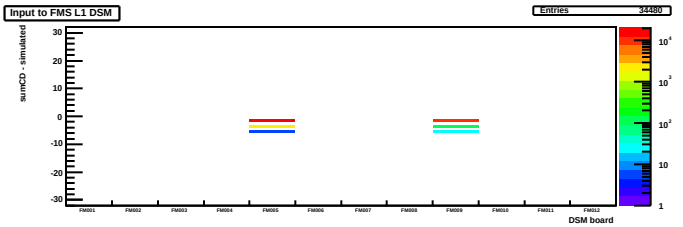

Input to FMS L1 DSM
Entries 2.106671e+07
sumBC
DSM board

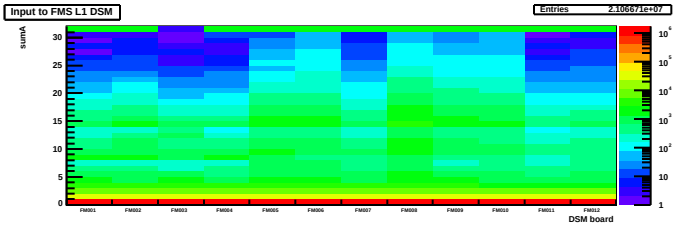

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

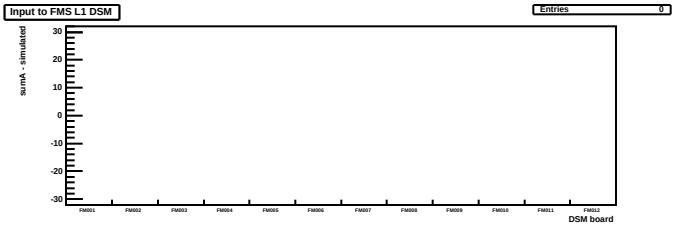

Input to FMS L1 DSM
Entries 2.106671e+07
sumB
DSM board

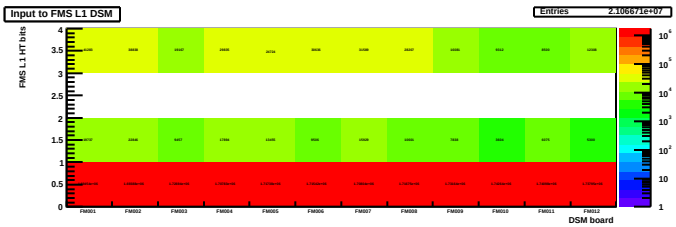

Input to FMS L1 DSM
Entries 3958
sumB - simulated
DSM board

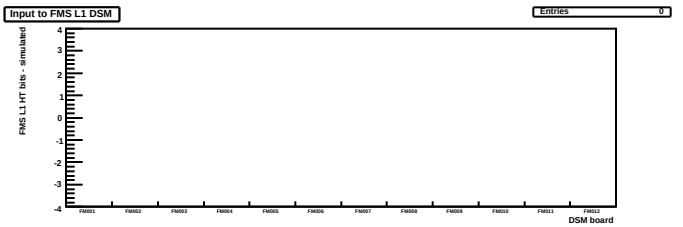

Input to FMS L1 DSM
Entries 2.106671e+07
sumCD
DSM board

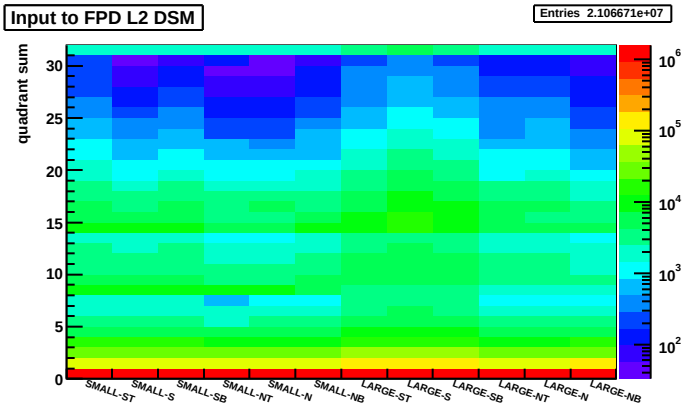

Input to FMS L1 DSM
Entries 34480
sumCD - simulated
DSM board

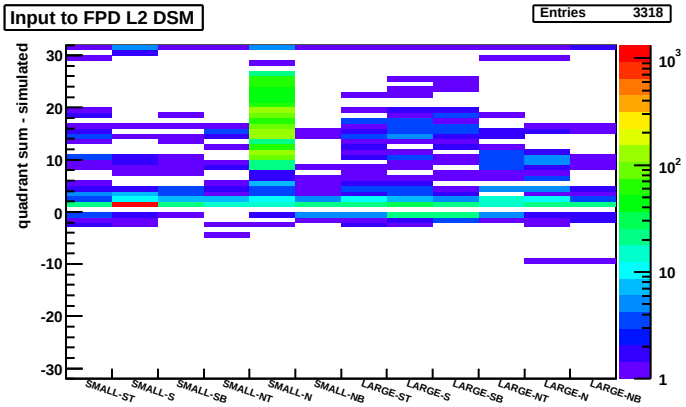

Input to FMS L1 DSM
Entries 2.106671e+07
sumA
DSM board

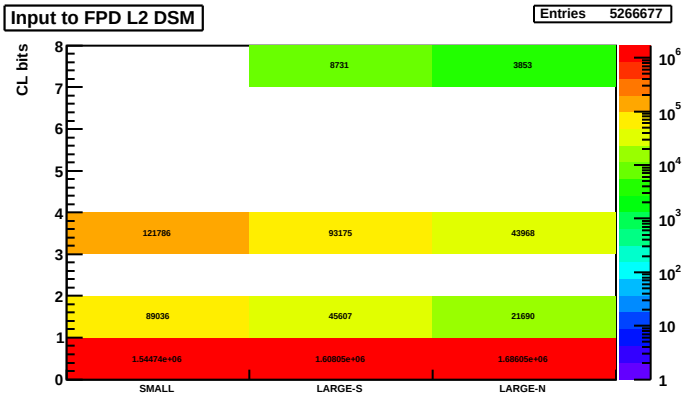

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

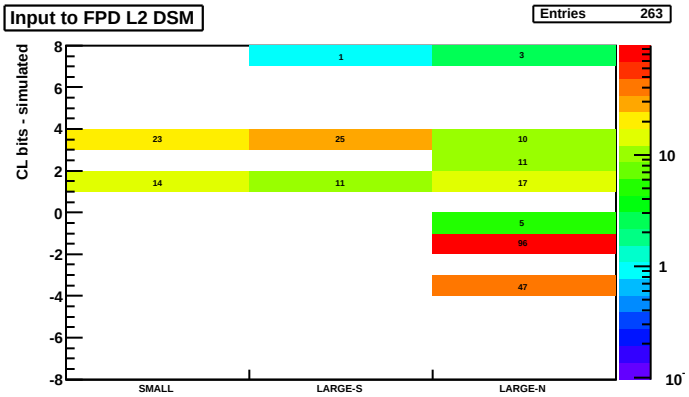

Input to FMS L1 DSM
Entries 2.106671e+07
FMS L1 HT bits
DSM board

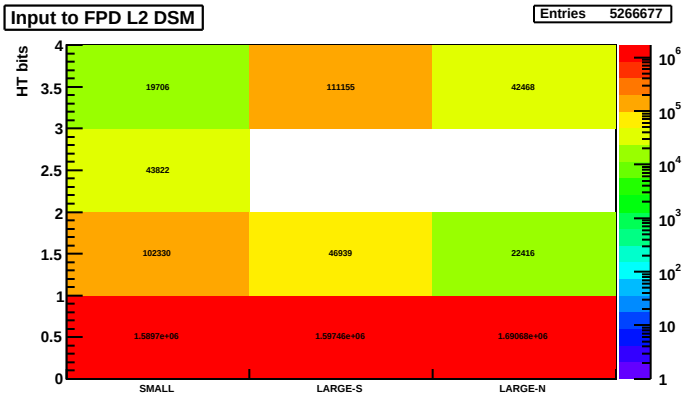

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

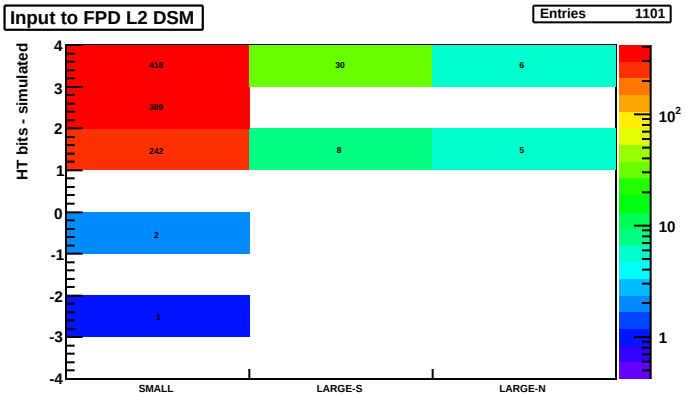

Input to FPD L2 DSM
Entries 2.106671e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

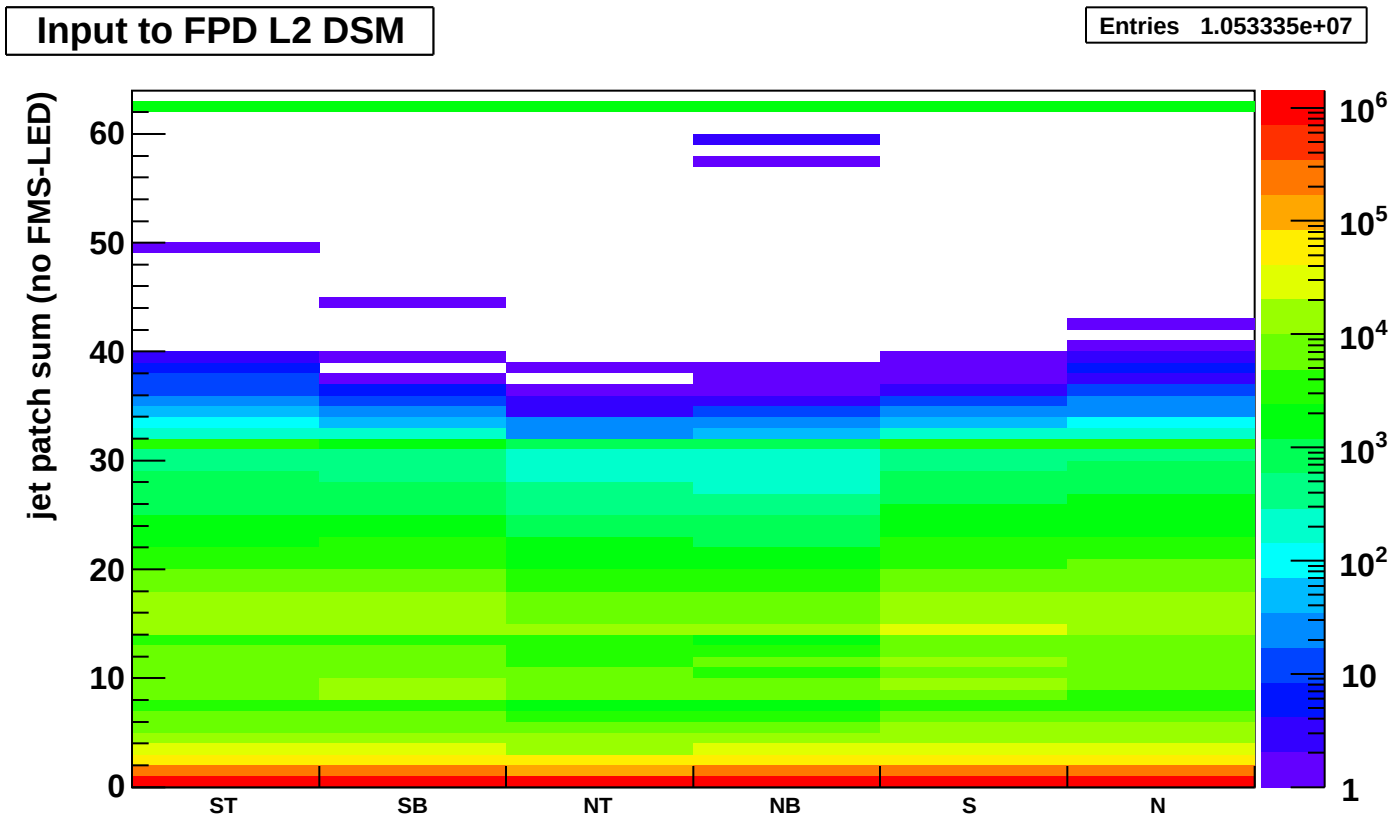

Input to FPD L2 DSM
Entries 3318
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

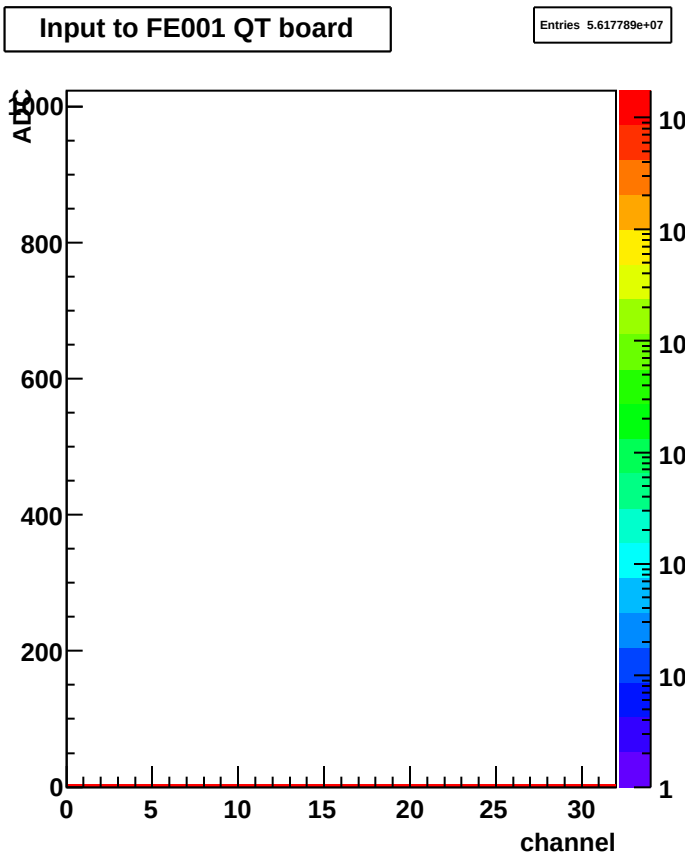

Input to FPD L2 DSM
Entries 5266677
CL bits
8731
3853
121786
93175
43968
89036
45607
21690
1.54474e+06
1.60805e+06
1.68605e+06
SMALL
LARGE-S
LARGE-N

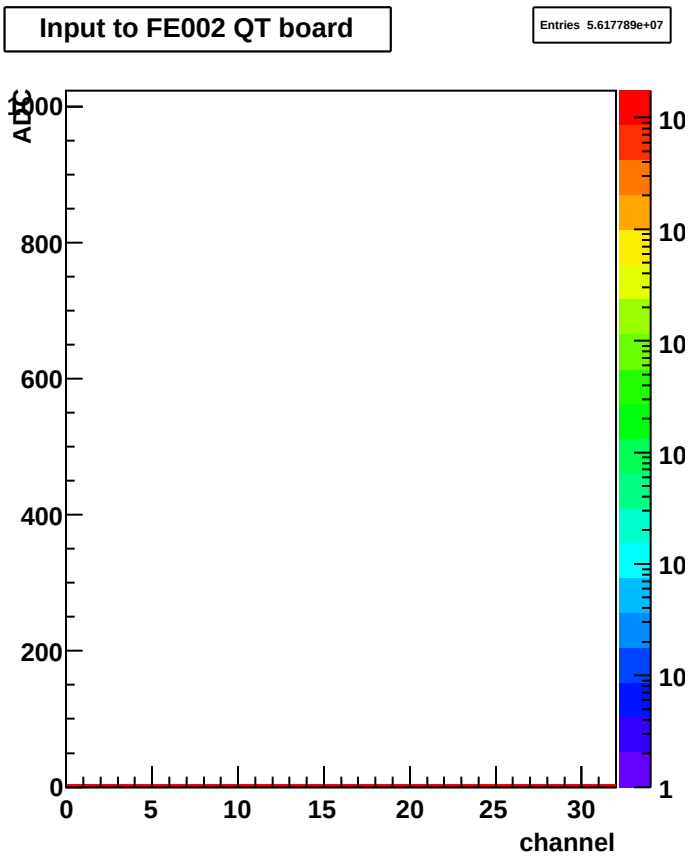

Input to FPD L2 DSM
Entries 263
CL bits - simulated
1
3
23
25
10
11
14
11
17
5
96
47
SMALL
LARGE-S
LARGE-N

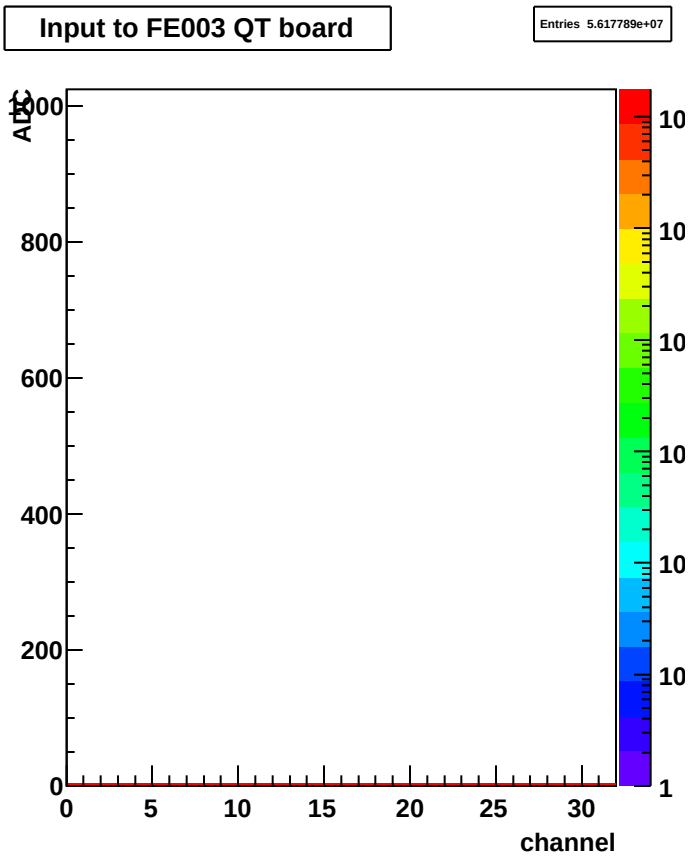

Input to FPD L2 DSM
Entries 5266677
HT bits
19706
111155
42468
43822
102330
46939
22416
1.5897e+06
1.59746e+06
1.69068e+06
SMALL
LARGE-S
LARGE-N

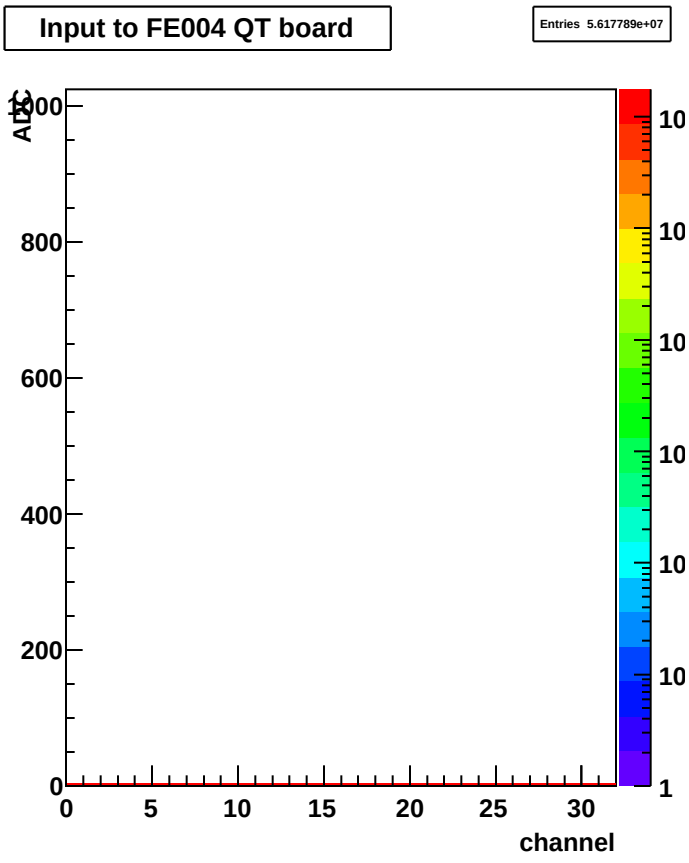

Input to FPD L2 DSM
Entries 1101
HT bits - simulated
418
30
6
389
242
8
5
2
1
SMALL
LARGE-S
LARGE-N

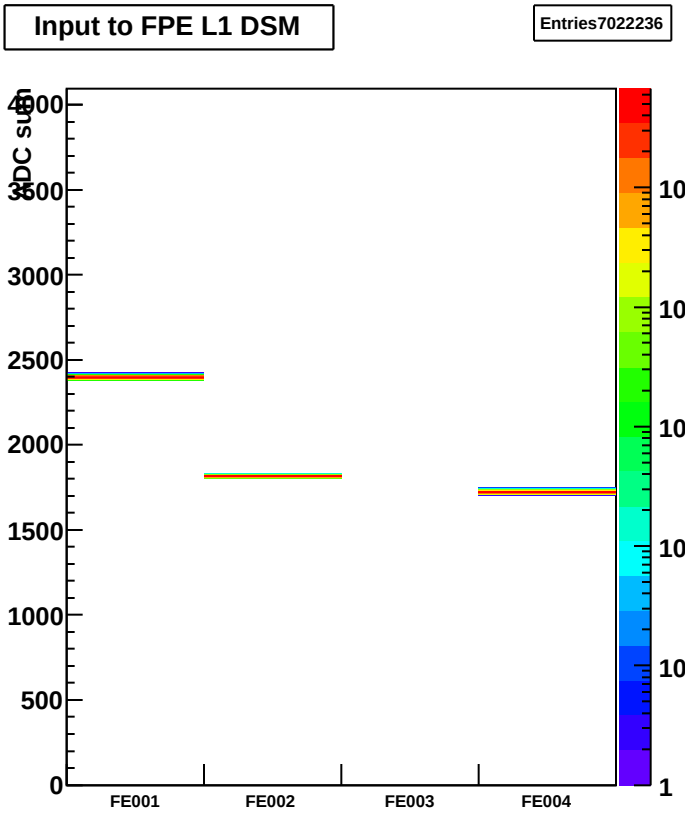

Input to FPD L2 DSM
Entries 1.053335e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 5.617789e+07
ADC
channel

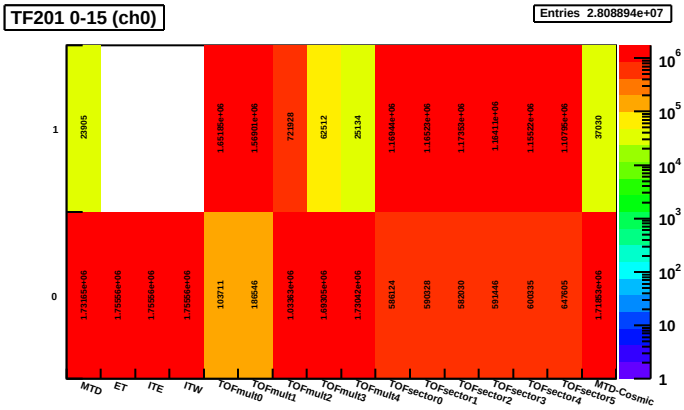

Input to FE002 QT board
Entries 5.617789e+07
ADC
channel


Input to FE003 QT board
Entries 5.617789e+07
ADC
channel

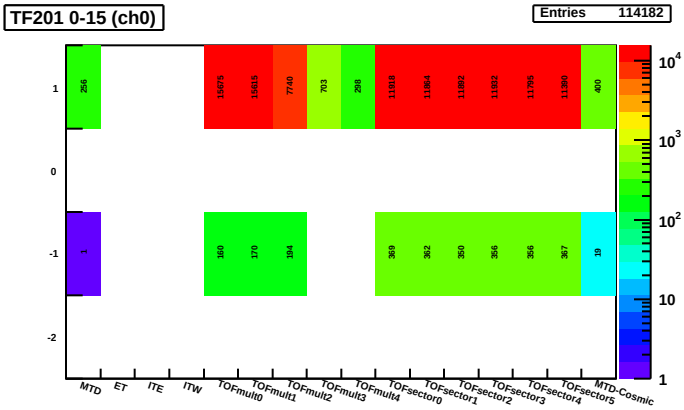

Input to FE004 QT board
Entries 5.617789e+07
ADC
channel


Input to FPE L1 DSM
Entries7022236
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

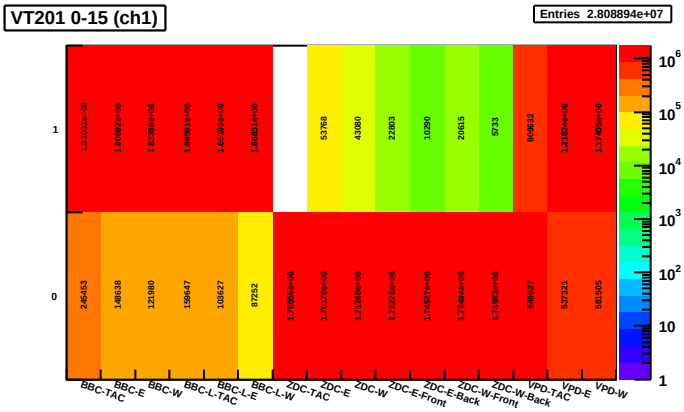
TF201 0-15 (ch0)



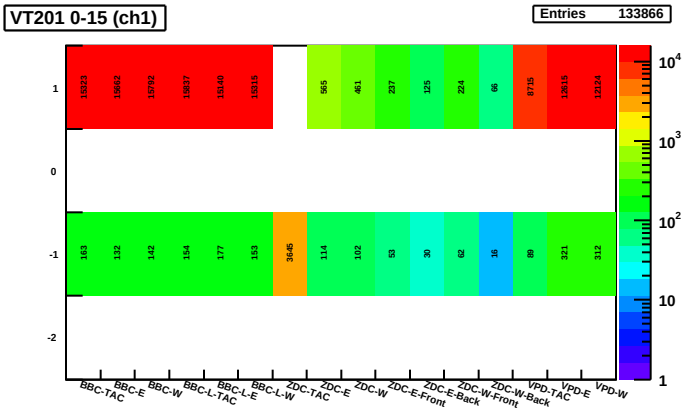
TF201 0-15 (ch0)



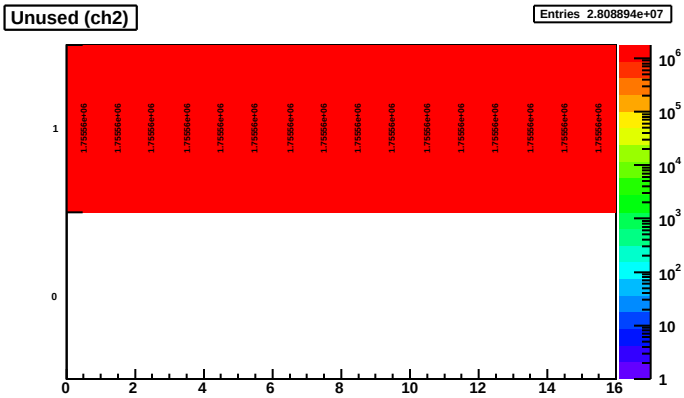
VT201 0-15 (ch1)

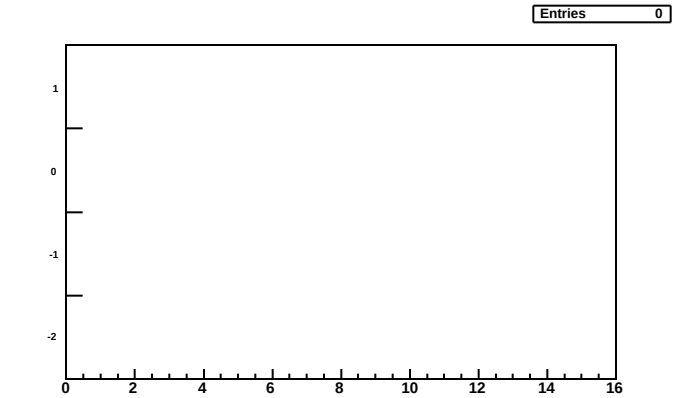


VT201 0-15 (ch1)

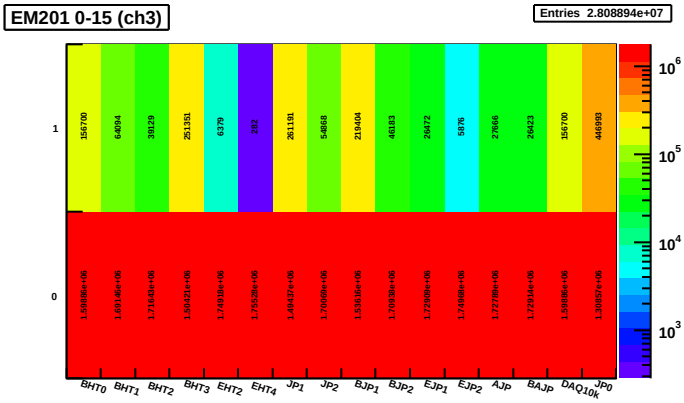


Unused (ch2)

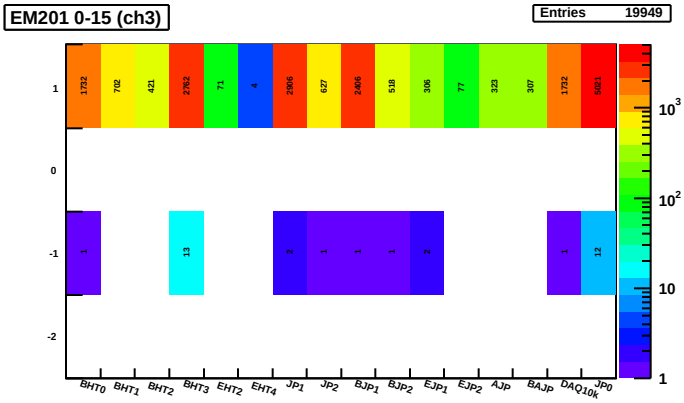


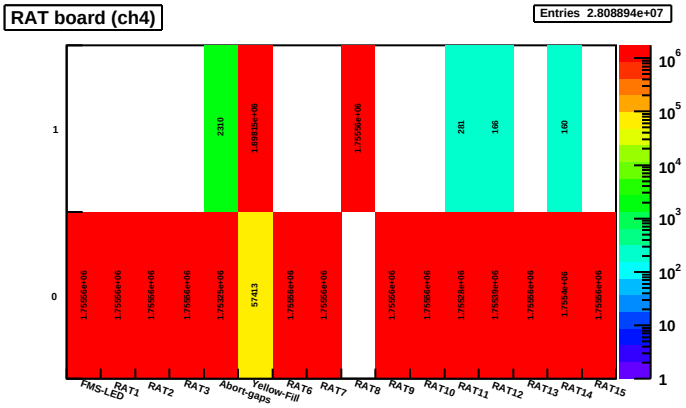


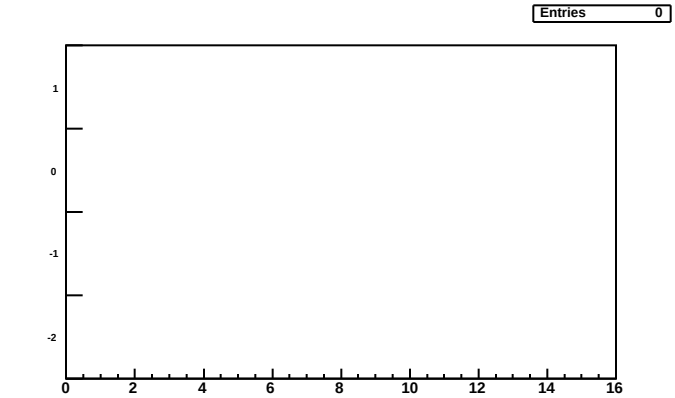
EM201 0-15 (ch3)

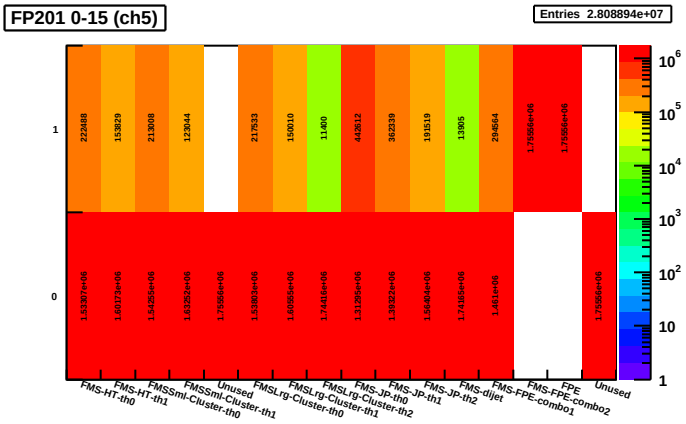


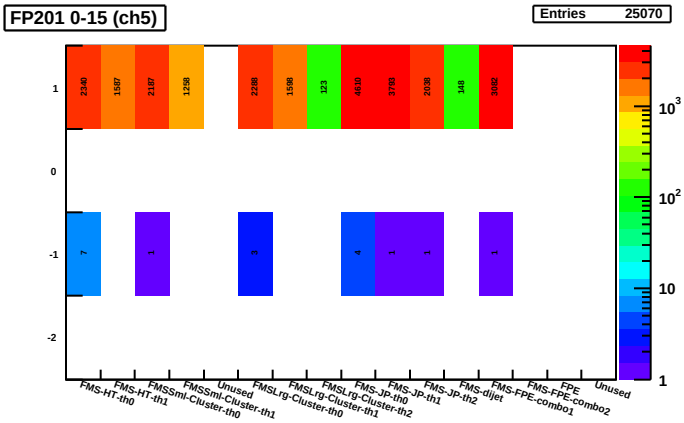
EM201 0-15 (ch3)

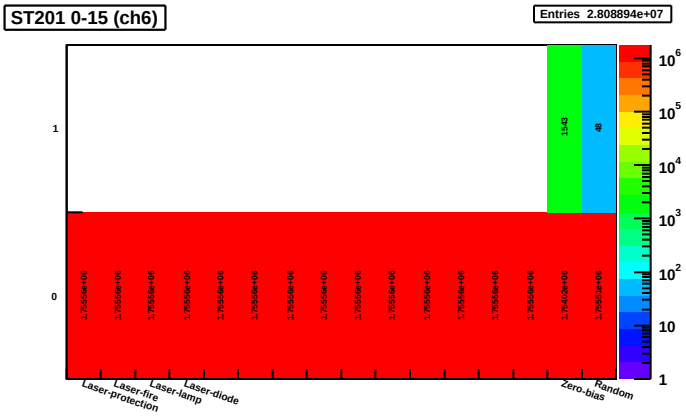


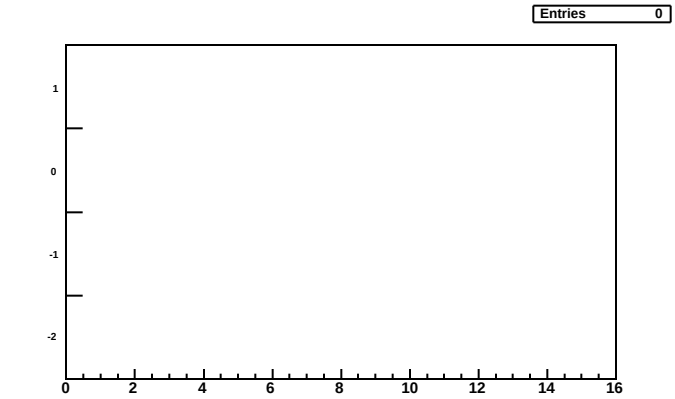

RAT board (ch4)
Entries 2.808894e+07
FMS-LED
RAT1
RAT2
RAT3
Abort-gaps
Yellow-Fill
RAT6
RAT7
RAT8
RAT9
RAT10
RAT11
RAT12
RAT13
RAT14
RAT15

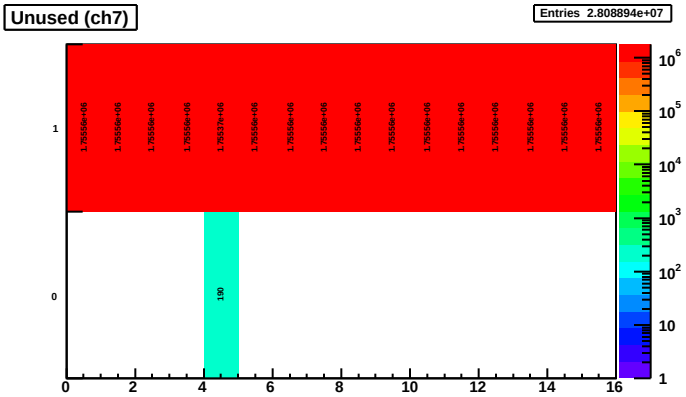

Entries 0

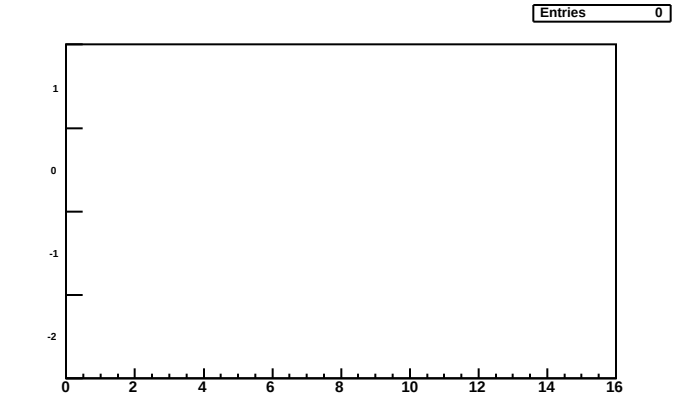

FP201 0-15 (ch5)
Entries 2.808894e+07
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
Unused
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diijet
FMS-FPE-combo1
FPE
FMS-FPE-combo2
Unused


FP201 0-15 (ch5)
Entries 25070
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
Unused
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diijet
FMS-FPE-combo1
FPE
FMS-FPE-combo2
Unused


ST201 0-15 (ch6)
Entries 2.808894e+07
Laser-protection
Laser-fire
Laser-lamp
Laser-diode
Zero-bias
Random


Entries 0


Unused (ch7)
Entries 2.808894e+07


Entries 0