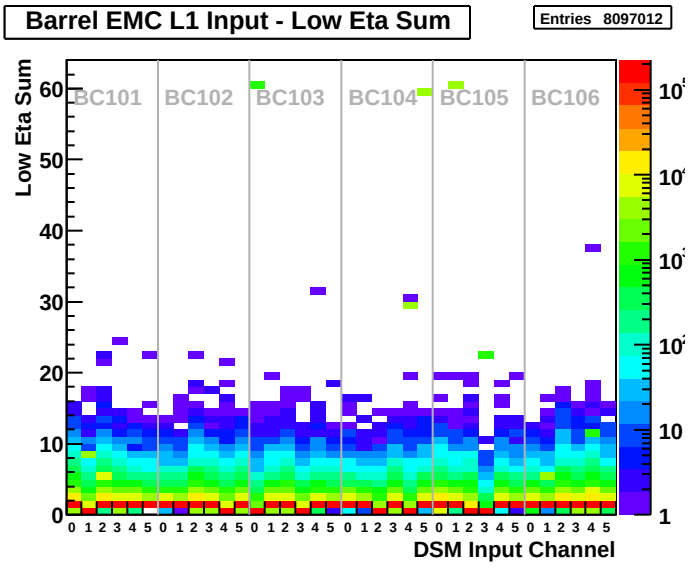
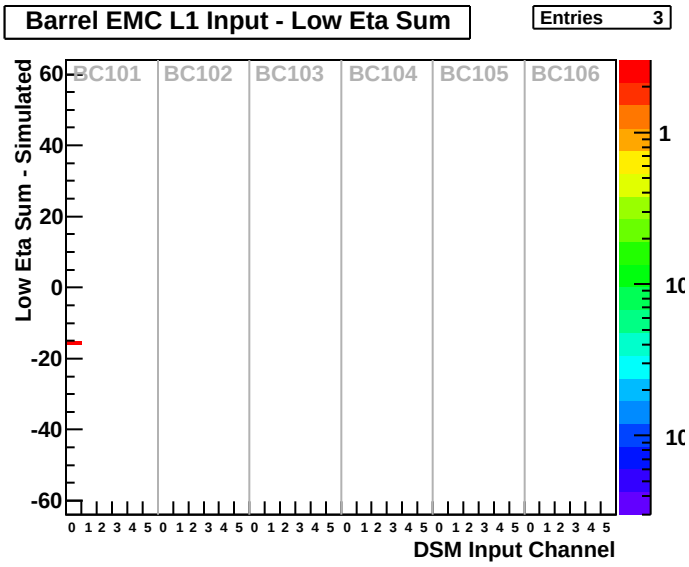
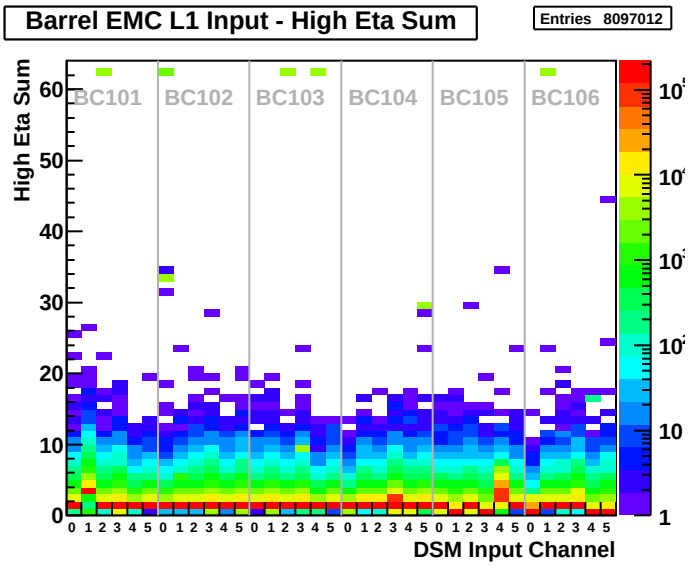
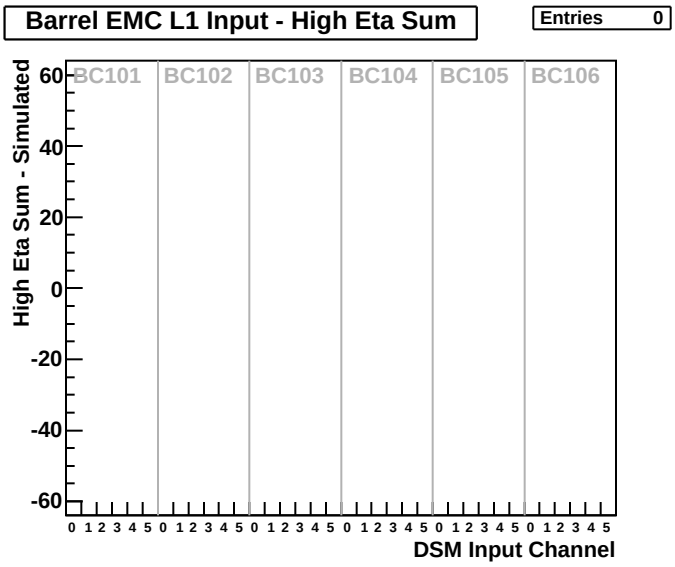
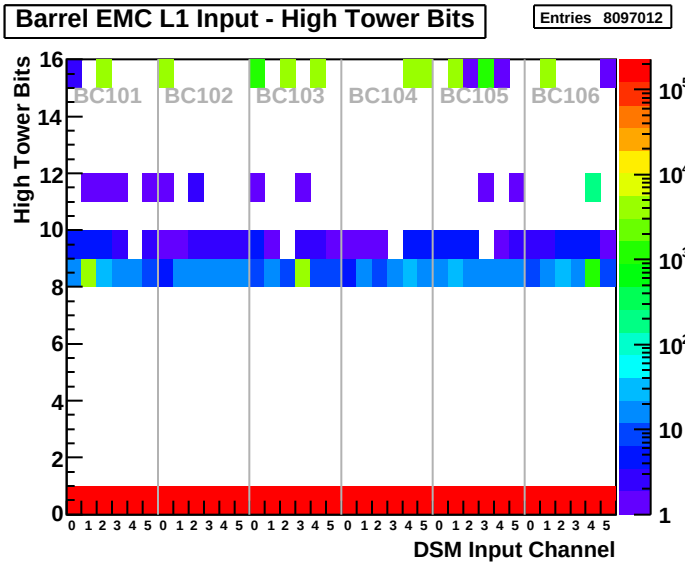
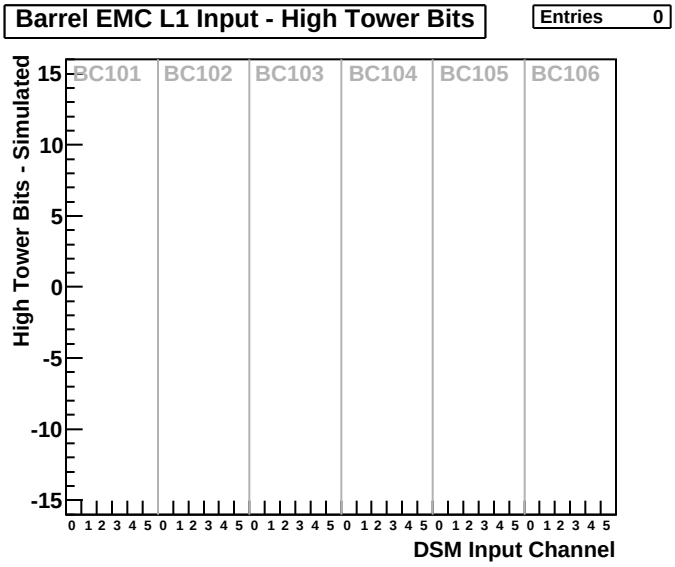

Barrel EMC L1 Input - Low Eta Sum
Entries 8097012
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

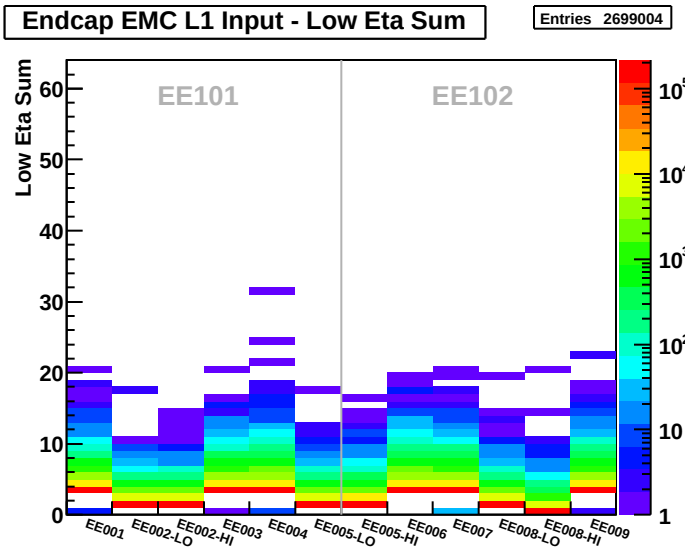

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

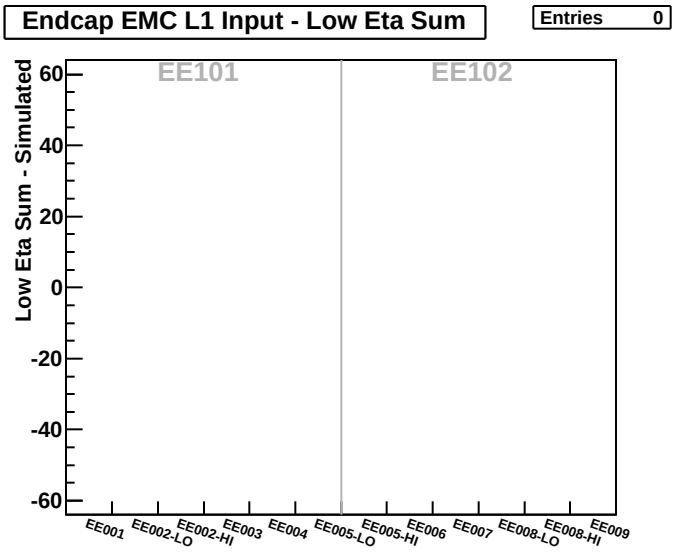

Barrel EMC L1 Input - High Eta Sum
Entries 8097012
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

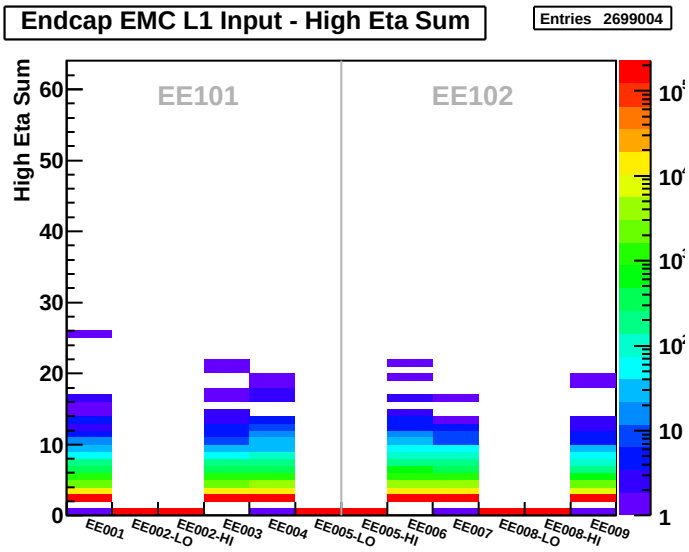

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

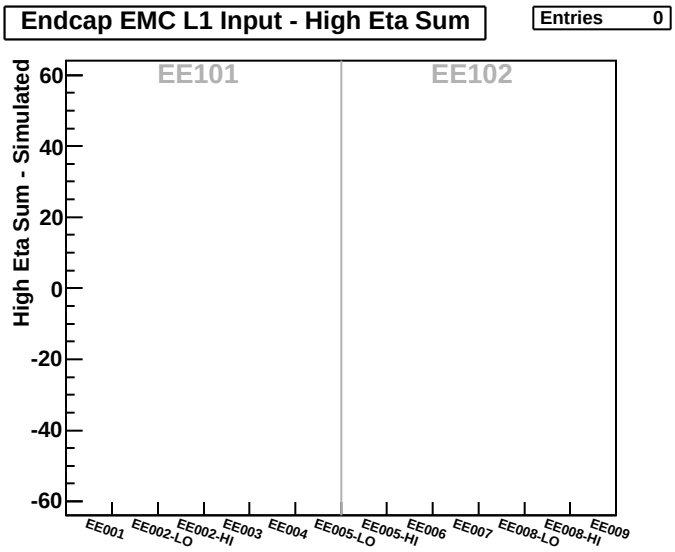

Barrel EMC L1 Input - High Tower Bits
Entries 8097012
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

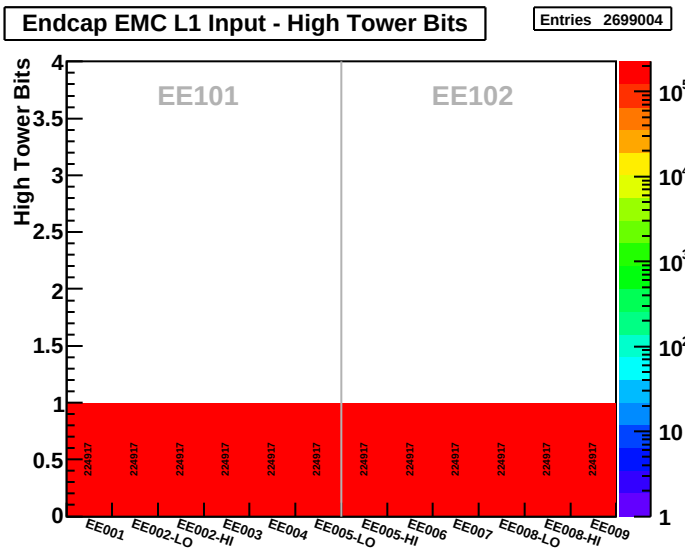

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

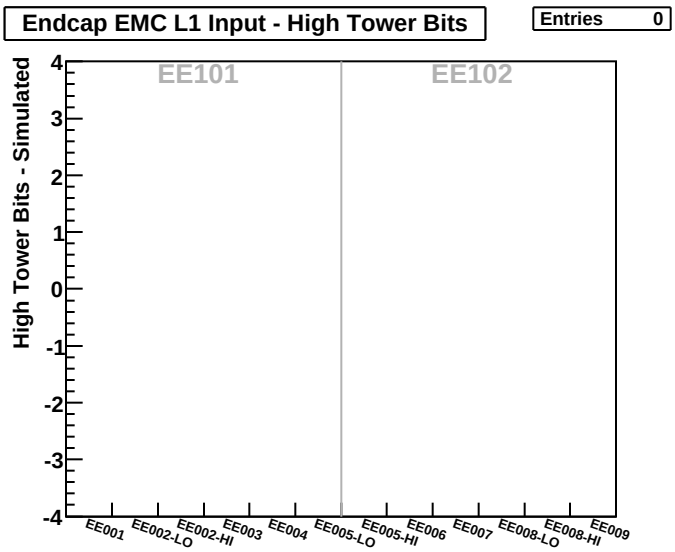

Endcap EMC L1 Input - Low Eta Sum
Entries 2699004
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

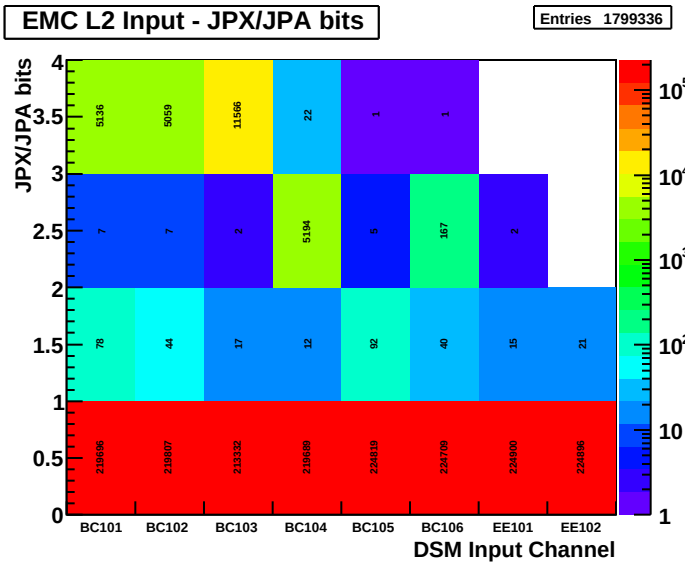

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

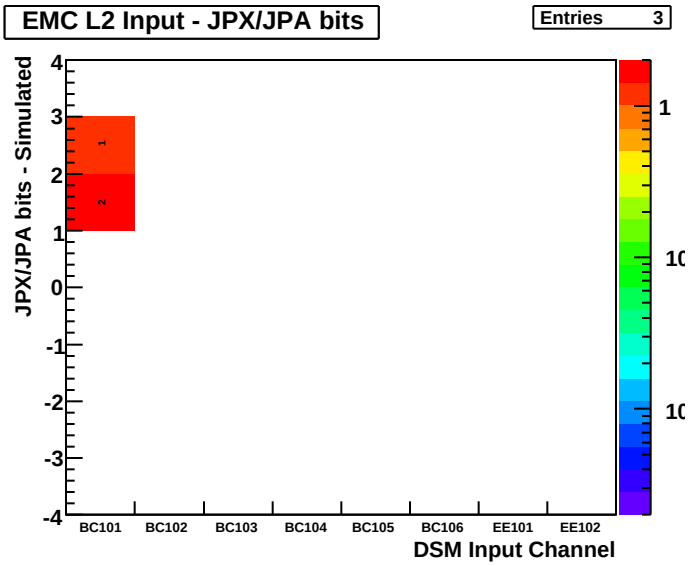

Endcap EMC L1 Input - High Eta Sum
Entries 2699004
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

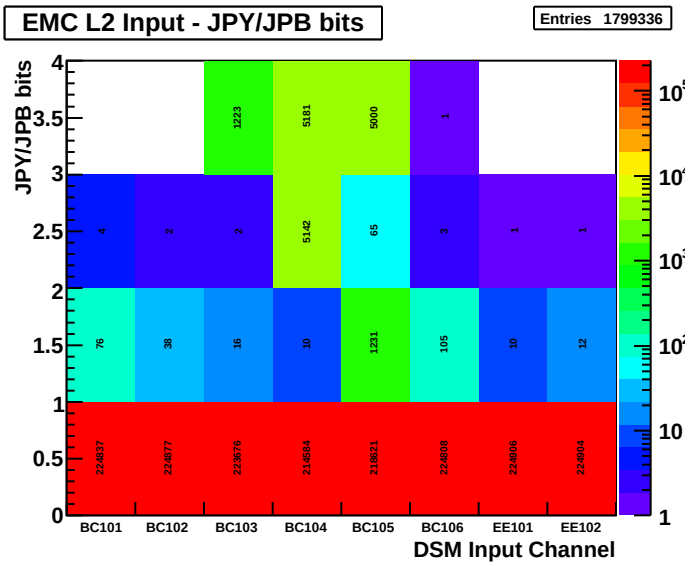

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

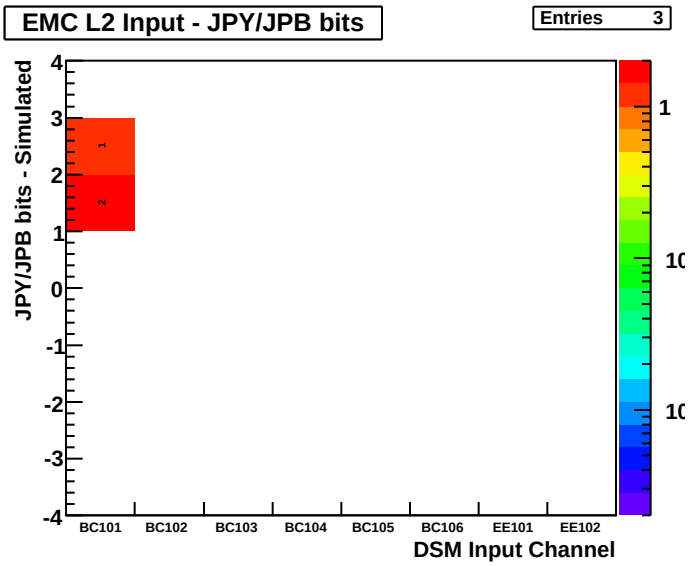

Endcap EMC L1 Input - High Tower Bits
Entries 2699004
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

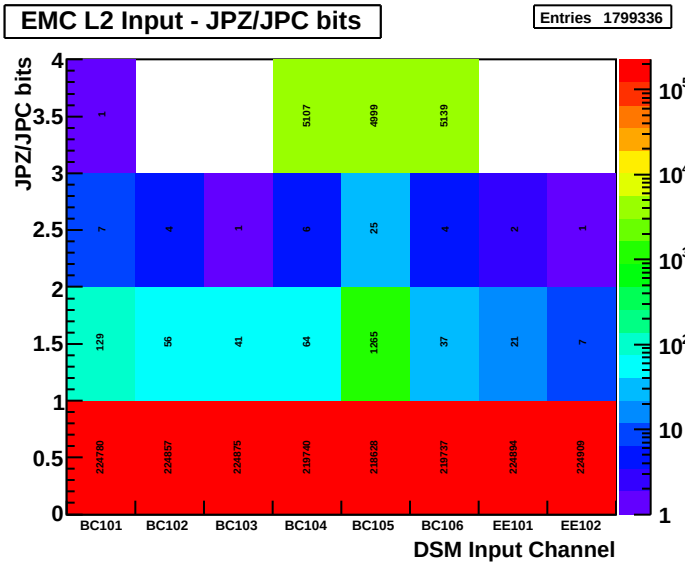

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

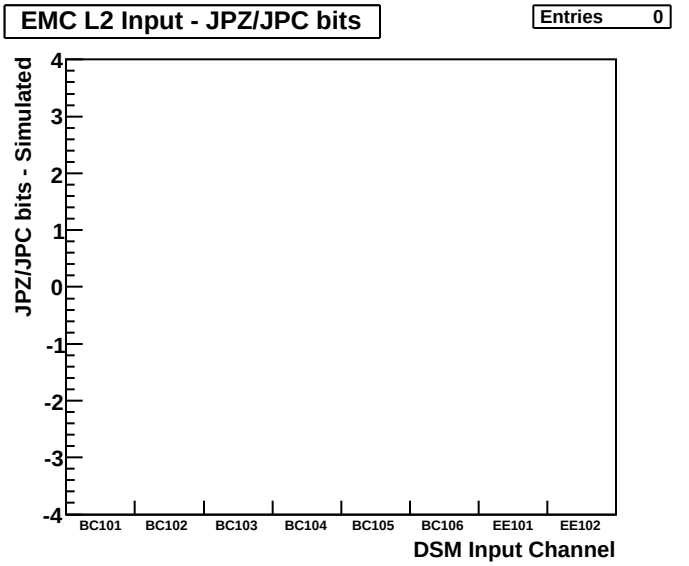

EMC L2 Input - JPX/JPA bits
Entries 1799336
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

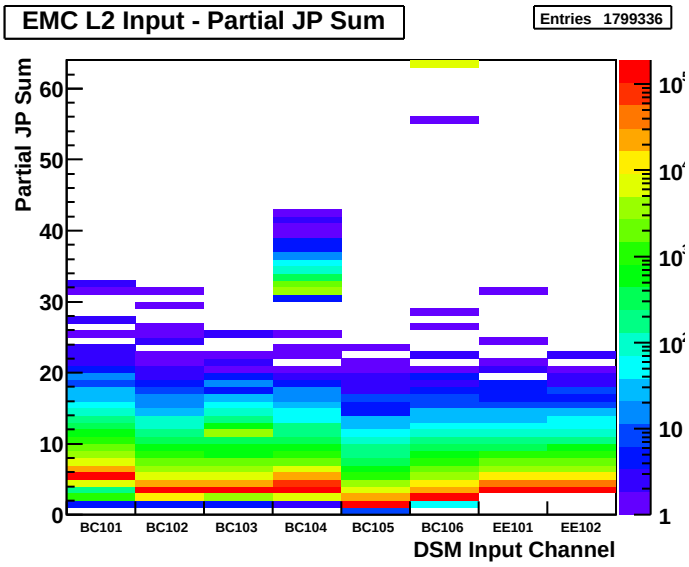

EMC L2 Input - JPX/JPA bits
Entries 3
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

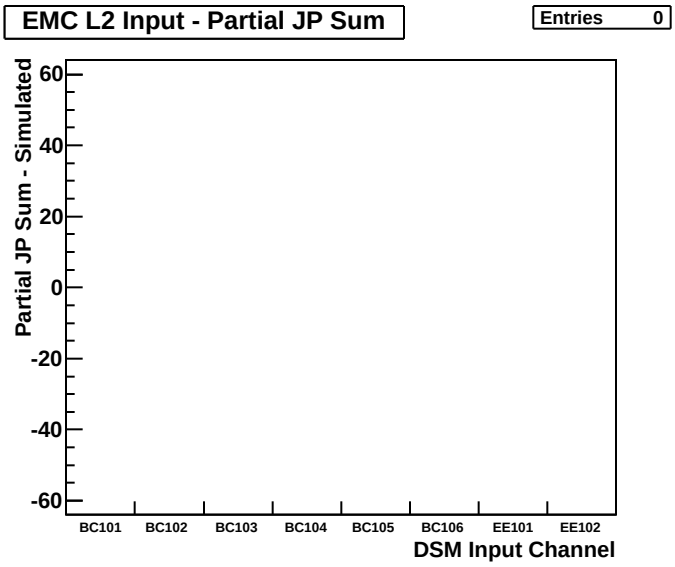

EMC L2 Input - JPY/JPB bits
Entries 1799336
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

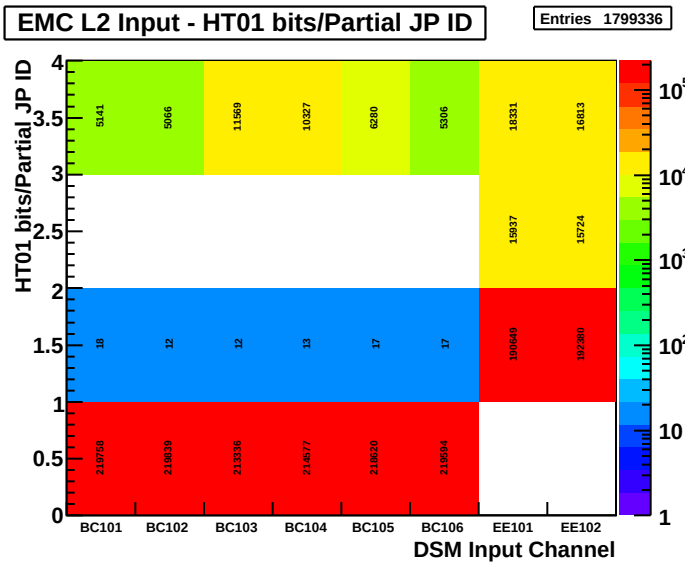

EMC L2 Input - JPY/JPB bits
Entries 3
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

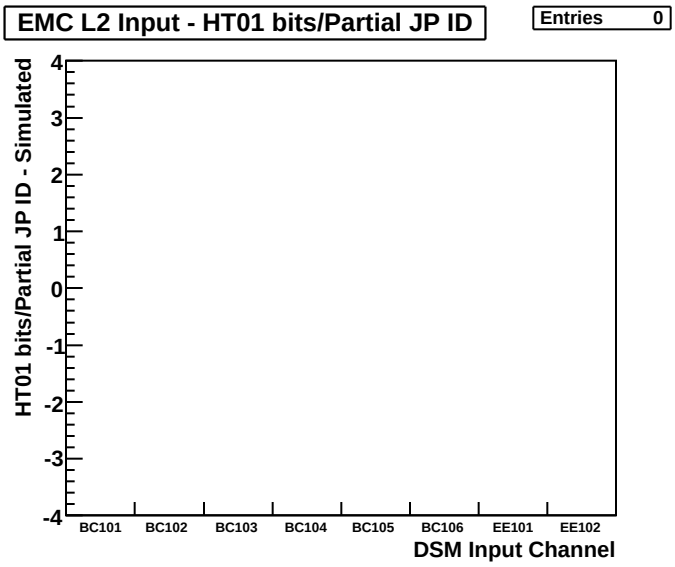

EMC L2 Input - JPZ/JPC bits
Entries 1799336
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

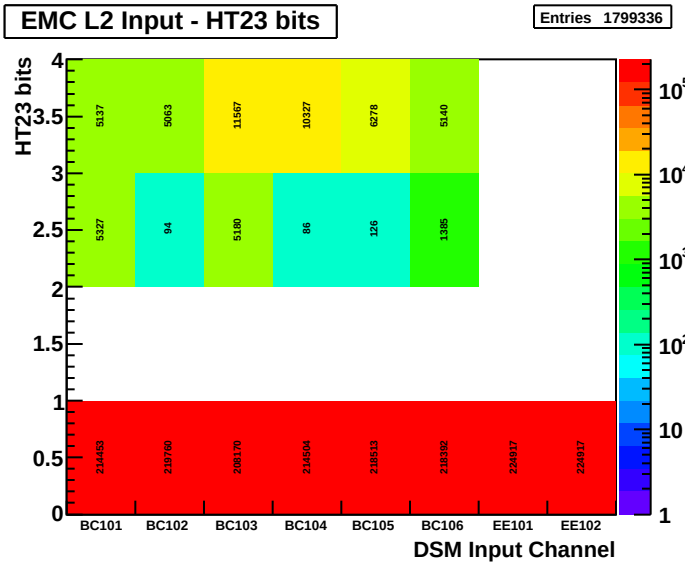

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

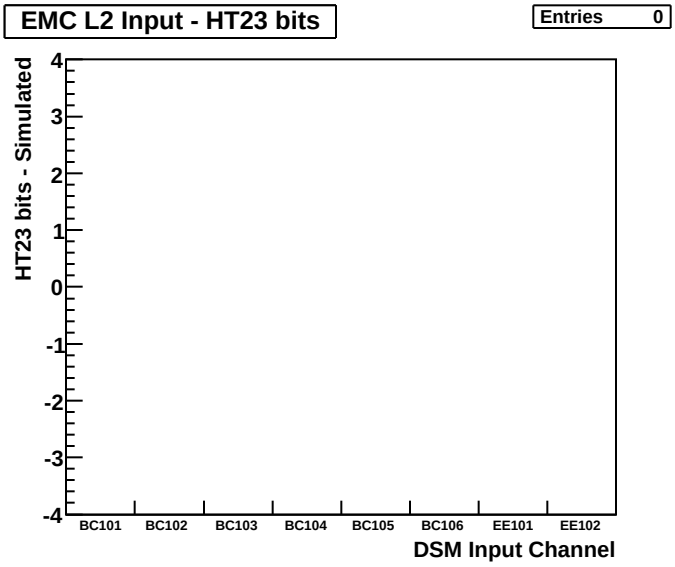

EMC L2 Input - Partial JP Sum
Entries 1799336
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

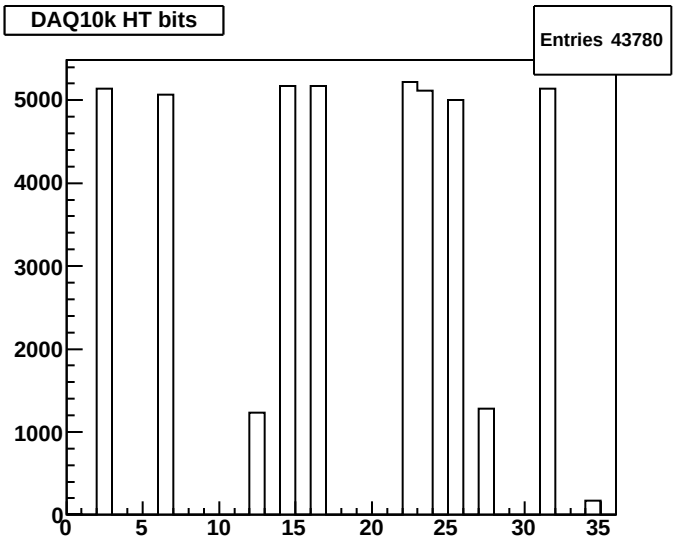

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

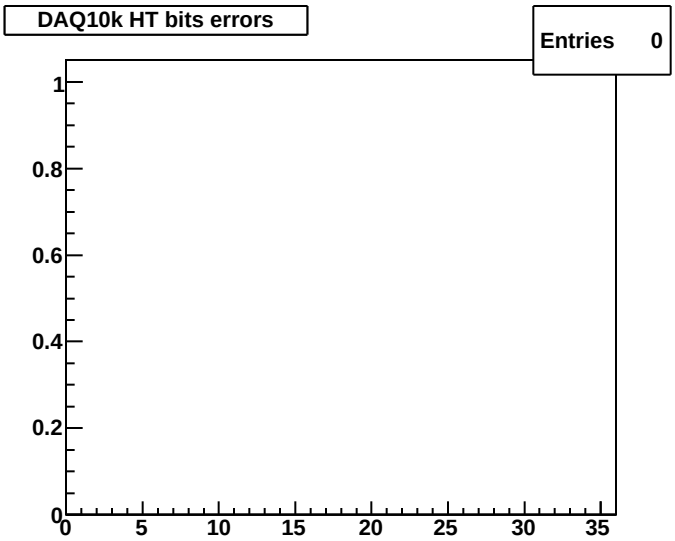

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1799336
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

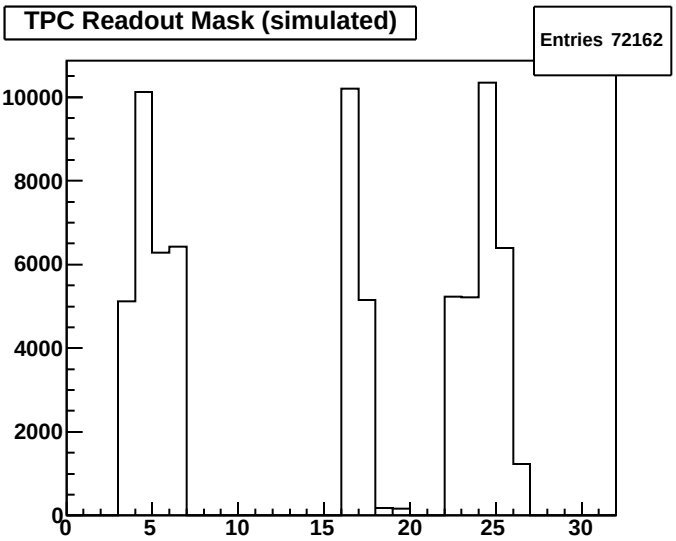

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

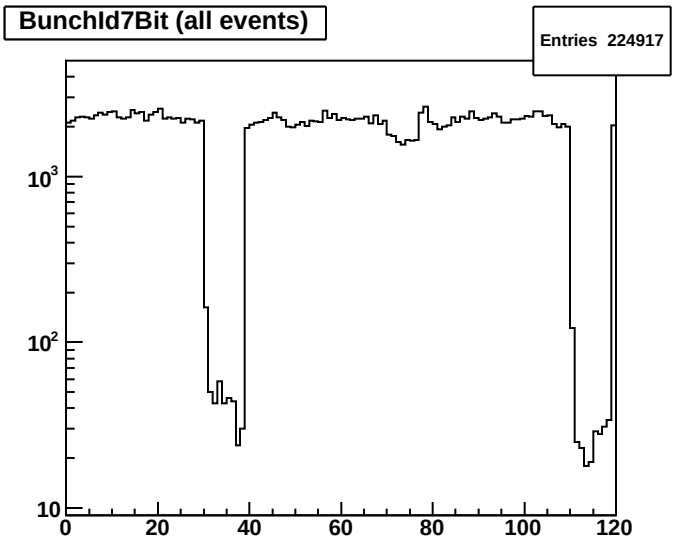

EMC L2 Input - HT23 bits
Entries 1799336
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

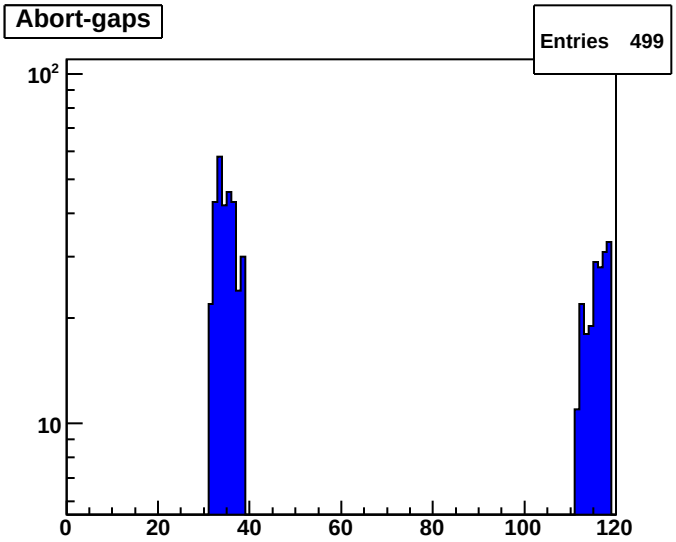

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

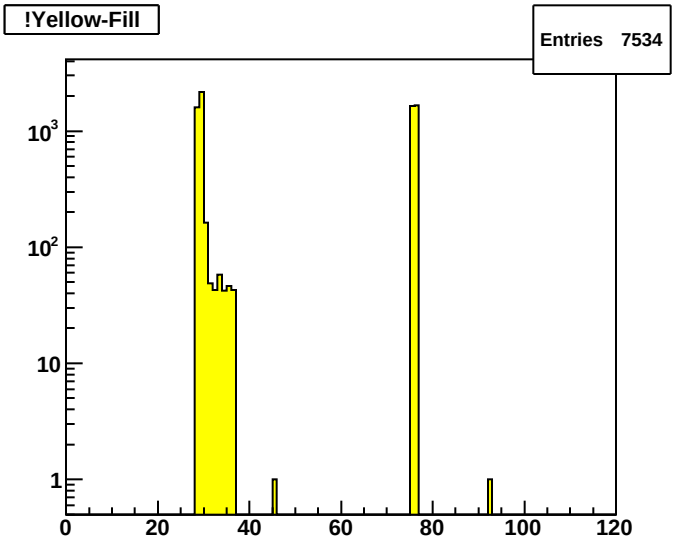

DAQ10k HT bits
Entries 43780

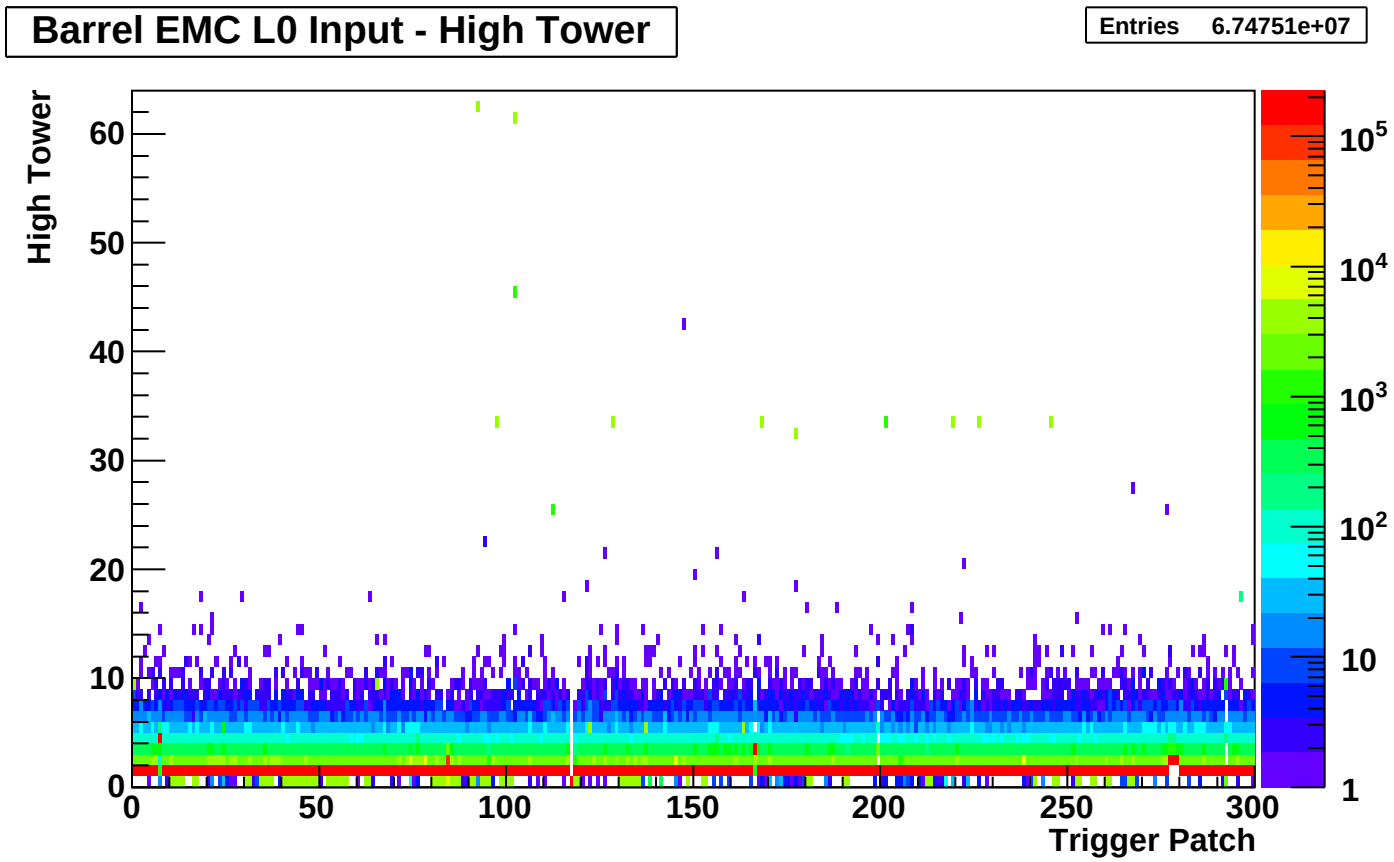

DAQ10k HT bits errors
Entries 0

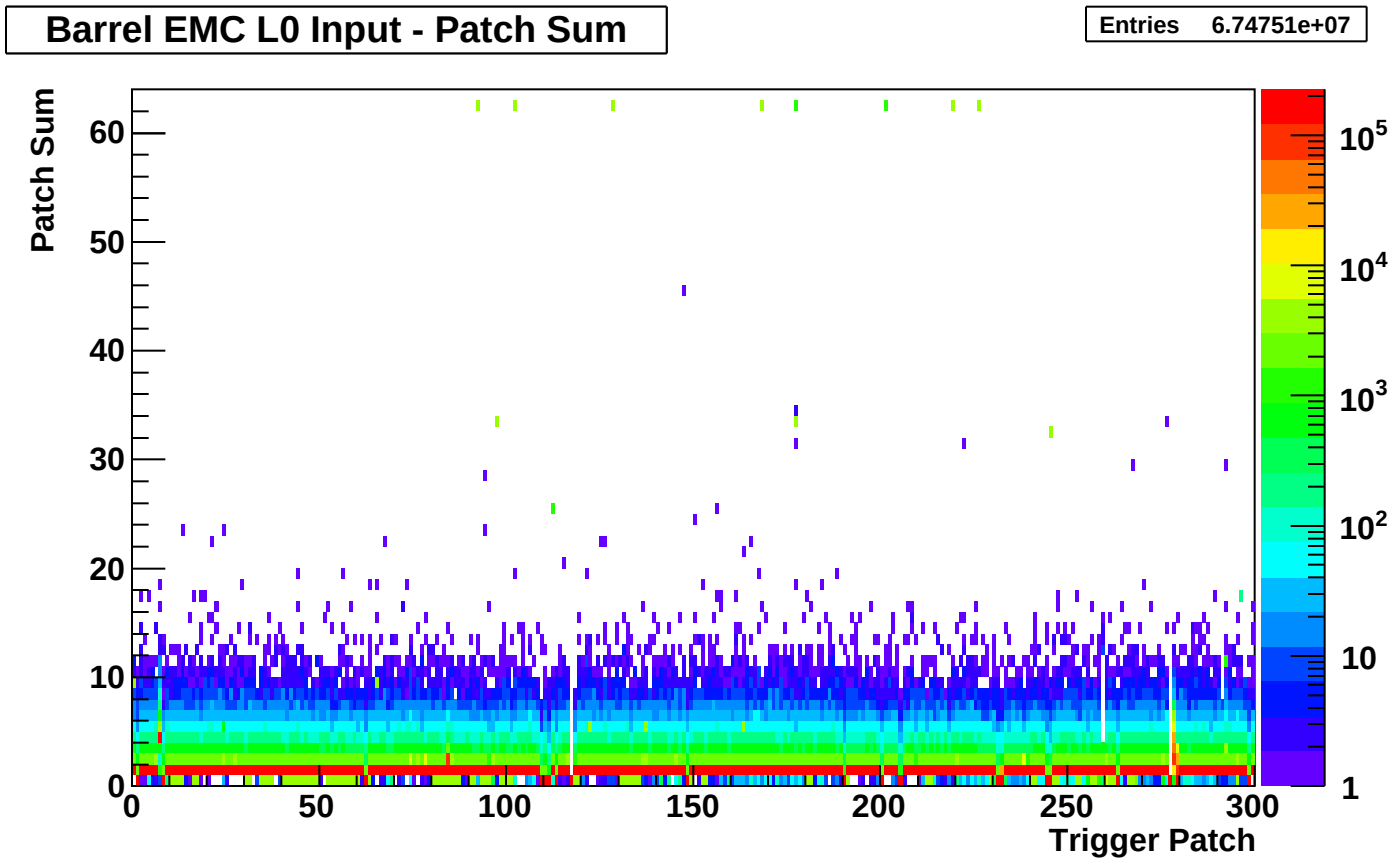

TPC Readout Mask (simulated)
Entries 72162

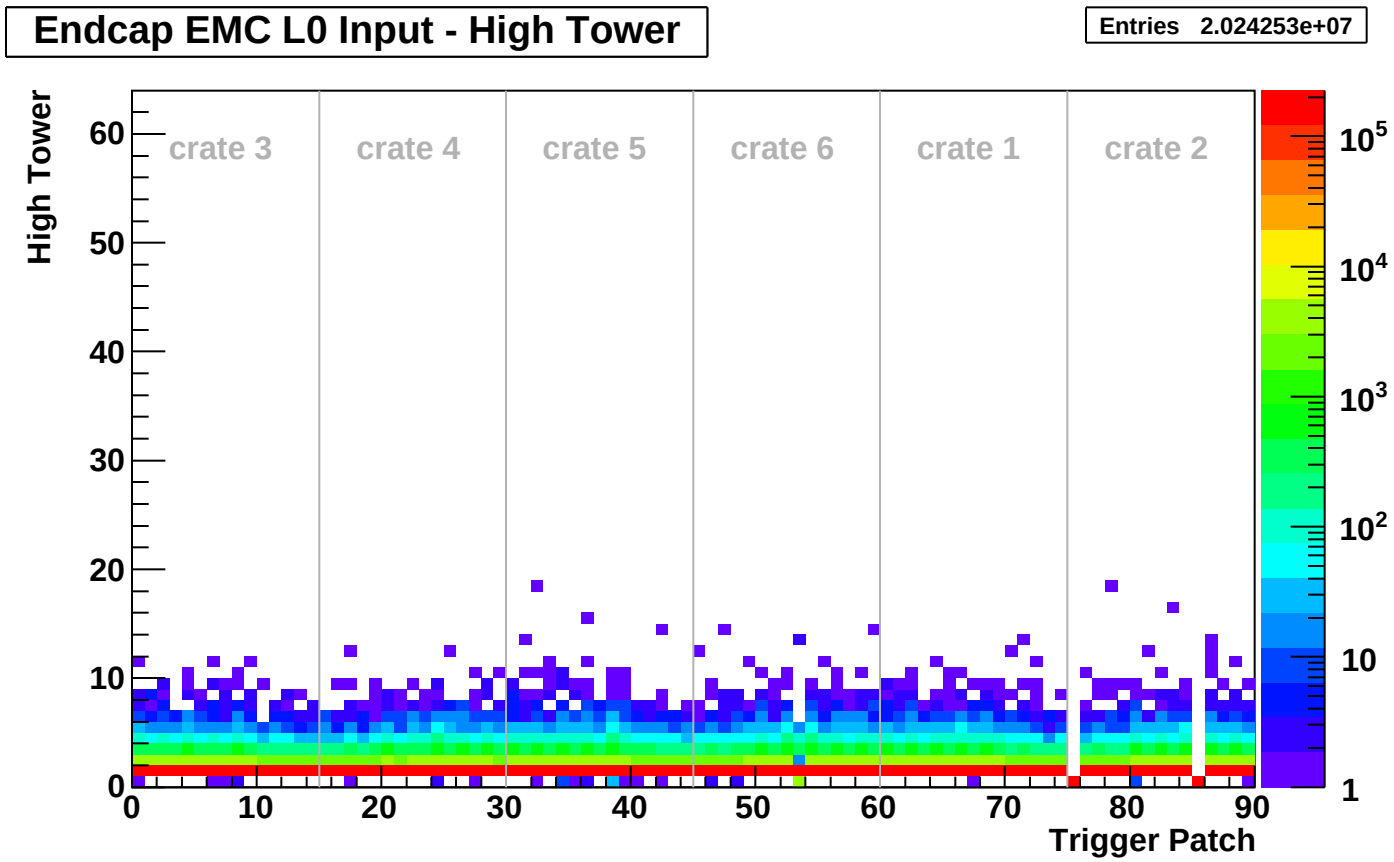

BunchId7Bit (all events)
Entries 224917

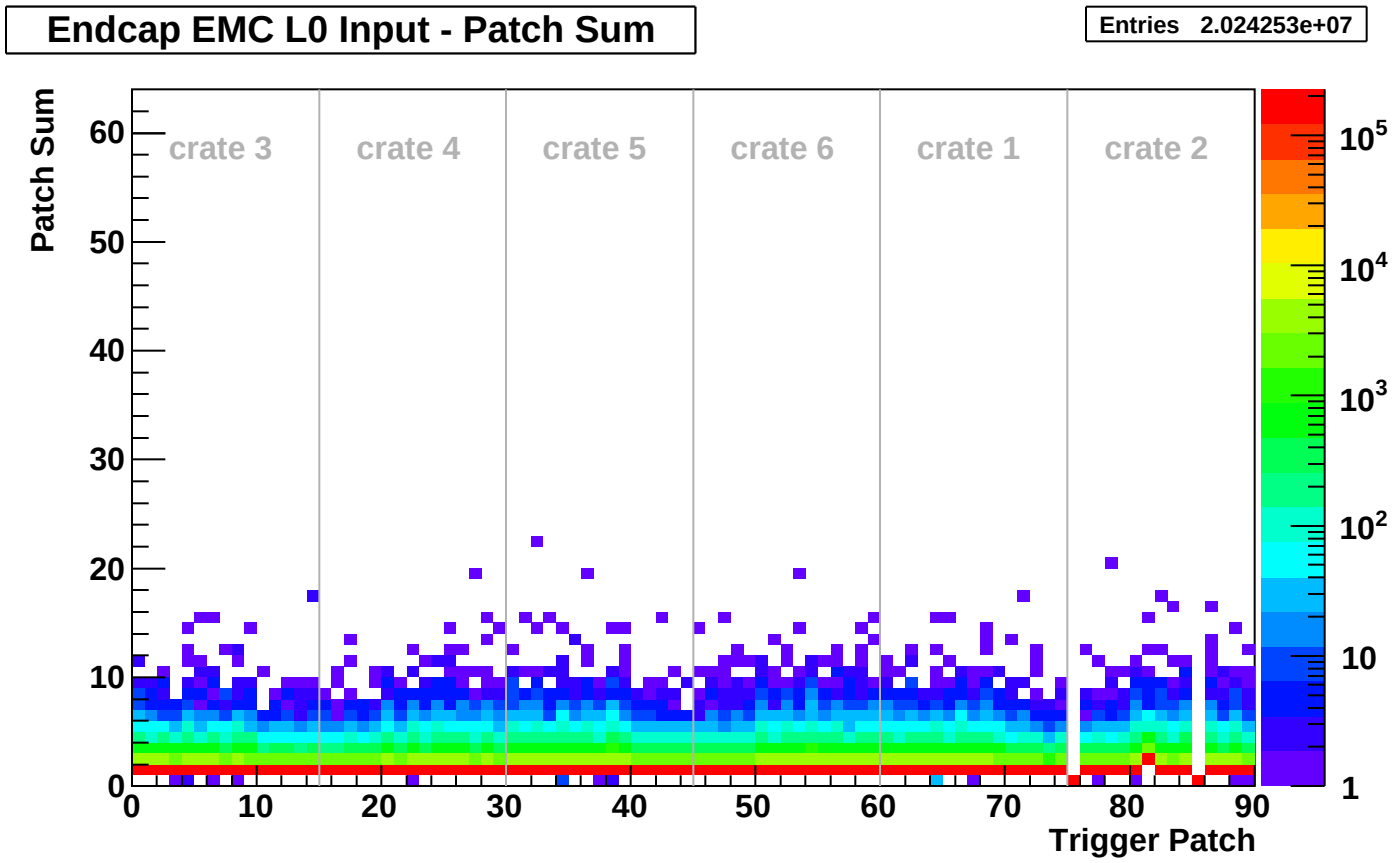

Abort-gaps
Entries 499

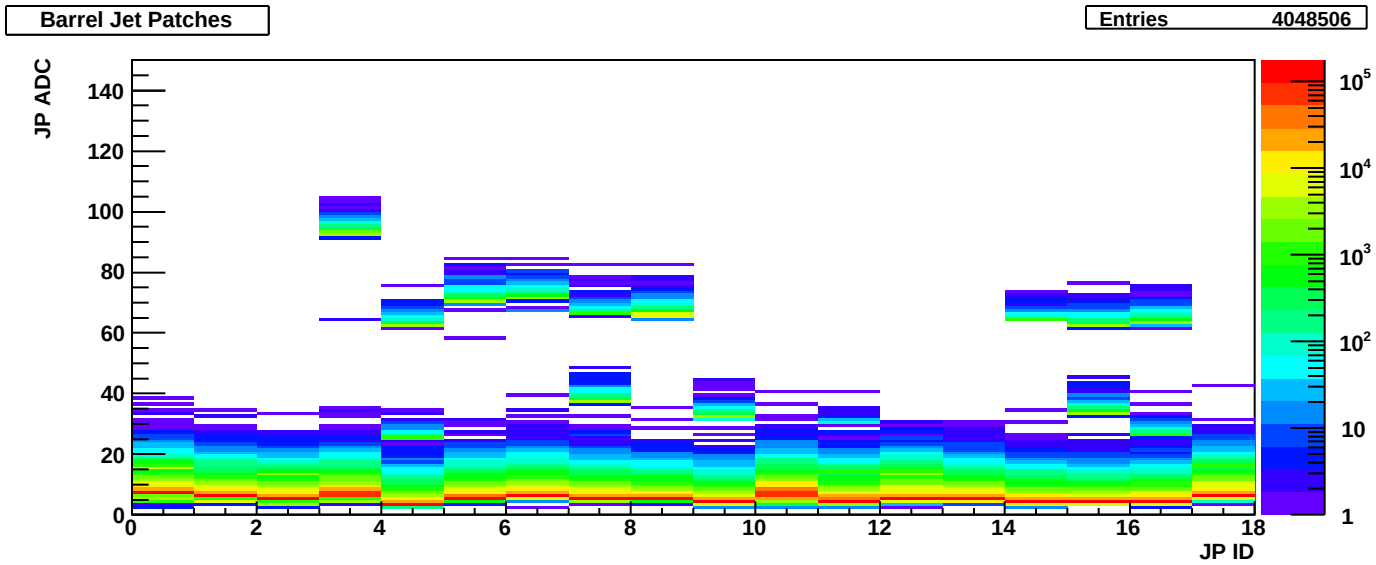

!Yellow-Fill
Entries 7534

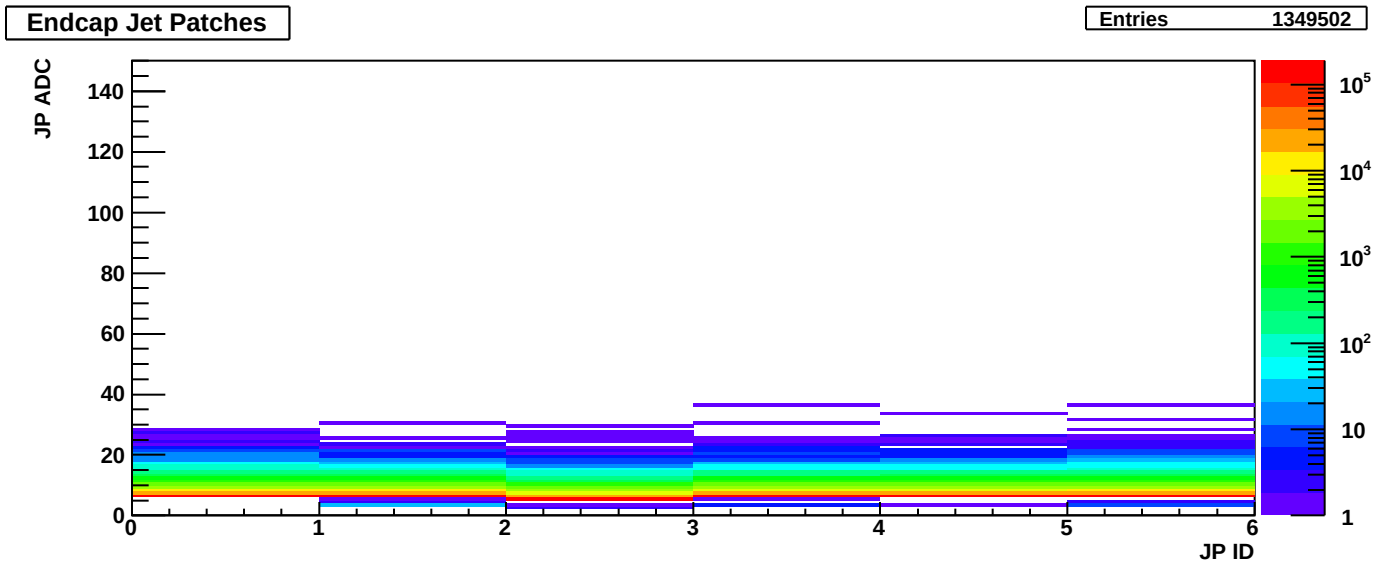

Barrel EMC L0 Input - High Tower
Entries 6.74751e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

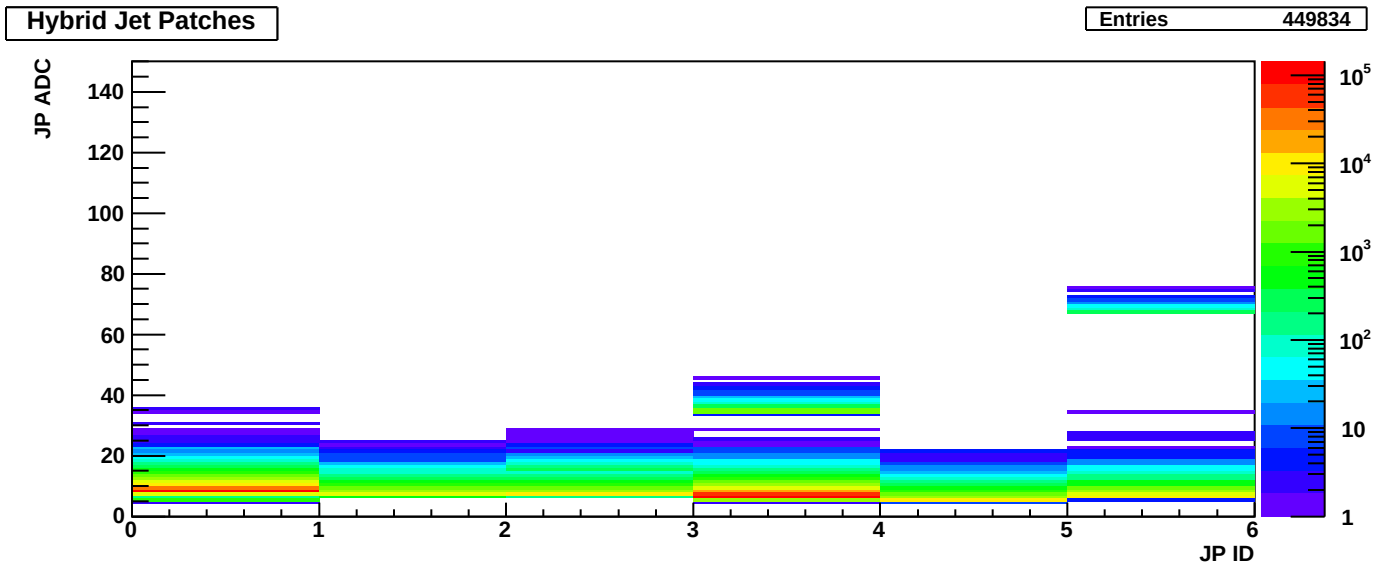

Barrel EMC L0 Input - Patch Sum
Entries 6.74751e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

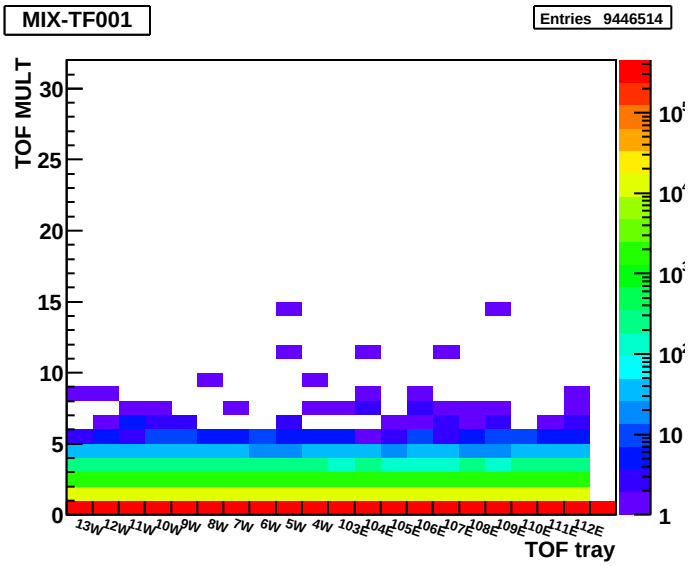

Endcap EMC L0 Input - High Tower
Entries 2.024253e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

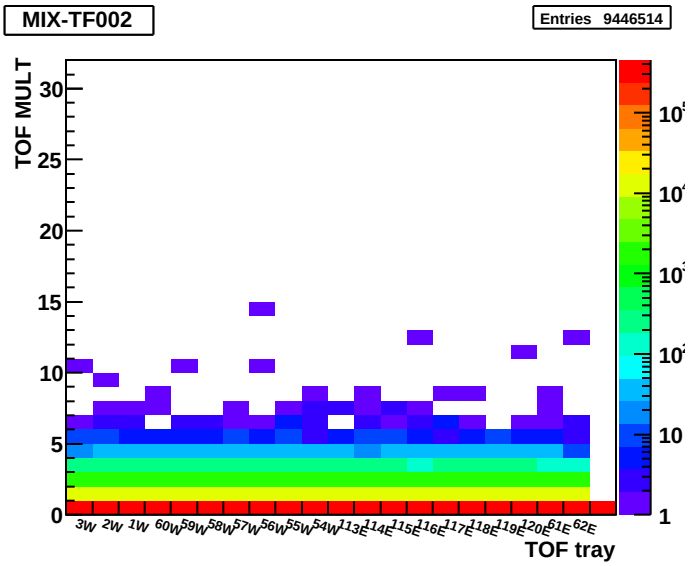

Endcap EMC L0 Input - Patch Sum
Entries 2.024253e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

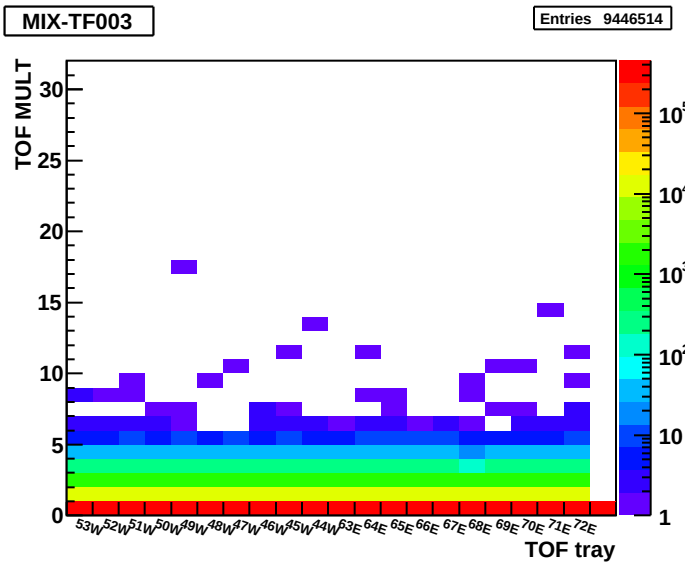

Barrel Jet Patches
Entries 4048506
JP ADC
JP ID

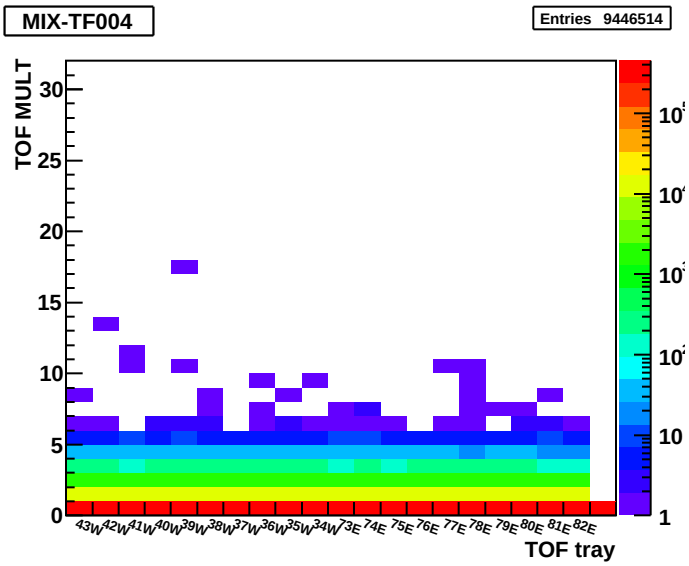

Endcap Jet Patches
Entries 1349502
JP ADC
JP ID

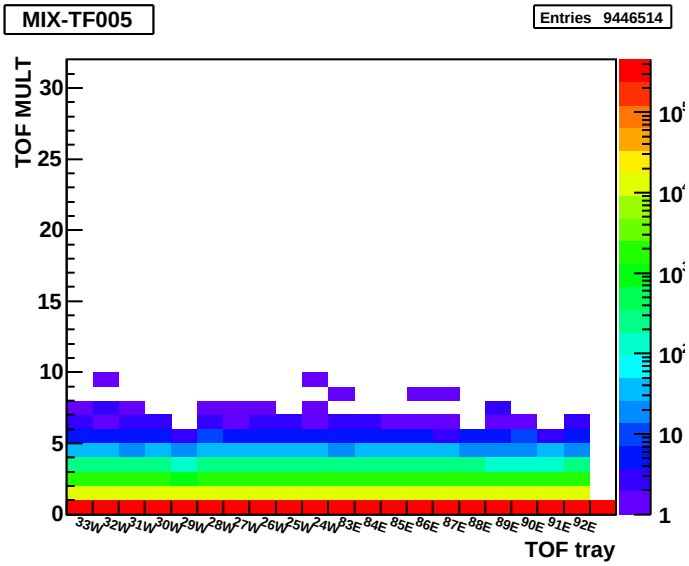

Hybrid Jet Patches
Entries 449834
JP ADC
JP ID

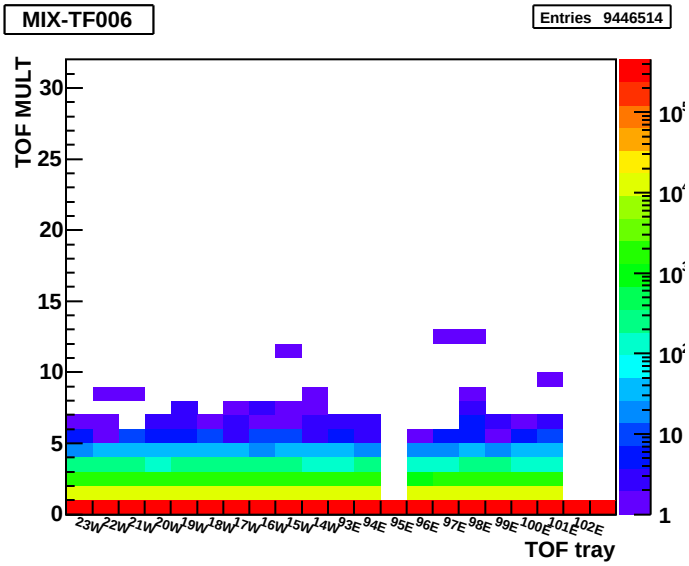

MIX-TF001
Entries 9446514
TOF MULT
TOF tray

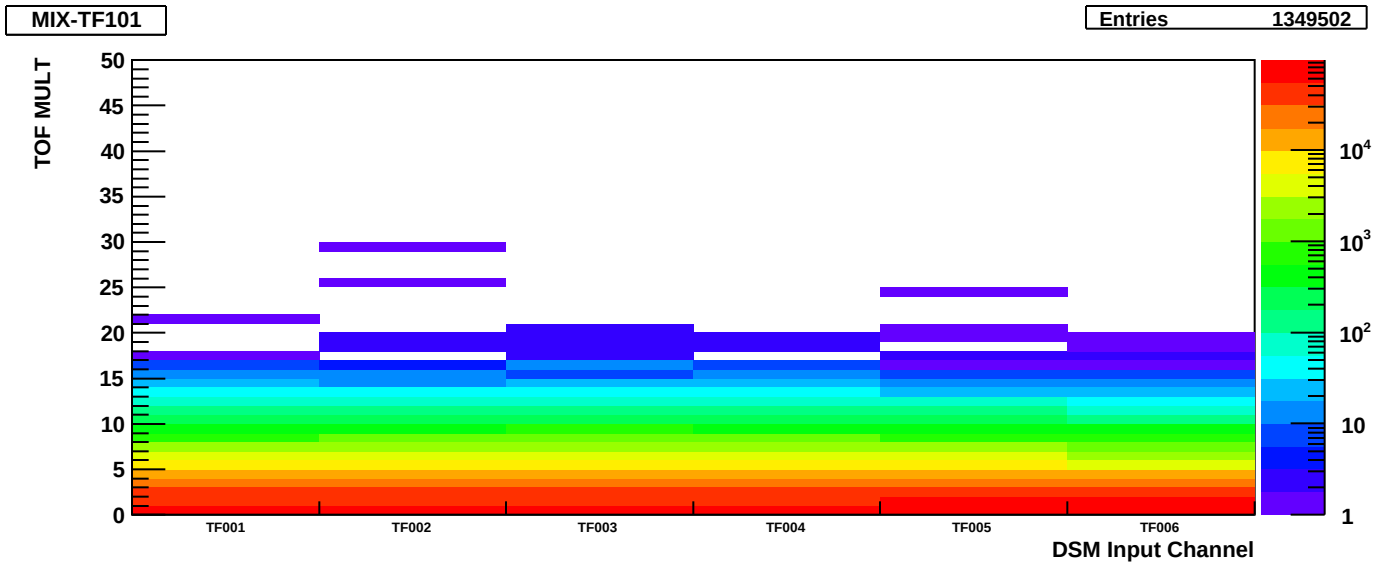

MIX-TF002
Entries 9446514
TOF MULT
TOF tray

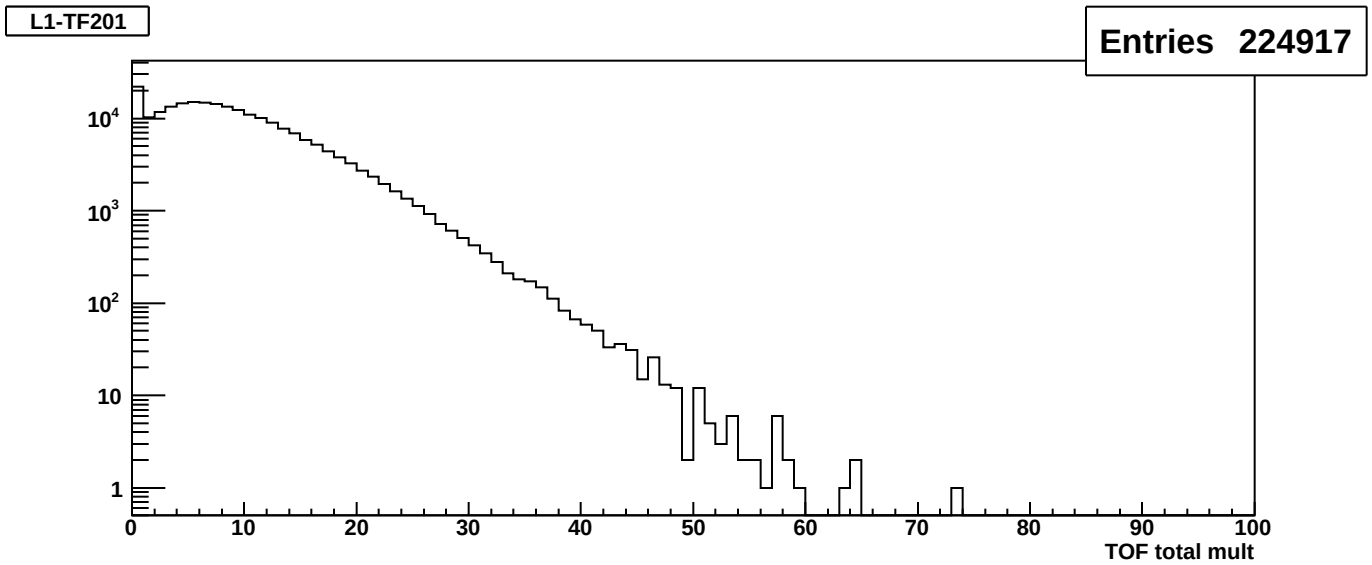

MIX-TF003
Entries 9446514
TOF MULT
TOF tray

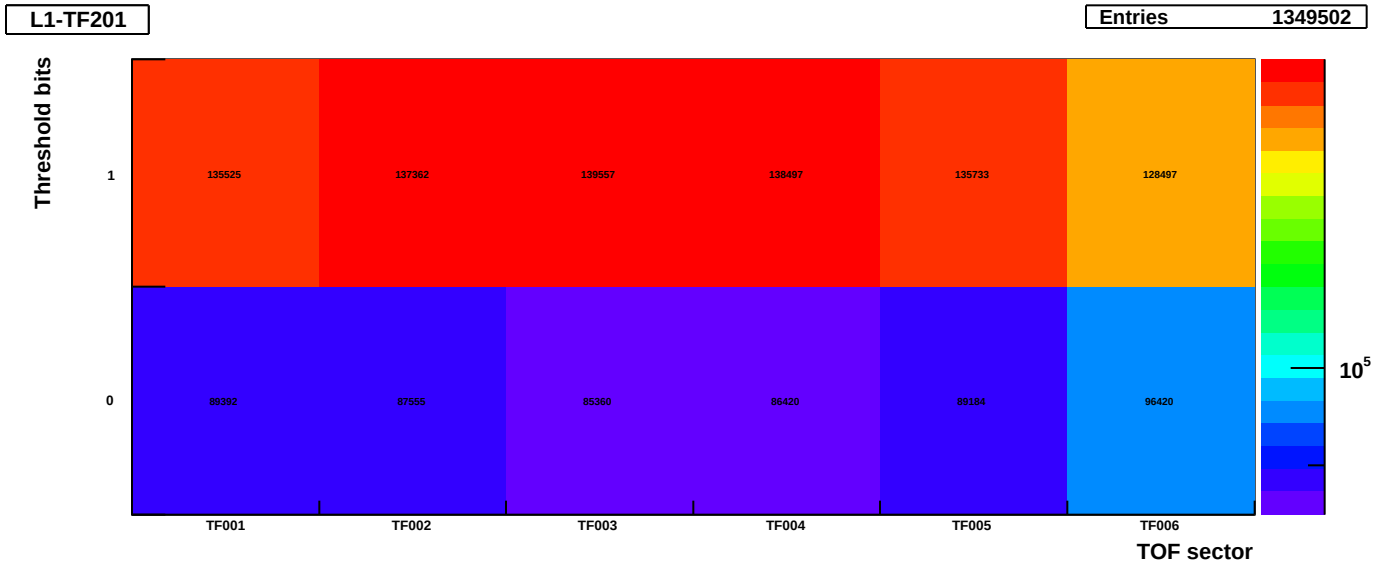

MIX-TF004
Entries 9446514
TOF MULT
TOF tray

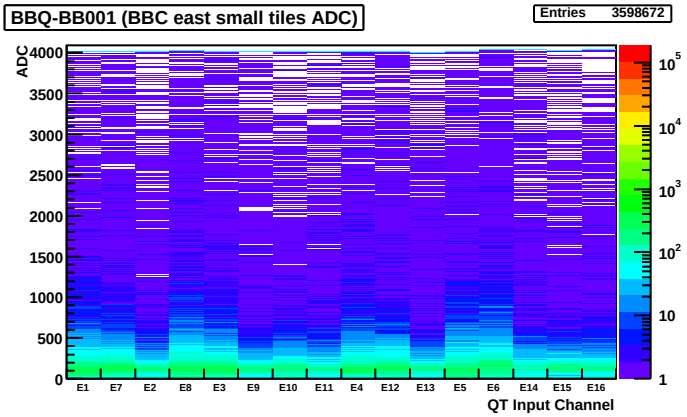

MIX-TF005
Entries 9446514
TOF MULT
TOF tray

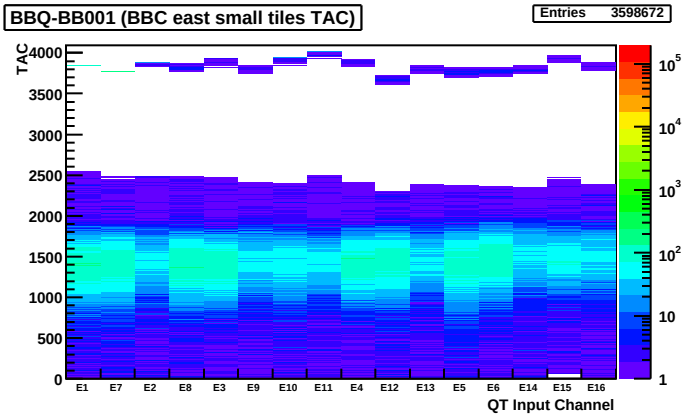

MIX-TF006
Entries 9446514
TOF MULT
TOF tray

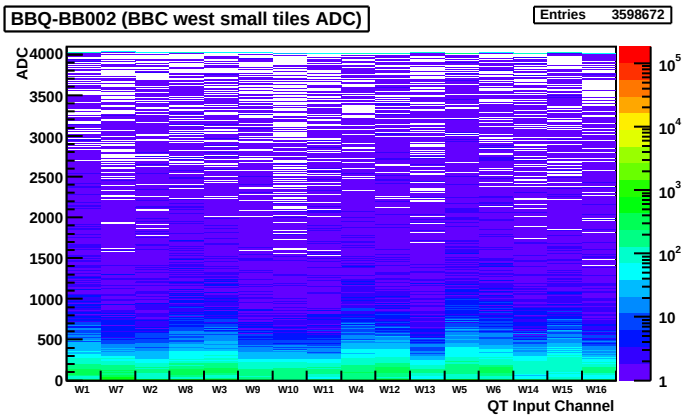

MIX-TF101
Entries 1349502
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

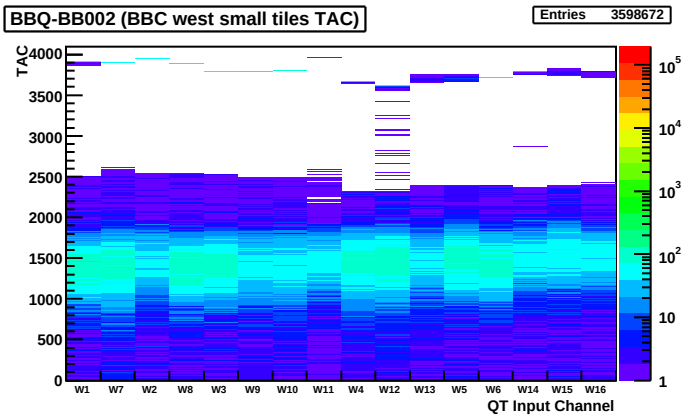

L1-TF201
Entries 224917
TOF total mult

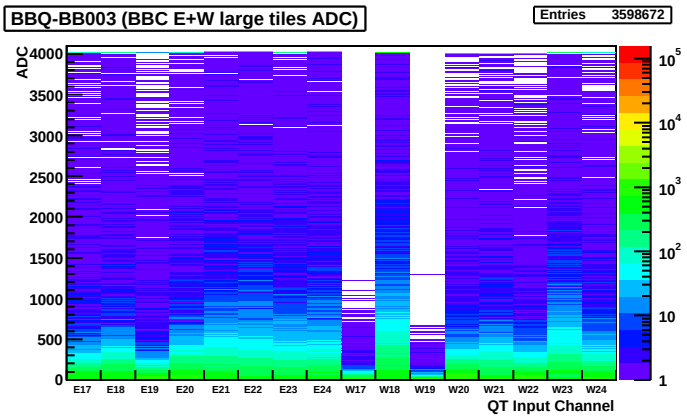

L1-TF201
Entries 1349502
Threshold bits
135525
137362
139557
138497
135733
128497
89392
87555
85360
86420
89184
96420
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

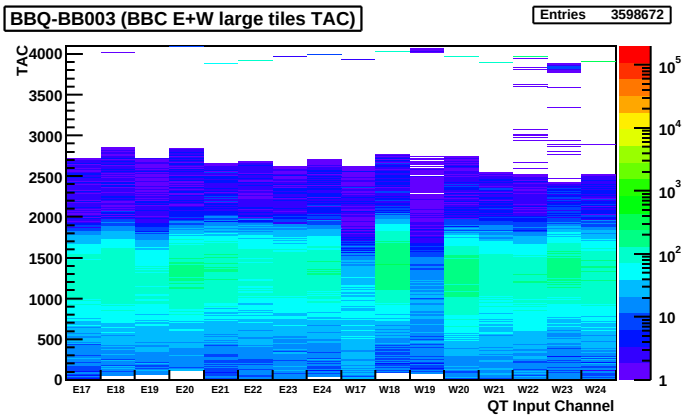

BBQ-BB001 (BBC east small tiles ADC)
Entries 3598672
ADC
QT Input Channel

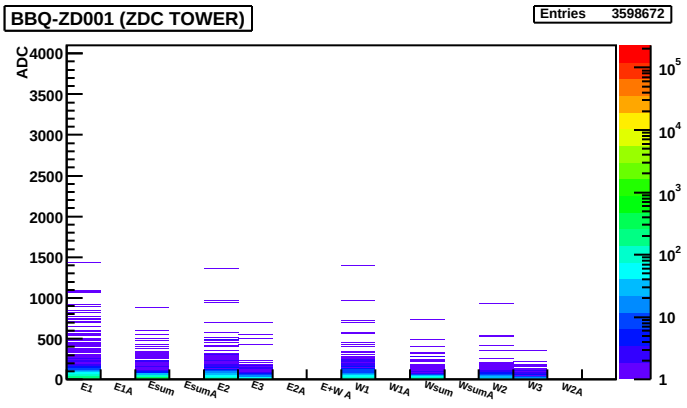

BBQ-BB001 (BBC east small tiles TAC)
Entries 3598672
TAC
QT Input Channel

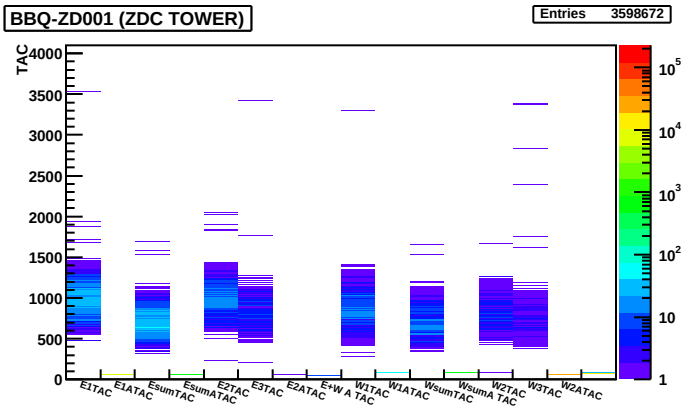

BBQ-BB002 (BBC west small tiles ADC)
Entries 3598672
ADC
QT Input Channel

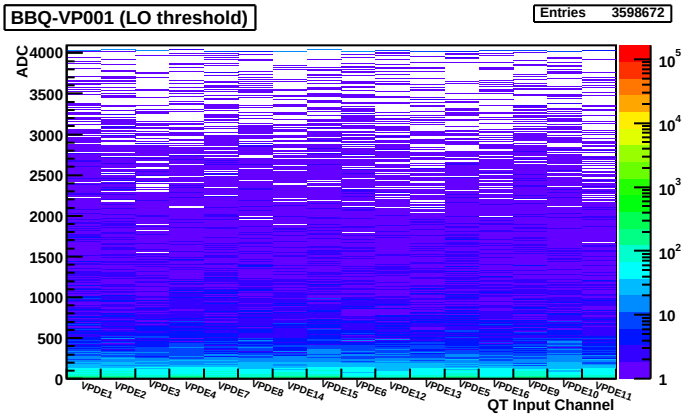

BBQ-BB002 (BBC west small tiles TAC)
Entries 3598672
TAC
QT Input Channel

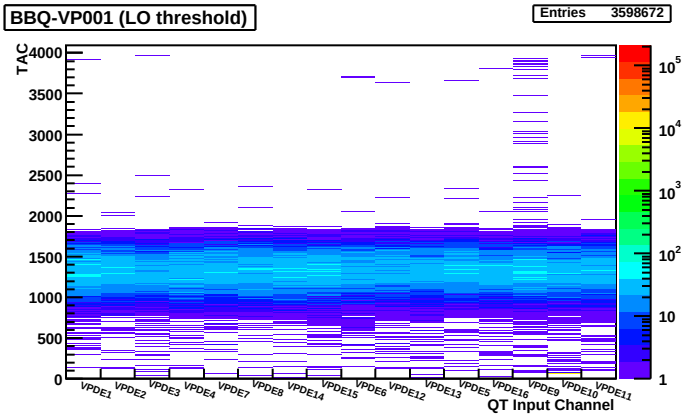

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3598672
ADC
QT Input Channel

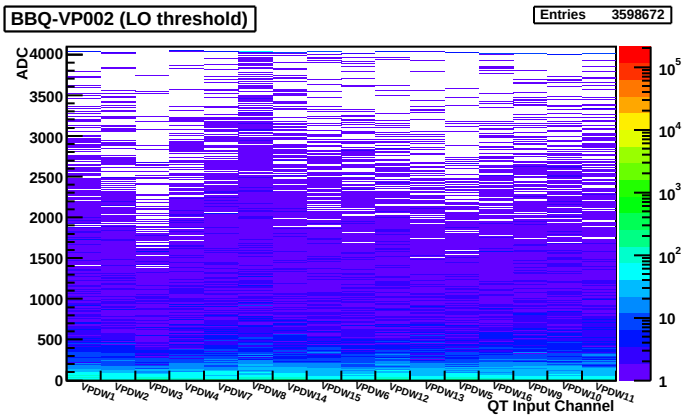

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3598672
TAC
QT Input Channel

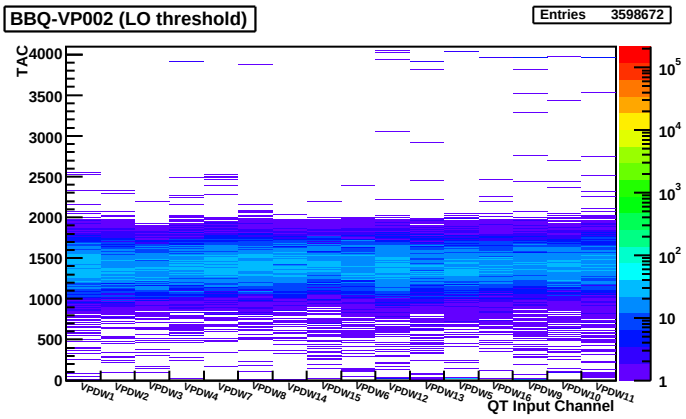

BBQ-ZD001 (ZDC TOWER)
Entries 3598672
ADC

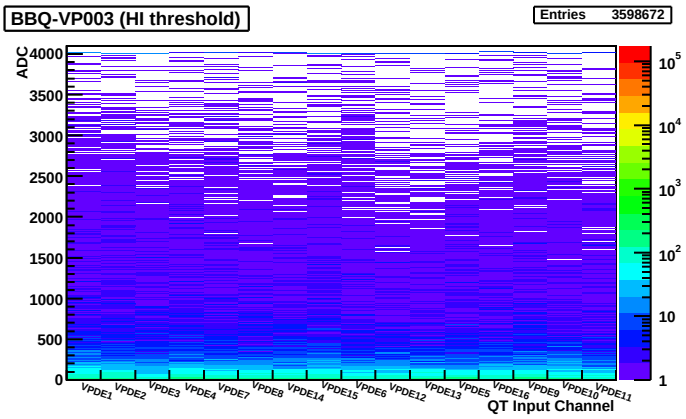

BBQ-ZD001 (ZDC TOWER)
Entries 3598672
TAC

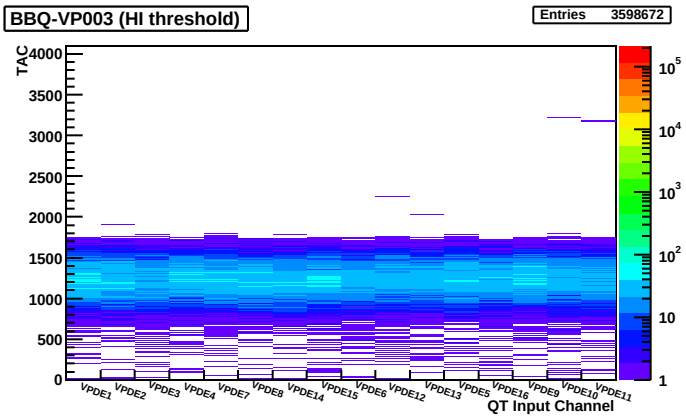

BBQ-VP001 (LO threshold)
Entries 3598672
ADC
QT Input Channel

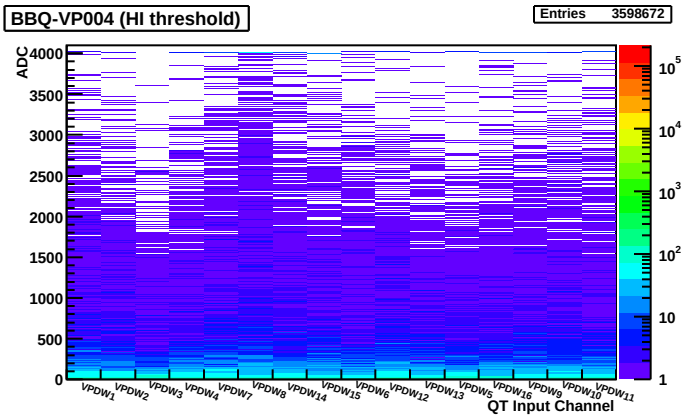

BBQ-VP001 (LO threshold)
Entries 3598672
TAC
QT Input Channel

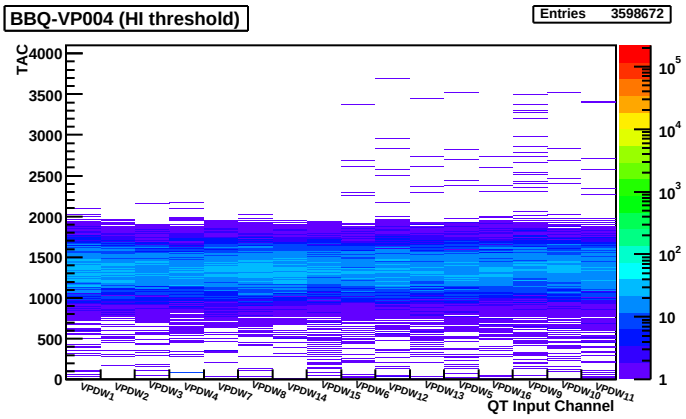

BBQ-VP002 (LO threshold)
Entries 3598672
ADC
QT Input Channel

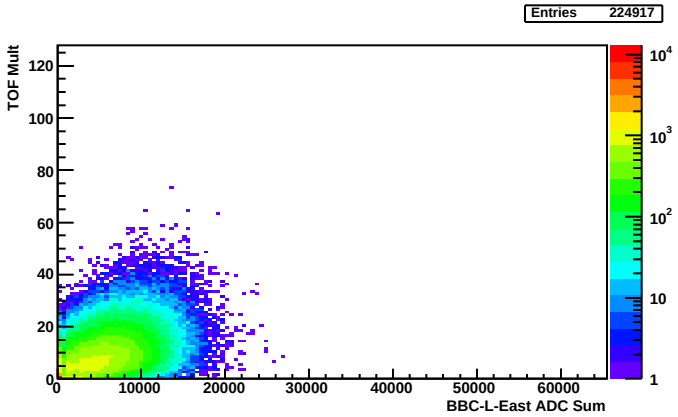

BBQ-VP002 (LO threshold)
Entries 3598672
TAC
QT Input Channel

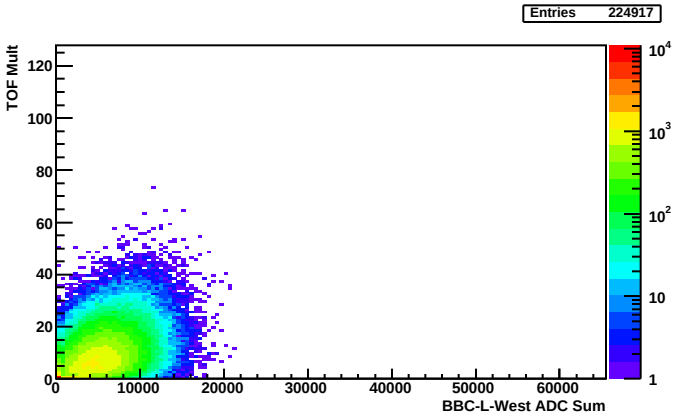

BBQ-VP003 (HI threshold)
Entries 3598672
ADC
QT Input Channel

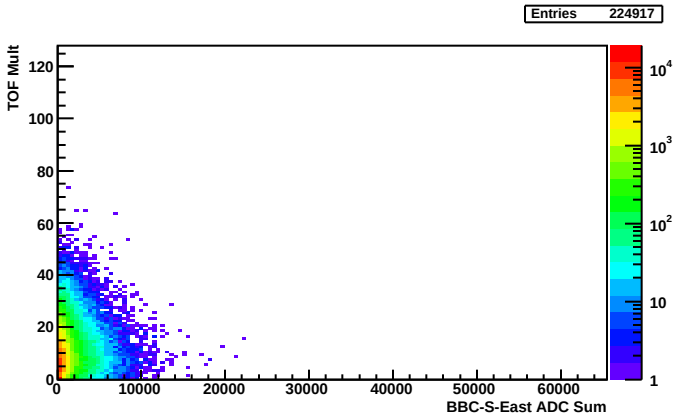

BBQ-VP003 (HI threshold)
Entries 3598672
TAC
QT Input Channel

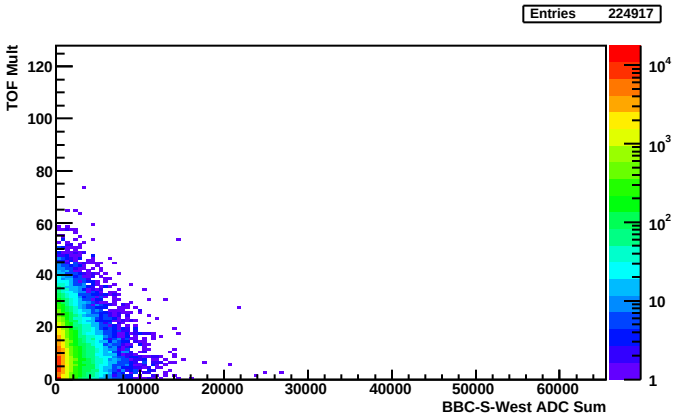

BBQ-VP004 (HI threshold)
Entries 3598672
ADC
QT Input Channel

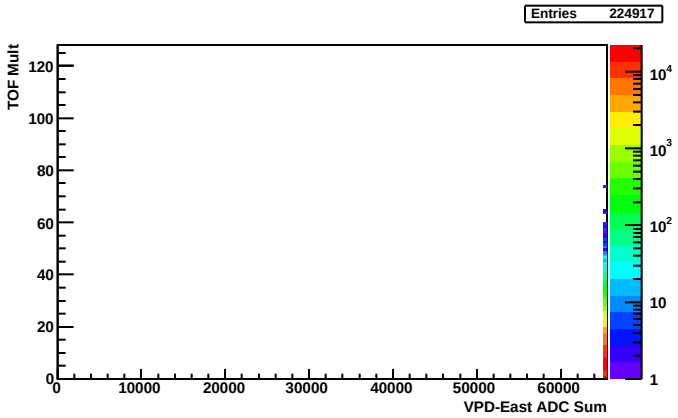

BBQ-VP004 (HI threshold)
Entries 3598672
TAC
QT Input Channel

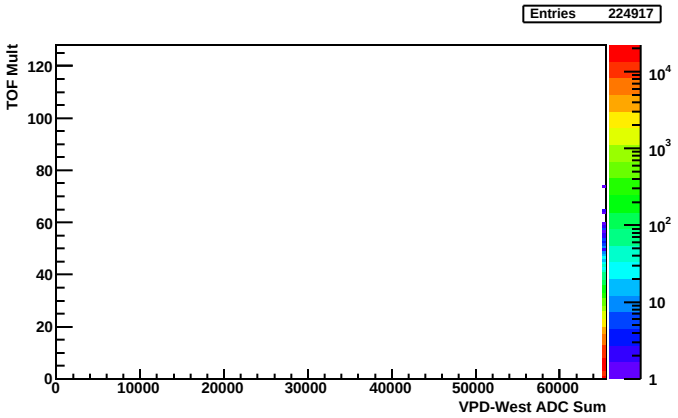

Entries 224917
TOF Mult
BBC-L-East ADC Sum

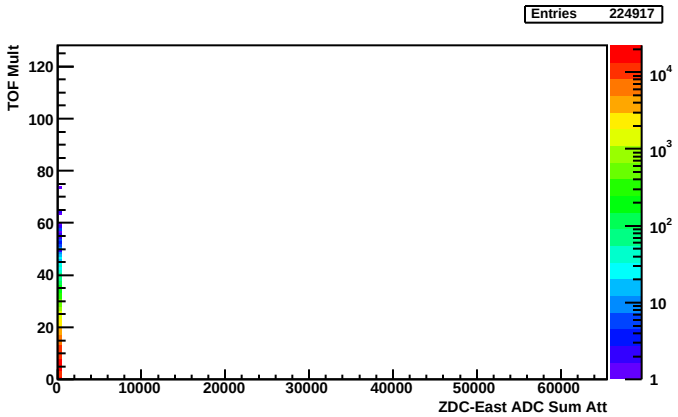

Entries 224917
TOF Mult
BBC-L-West ADC Sum

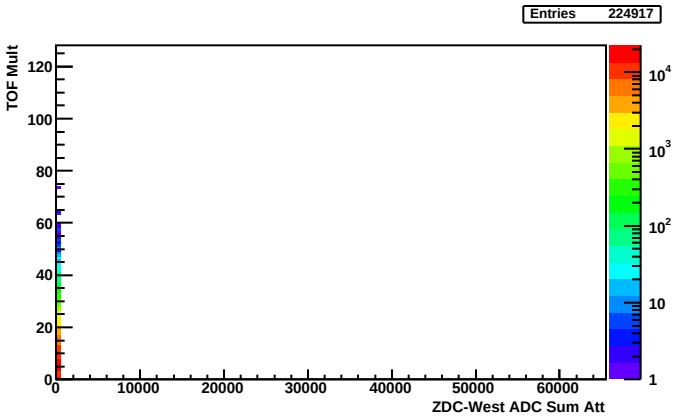

Entries 224917
TOF Mult
BBC-S-East ADC Sum

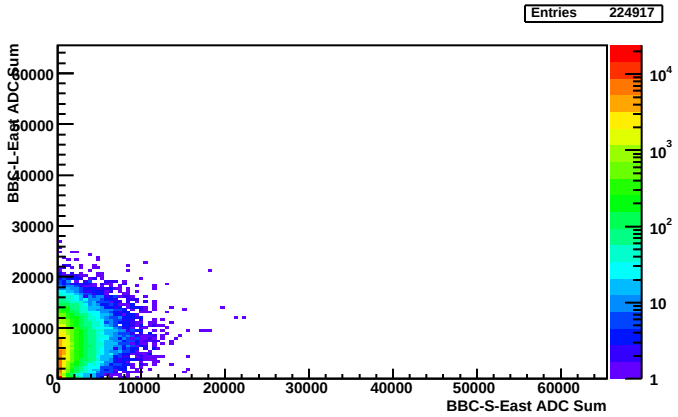

Entries 224917
TOF Mult
BBC-S-West ADC Sum

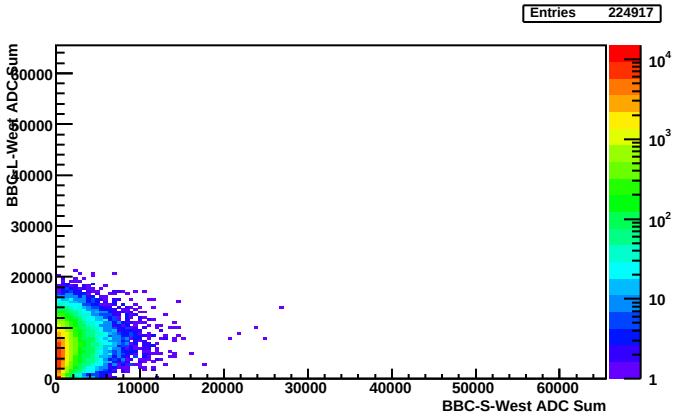

Entries 224917
TOF Mult
VPD-East ADC Sum

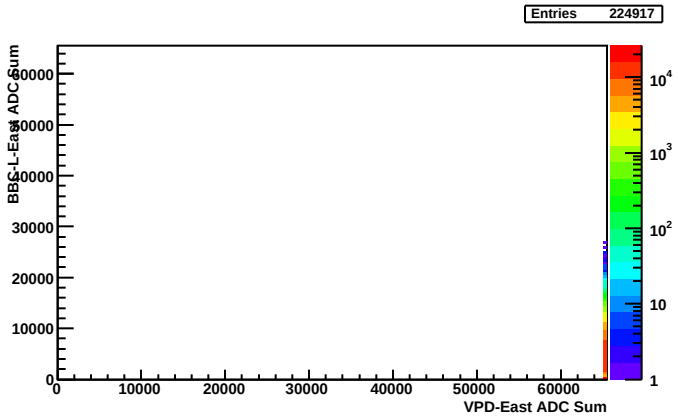

Entries 224917
TOF Mult
VPD-West ADC Sum

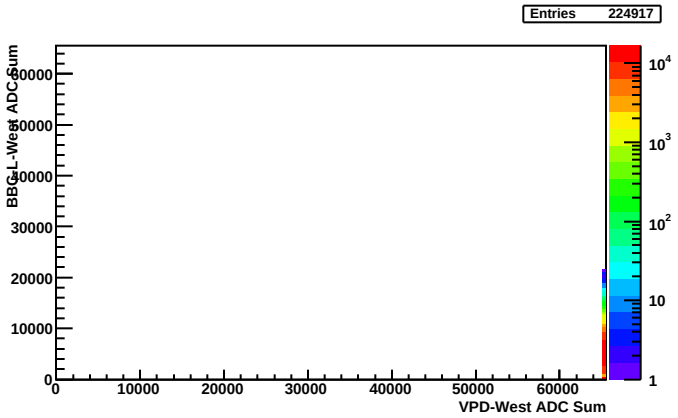

Entries 224917
TOF Mult
ZDC-East ADC Sum Att

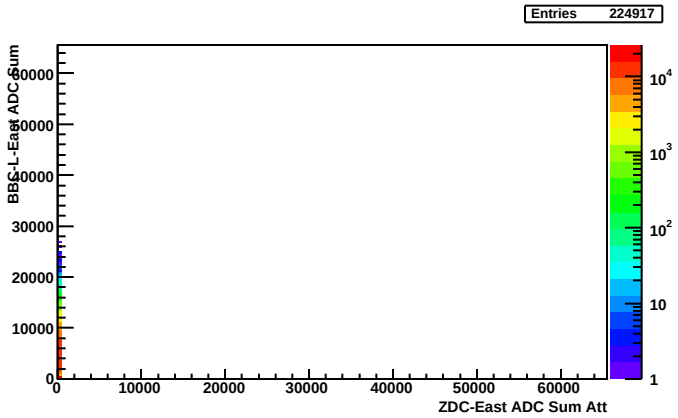

Entries 224917
TOF Mult
ZDC-West ADC Sum Att

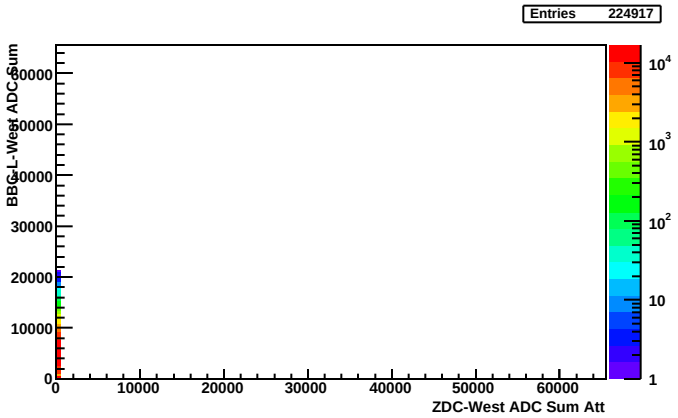

Entries 224917
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 224917
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 224917
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 224917
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 224917
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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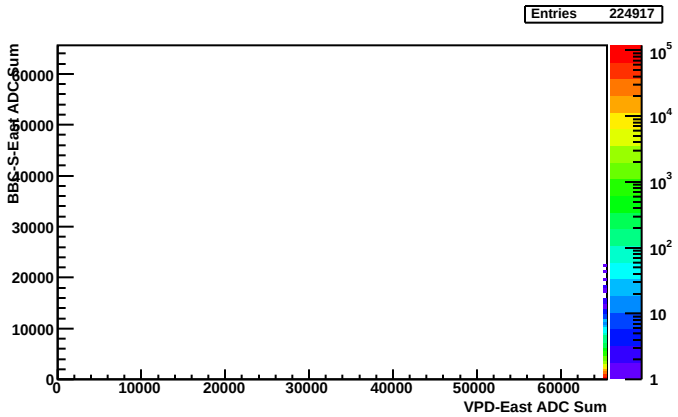
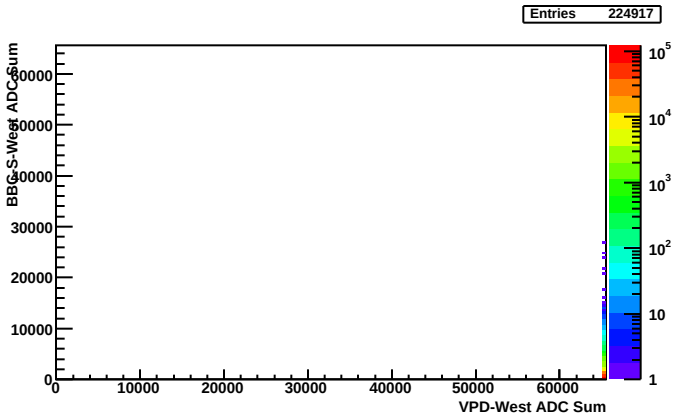
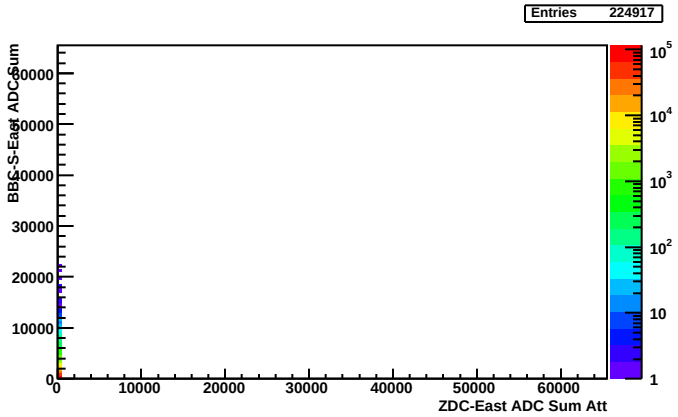
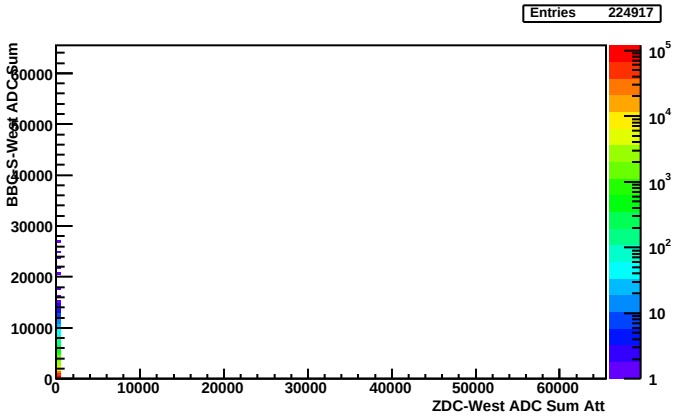
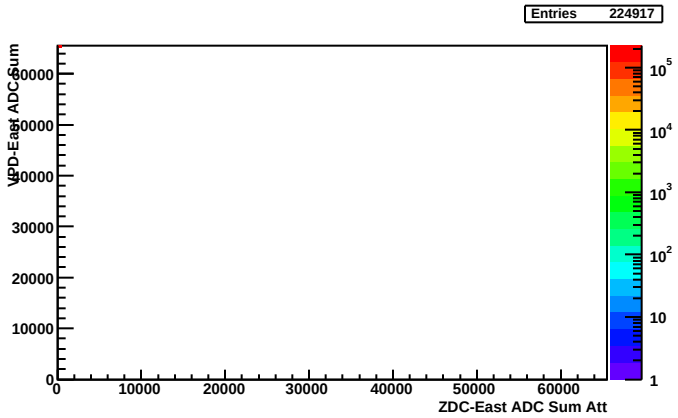
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

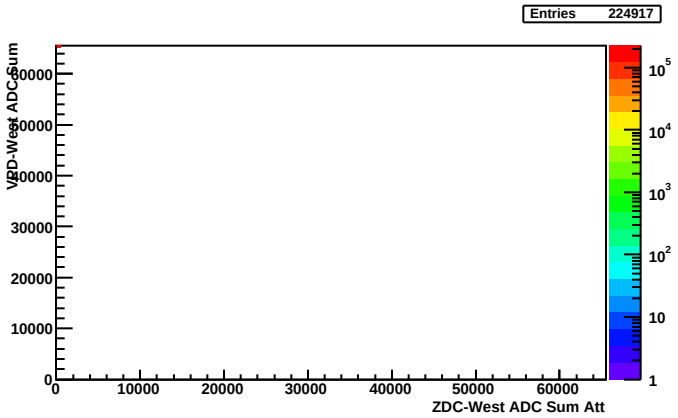

Entries 224917
BBC-S-East ADC Sum
VPD-East ADC Sum

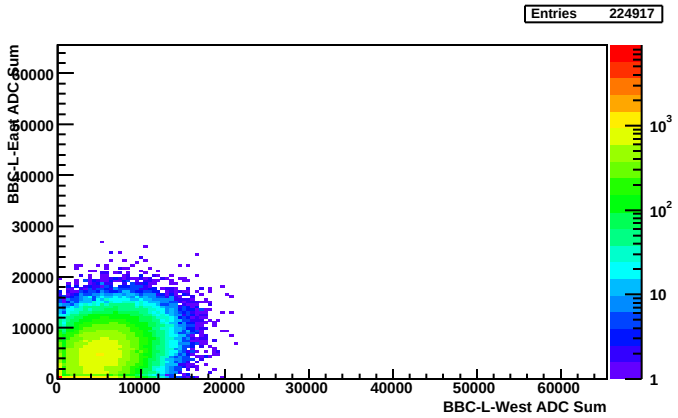

Entries 224917
BBC-S-West ADC Sum
VPD-West ADC Sum

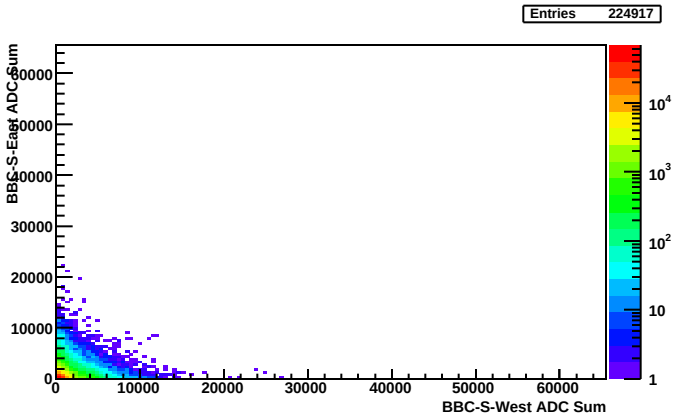

Entries 224917
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

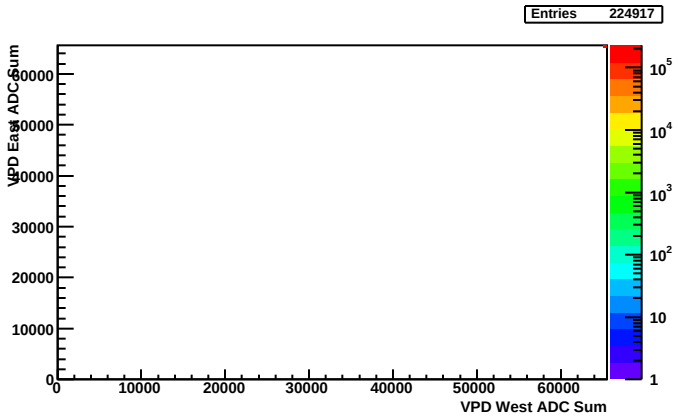

Entries 224917
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

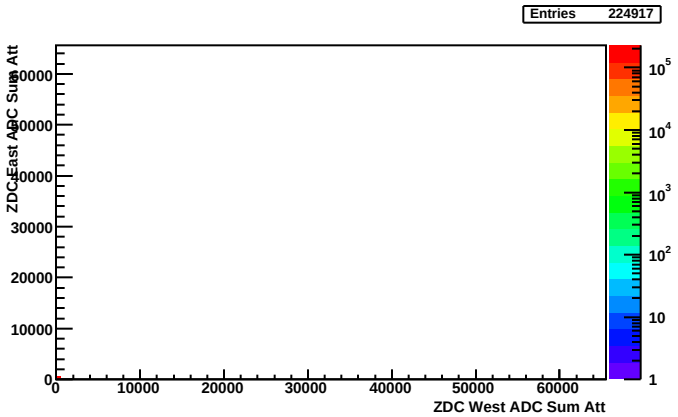

Entries 224917
VPD-East ADC Sum
ZDC-East ADC Sum Att

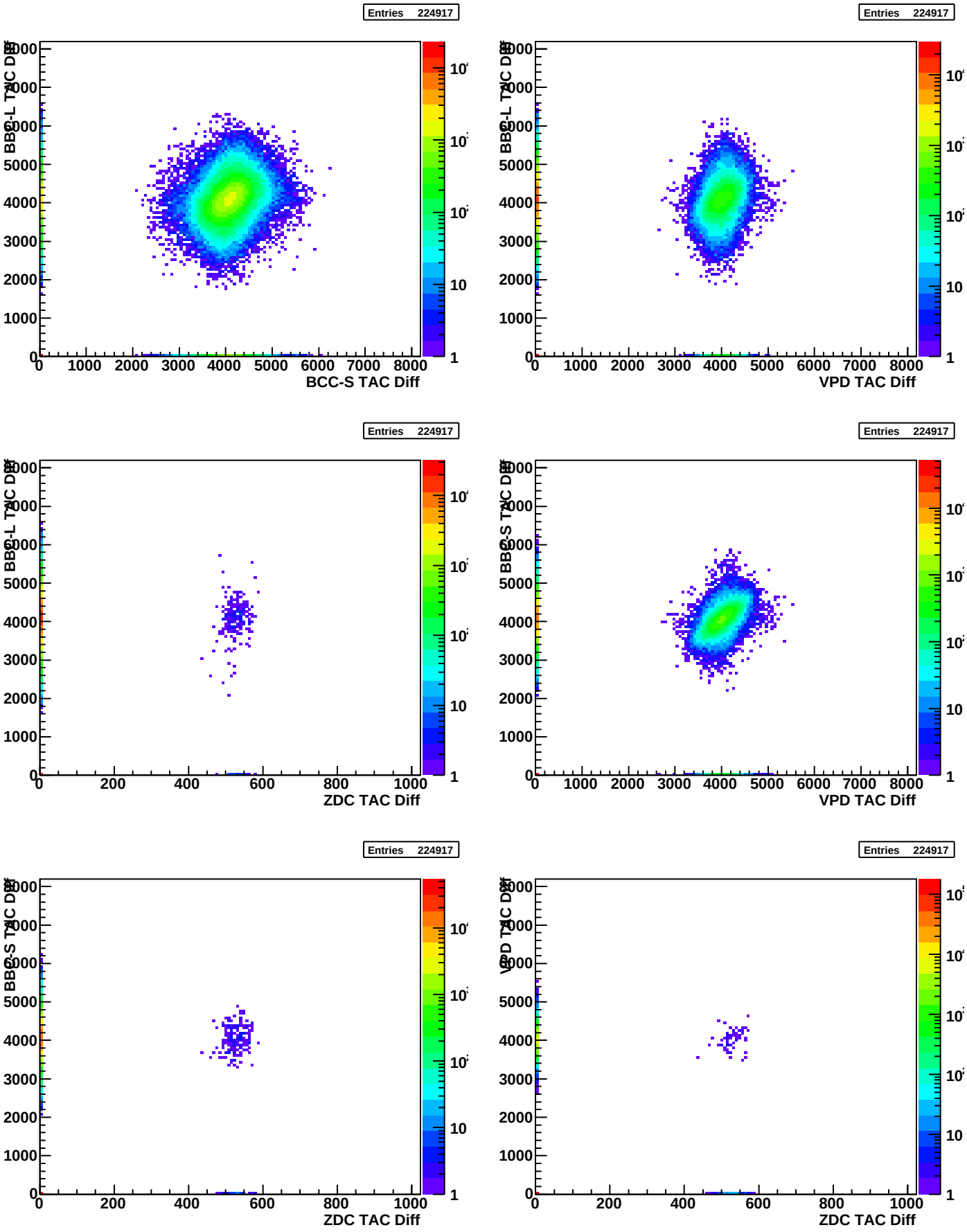

Entries 224917
VPD-West ADC Sum
ZDC-West ADC Sum Att

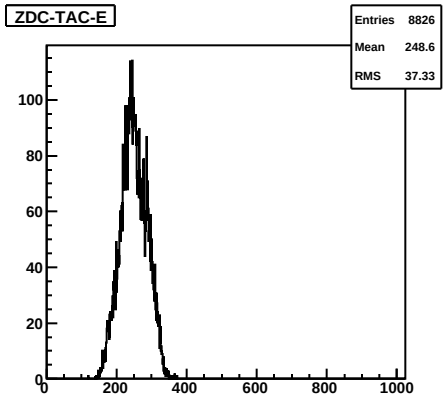

Entries 224917
BBC-L-East ADC Sum
BBC-L-West ADC Sum

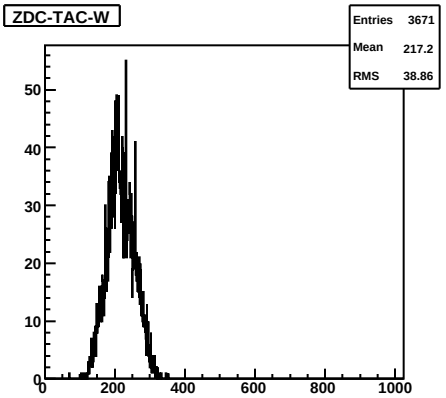

Entries 224917
BBC-S-East ADC Sum
BBC-S-West ADC Sum

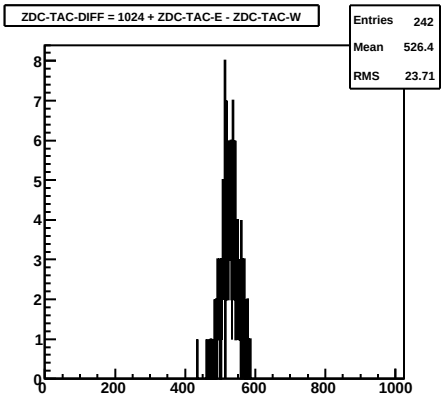

Entries 224917
VPD East ADC Sum
VPD West ADC Sum

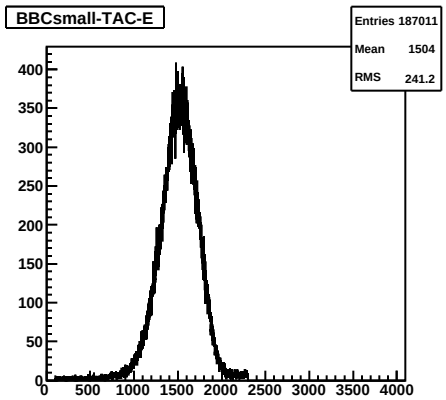

Entries 224917
ZDC East ADC Sum Att
ZDC West ADC Sum Att

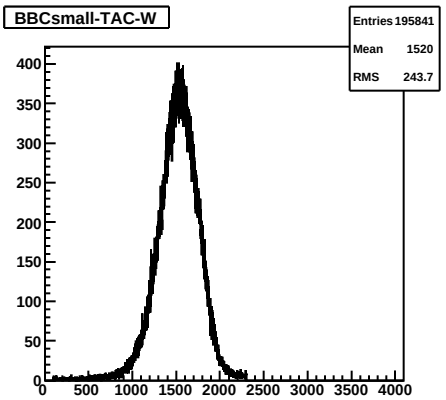

Entries 224917
BBC-L TAC Diff
BCC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff
BBC-S TAC Diff

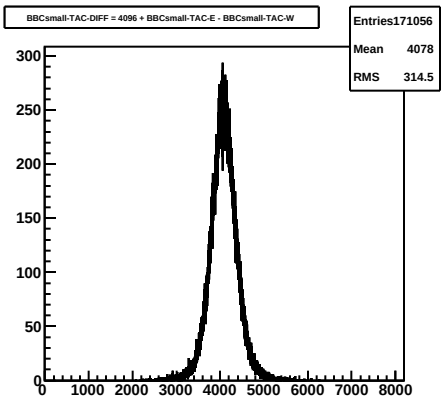

ZDC-TAC-E
Entries 8826
Mean 248.6
RMS 37.33

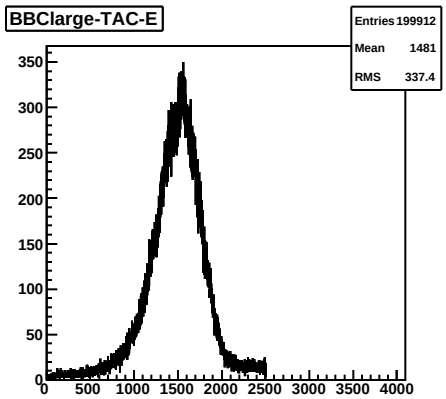

ZDC-TAC-W
Entries 3671
Mean 217.2
RMS 38.86

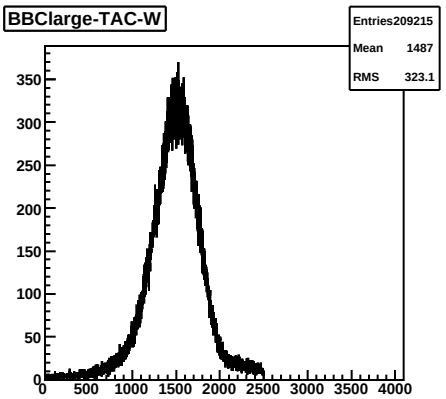

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 242
Mean 526.4
RMS 23.71

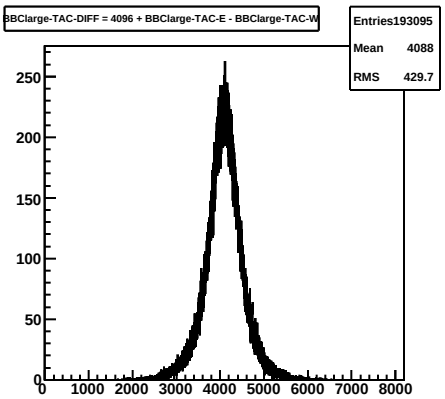

BBCsmall-TAC-E
Entries 187011
Mean 1504
RMS 241.2

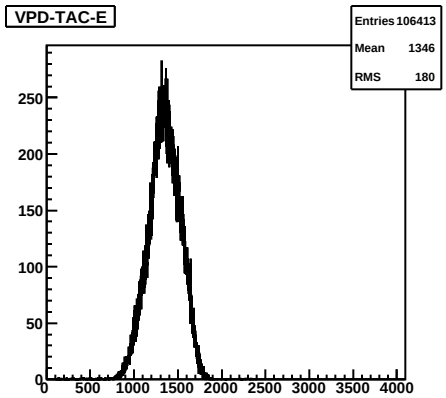

BBCsmall-TAC-W
Entries 195841
Mean 1520
RMS 243.7

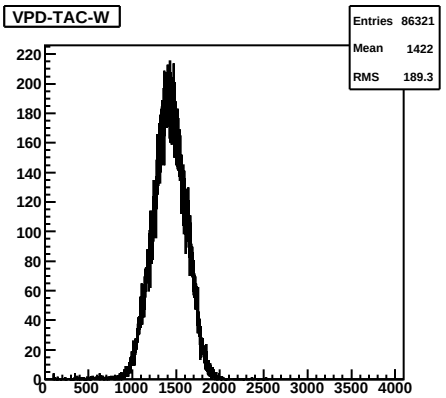

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries171056
Mean 4078
RMS 314.5

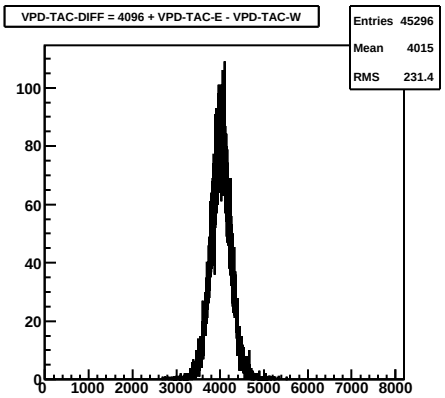

BBClarge-TAC-E
Entries 199912
Mean 1481
RMS 337.4

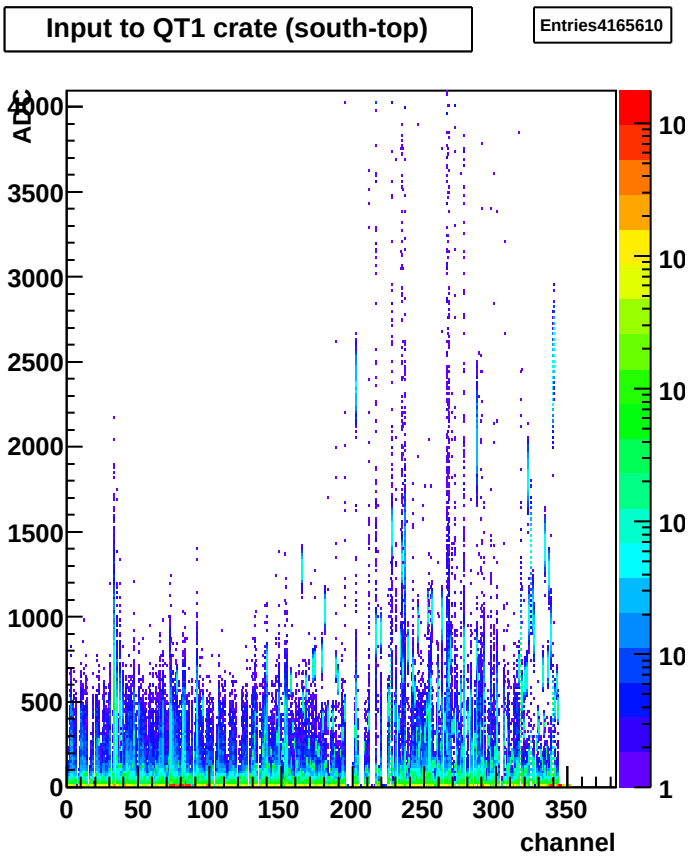

BBClarge-TAC-W
Entries209215
Mean 1487
RMS 323.1

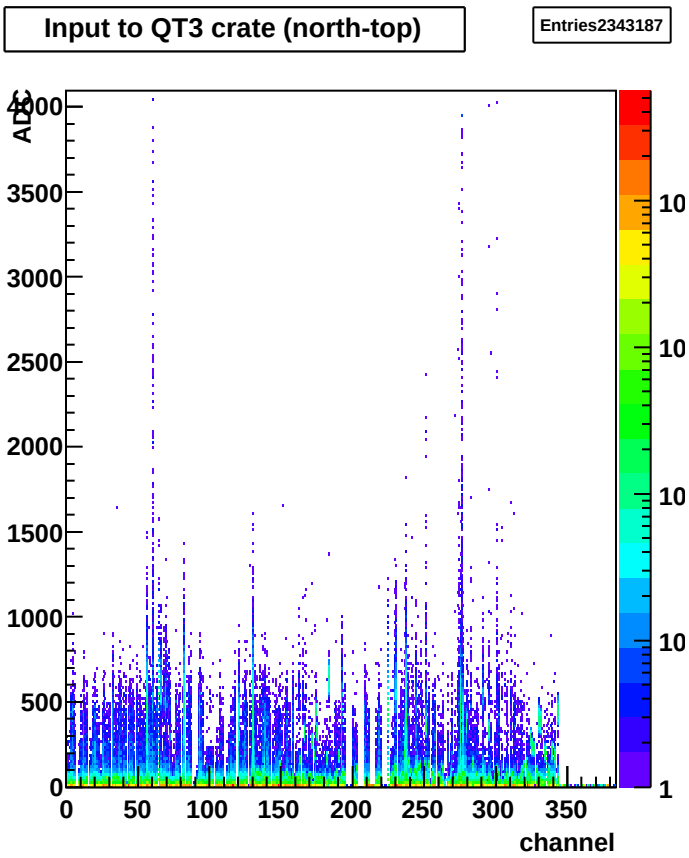

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries193095
Mean 4088
RMS 429.7

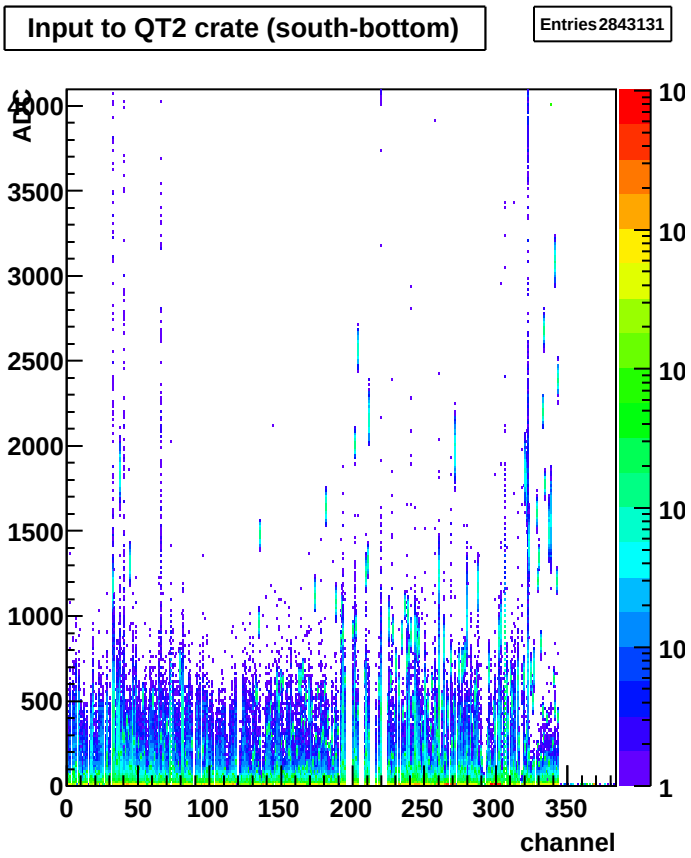

VPD-TAC-E
Entries 106413
Mean 1346
RMS 180

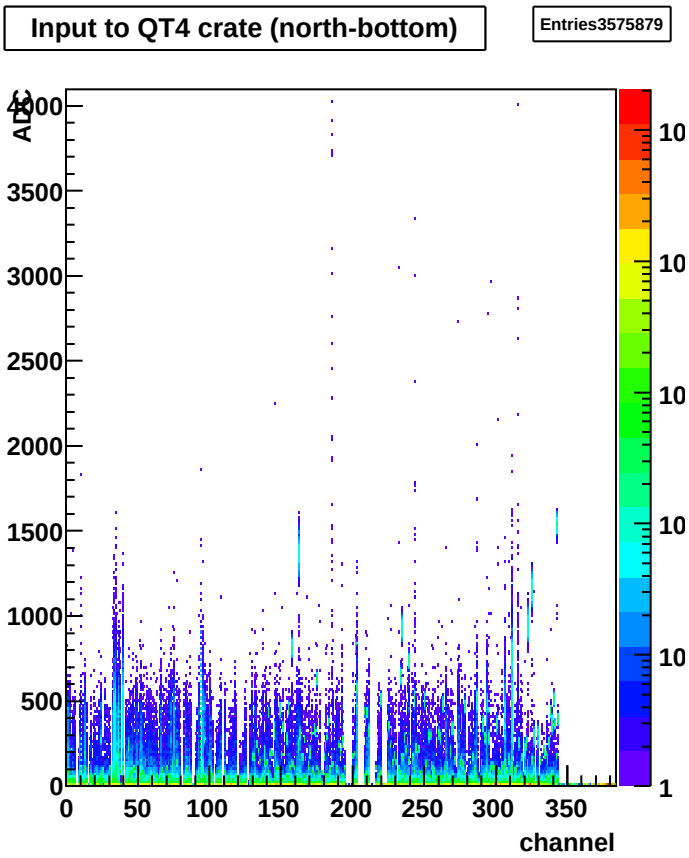

VPD-TAC-W
Entries 86321
Mean 1422
RMS 189.3

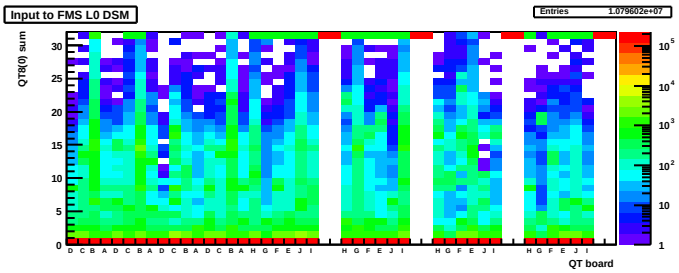

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 45296
Mean 4015
RMS 231.4

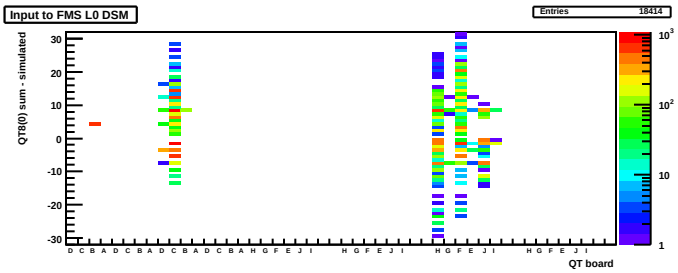

Input to QT1 crate (south-top)
Entries4165610
ADC
channel

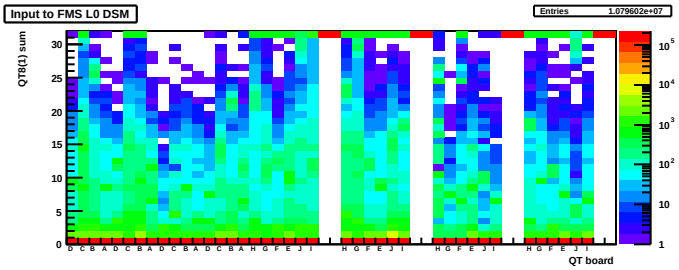

Input to QT3 crate (north-top)
Entries2343187
ADC
channel

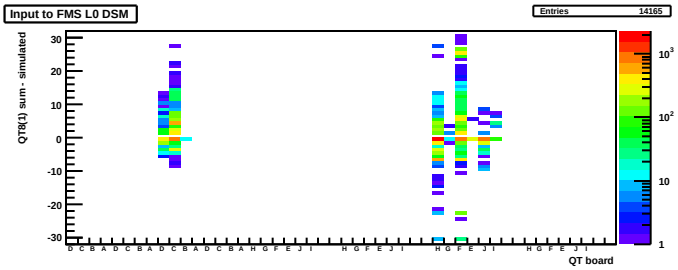

Input to QT2 crate (south-bottom)
Entries2843131
ADC
channel

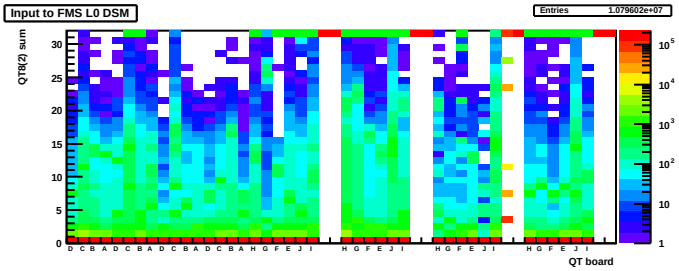

Input to QT4 crate (north-bottom)
Entries3575879
ADC
channel

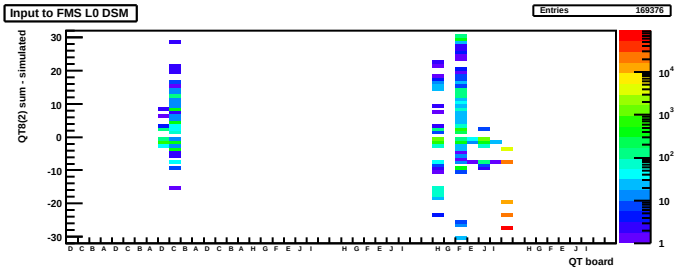

Input to FMS L0 DSM
Entries 1.079602e+07
QT8(0) sum
QT board

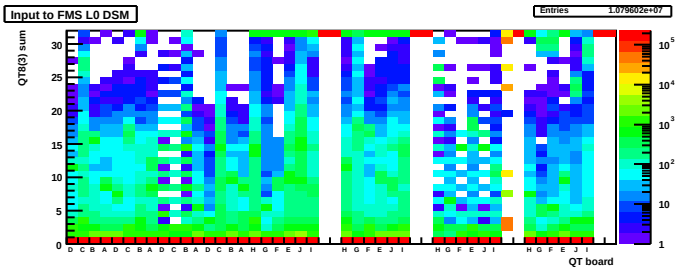

Input to FMS L0 DSM
Entries 18414
QT8(0) sum - simulated
QT board

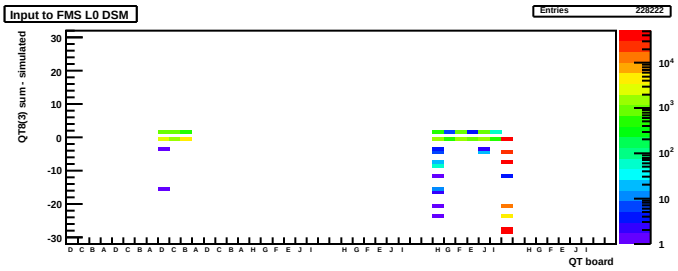

Input to FMS L0 DSM
Entries 1.079602e+07
QT8(1) sum
QT board

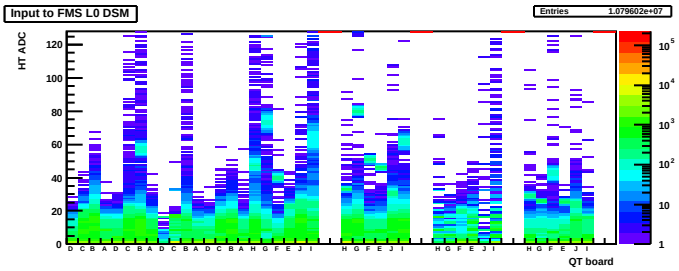

Input to FMS L0 DSM
Entries 14165
QT8(1) sum - simulated
QT board

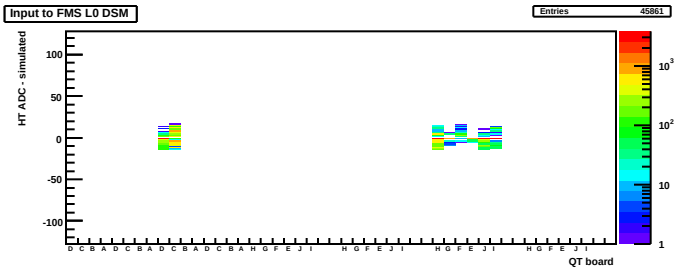

Input to FMS L0 DSM
Entries 1.079602e+07
QT8(2) sum
QT board

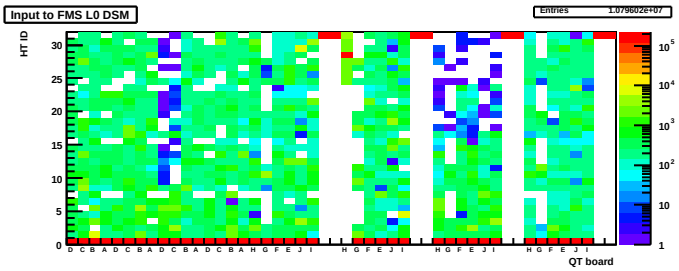

Input to FMS L0 DSM
Entries 169376
QT8(2) sum - simulated
QT board

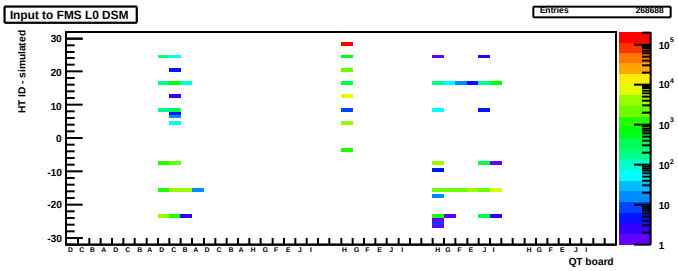

Input to FMS L0 DSM
Entries 1.079602e+07
QT8(3) sum
QT board

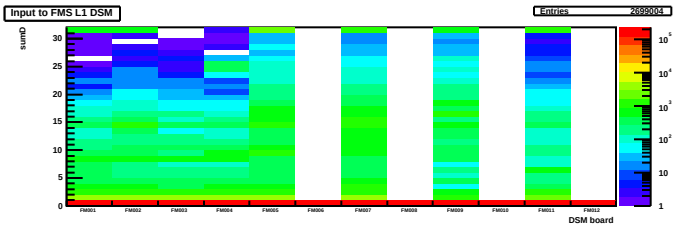

Input to FMS L0 DSM
Entries 228222
QT8(3) sum - simulated
QT board

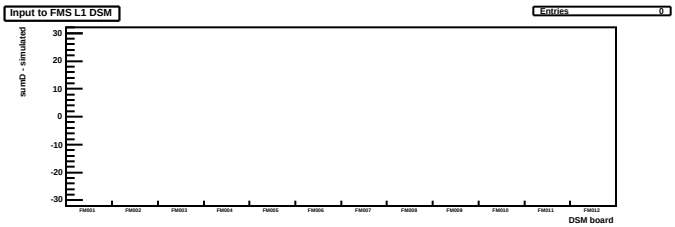

Input to FMS L0 DSM
Entries 1.079602e+07
HT ADC
QT board

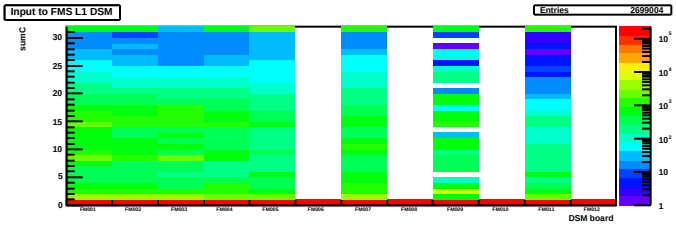

Input to FMS L0 DSM
Entries 45861
HT ADC - simulated
QT board

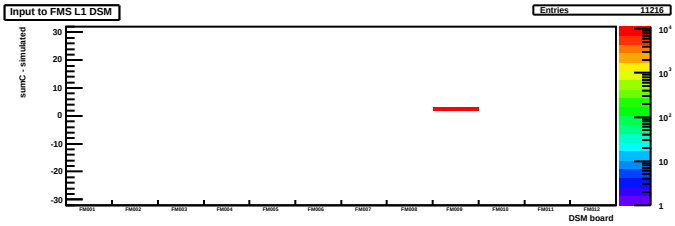

Input to FMS L0 DSM
Entries 1.079602e+07
HT ID
QT board

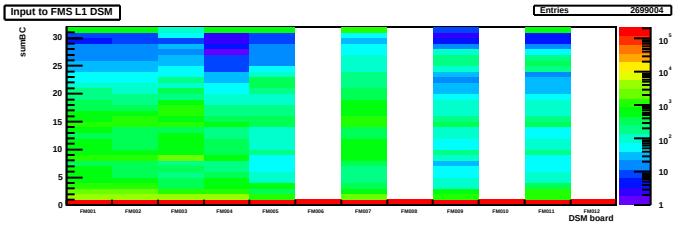

Input to FMS L0 DSM
Entries 268688
HT ID - simulated
QT board

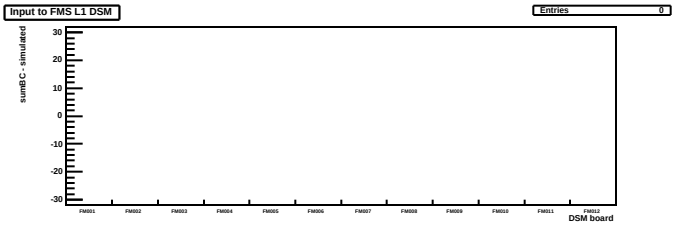

Input to FMS L1 DSM
Entries 2699004
sumD
DSM board

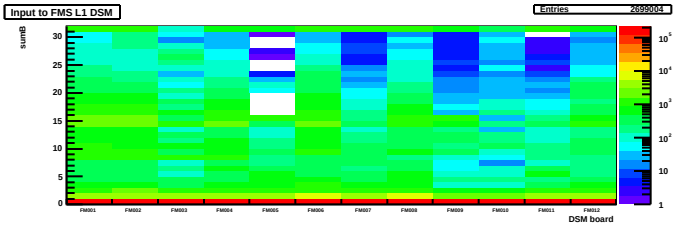

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

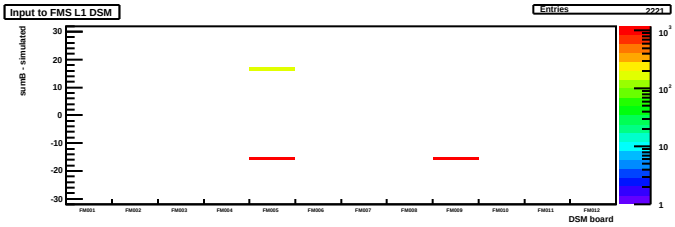

Input to FMS L1 DSM
Entries 2699004
sumC
DSM board

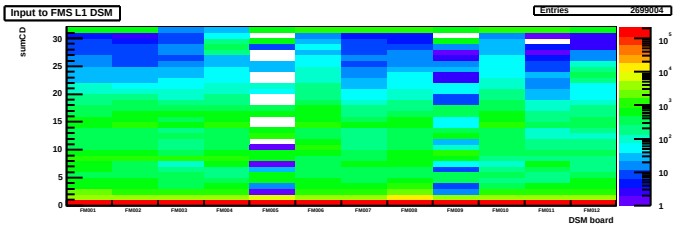

Input to FMS L1 DSM
Entries 11216
sumC - simulated
DSM board

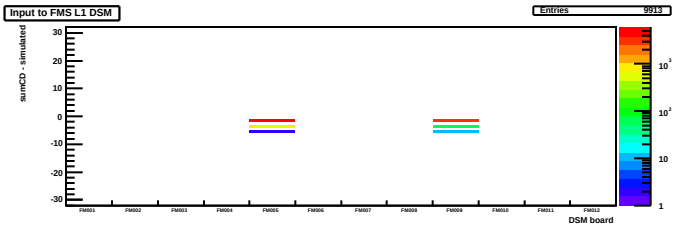

Input to FMS L1 DSM
Entries 2699004
sumBC
DSM board

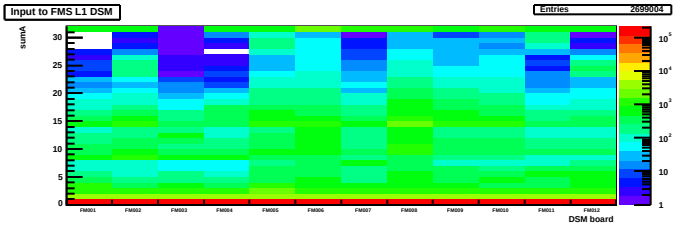

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

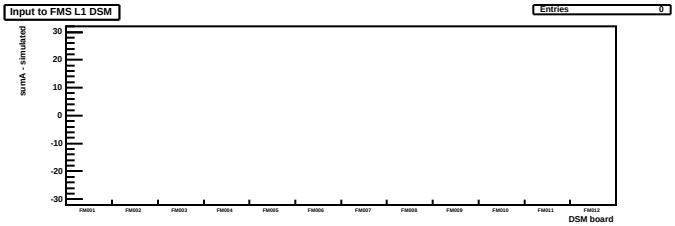

Input to FMS L1 DSM
Entries 2699004
sumB
DSM board

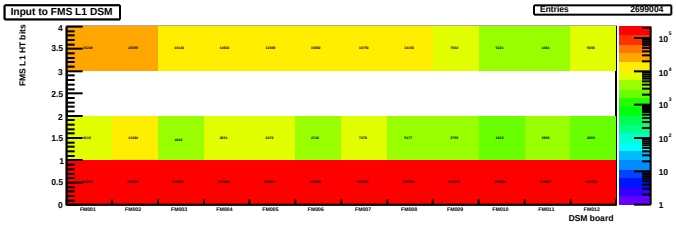

Input to FMS L1 DSM
Entries 2221
sumB - simulated
DSM board

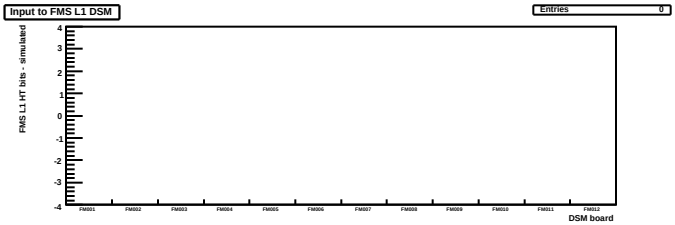

Input to FMS L1 DSM
Entries 2699004
sumCD
DSM board

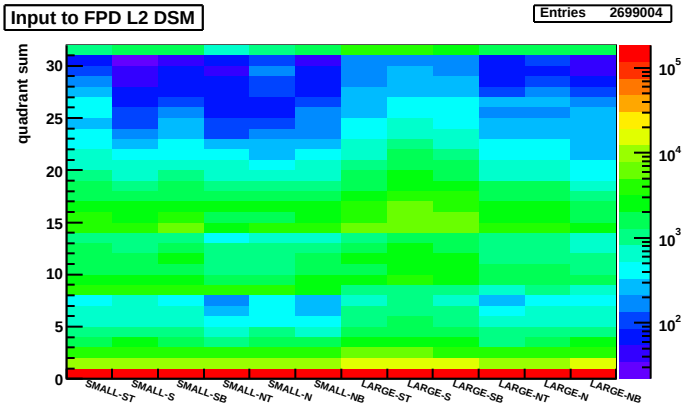

Input to FMS L1 DSM
Entries 9913
sumCD - simulated
DSM board

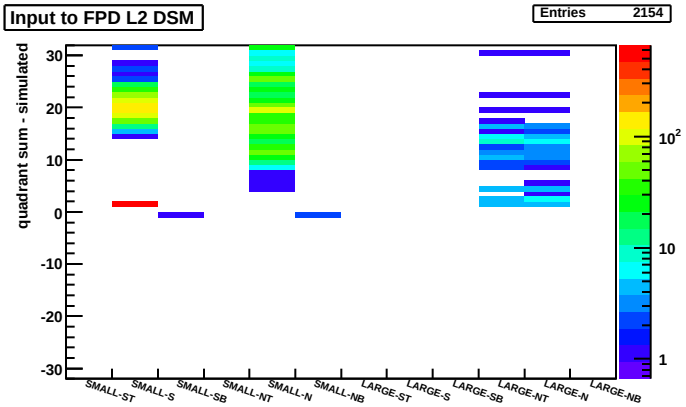

Input to FMS L1 DSM
Entries 2699004
sumA
DSM board

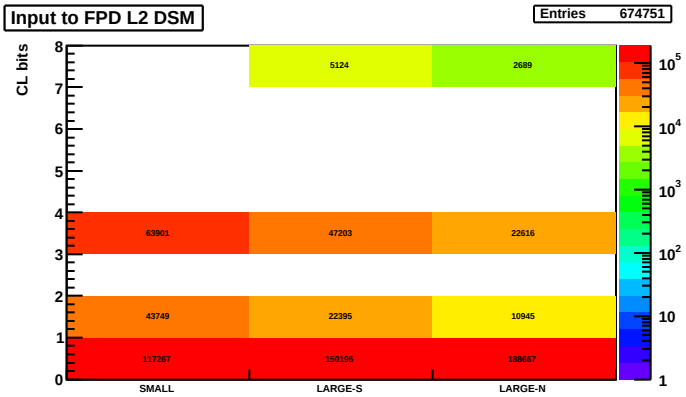

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

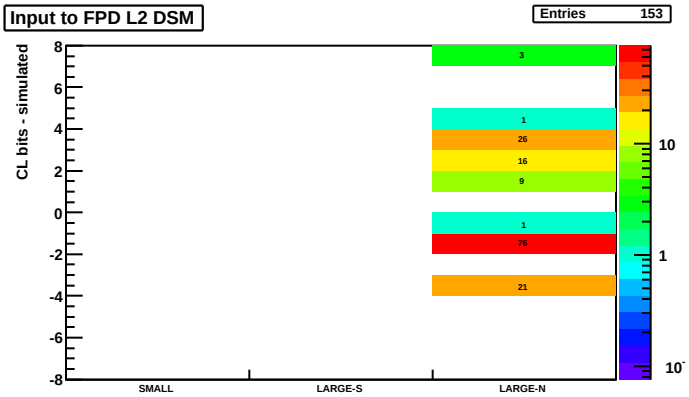

Input to FMS L1 DSM
Entries 2699004
FMS L1 HT bits
DSM board

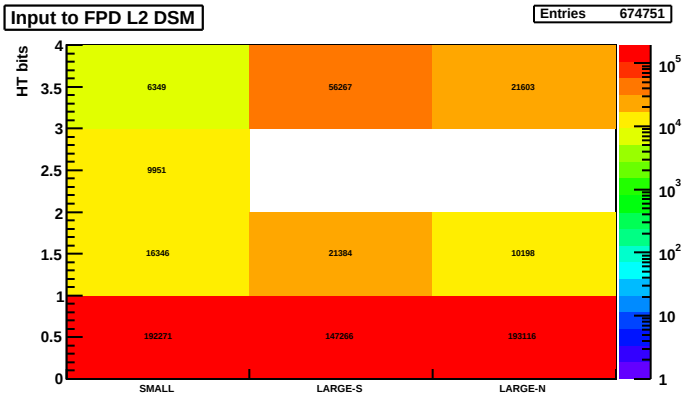

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

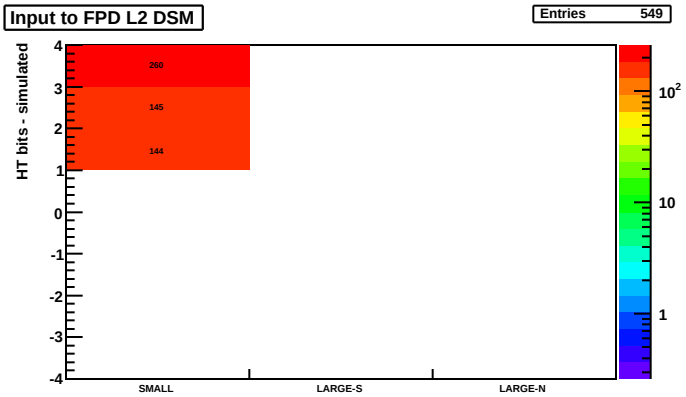

Input to FPD L2 DSM
Entries 2699004
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

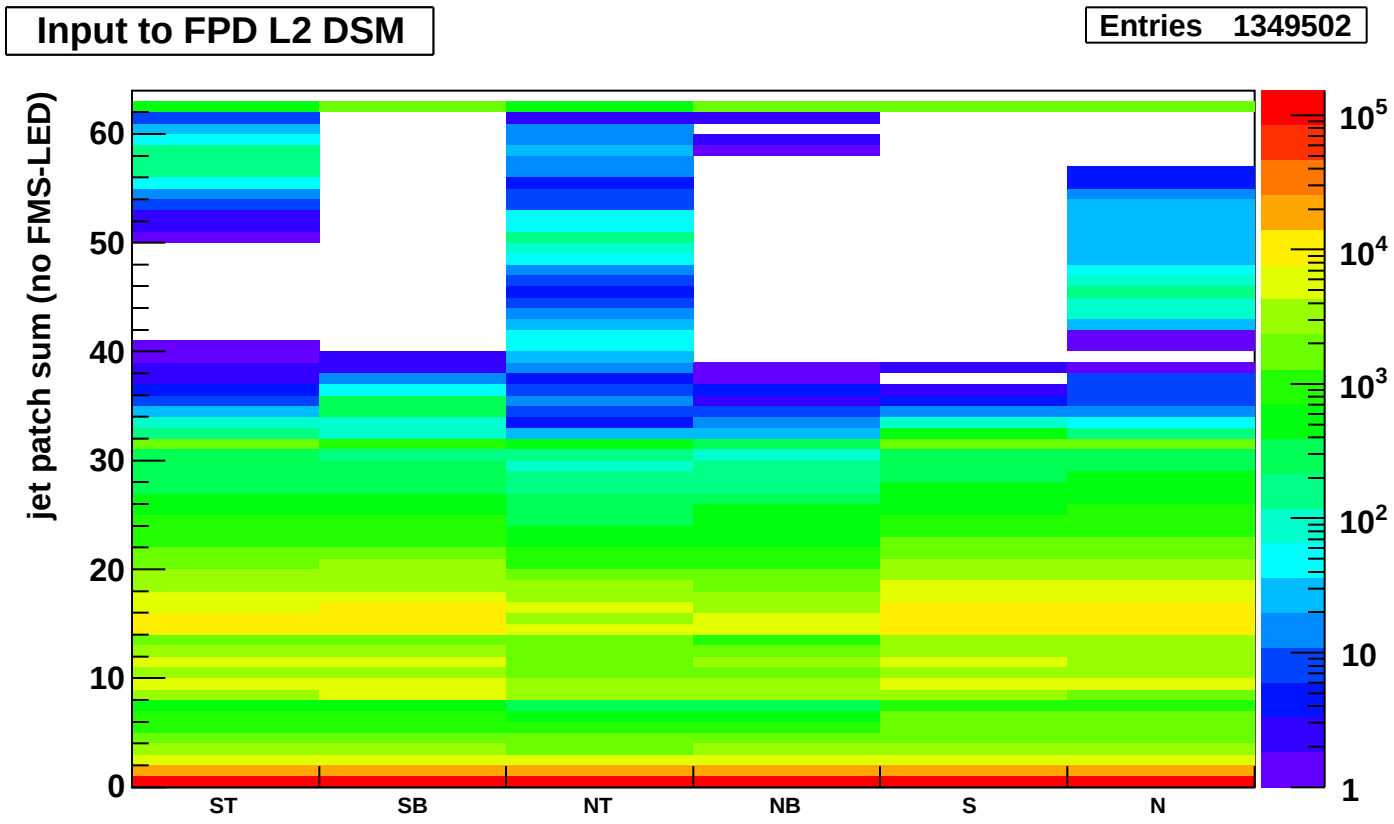

Input to FPD L2 DSM
Entries 2154
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

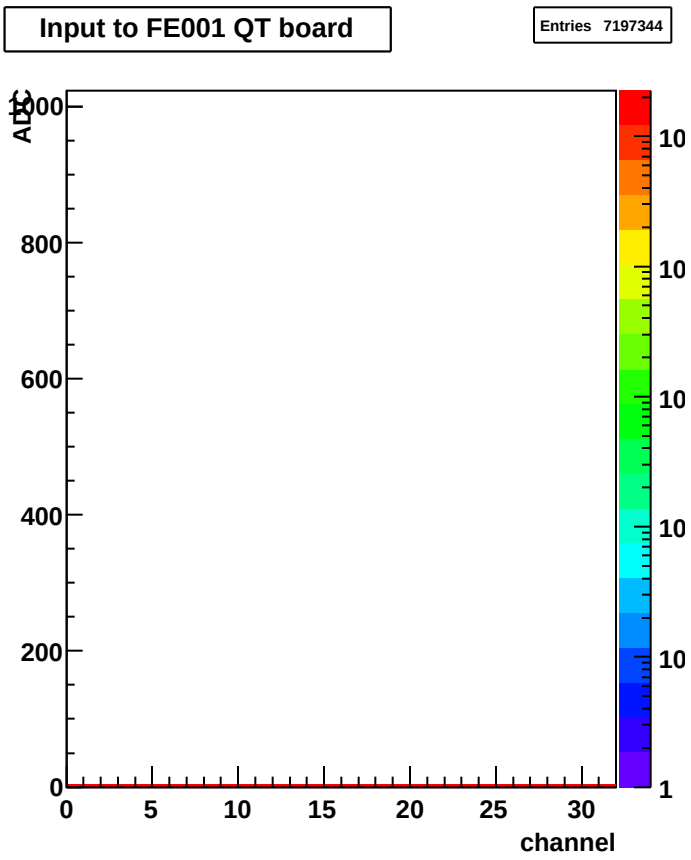

Input to FPD L2 DSM
Entries 674751
CL bits
5124
2689
63901
47203
22616
43749
22395
10945
117267
150195
188667
SMALL
LARGE-S
LARGE-N

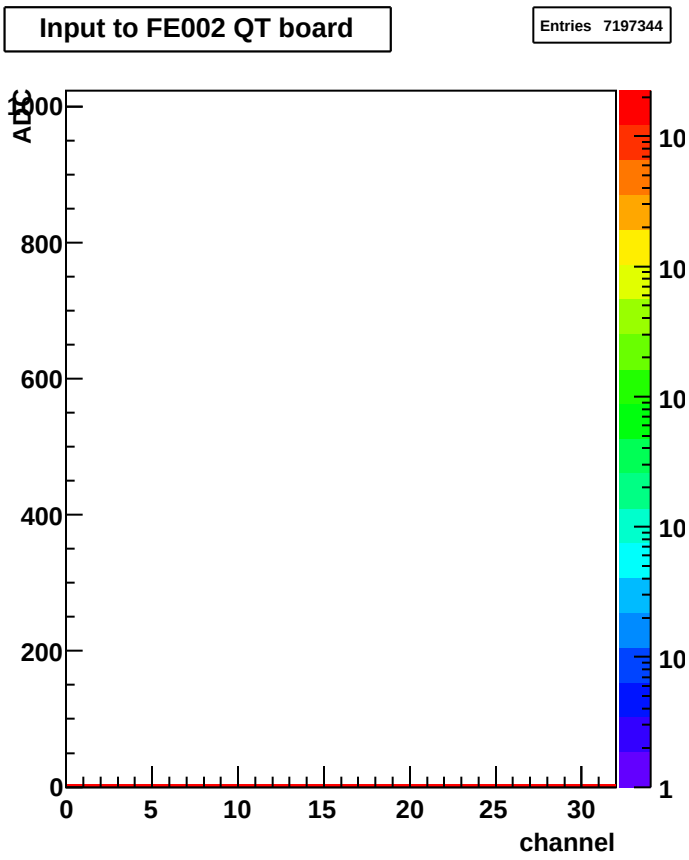

Input to FPD L2 DSM
Entries 153
CL bits - simulated
3
1
26
16
9
1
76
21
SMALL
LARGE-S
LARGE-N

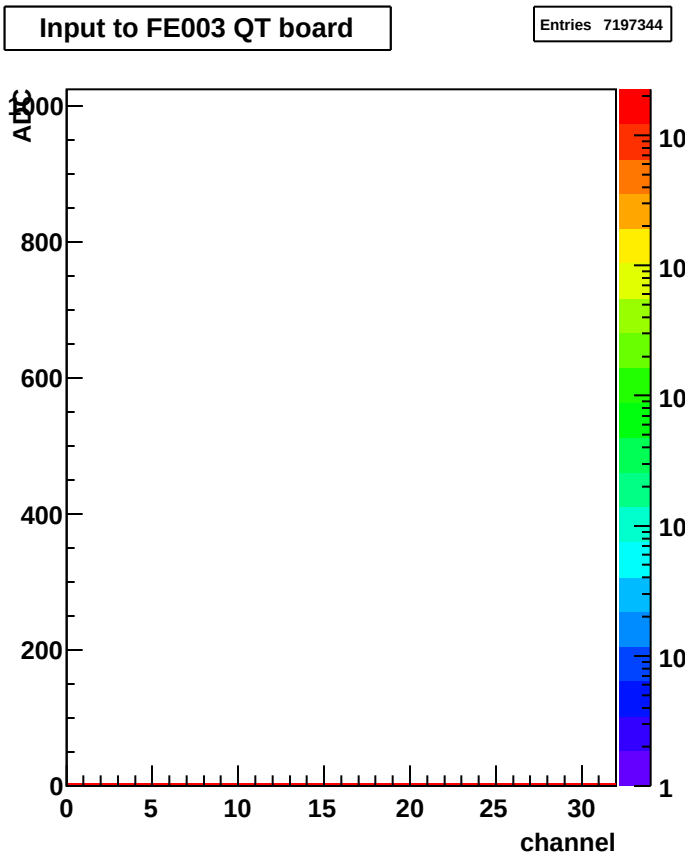

Input to FPD L2 DSM
Entries 674751
HT bits
6349
56267
21603
9951
16346
21384
10198
192271
147266
193116
SMALL
LARGE-S
LARGE-N

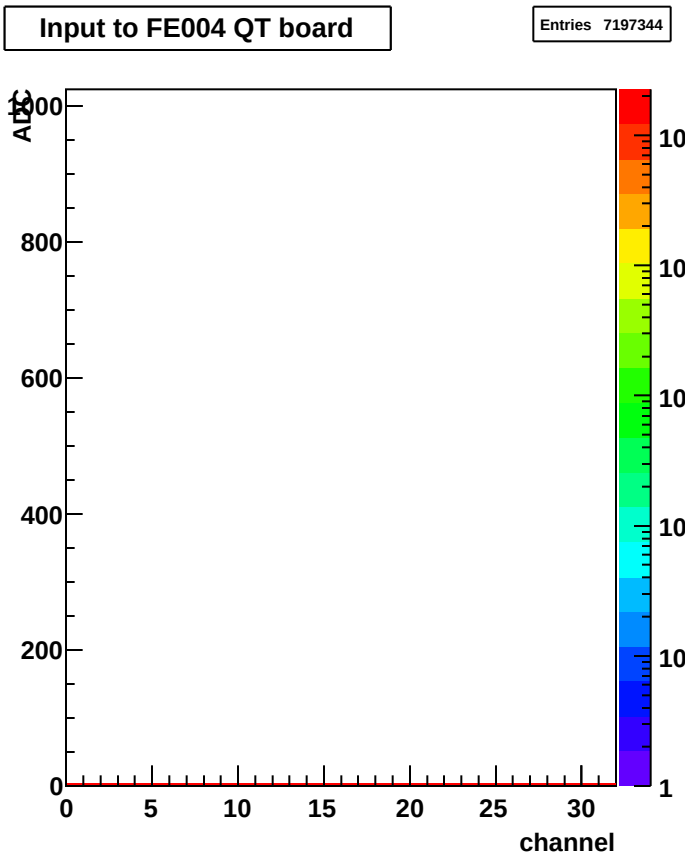

Input to FPD L2 DSM
Entries 549
HT bits - simulated
260
145
144
SMALL
LARGE-S
LARGE-N

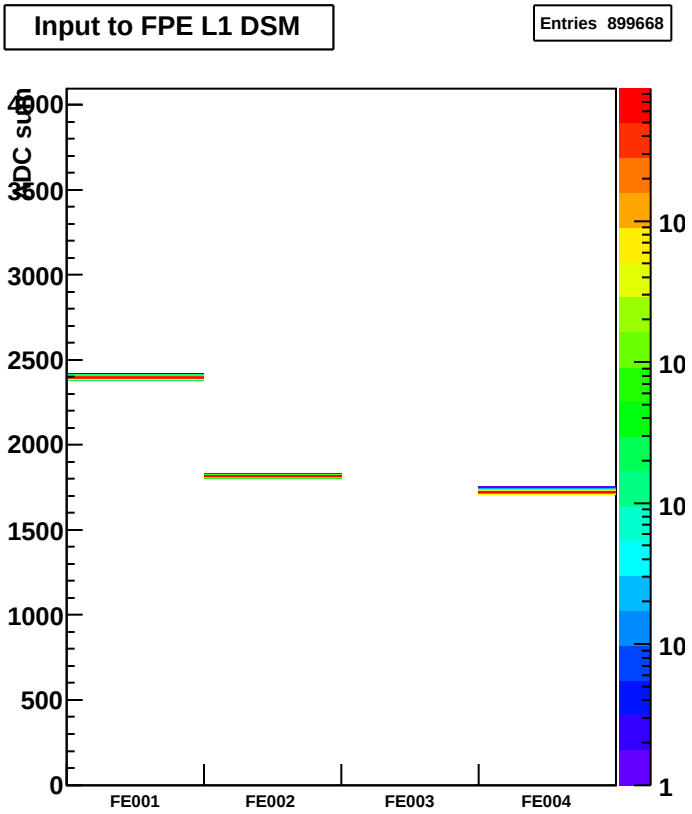

Input to FPD L2 DSM
Entries 1349502
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

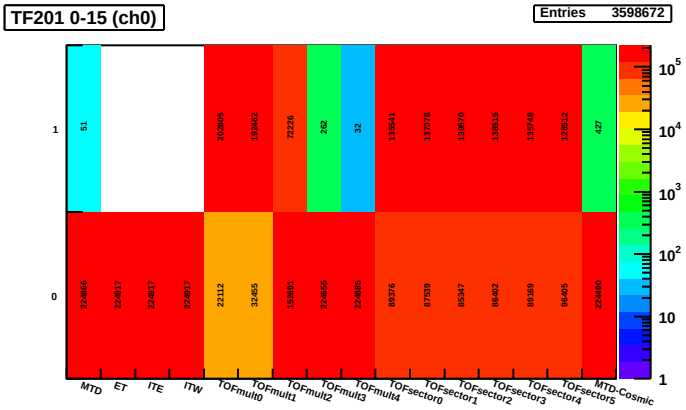

Input to FE001 QT board
Entries 7197344
ADC
channel

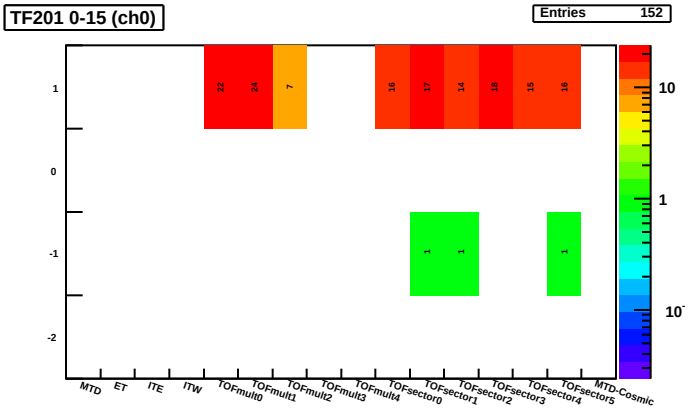

Input to FE002 QT board
Entries 7197344
ADC
channel

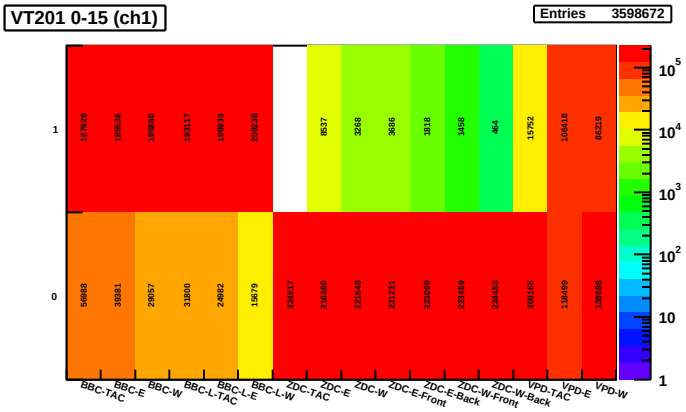

Input to FE003 QT board
Entries 7197344
ADC
channel

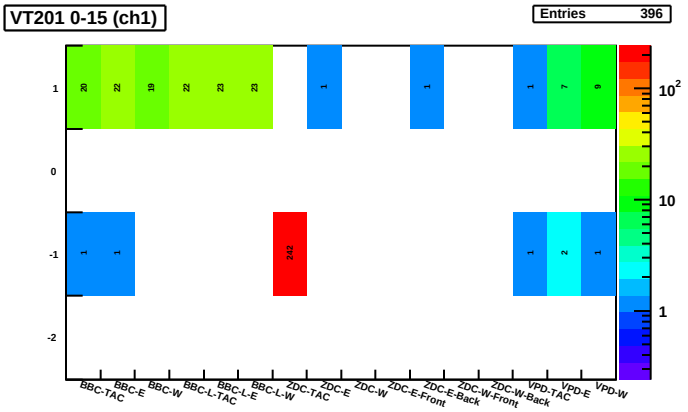

Input to FE004 QT board
Entries 7197344
ADC
channel

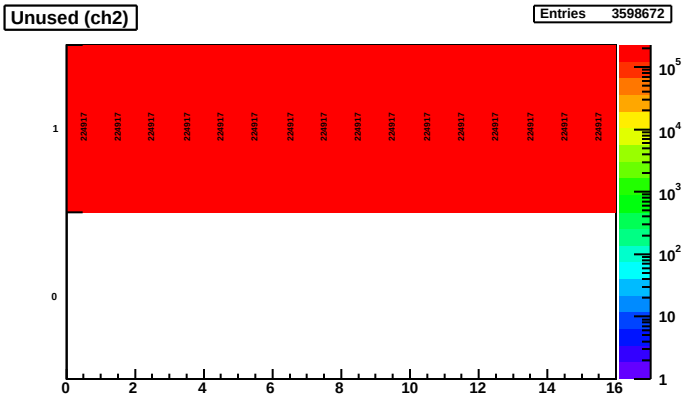

Input to FPE L1 DSM
Entries 899668
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

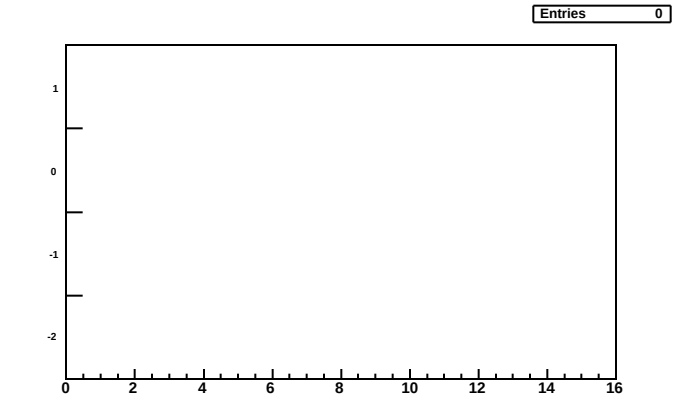

TF201 0-15 (ch0)
Entries 3598672
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 152
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3598672
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 396
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3598672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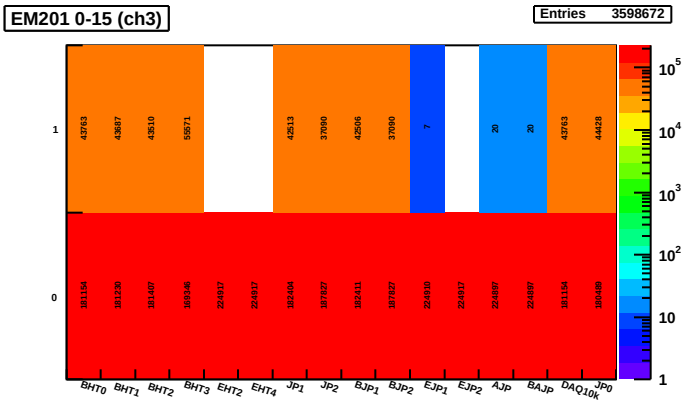
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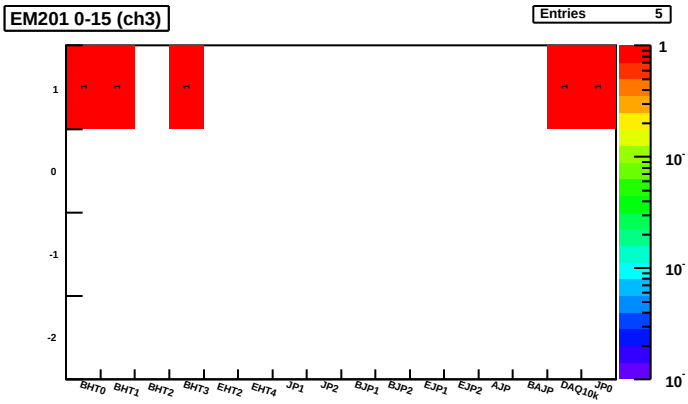
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Entries 5
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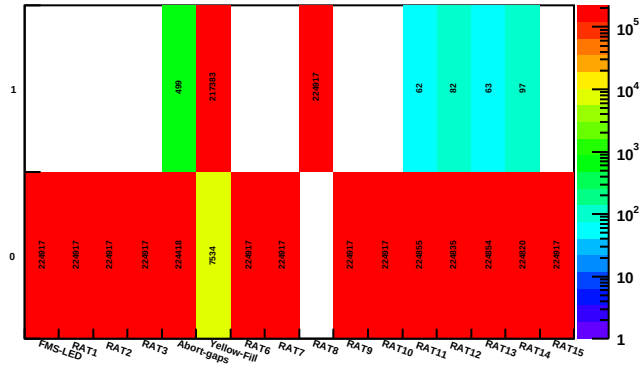
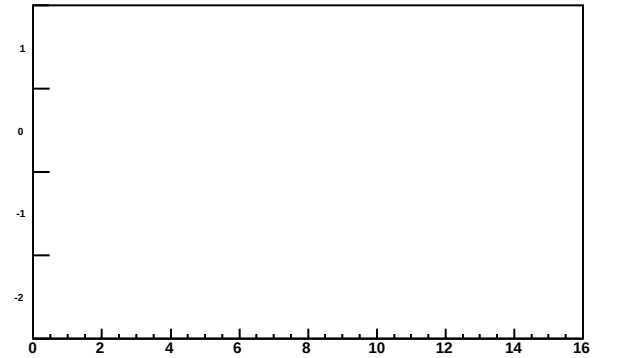
RAT board (ch4)





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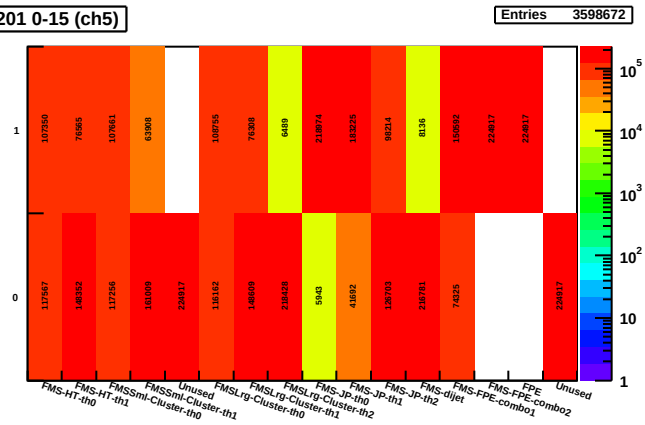

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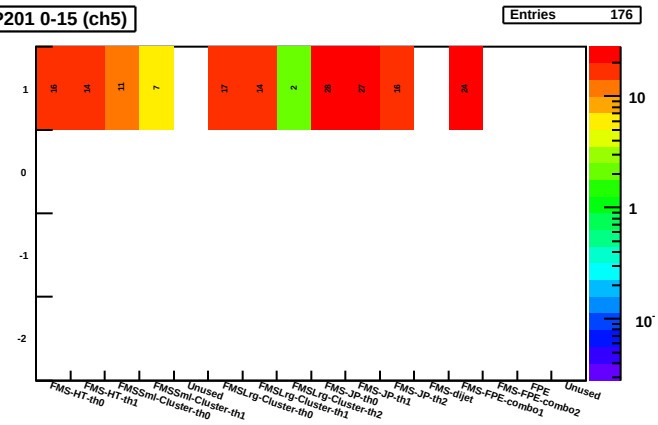
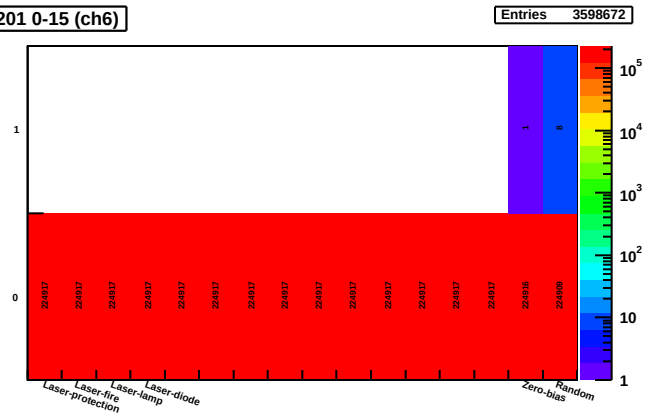
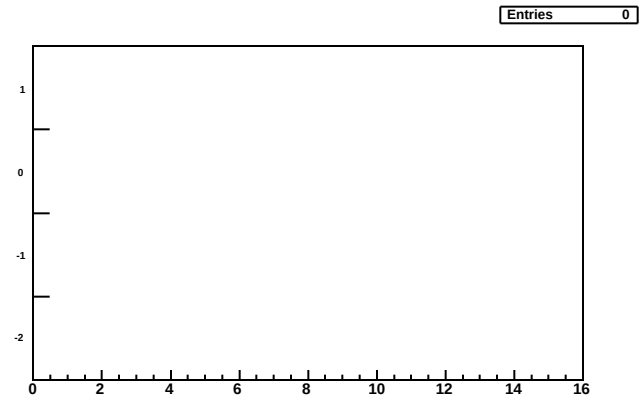
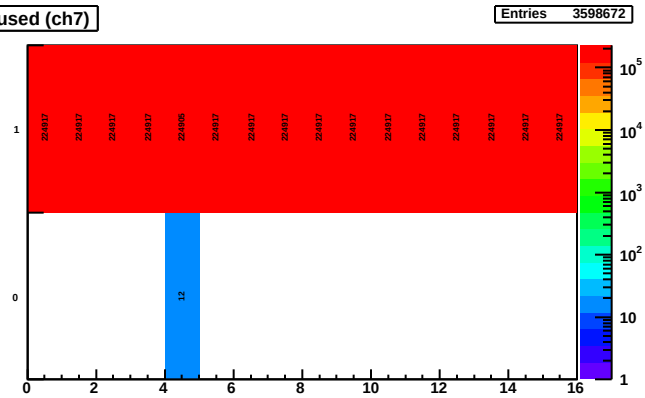
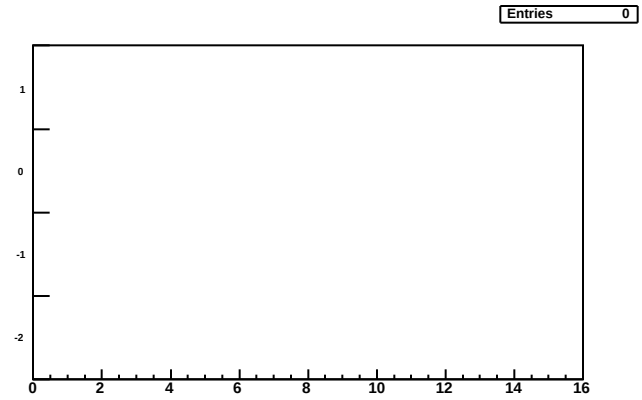


ST201 0-15 (ch6)





Unused (ch7)