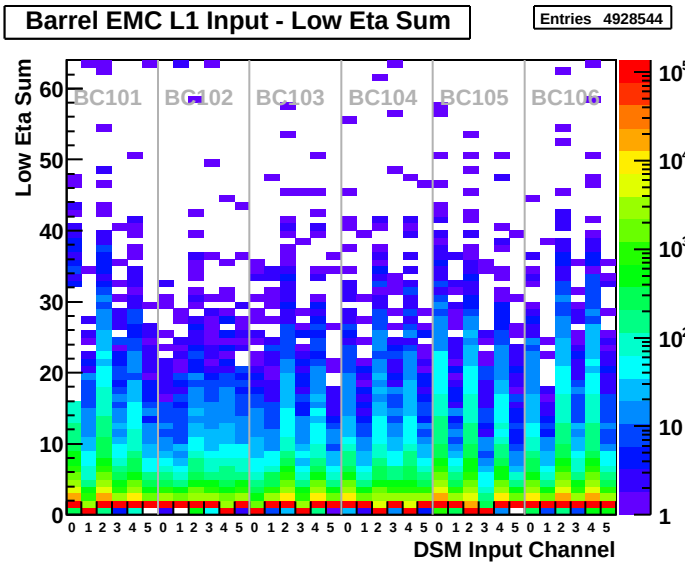
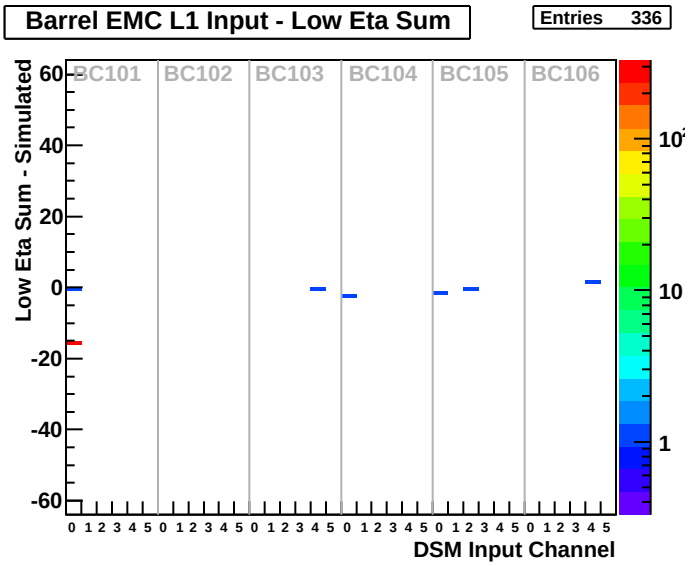
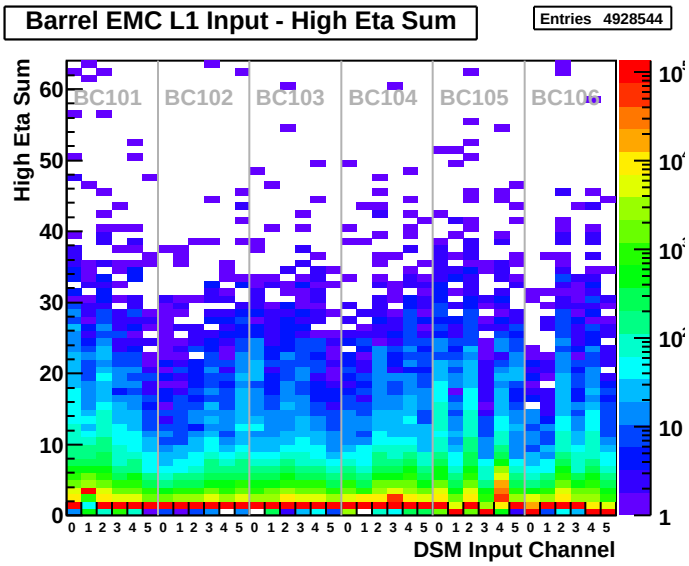
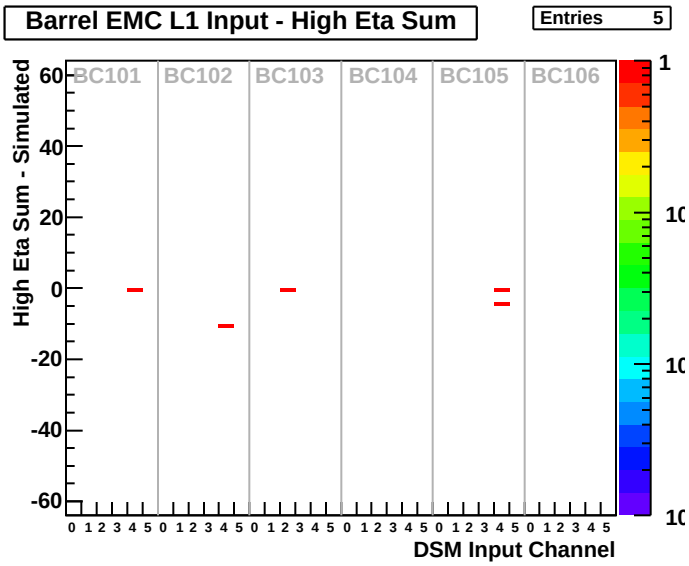
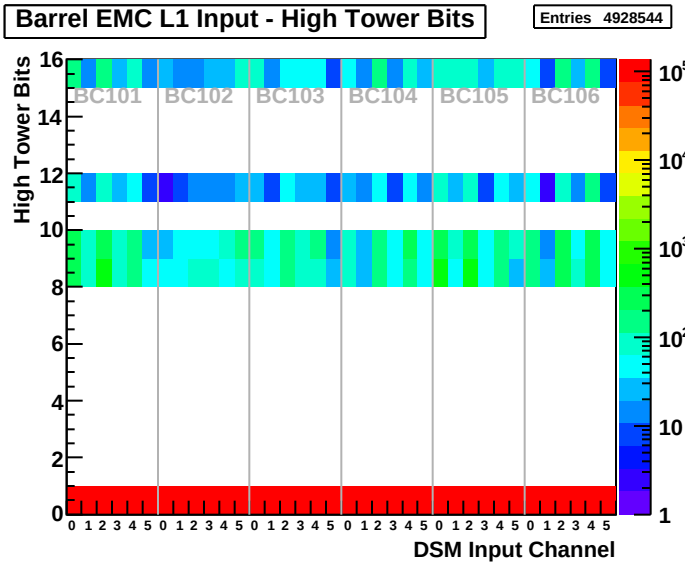
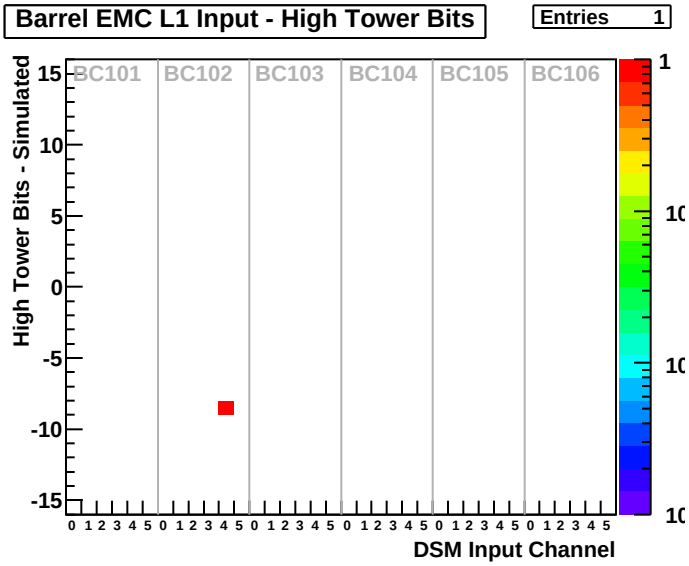

Barrel EMC L1 Input - Low Eta Sum
Entries 4928544
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

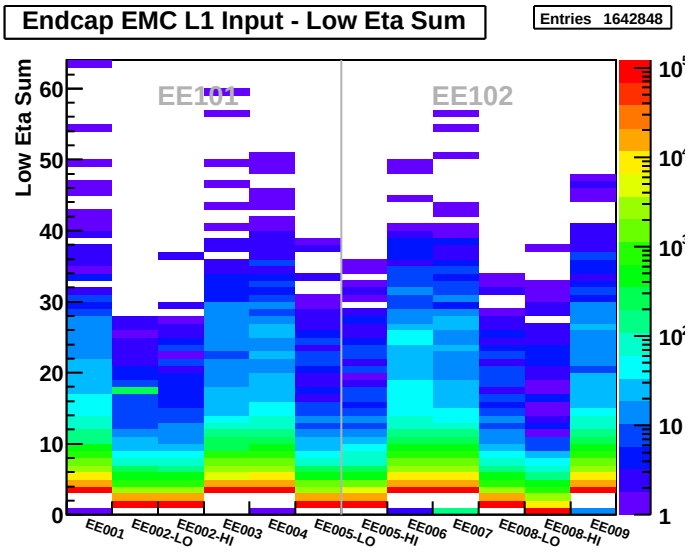

Barrel EMC L1 Input - Low Eta Sum
Entries 336
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

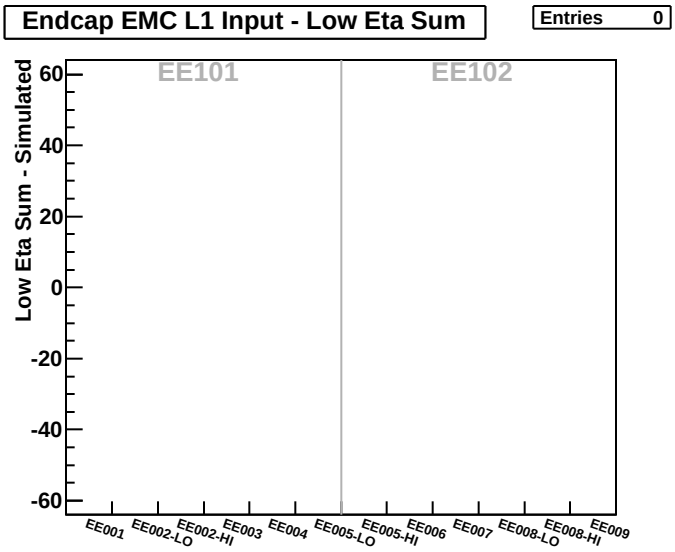

Barrel EMC L1 Input - High Eta Sum
Entries 4928544
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

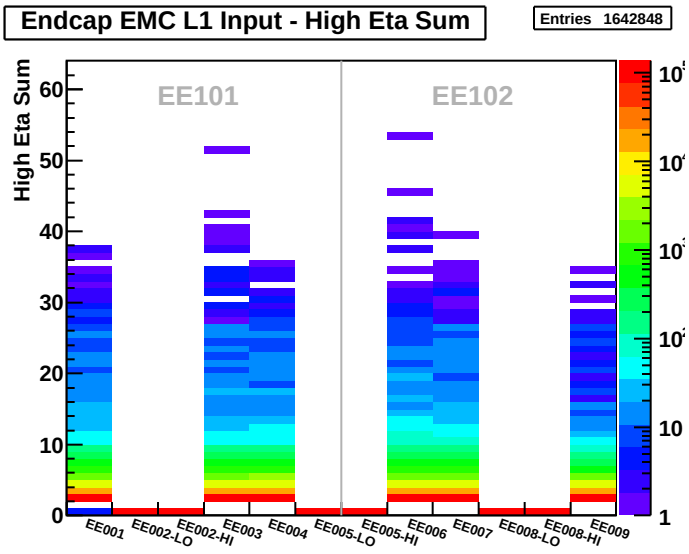

Barrel EMC L1 Input - High Eta Sum
Entries 5
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

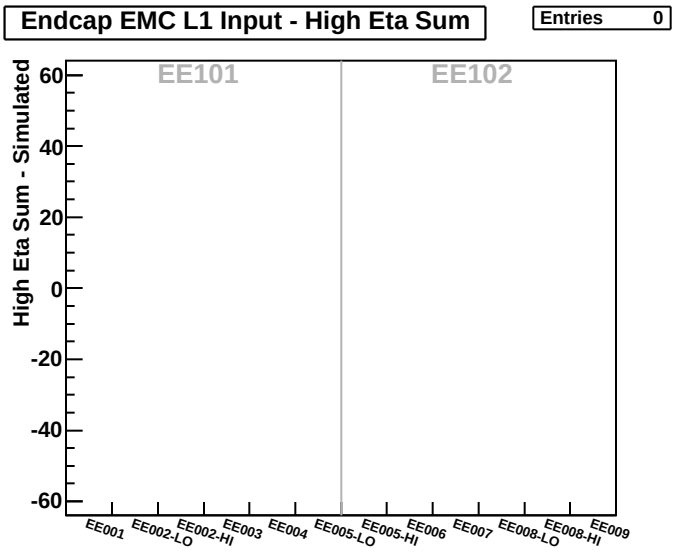

Barrel EMC L1 Input - High Tower Bits
Entries 4928544
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

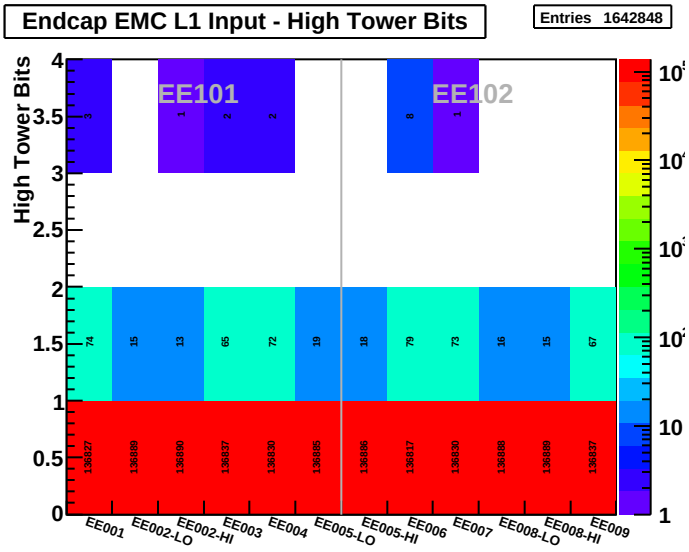

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

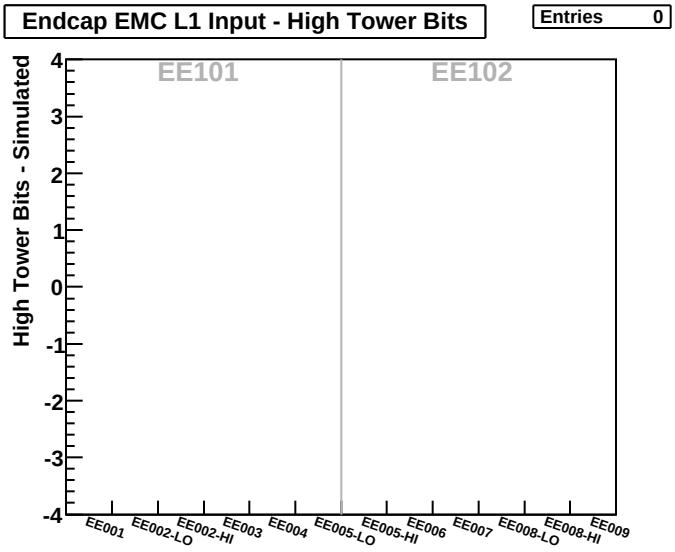

Endcap EMC L1 Input - Low Eta Sum
Entries 1642848
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

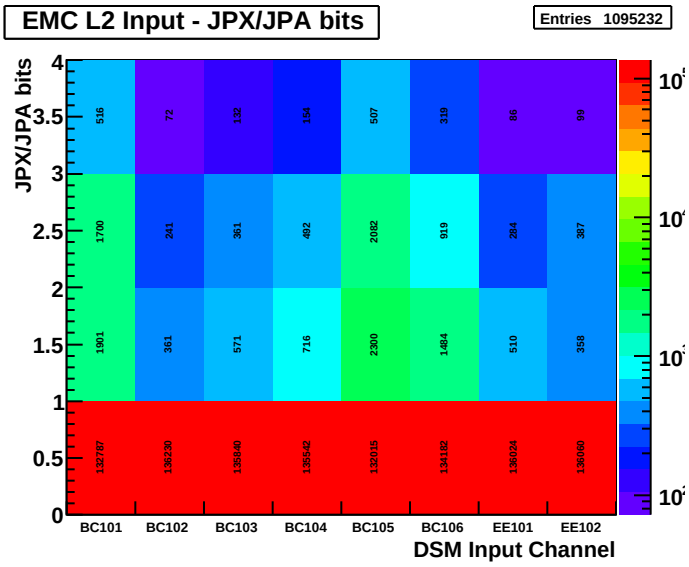

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

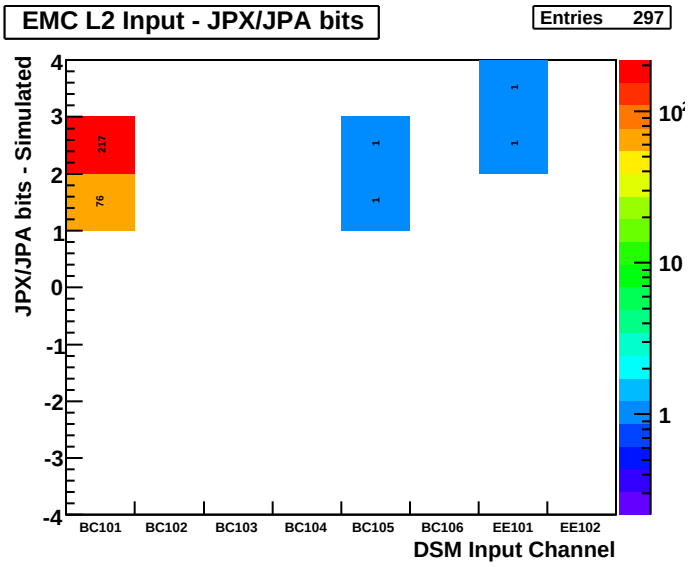

Endcap EMC L1 Input - High Eta Sum
Entries 1642848
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

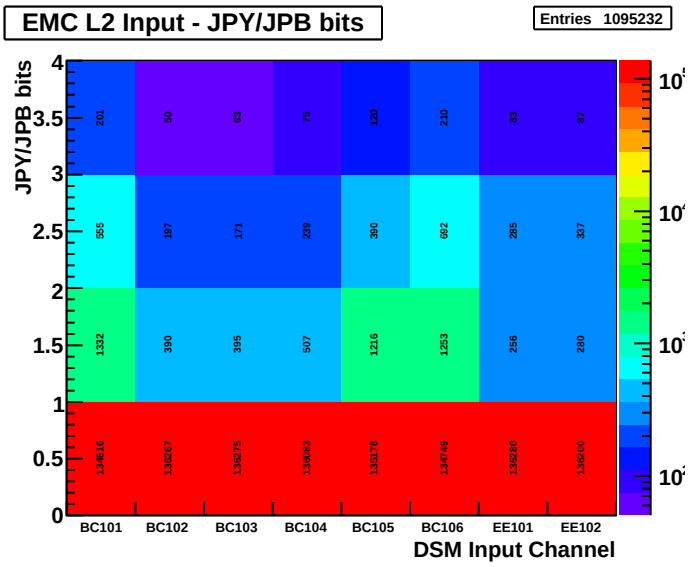

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

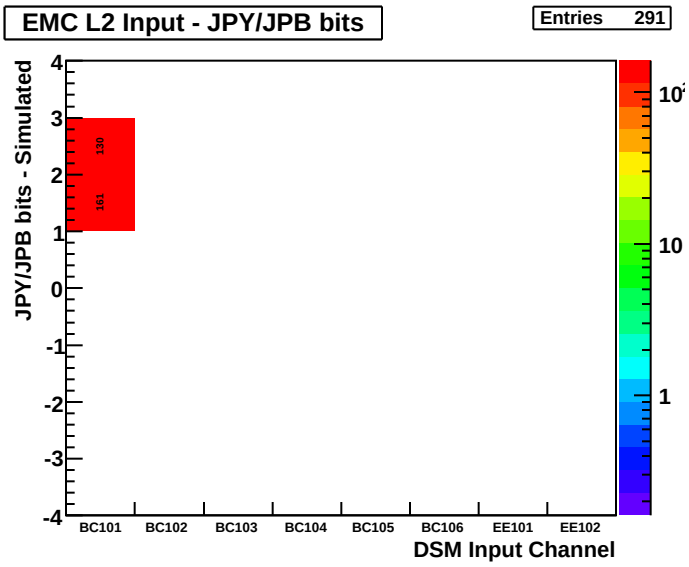

Endcap EMC L1 Input - High Tower Bits
Entries 1642848
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

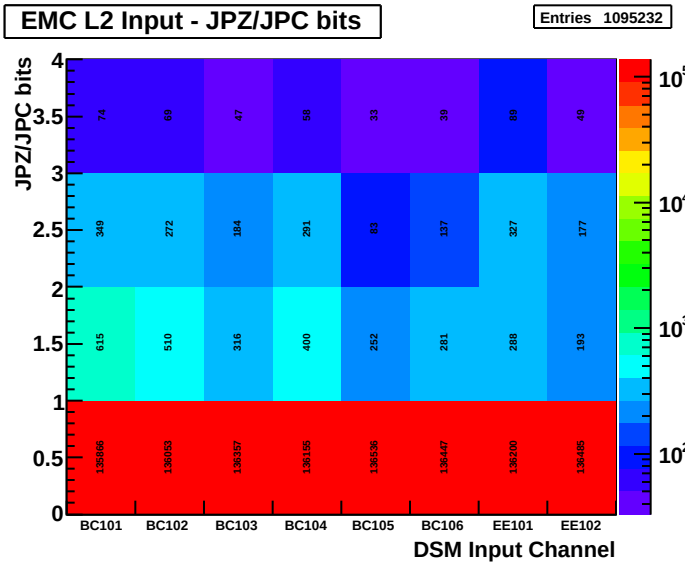

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

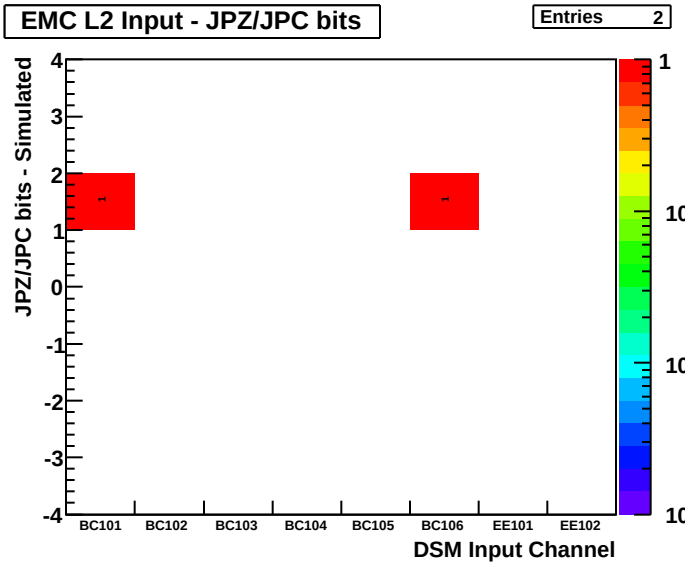

EMC L2 Input - JPX/JPA bits
Entries 1095232
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

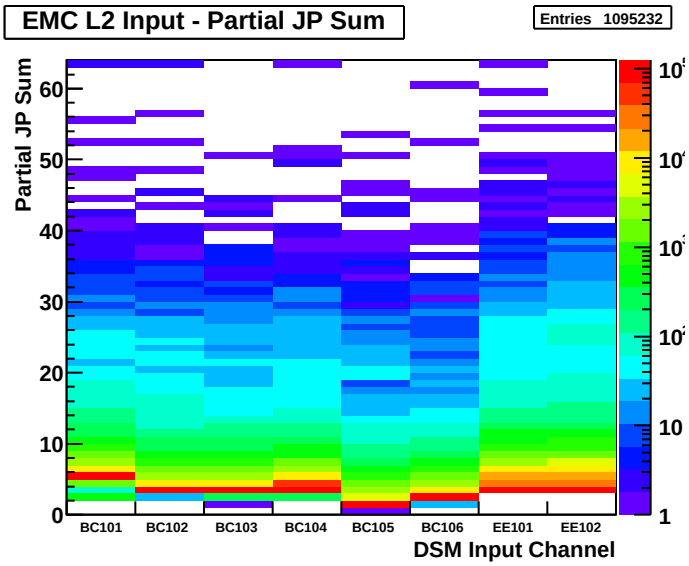

EMC L2 Input - JPX/JPA bits
Entries 297
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

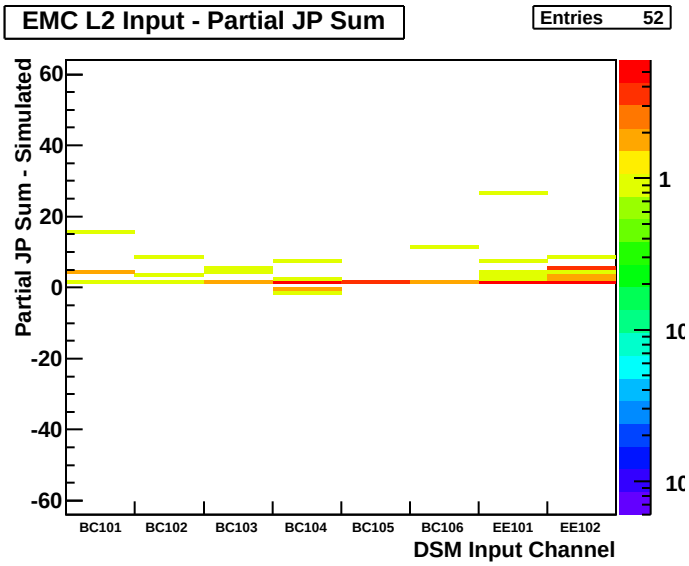

EMC L2 Input - JPY/JPB bits
Entries 1095232
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

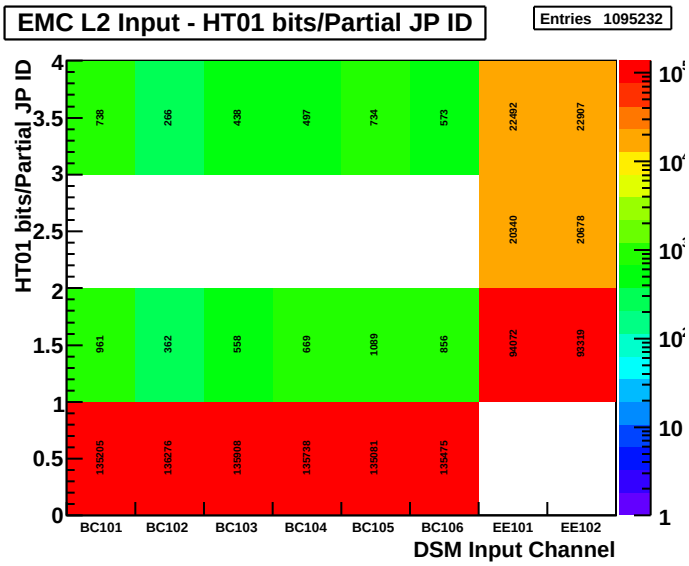

EMC L2 Input - JPY/JPB bits
Entries 291
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

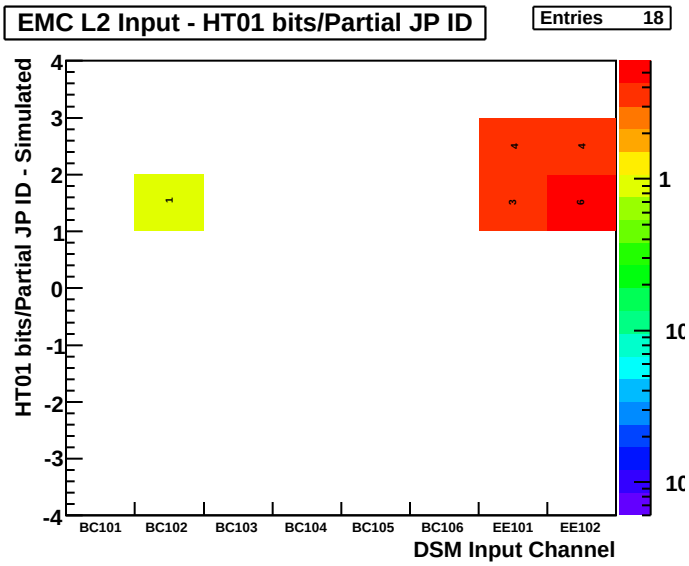

EMC L2 Input - JPZ/JPC bits
Entries 1095232
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

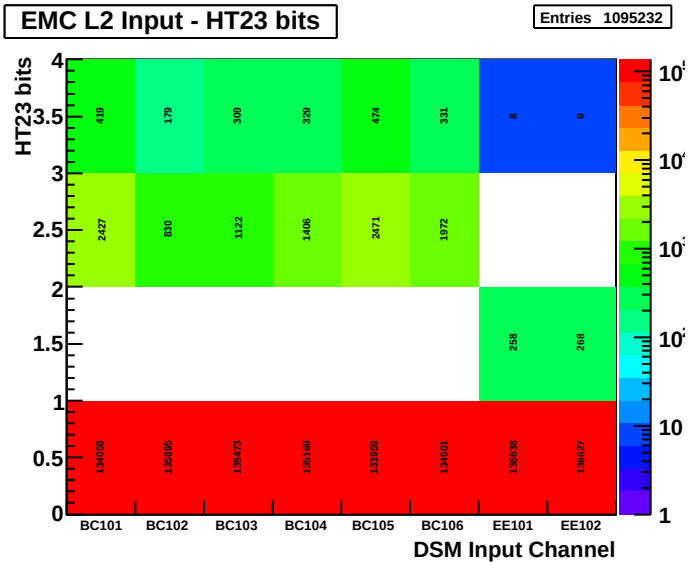

EMC L2 Input - JPZ/JPC bits
Entries 2
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

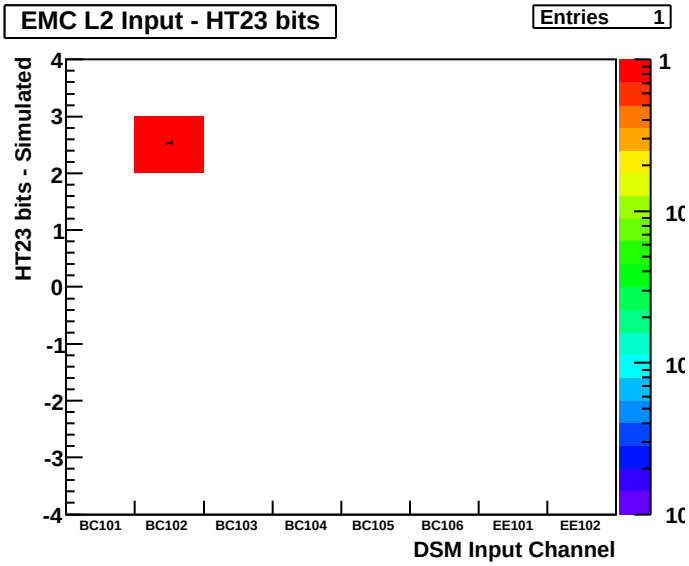

EMC L2 Input - Partial JP Sum
Entries 1095232
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

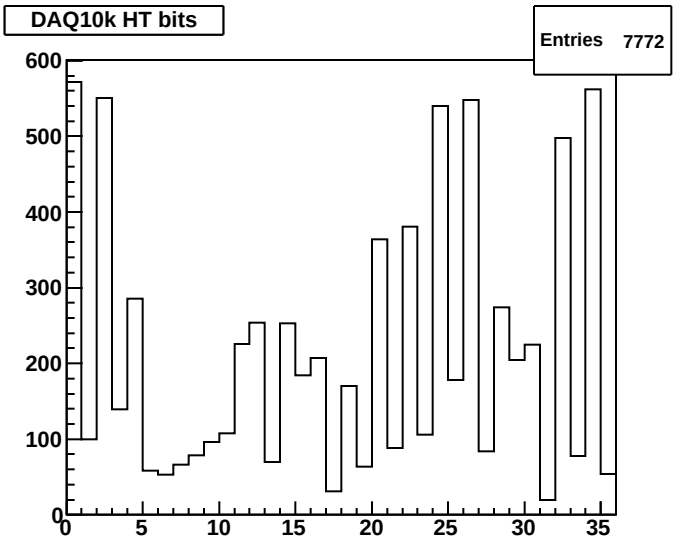

EMC L2 Input - Partial JP Sum
Entries 52
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

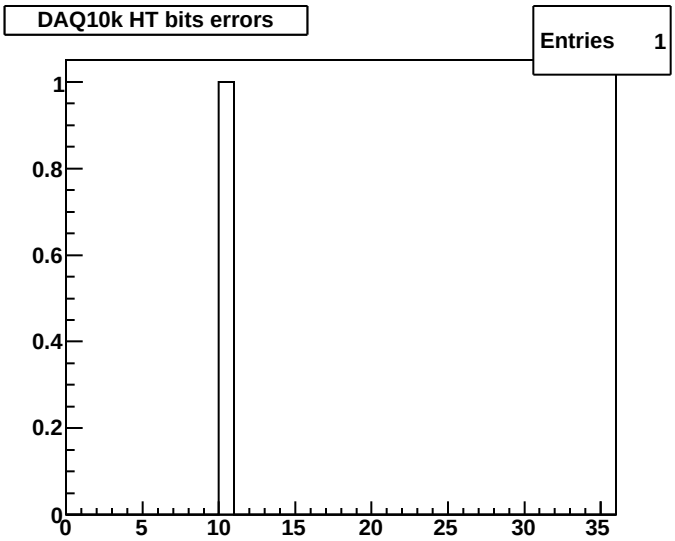

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1095232
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

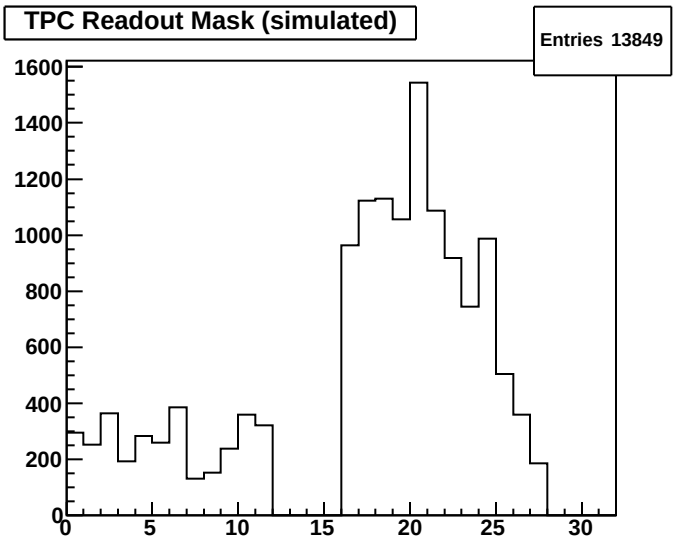

EMC L2 Input - HT01 bits/Partial JP ID
Entries 18
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

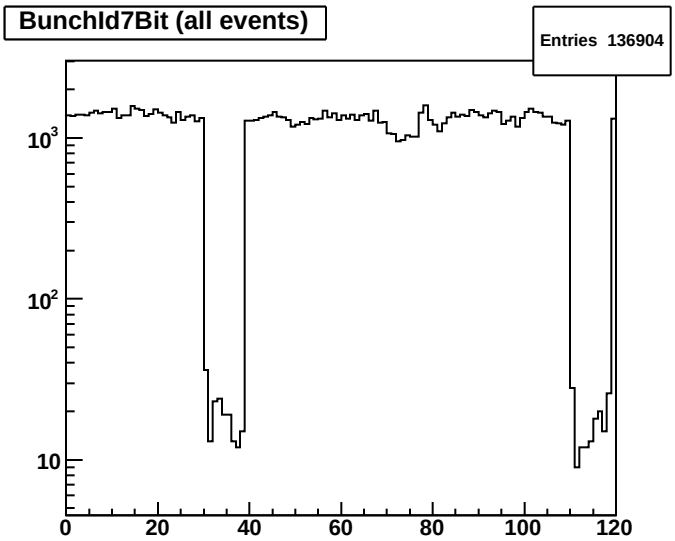

EMC L2 Input - HT23 bits
Entries 1095232
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

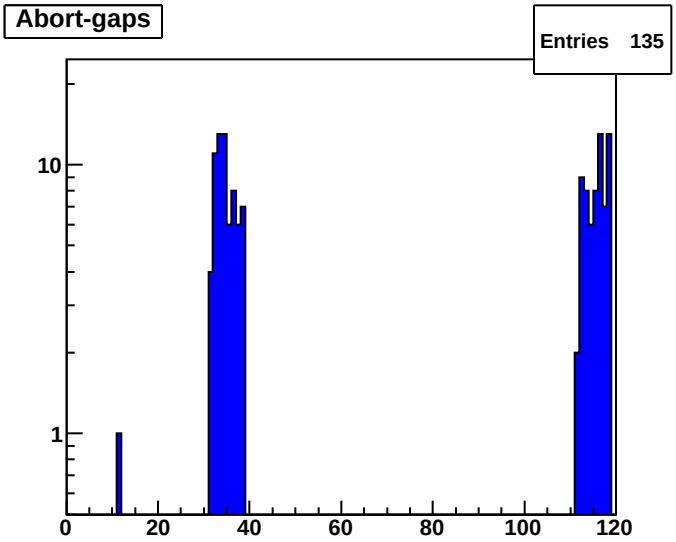

EMC L2 Input - HT23 bits
Entries 1
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

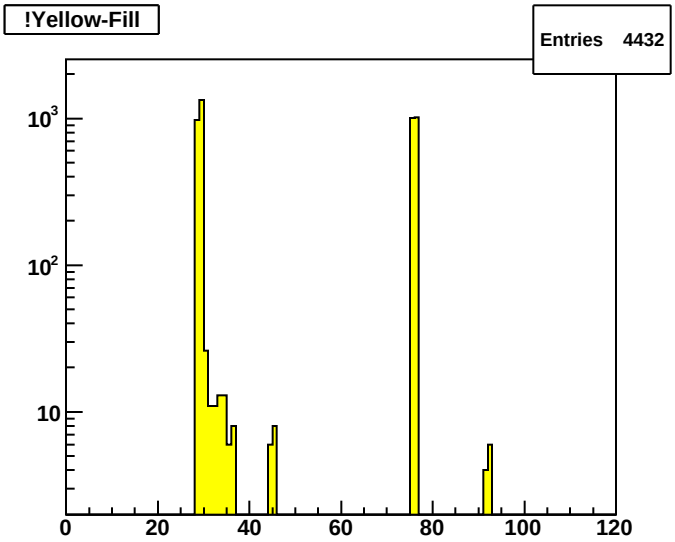

DAQ10k HT bits
Entries 7772

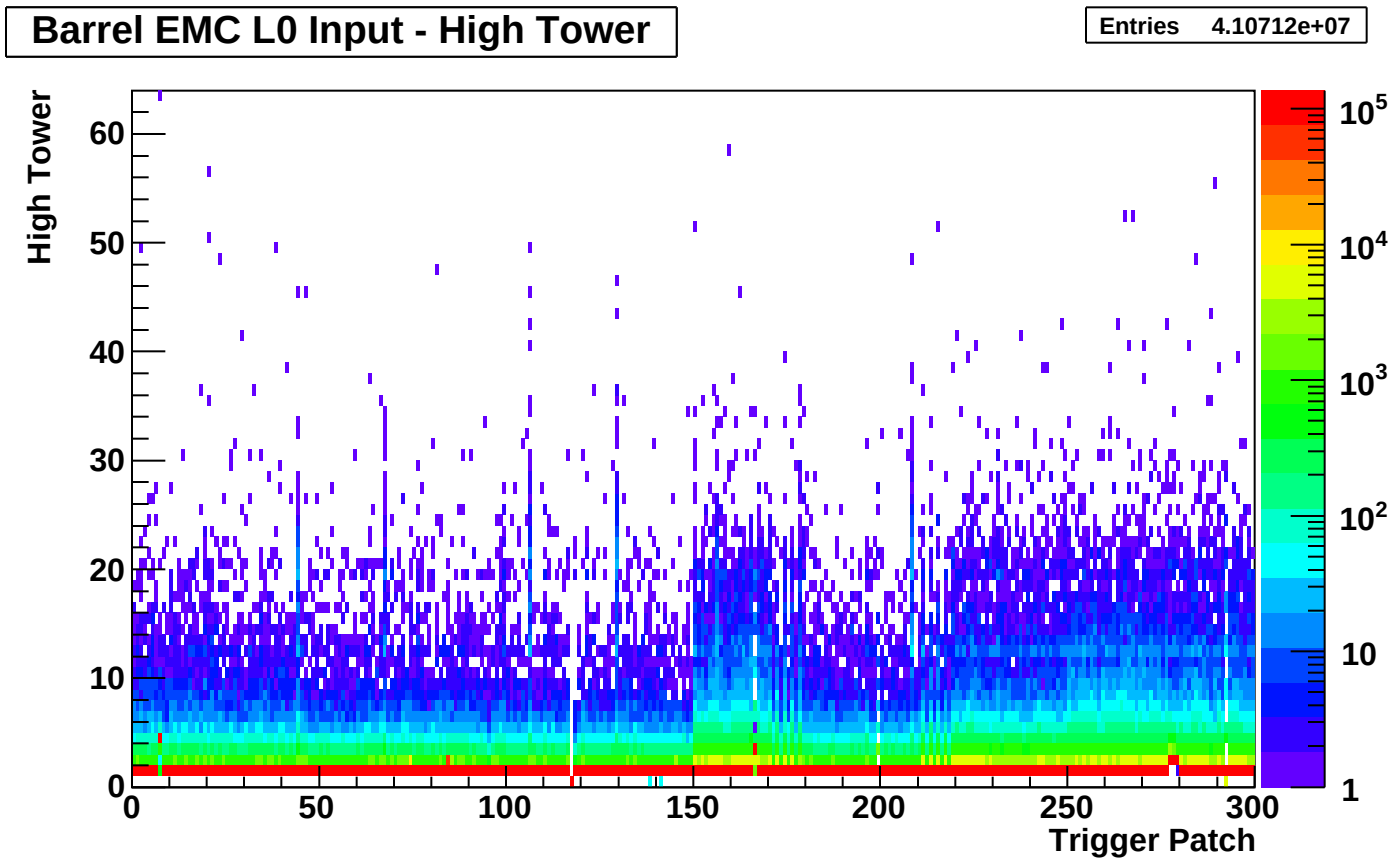

DAQ10k HT bits errors
Entries 1

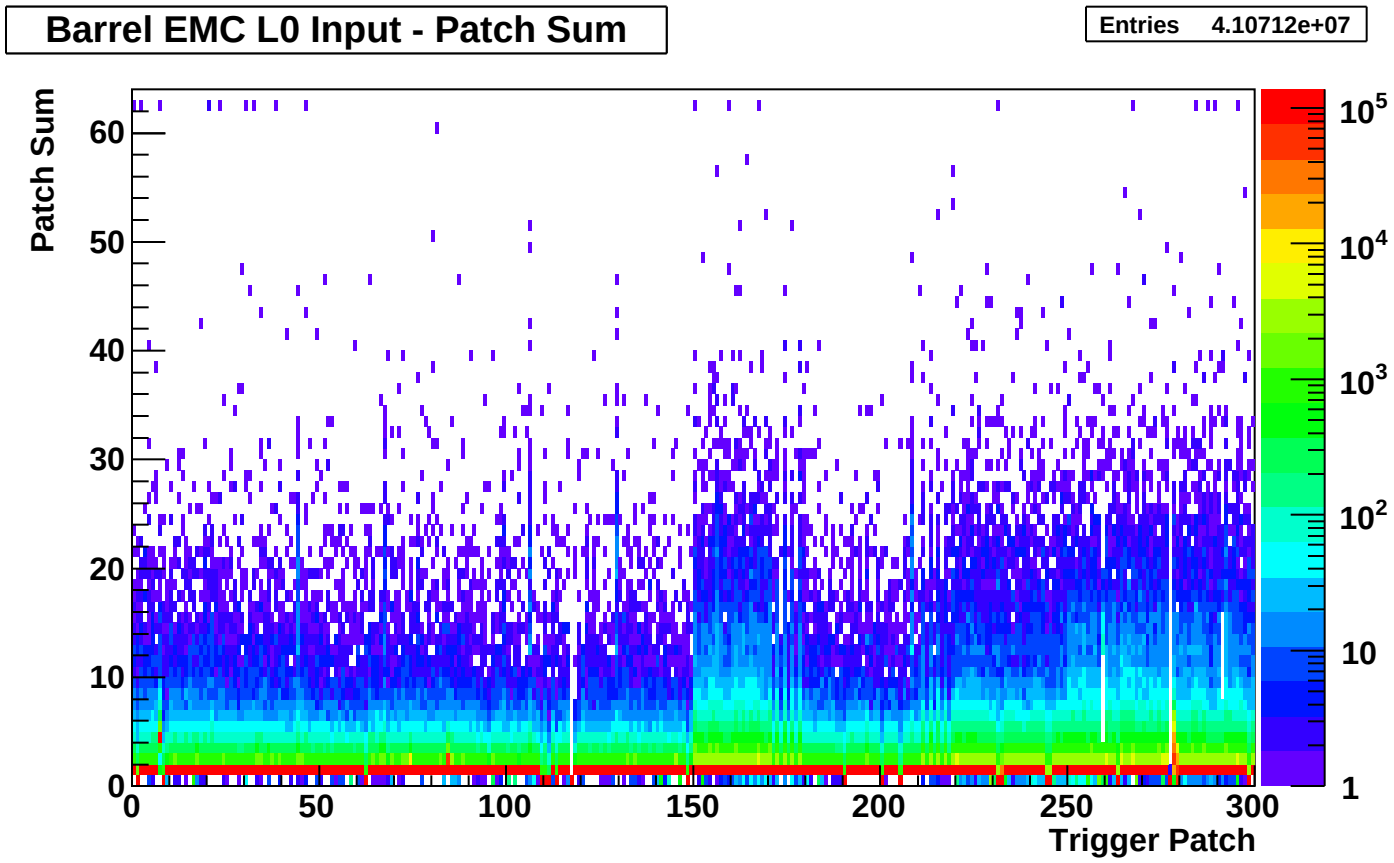

TPC Readout Mask (simulated)
Entries 13849

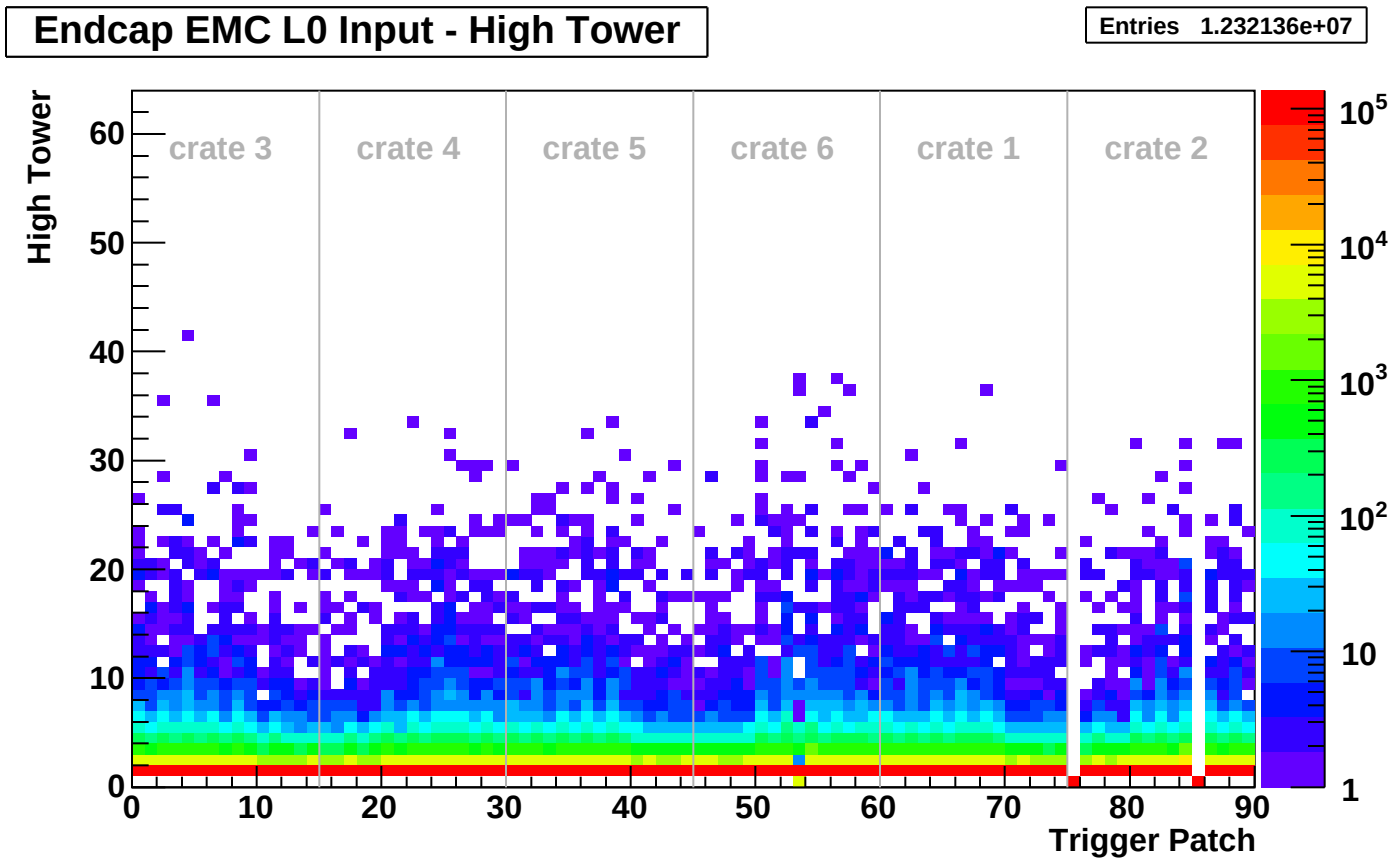

BunchId7Bit (all events)
Entries 136904

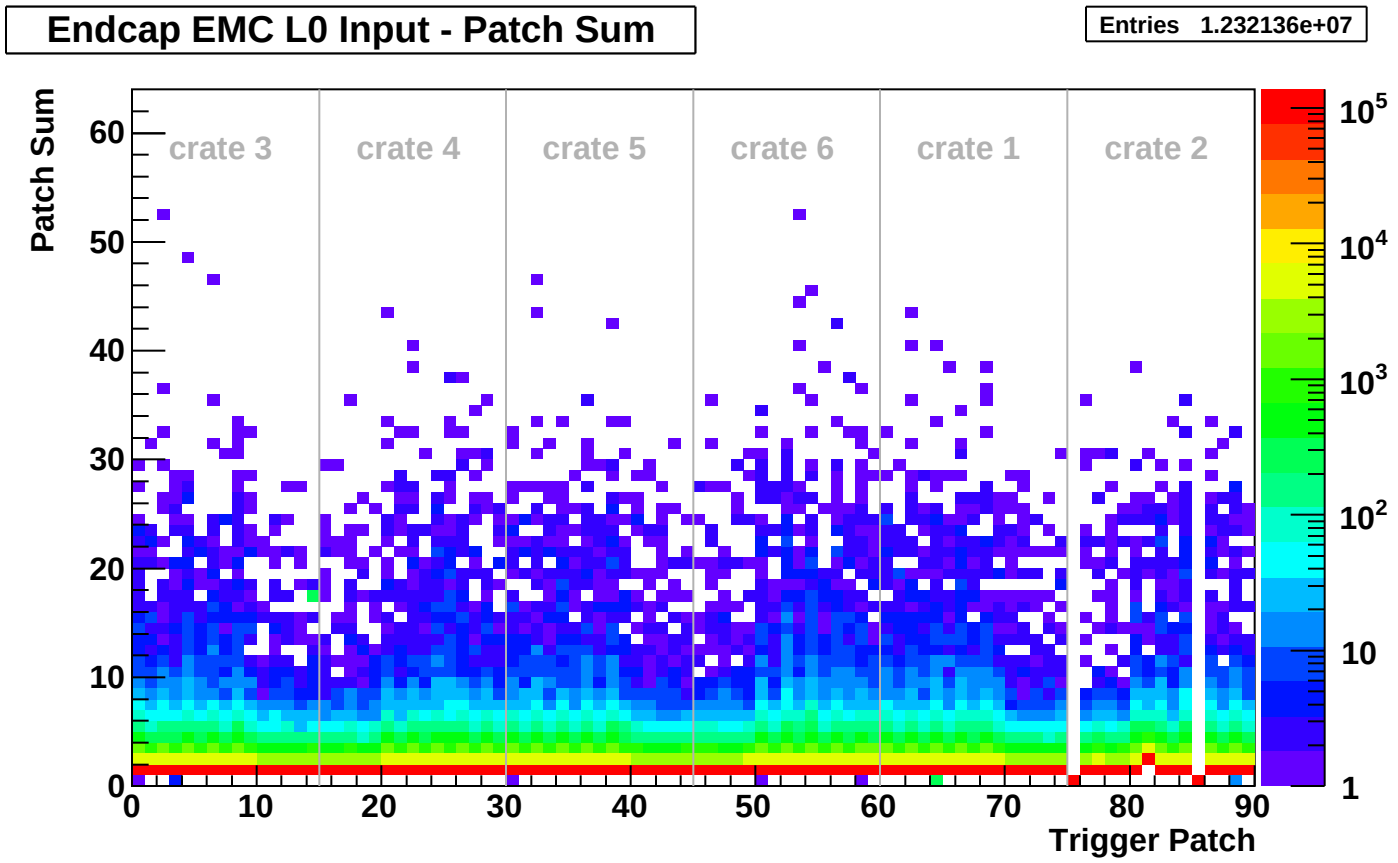

Abort-gaps
Entries 135

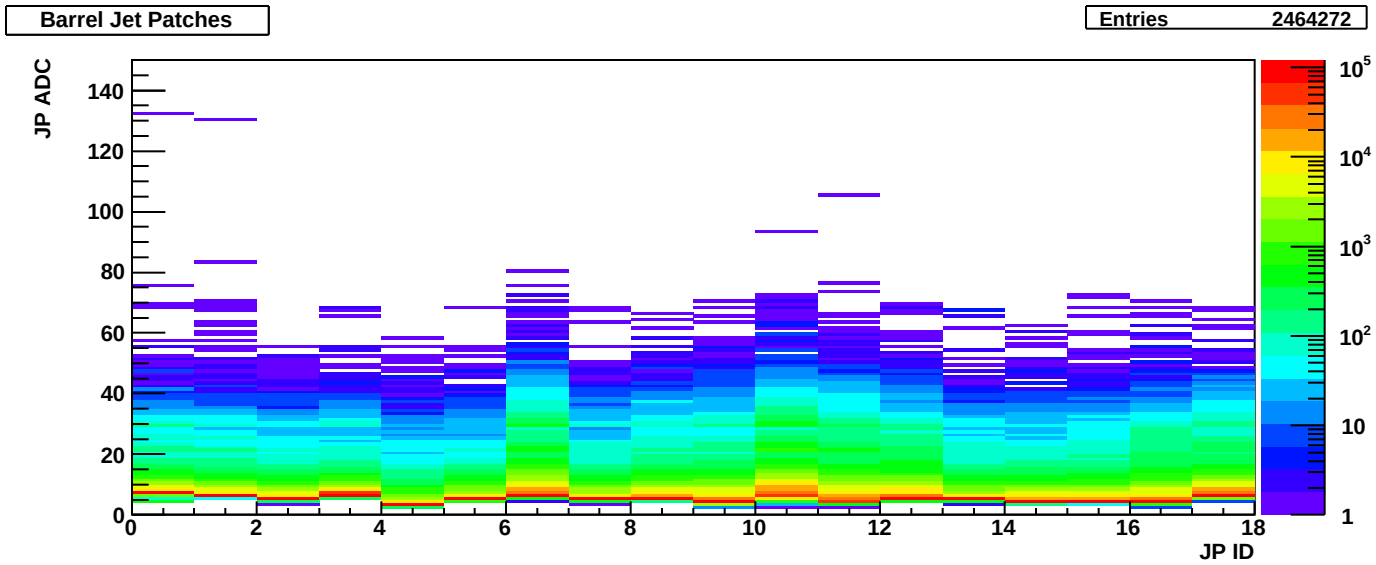

!Yellow-Fill
Entries 4432

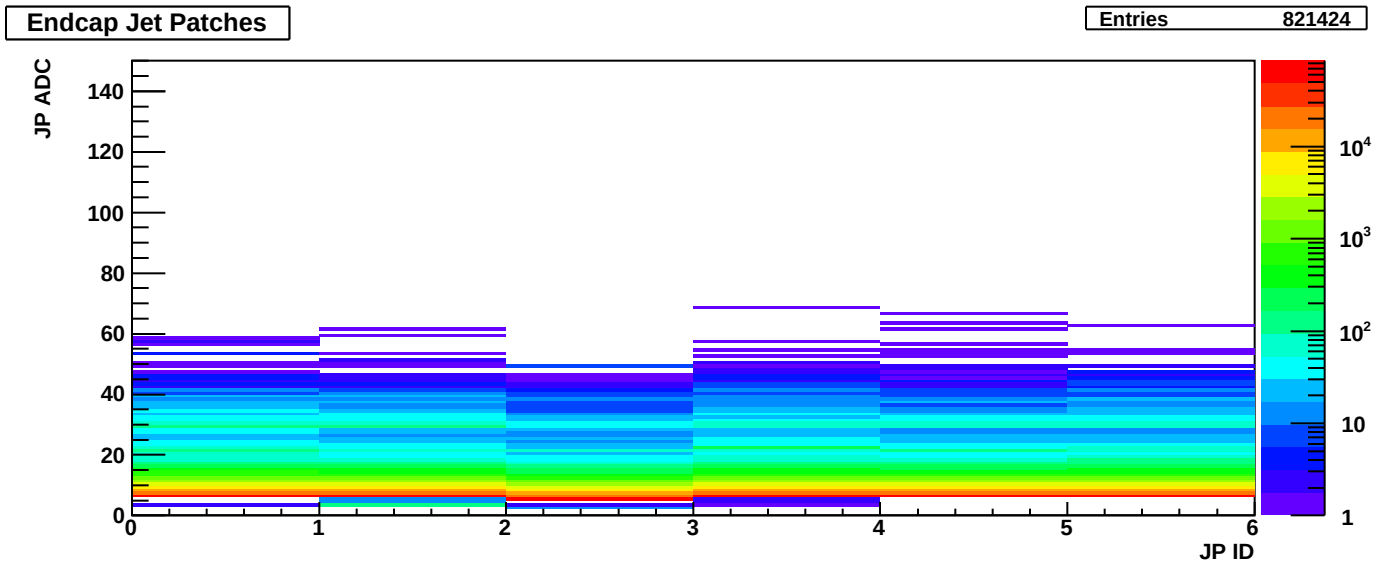

Barrel EMC L0 Input - High Tower
Entries 4.10712e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

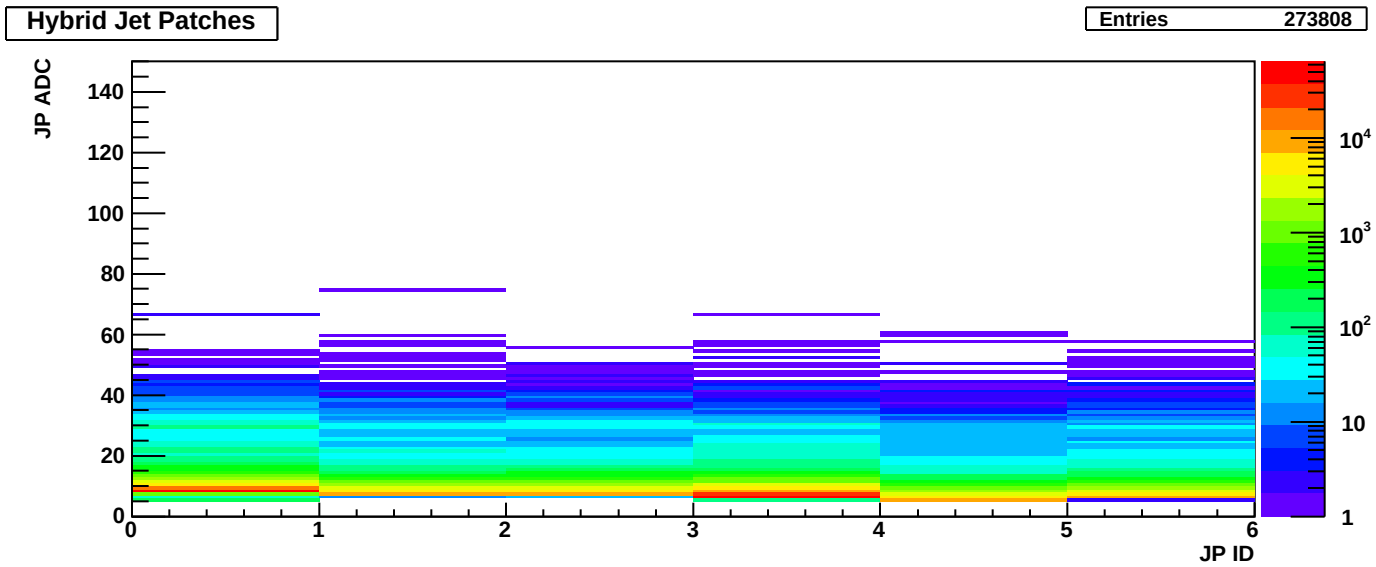

Barrel EMC L0 Input - Patch Sum
Entries 4.10712e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

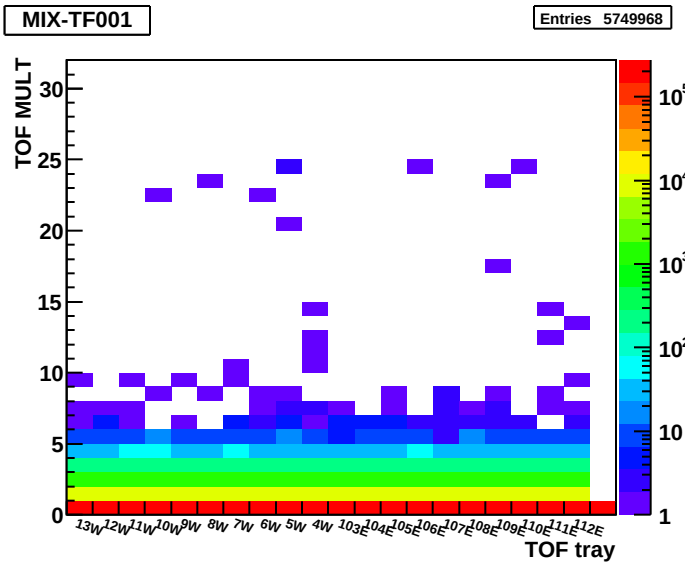

Endcap EMC L0 Input - High Tower
Entries 1.232136e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

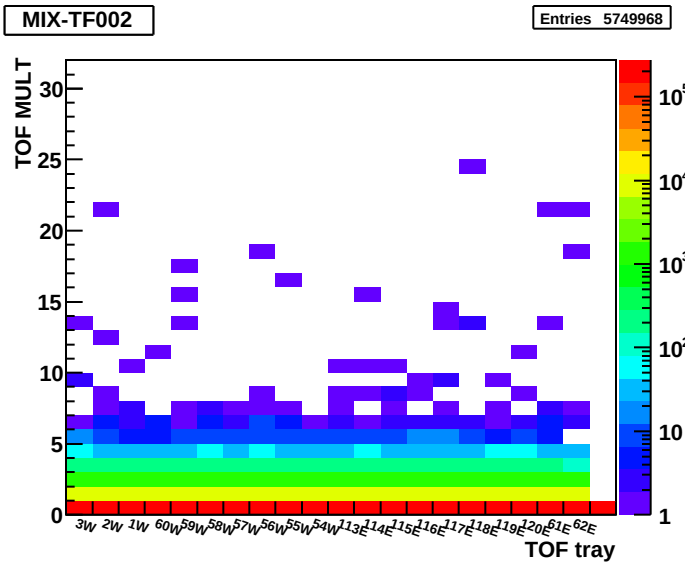

Endcap EMC L0 Input - Patch Sum
Entries 1.232136e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

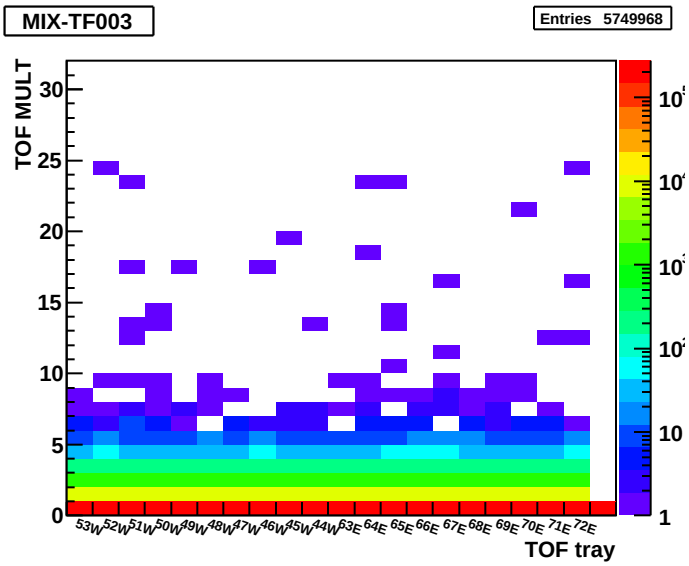

Barrel Jet Patches
Entries 2464272
JP ADC
JP ID

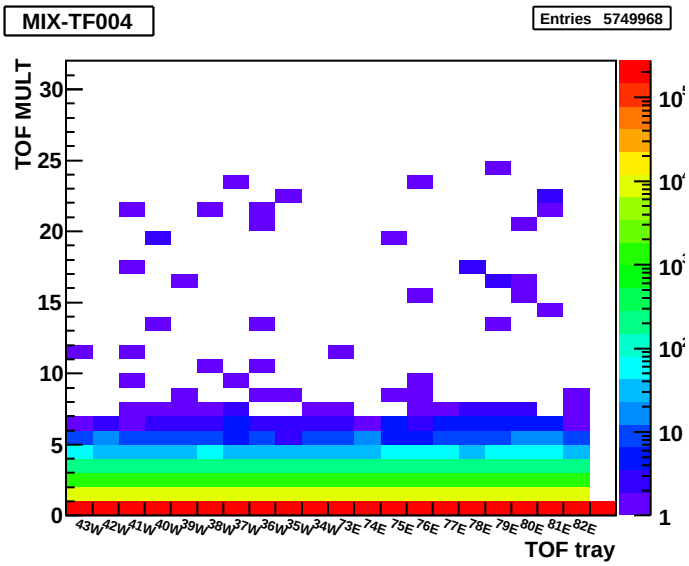

Endcap Jet Patches
Entries 821424
JP ADC
JP ID

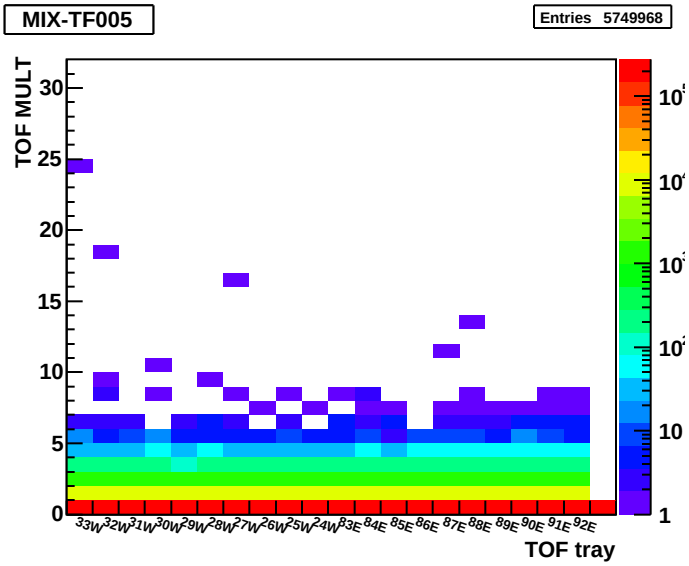

Hybrid Jet Patches
Entries 273808
JP ADC
JP ID

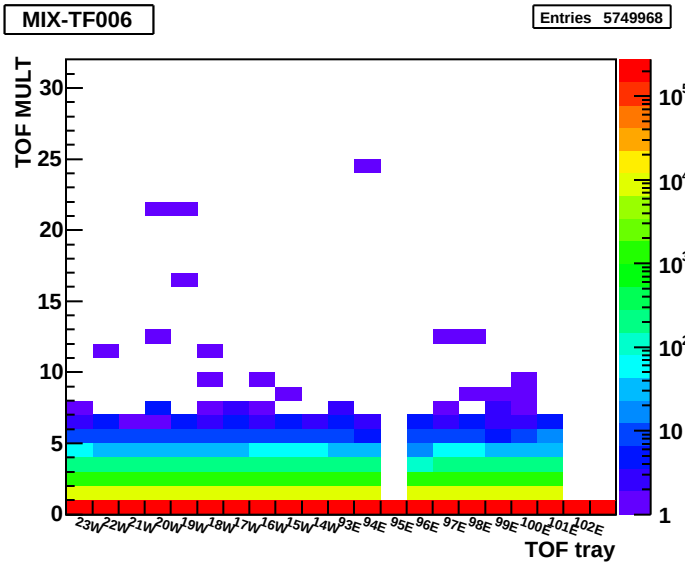

MIX-TF001
Entries 5749968
TOF MULT
TOF tray

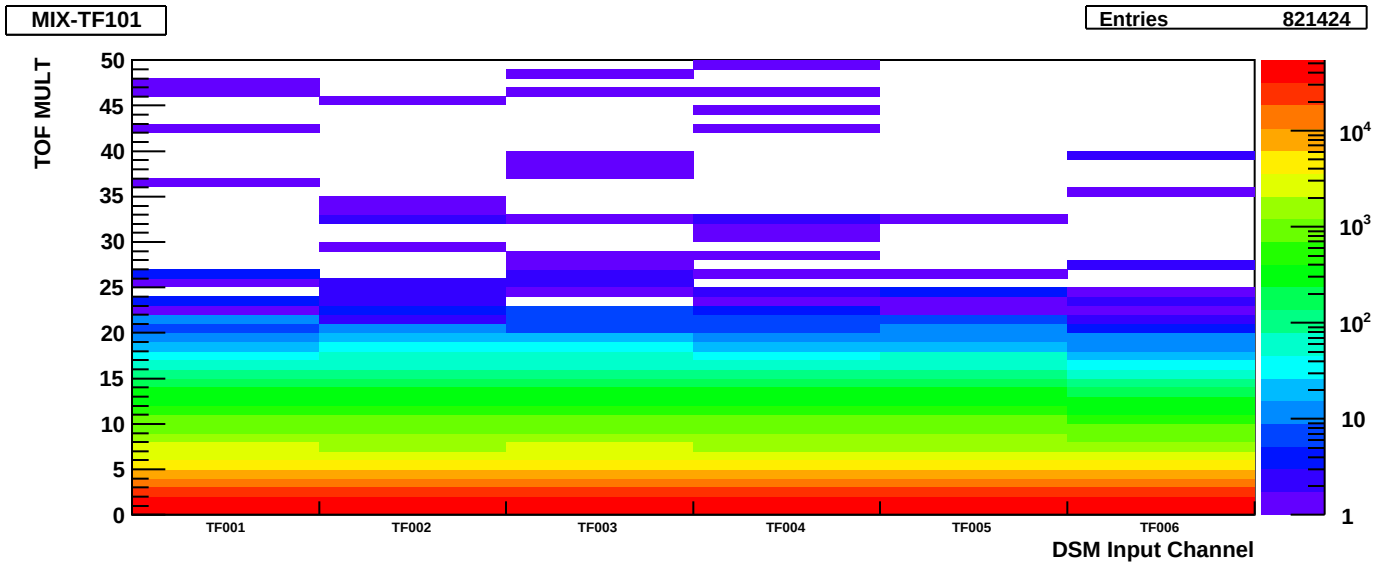

MIX-TF002
Entries 5749968
TOF MULT
TOF tray

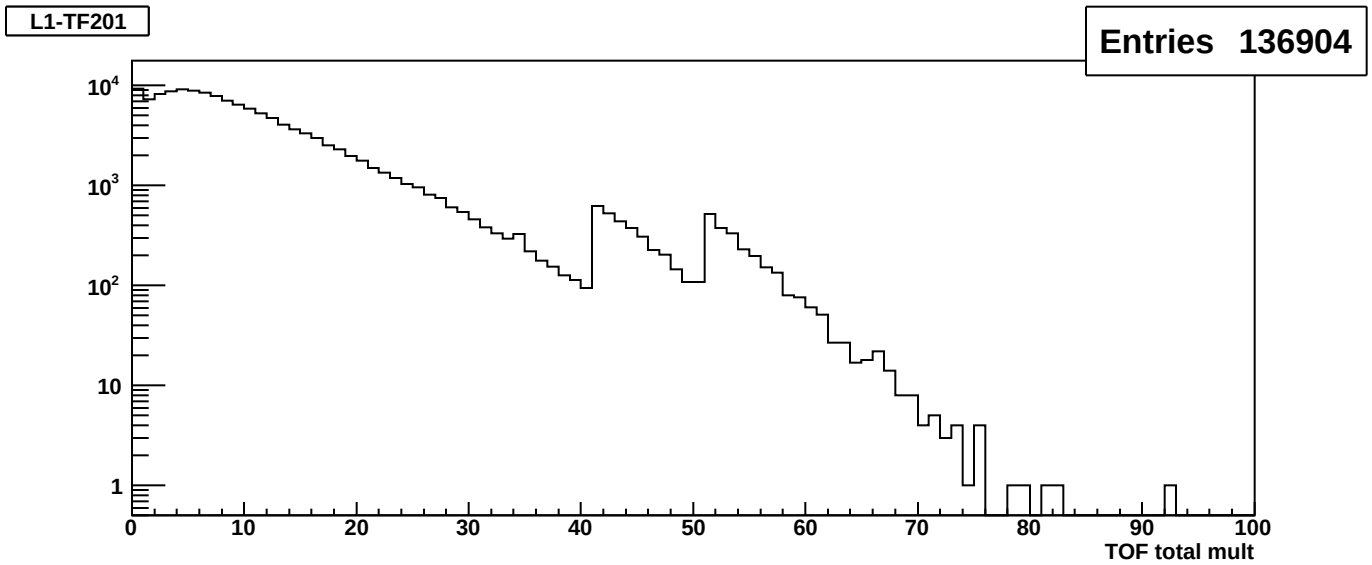

MIX-TF003
Entries 5749968
TOF MULT
TOF tray

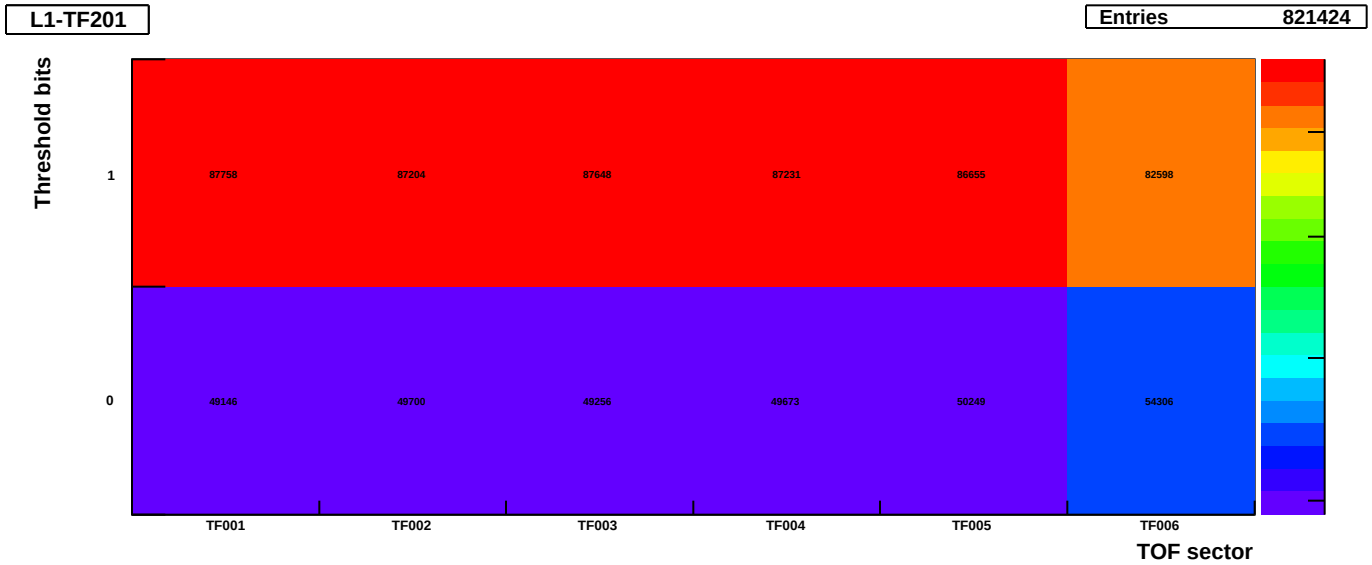

MIX-TF004
Entries 5749968
TOF MULT
TOF tray

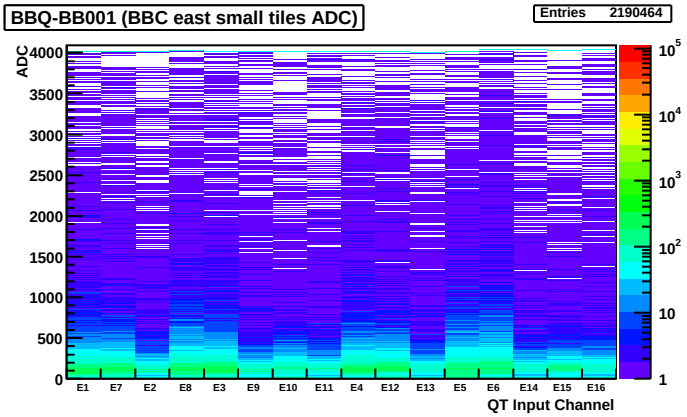

MIX-TF005
Entries 5749968
TOF MULT
TOF tray

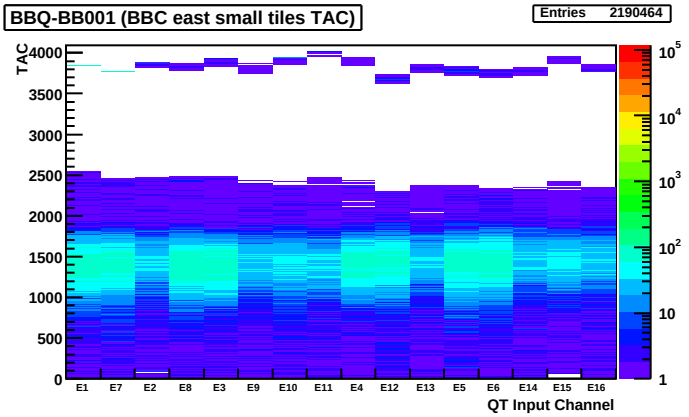

MIX-TF006
Entries 5749968
TOF MULT
TOF tray

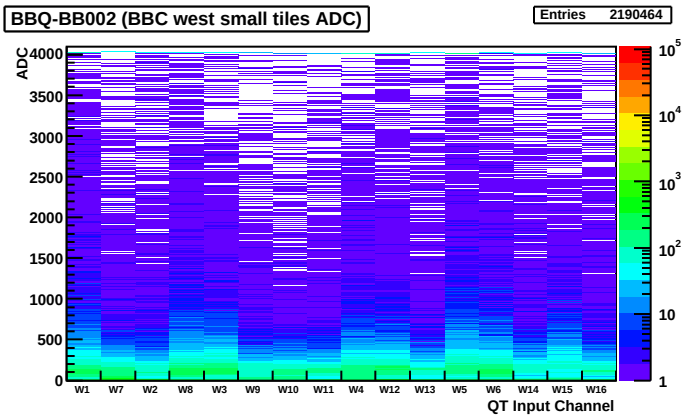

MIX-TF101
Entries 821424
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

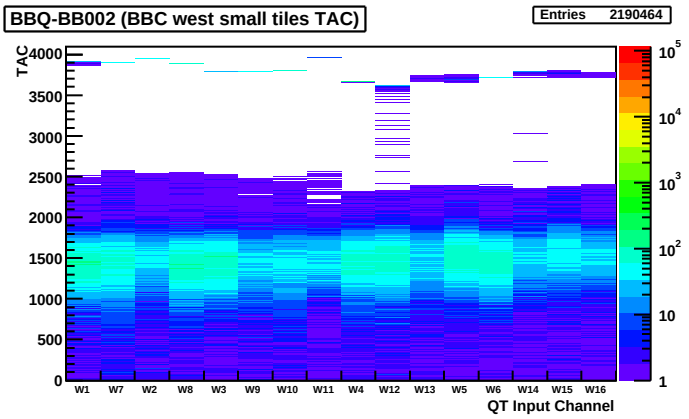

L1-TF201
Entries 136904
TOF total mult

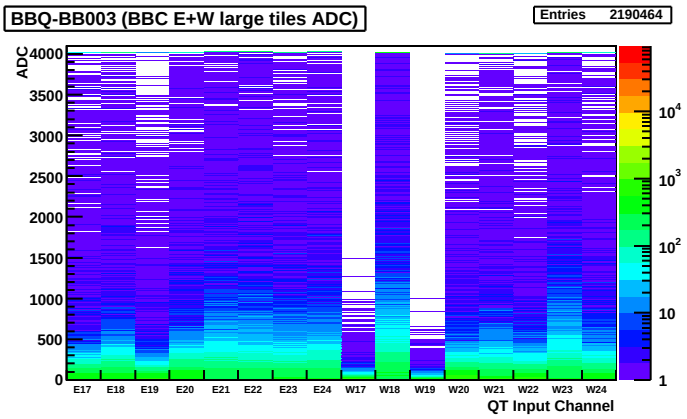

L1-TF201
Entries 821424
Threshold bits
87758
87204
87648
87231
86655
82598
49146
49700
49256
49673
50249
54306
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

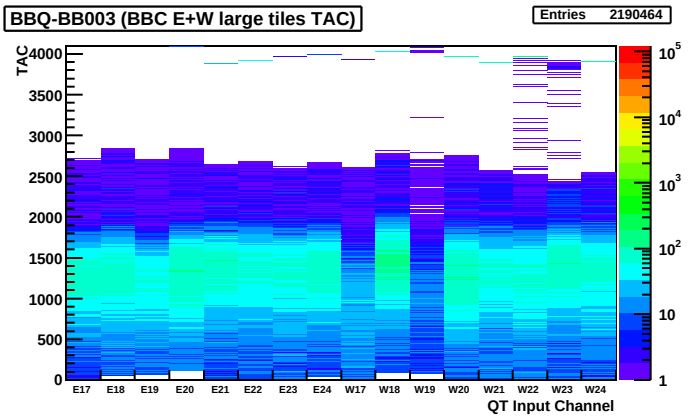

BBQ-BB001 (BBC east small tiles ADC)
Entries 2190464
ADC
QT Input Channel

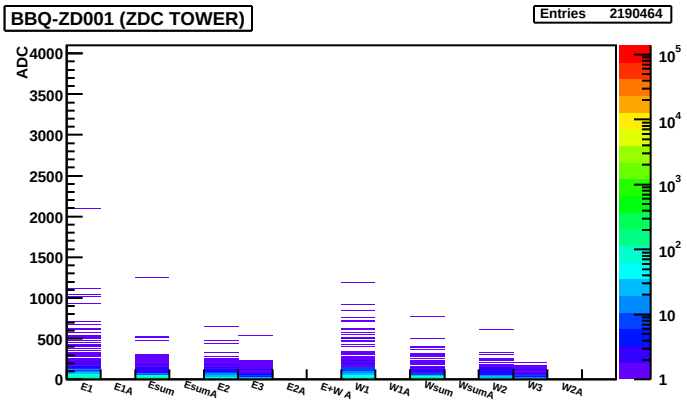

BBQ-BB001 (BBC east small tiles TAC)
Entries 2190464
TAC
QT Input Channel

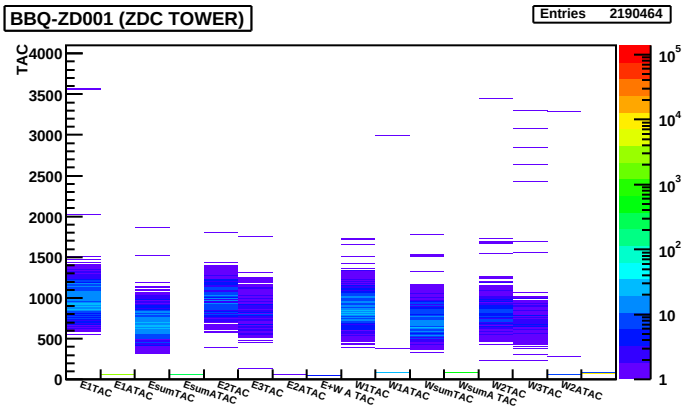

BBQ-BB002 (BBC west small tiles ADC)
Entries 2190464
ADC
QT Input Channel

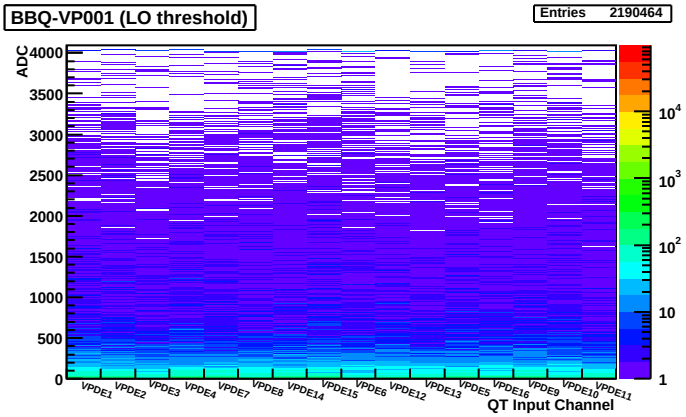

BBQ-BB002 (BBC west small tiles TAC)
Entries 2190464
TAC
QT Input Channel

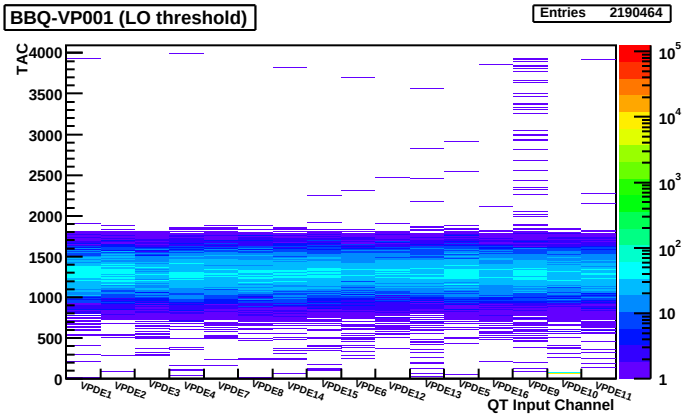

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2190464
ADC
QT Input Channel

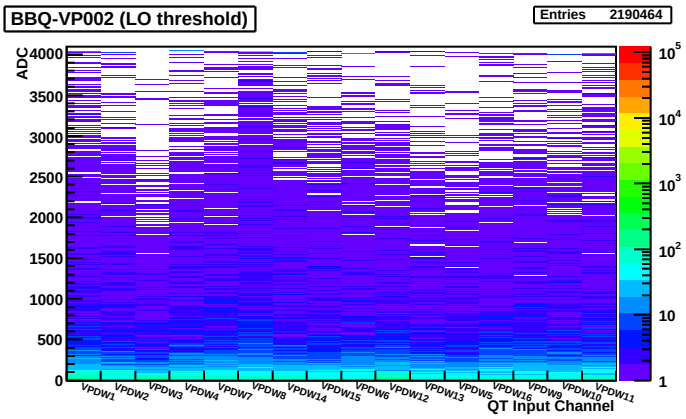

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2190464
TAC
QT Input Channel

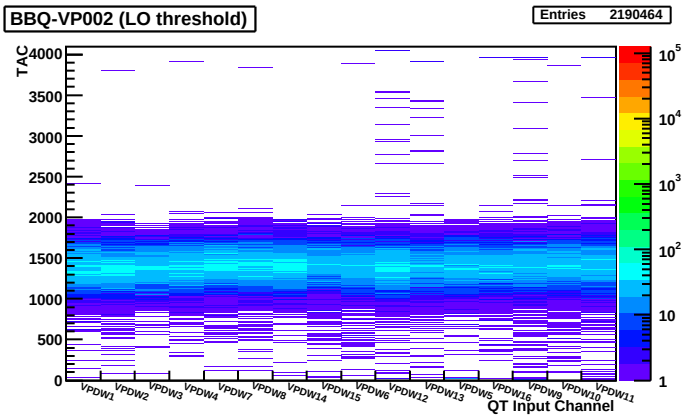

BBQ-ZD001 (ZDC TOWER)
Entries 2190464
ADC

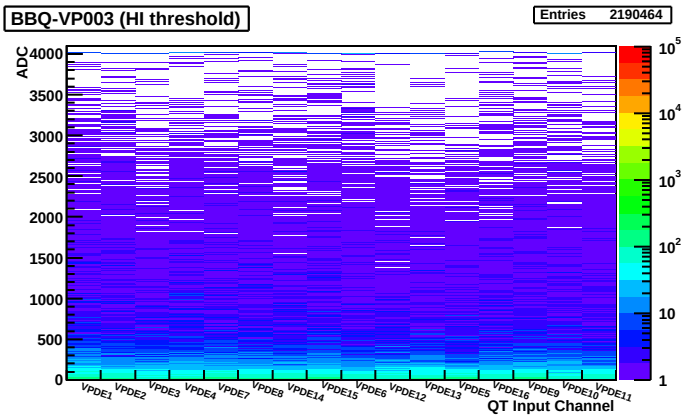

BBQ-ZD001 (ZDC TOWER)
Entries 2190464
TAC

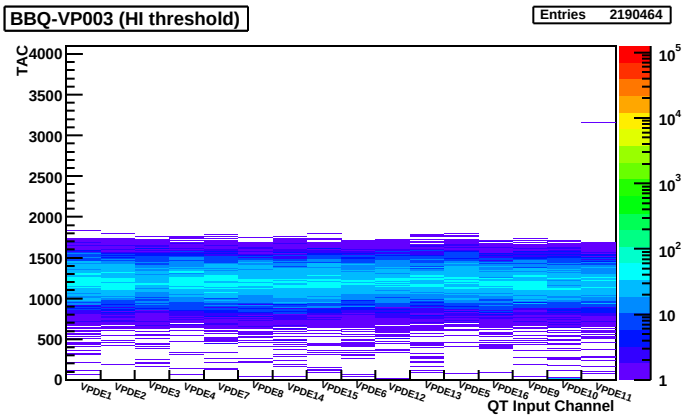

BBQ-VP001 (LO threshold)
Entries 2190464
ADC
QT Input Channel

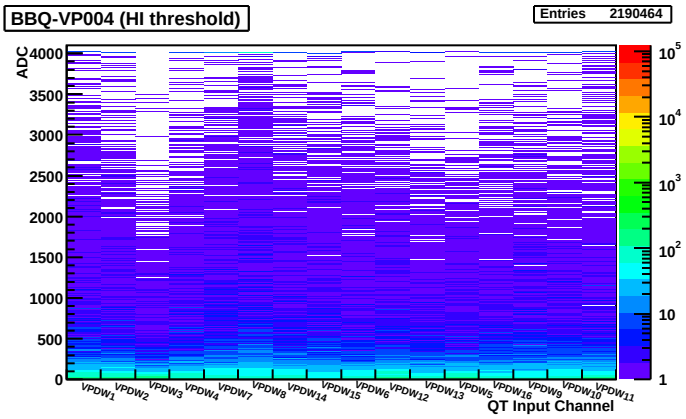

BBQ-VP001 (LO threshold)
Entries 2190464
TAC
QT Input Channel

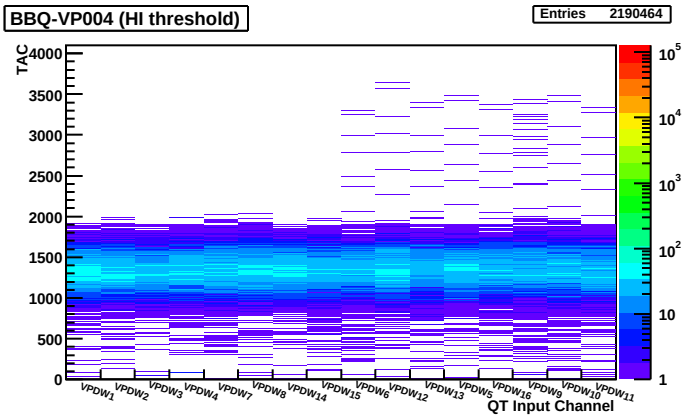

BBQ-VP002 (LO threshold)
Entries 2190464
ADC
QT Input Channel

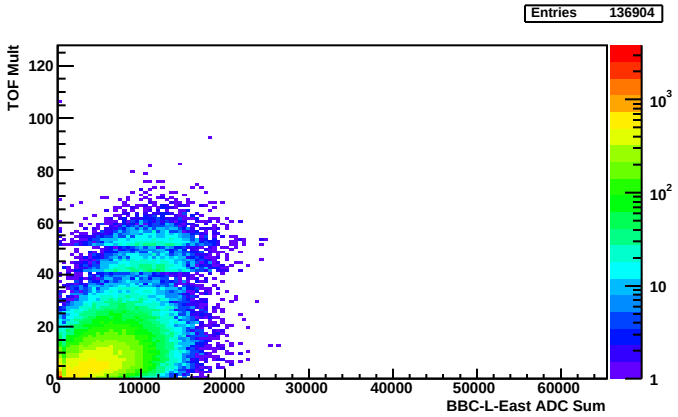

BBQ-VP002 (LO threshold)
Entries 2190464
TAC
QT Input Channel

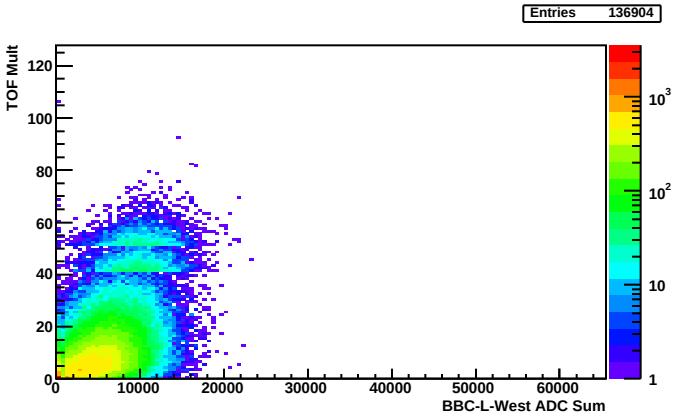

BBQ-VP003 (HI threshold)
Entries 2190464
ADC
QT Input Channel

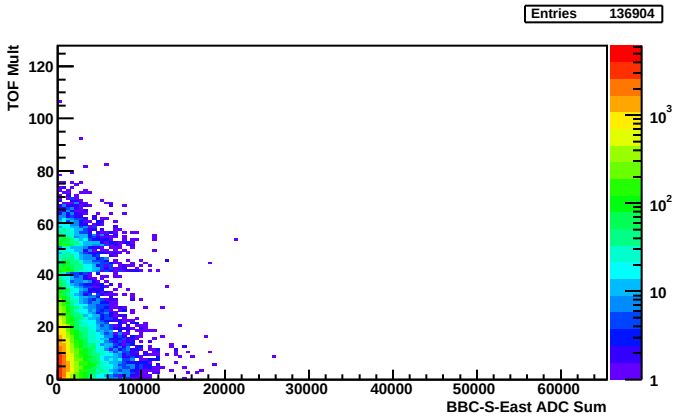

BBQ-VP003 (HI threshold)
Entries 2190464
TAC
QT Input Channel

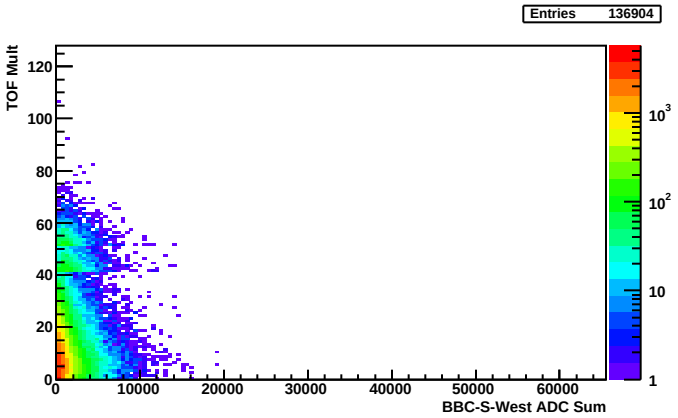

BBQ-VP004 (HI threshold)
Entries 2190464
ADC
QT Input Channel

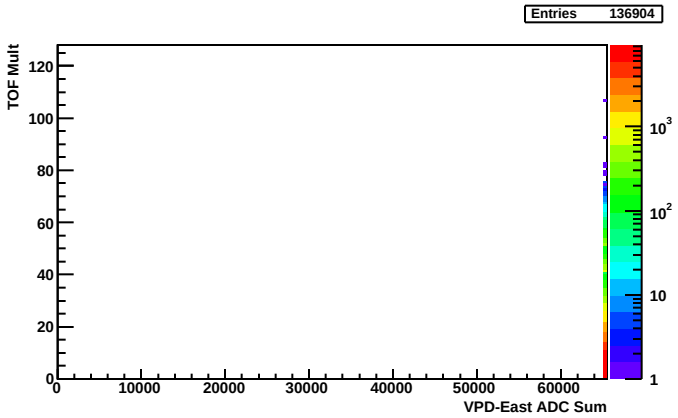

BBQ-VP004 (HI threshold)
Entries 2190464
TAC
QT Input Channel

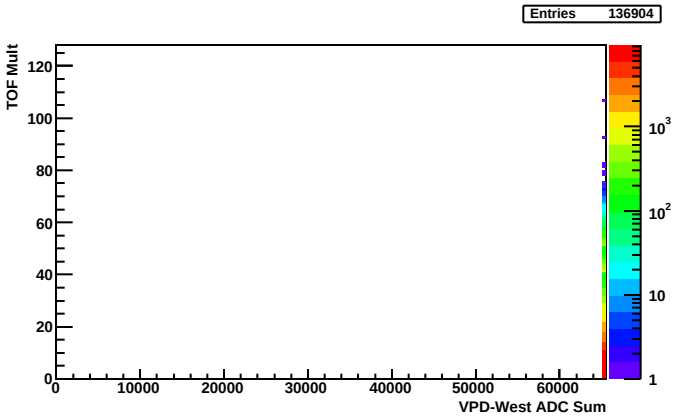

Entries 136904
TOF Mult
BBC-L-East ADC Sum

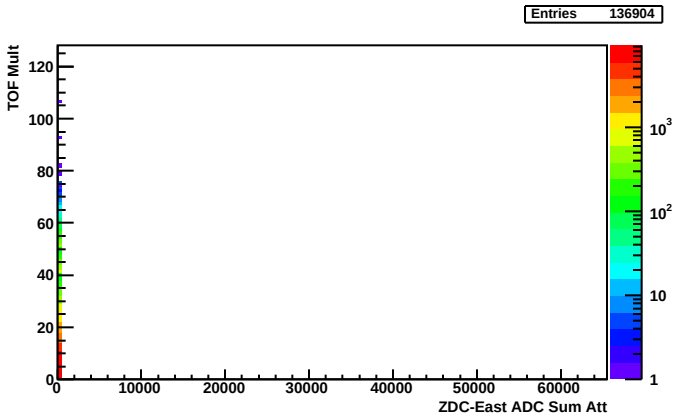

Entries 136904
TOF Mult
BBC-L-West ADC Sum

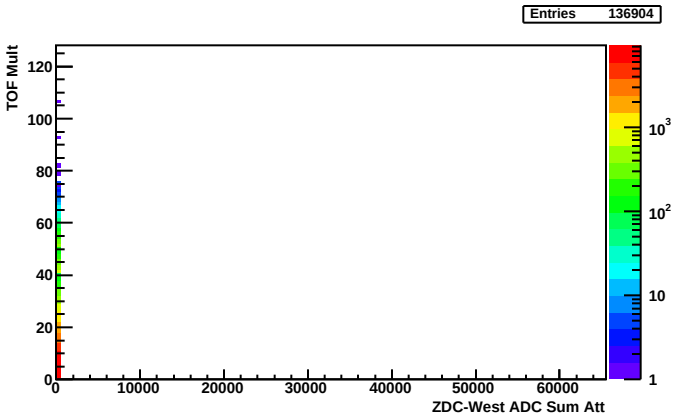

Entries 136904
TOF Mult
BBC-S-East ADC Sum

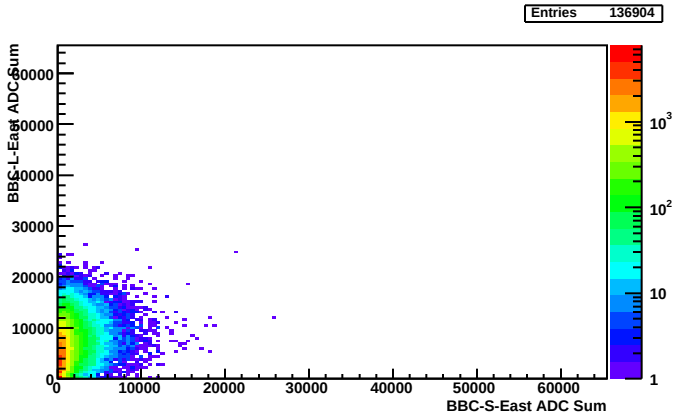

Entries 136904
TOF Mult
BBC-S-West ADC Sum

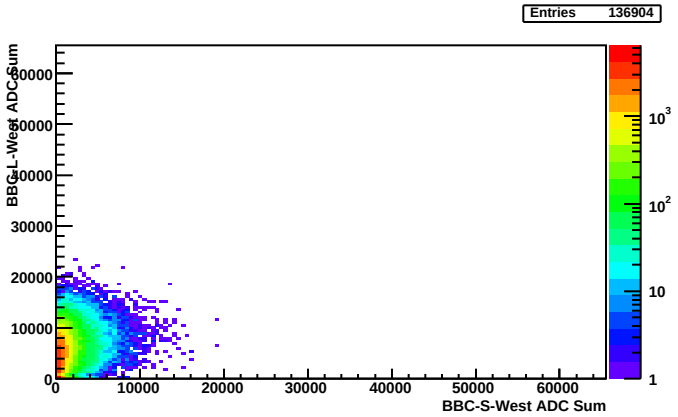

Entries 136904
TOF Mult
VPD-East ADC Sum

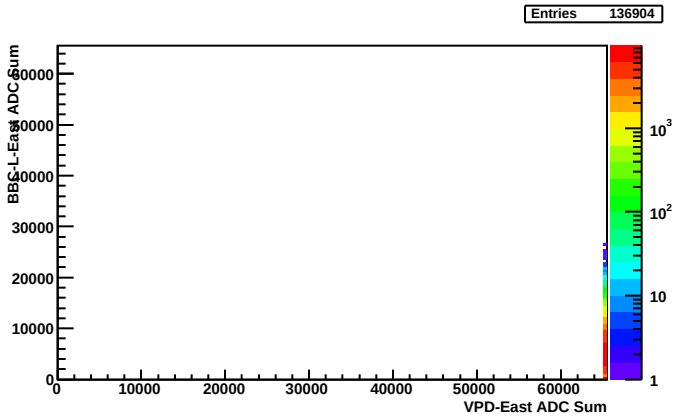

Entries 136904
TOF Mult
VPD-West ADC Sum

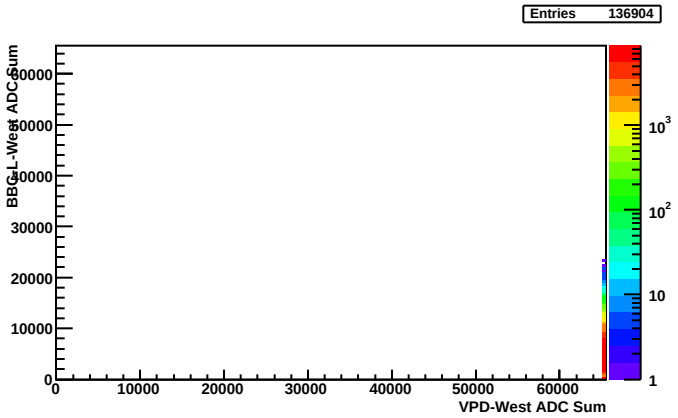

Entries 136904
TOF Mult
ZDC-East ADC Sum Att

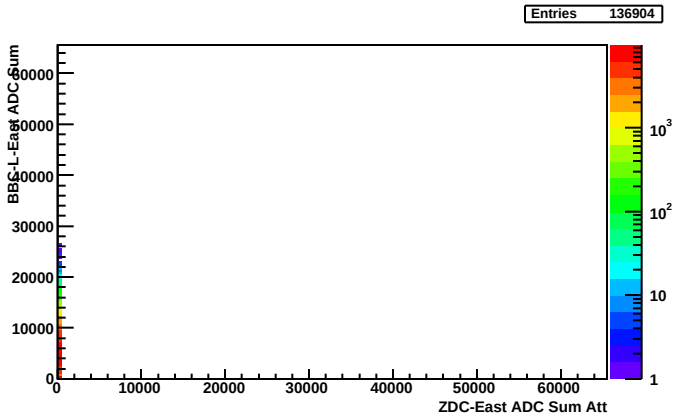

Entries 136904
TOF Mult
ZDC-West ADC Sum Att

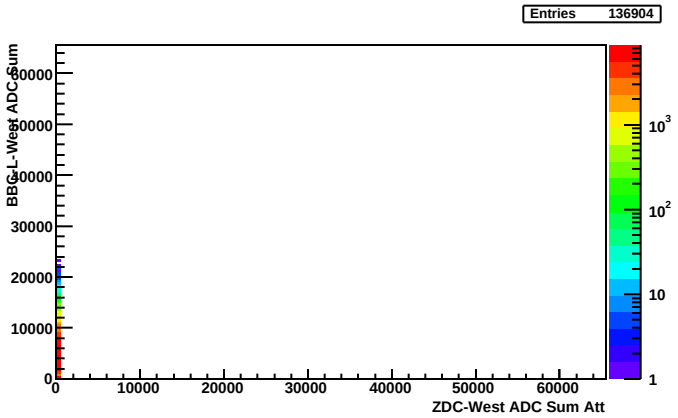

Entries 136904
BBC-L-East ADC Sum
BBC-S-East ADC Sum

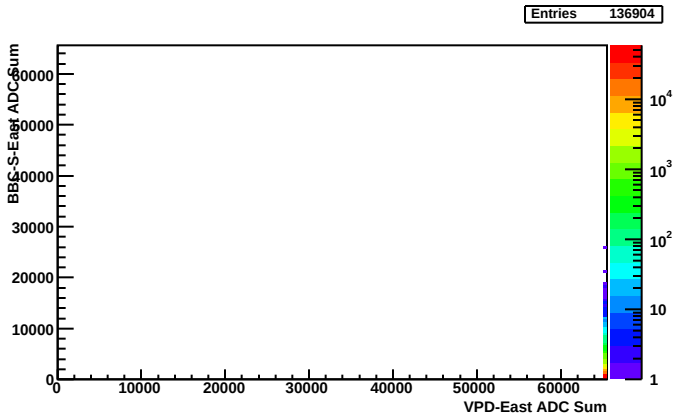

Entries 136904
BBC-L-West ADC Sum
BBC-S-West ADC Sum

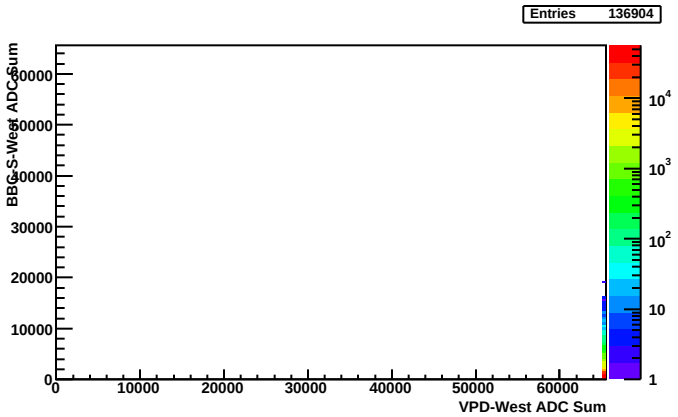

Entries 136904
BBC-L-East ADC Sum
VPD-East ADC Sum

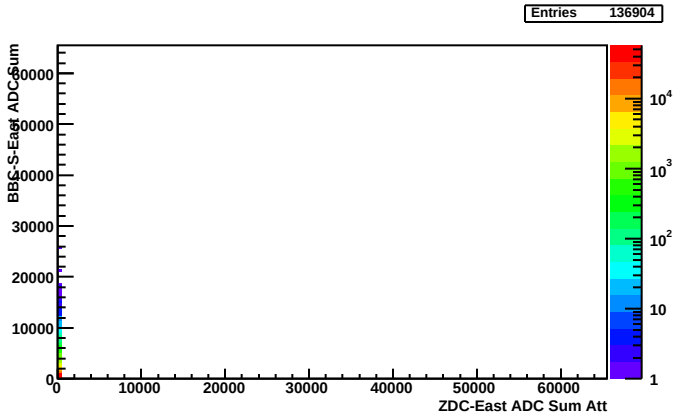

Entries 136904
BBC-L-West ADC Sum
VPD-West ADC Sum

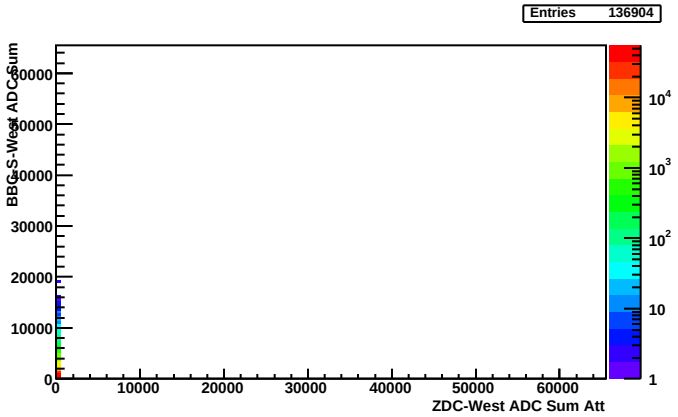

Entries 136904
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

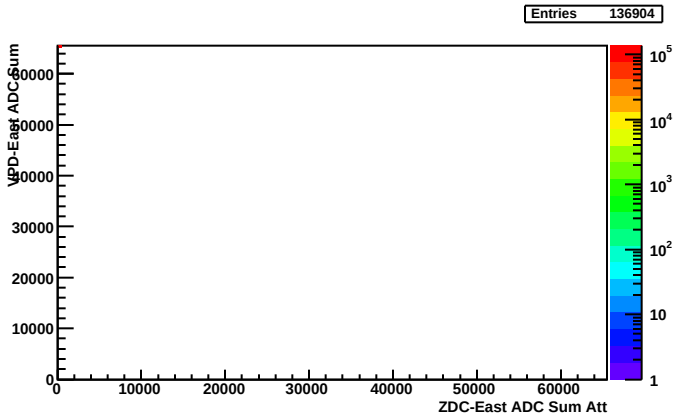

Entries 136904
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

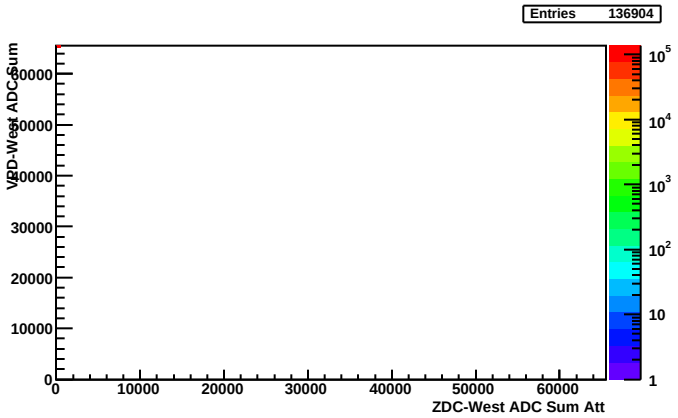

Entries 136904
BBC-S-East ADC Sum
VPD-East ADC Sum

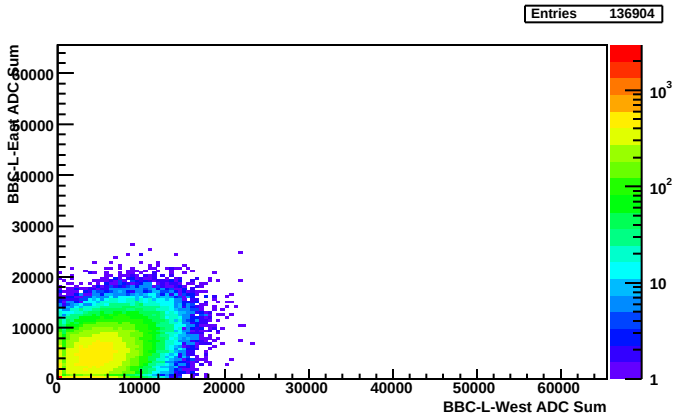

Entries 136904
BBC-S-West ADC Sum
VPD-West ADC Sum

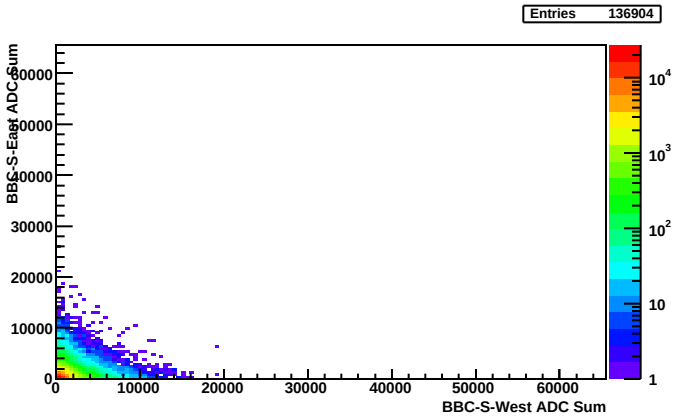

Entries 136904
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

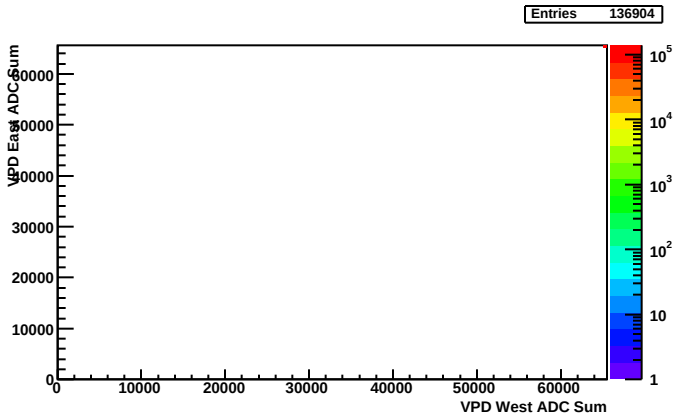

Entries 136904
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

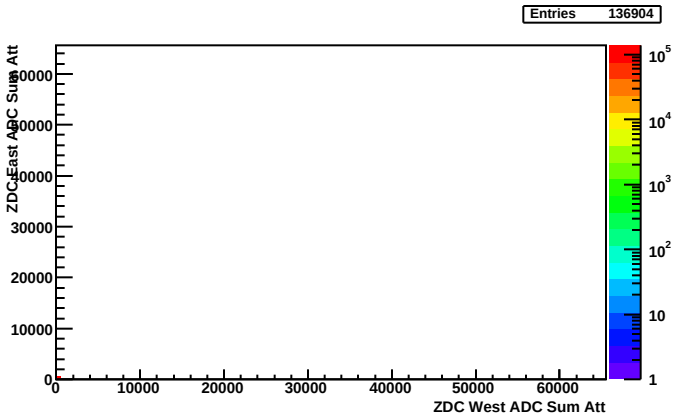

Entries 136904
VPD-East ADC Sum
ZDC-East ADC Sum Att

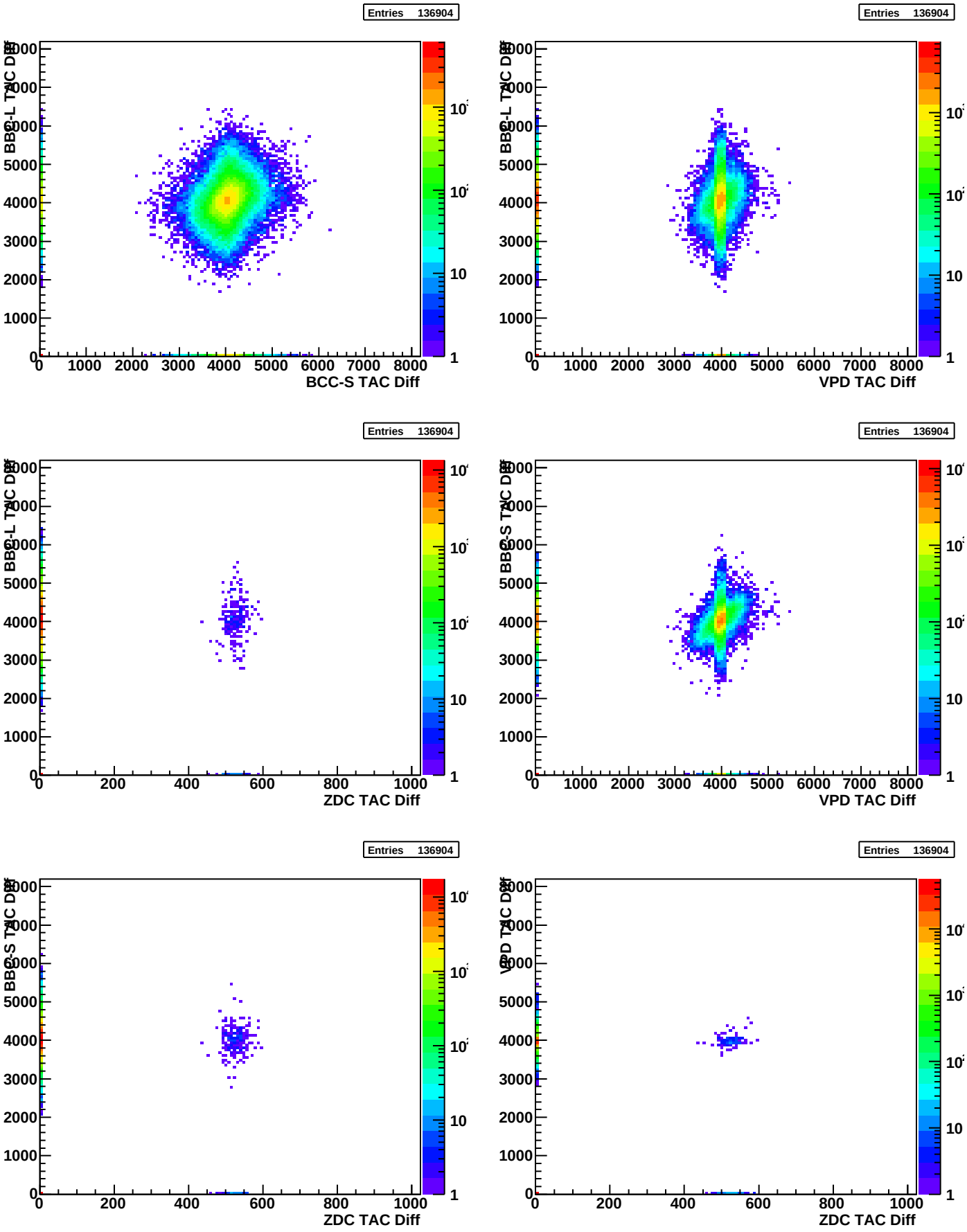

Entries 136904
VPD-West ADC Sum
ZDC-West ADC Sum Att

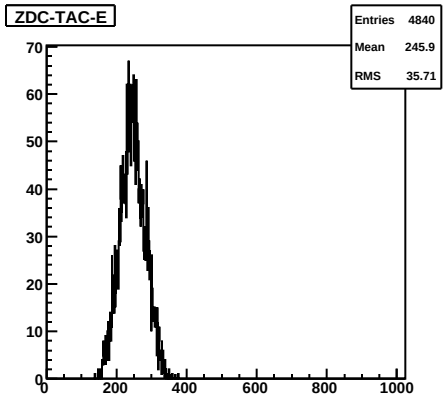

Entries 136904
BBC-L-East ADC Sum
BBC-L-West ADC Sum

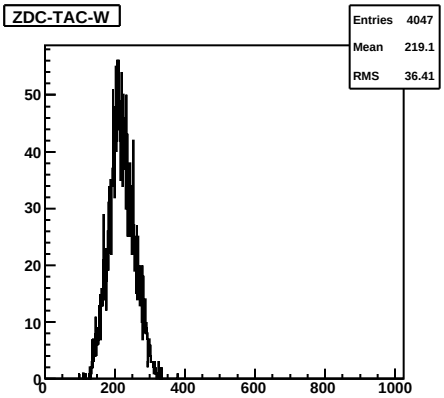

Entries 136904
BBC-S-East ADC Sum
BBC-S-West ADC Sum

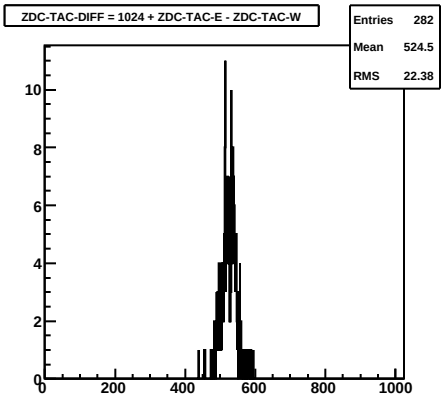

Entries 136904
VPD East ADC Sum
VPD West ADC Sum

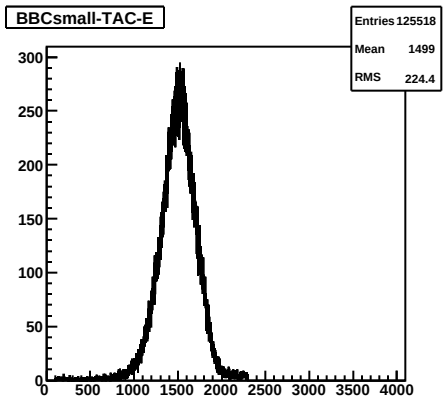

Entries 136904
ZDC East ADC Sum Att
ZDC West ADC Sum Att

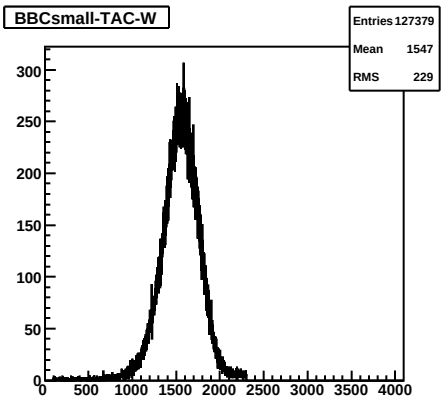

Entries 136904
BBC-L TAC Diff
BCC-S TAC Diff
Entries 136904
BBC-L TAC Diff
VPD TAC Diff
Entries 136904
BBC-L TAC Diff
ZDC TAC Diff
Entries 136904
BBC-S TAC Diff
VPD TAC Diff
Entries 136904
BBC-S TAC Diff
ZDC TAC Diff
Entries 136904
VPD TAC Diff
ZDC TAC Diff

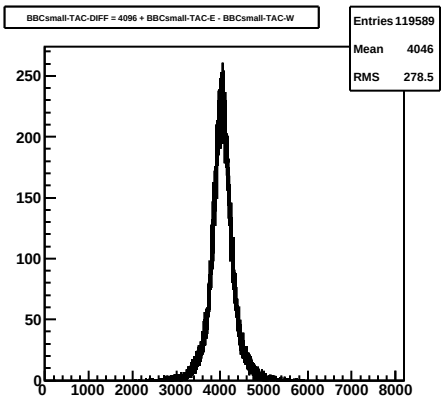

ZDC-TAC-E
Entries 4840
Mean 245.9
RMS 35.71

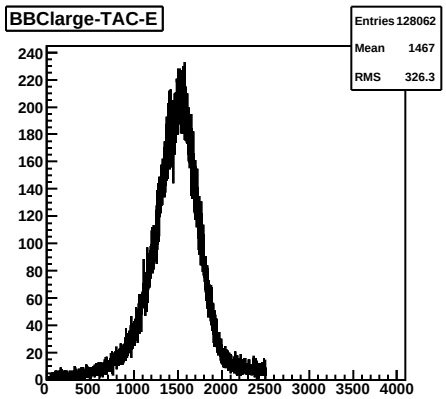

ZDC-TAC-W
Entries 4047
Mean 219.1
RMS 36.41

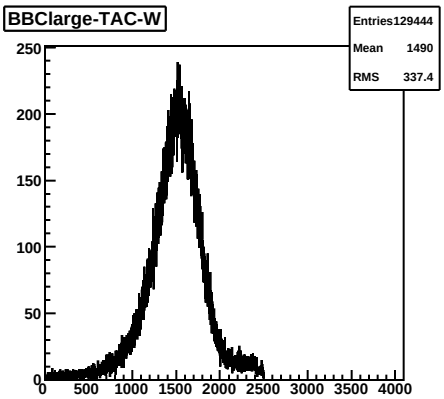

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 282
Mean 524.5
RMS 22.38

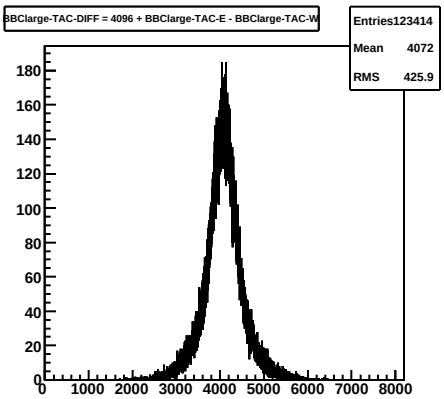

BBCsmall-TAC-E
Entries 125518
Mean 1499
RMS 224.4

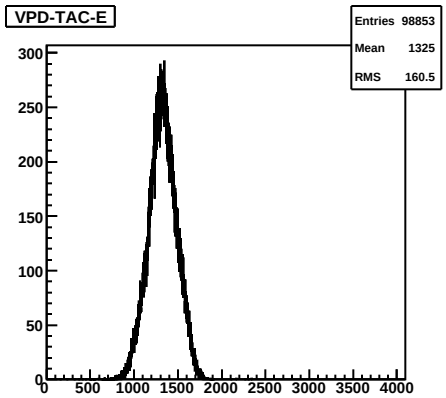

BBCsmall-TAC-W
Entries 127379
Mean 1547
RMS 229

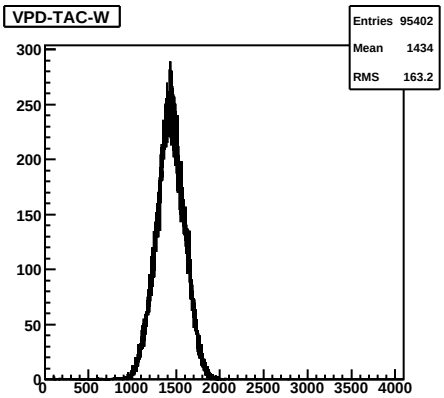

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 119589
Mean 4046
RMS 278.5

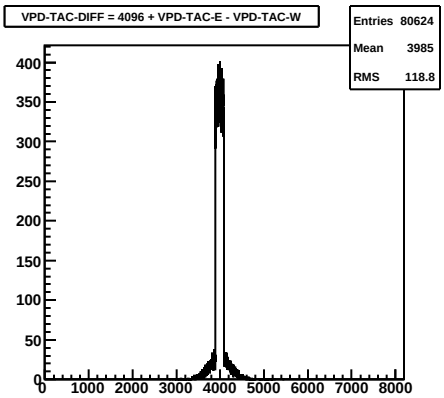

BBClarge-TAC-E
Entries 128062
Mean 1467
RMS 326.3

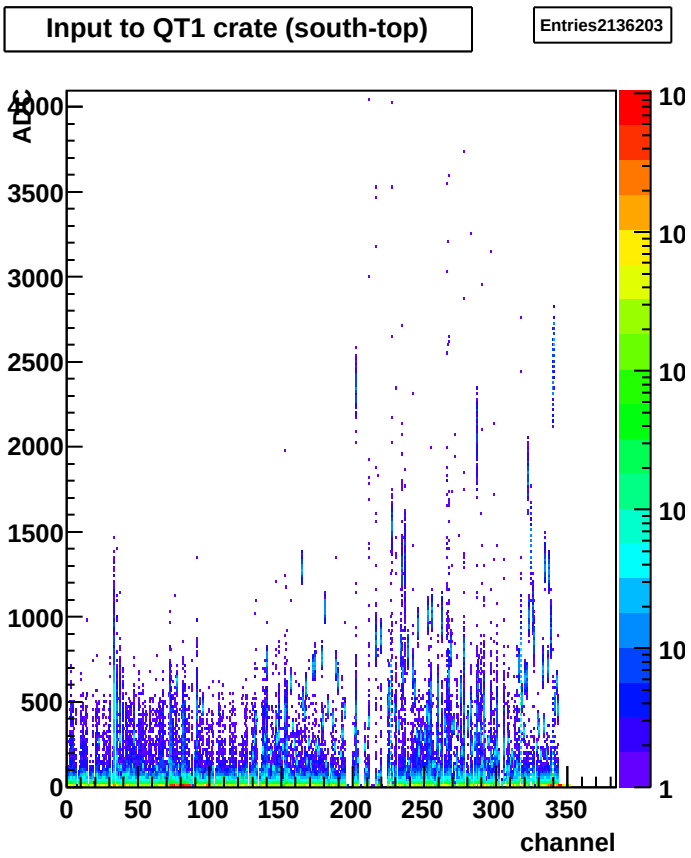

BBClarge-TAC-W
Entries 129444
Mean 1490
RMS 337.4

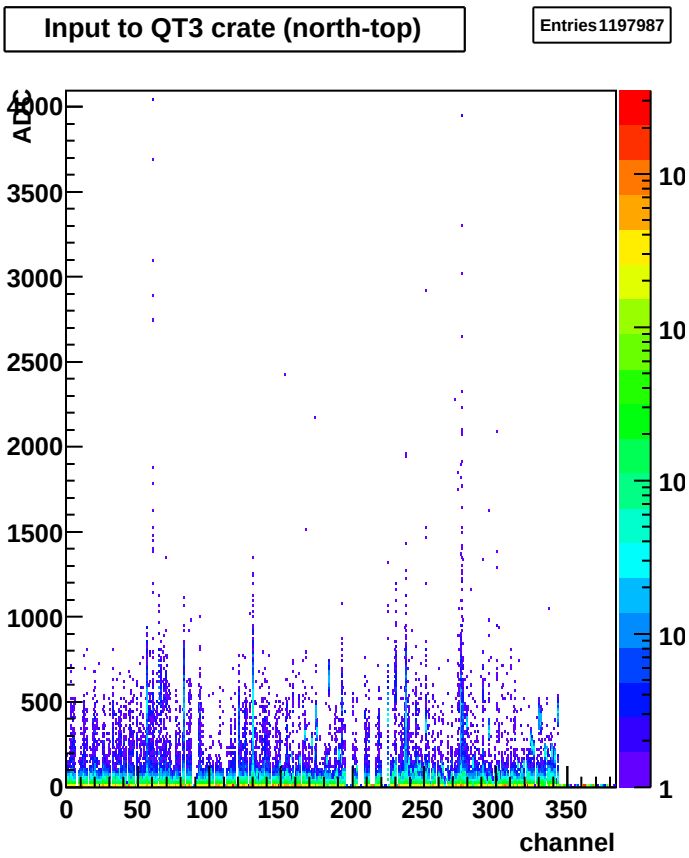

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 123414
Mean 4072
RMS 425.9

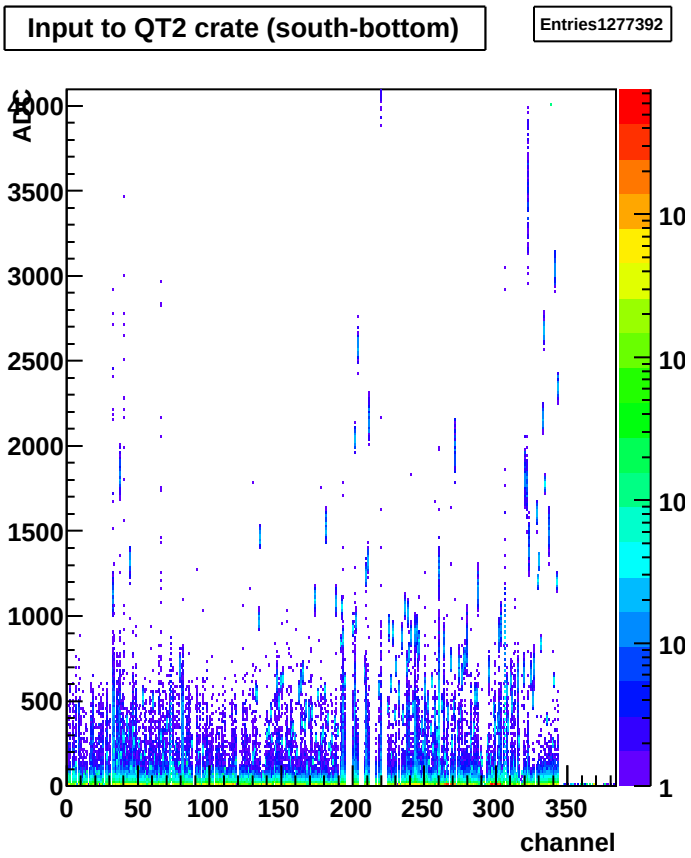

VPD-TAC-E
Entries 98853
Mean 1325
RMS 160.5

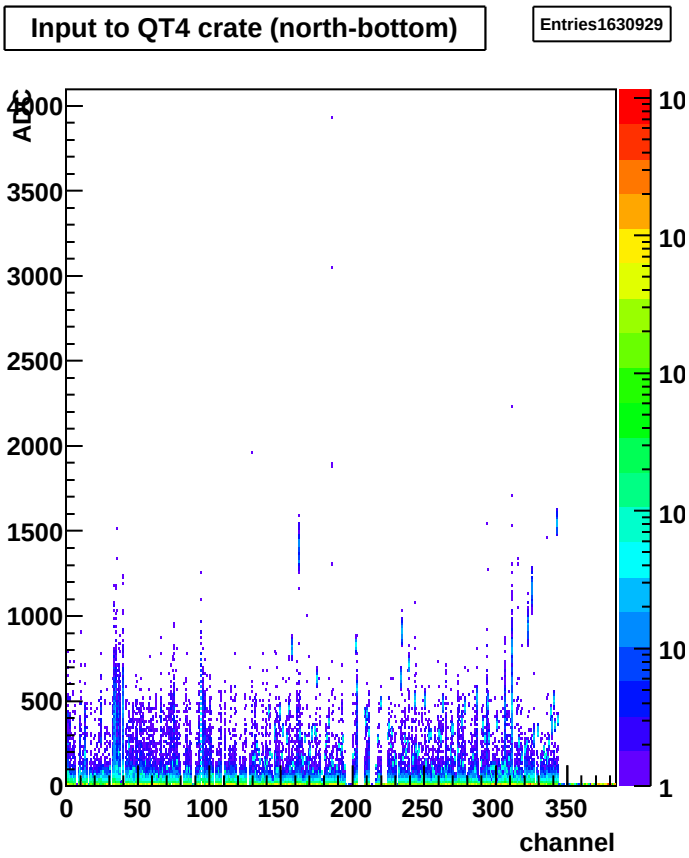

VPD-TAC-W
Entries 95402
Mean 1434
RMS 163.2

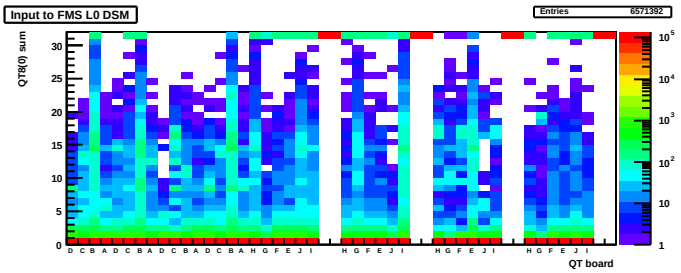

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 80624
Mean 3985
RMS 118.8

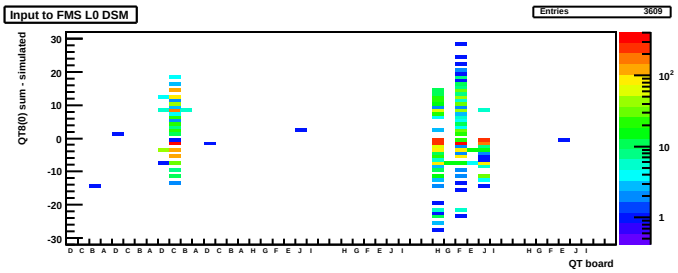

Input to QT1 crate (south-top)
Entries2136203
ADC
channel

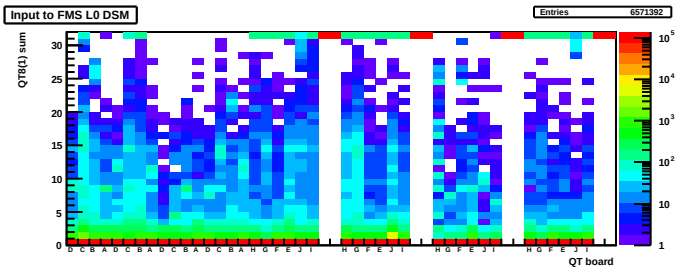

Input to QT3 crate (north-top)
Entries1197987
ADC
channel

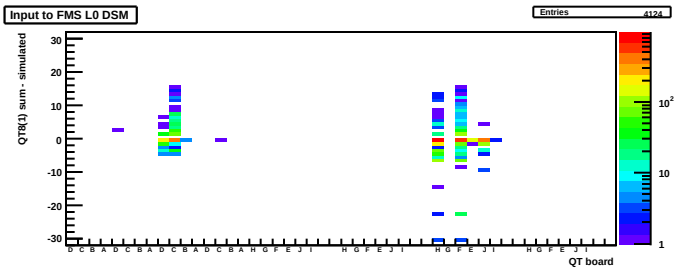

Input to QT2 crate (south-bottom)
Entries1277392
ADC
channel

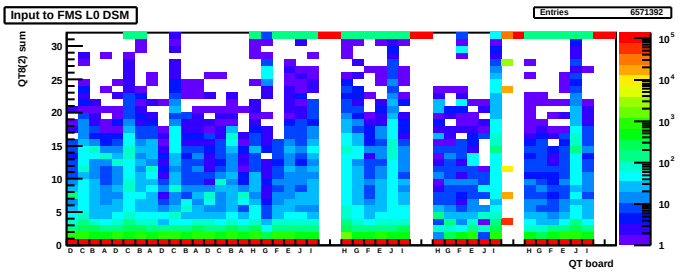

Input to QT4 crate (north-bottom)
Entries1630929
ADC
channel

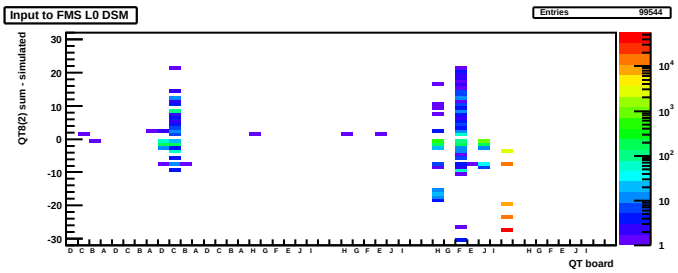

Input to FMS L0 DSM
Entries 6571392
QT8(0) sum
QT board

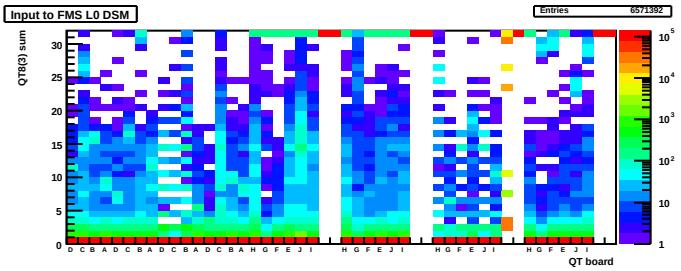

Input to FMS L0 DSM
Entries 3609
QT8(0) sum - simulated
QT board

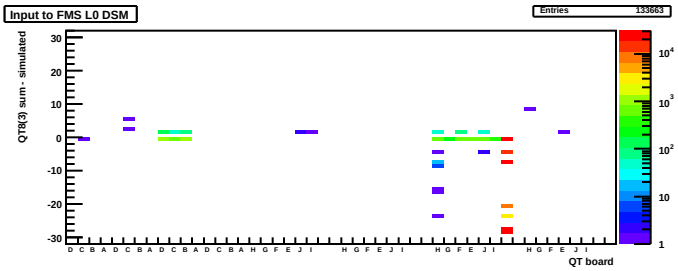

Input to FMS L0 DSM
Entries 6571392
QT8(1) sum
QT board

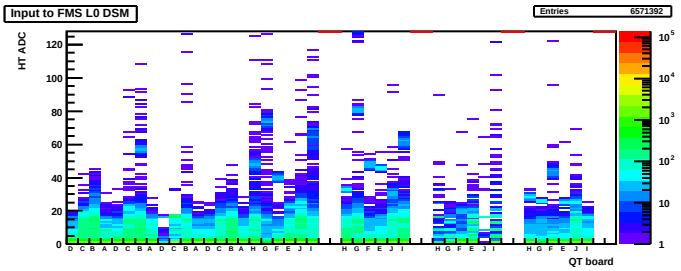

Input to FMS L0 DSM
Entries 4124
QT8(1) sum - simulated
QT board

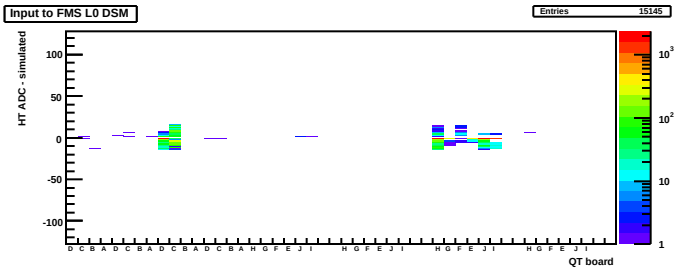

Input to FMS L0 DSM
Entries 6571392
QT8(2) sum
QT board

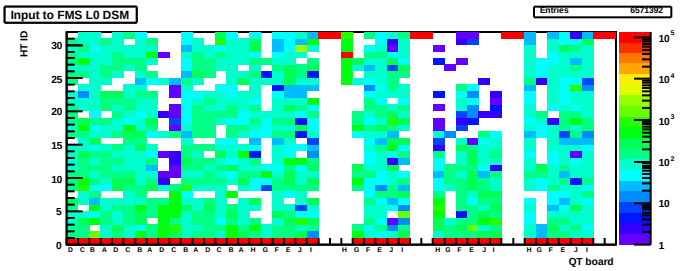

Input to FMS L0 DSM
Entries 99544
QT8(2) sum - simulated
QT board

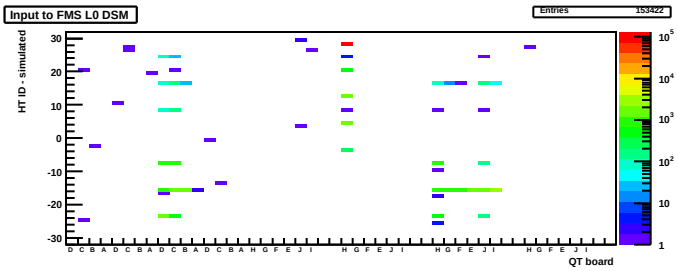

Input to FMS L0 DSM
Entries 6571392
QT8(3) sum
QT board

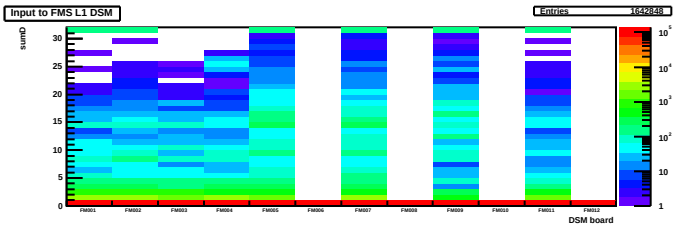

Input to FMS L0 DSM
Entries 133663
QT8(3) sum - simulated
QT board

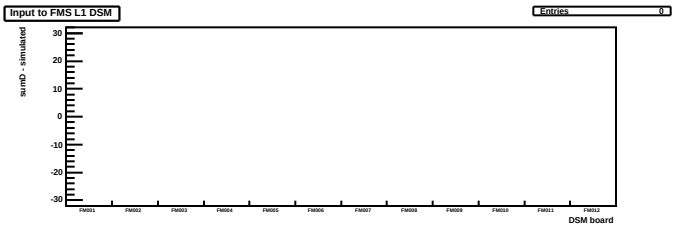

Input to FMS L0 DSM
Entries 6571392
HT ADC
QT board

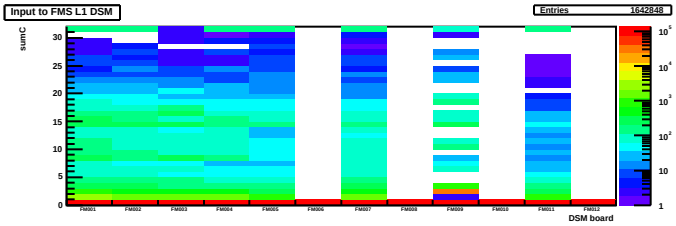

Input to FMS L0 DSM
Entries 15145
HT ADC - simulated
QT board

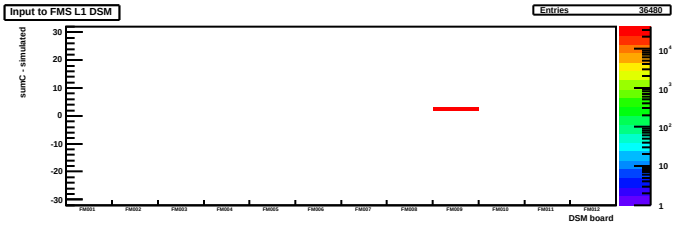

Input to FMS L0 DSM
Entries 6571392
HT ID
QT board

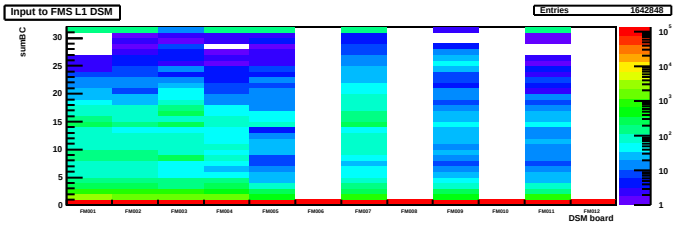

Input to FMS L0 DSM
Entries 153422
HT ID - simulated
QT board

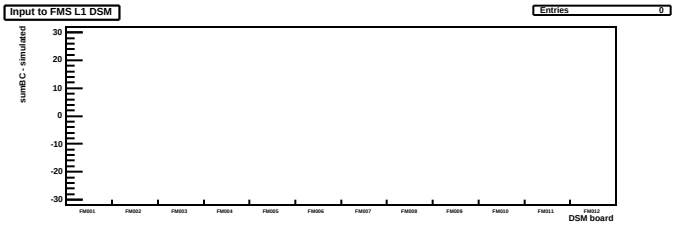

Input to FMS L1 DSM
Entries 1642848
sumD
DSM board

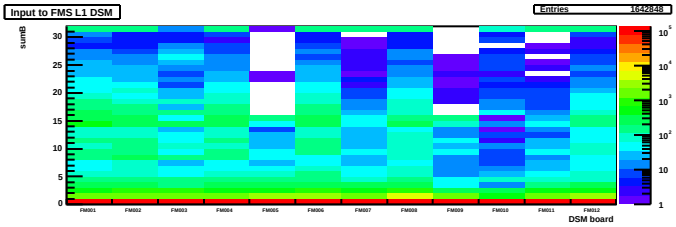

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

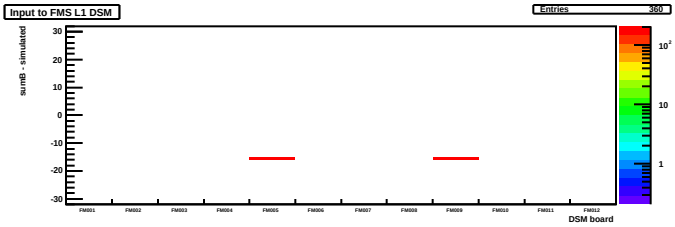

Input to FMS L1 DSM
Entries 1642848
sumC
DSM board

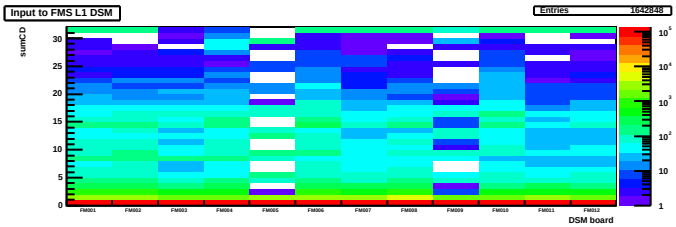

Input to FMS L1 DSM
Entries 36480
sumC - simulated
DSM board

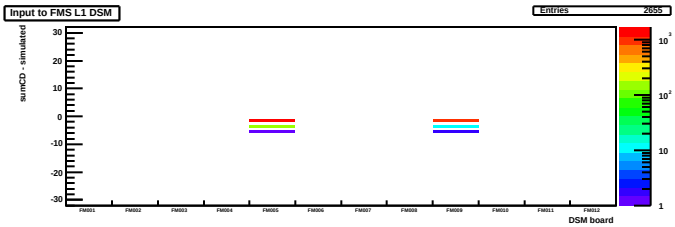

Input to FMS L1 DSM
Entries 1642848
sumBC
DSM board

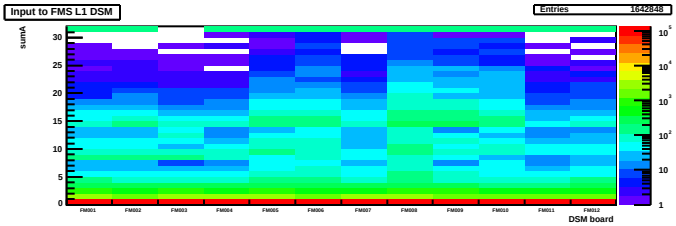

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

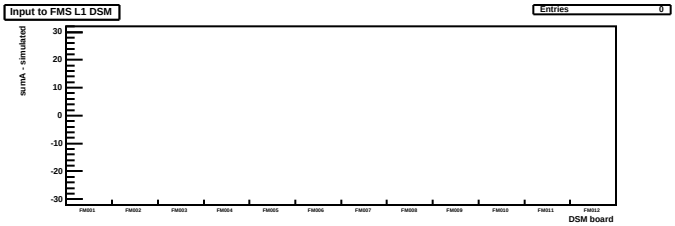

Input to FMS L1 DSM
Entries 1642848
sumB
DSM board

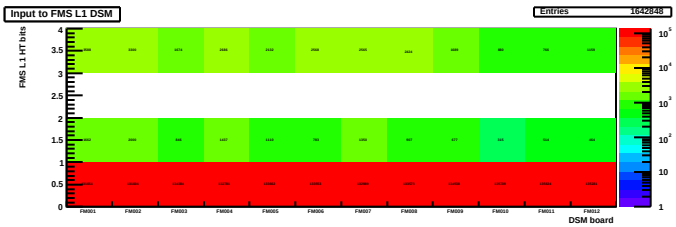

Input to FMS L1 DSM
Entries 360
sumB - simulated
DSM board

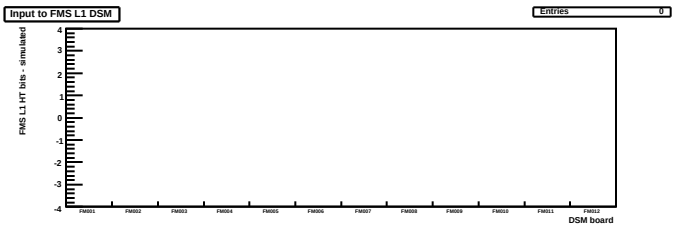

Input to FMS L1 DSM
Entries 1642848
sumCD
DSM board

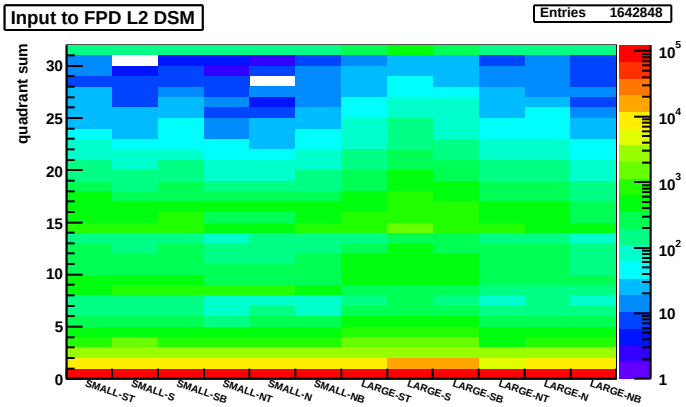

Input to FMS L1 DSM
Entries 2655
sumCD - simulated
DSM board

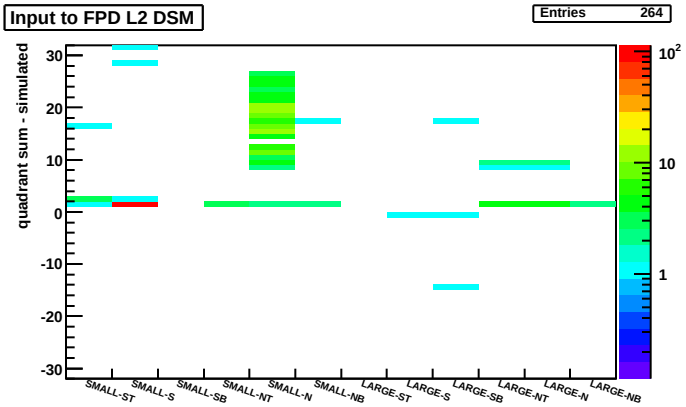

Input to FMS L1 DSM
Entries 1642848
sumA
DSM board

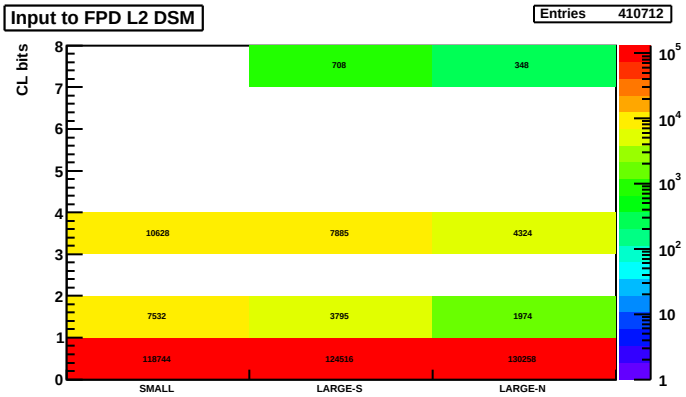

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

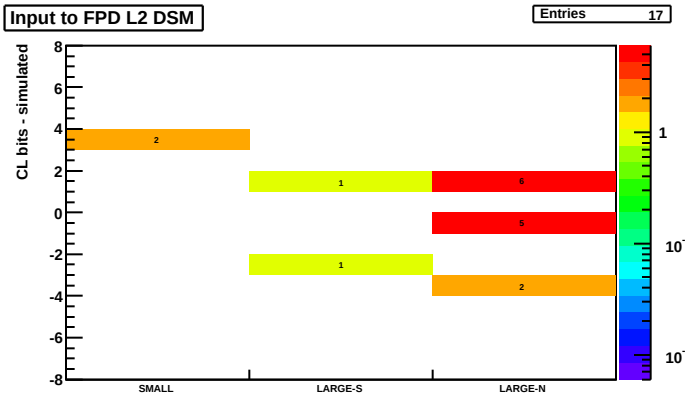

Input to FMS L1 DSM
Entries 1642848
FMS L1 HT bits
DSM board

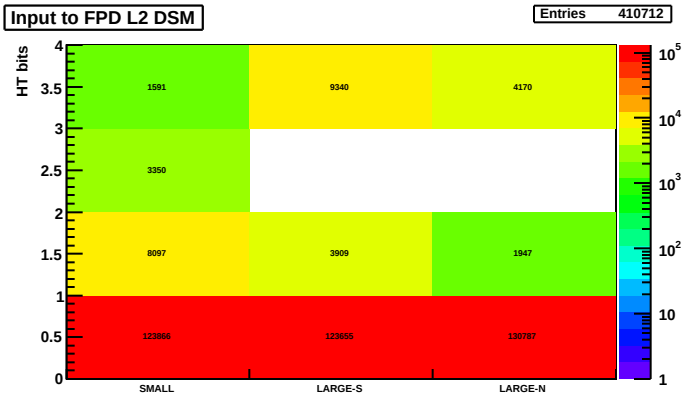

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

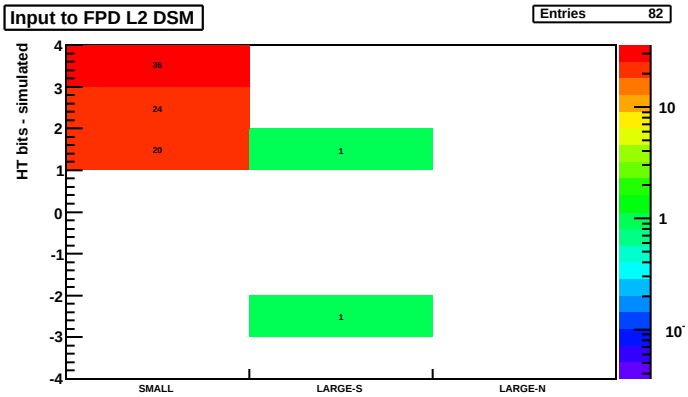

Input to FPD L2 DSM
Entries 1642848
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

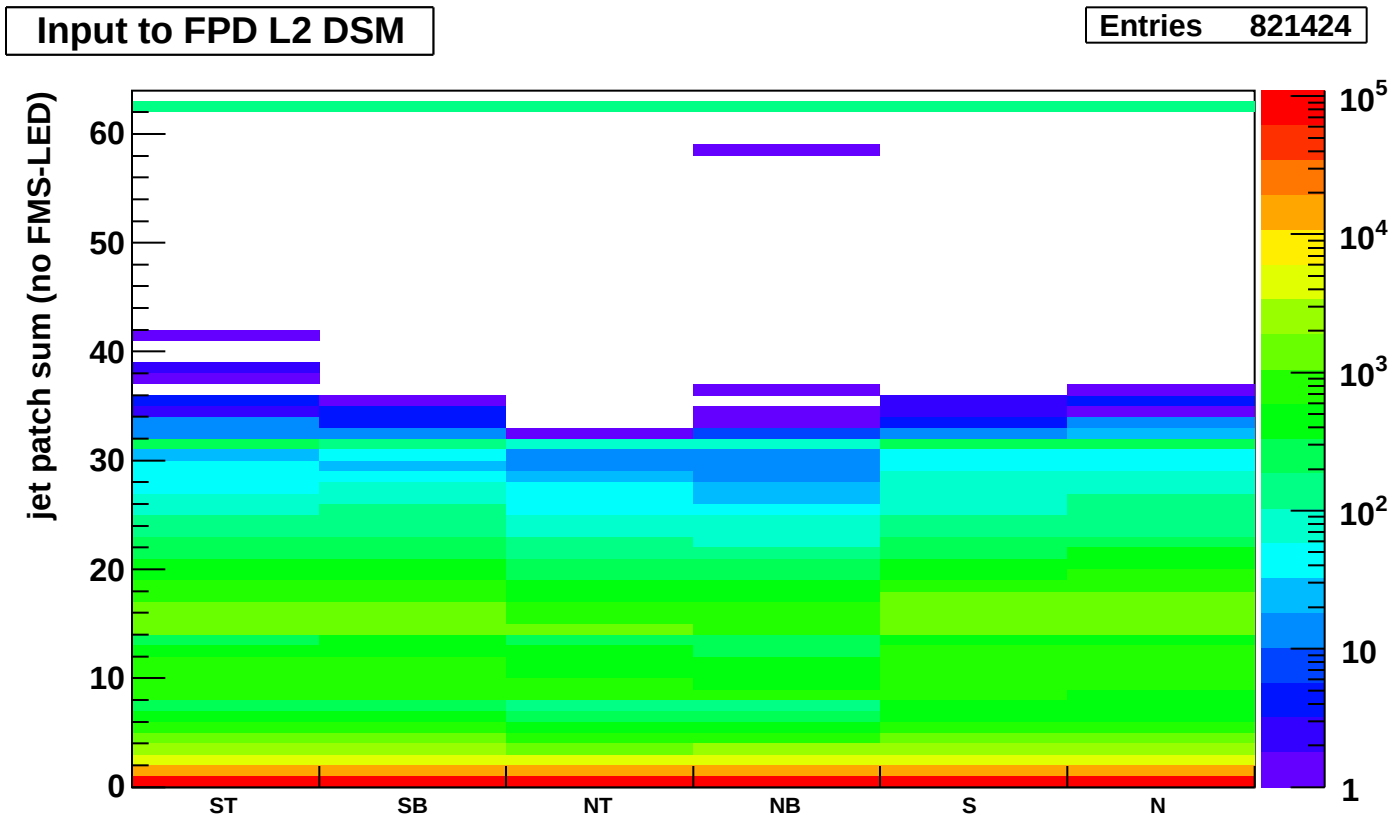

Input to FPD L2 DSM
Entries 264
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

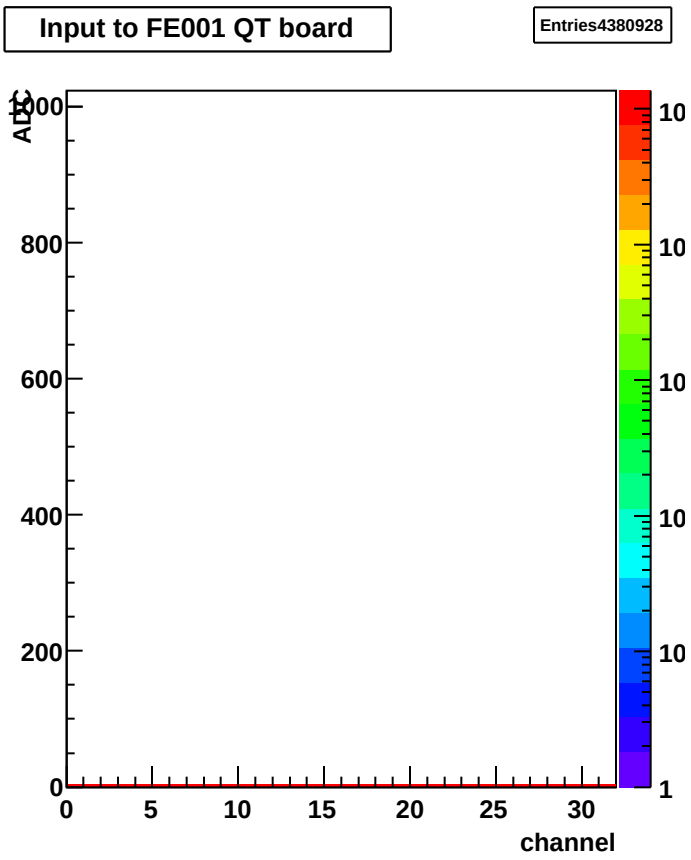

Input to FPD L2 DSM
Entries 410712
CL bits
708
348
10628
7885
4324
7532
3795
1974
118744
124516
130258
SMALL
LARGE-S
LARGE-N

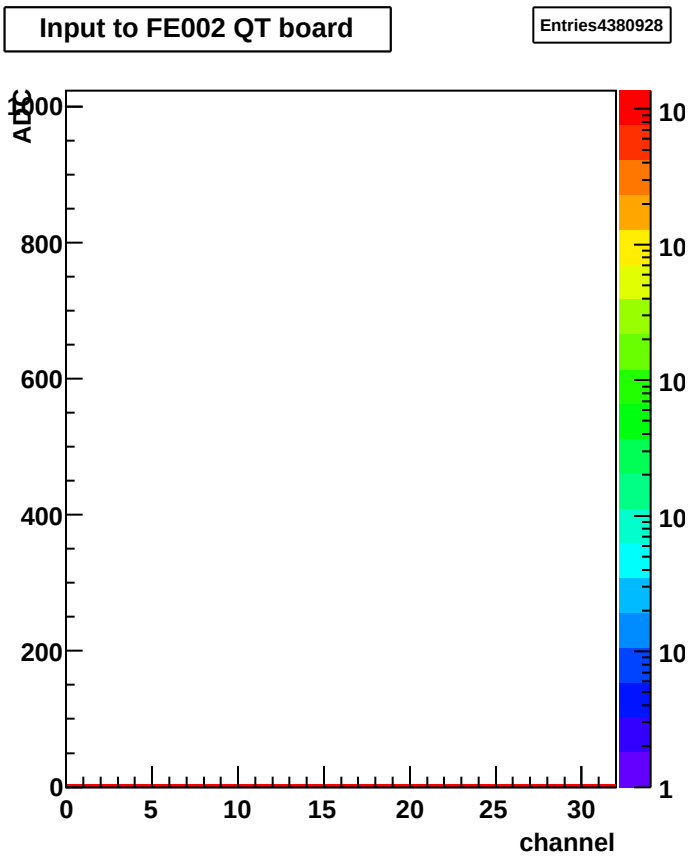

Input to FPD L2 DSM
Entries 17
CL bits - simulated
2
1
6
5
1
2
SMALL
LARGE-S
LARGE-N

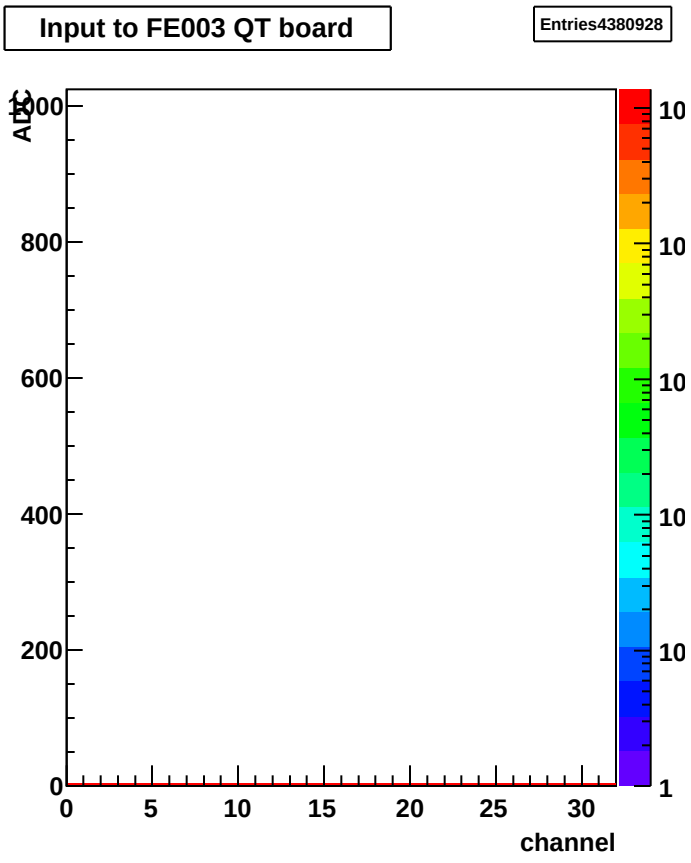

Input to FPD L2 DSM
Entries 410712
HT bits
1591
9340
4170
3350
8097
3909
1947
123866
123655
130787
SMALL
LARGE-S
LARGE-N

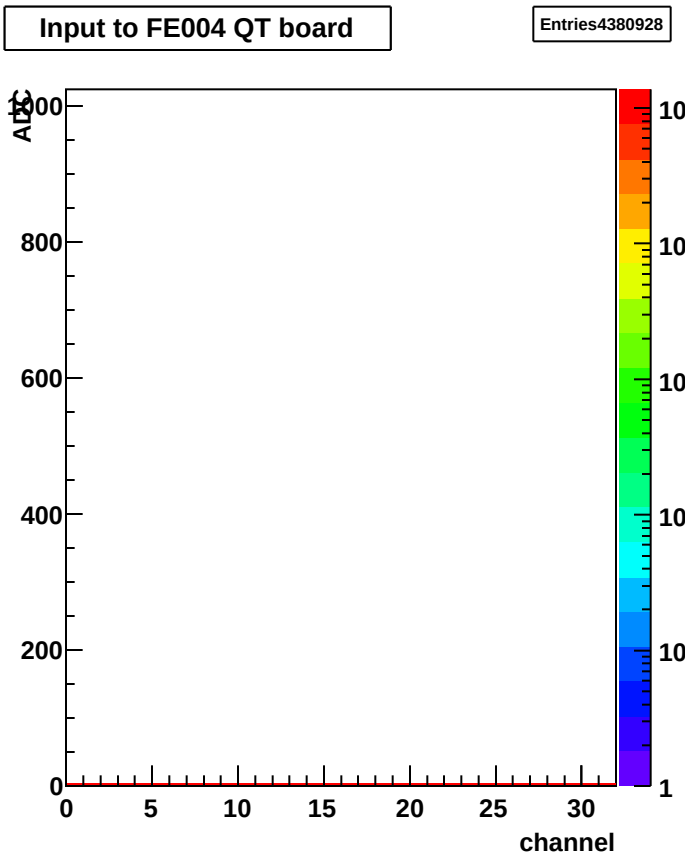

Input to FPD L2 DSM
Entries 82
HT bits - simulated
36
24
20
1
1
SMALL
LARGE-S
LARGE-N

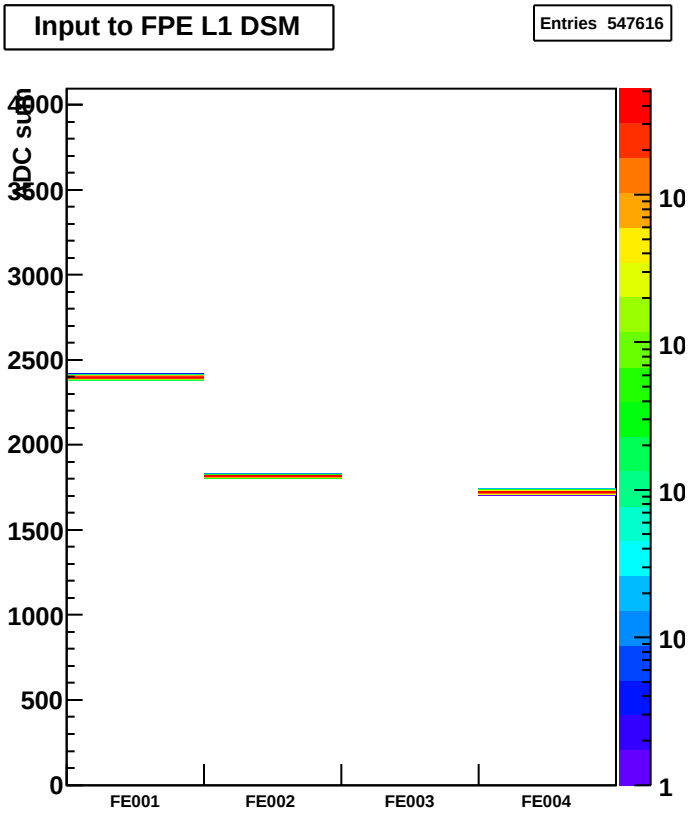

Input to FPD L2 DSM
Entries 821424
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries4380928
ADC
channel

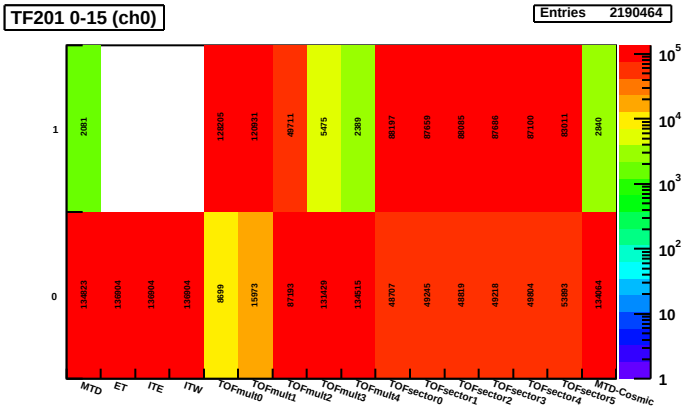

Input to FE002 QT board
Entries4380928
ADC
channel


Input to FE003 QT board
Entries4380928
ADC
channel

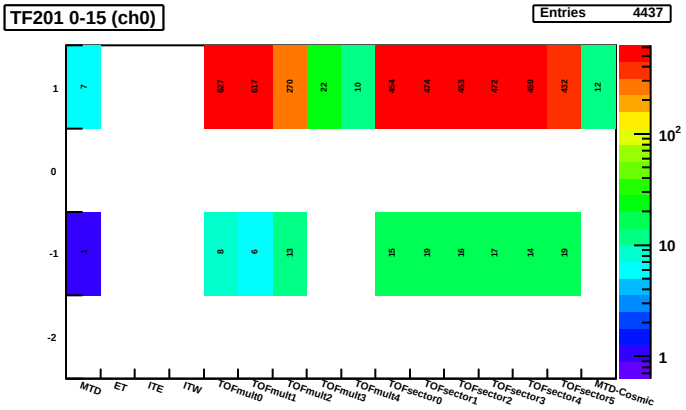

Input to FE004 QT board
Entries4380928
ADC
channel


Input to FPE L1 DSM
Entries 547616
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

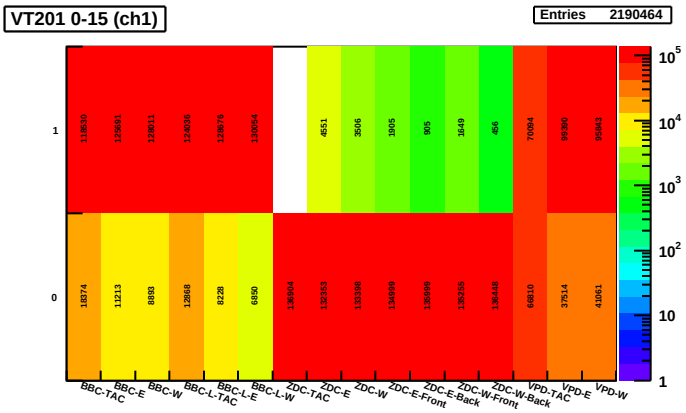
TF201 0-15 (ch0)



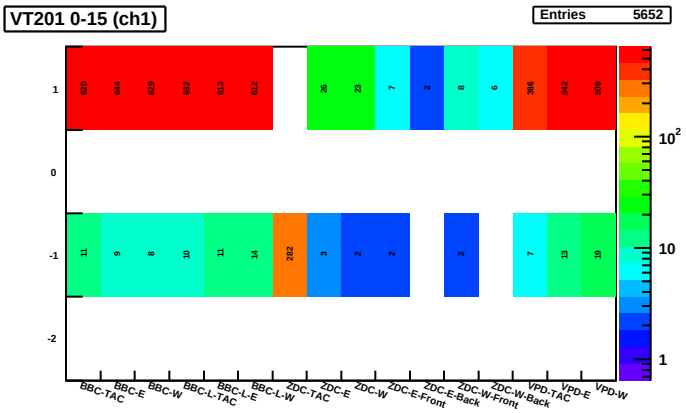
TF201 0-15 (ch0)



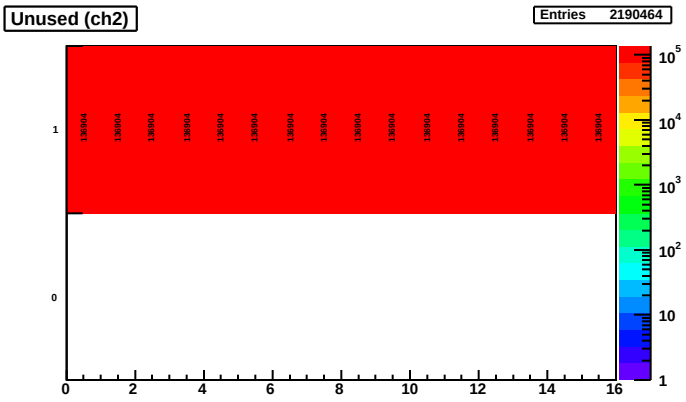
VT201 0-15 (ch1)

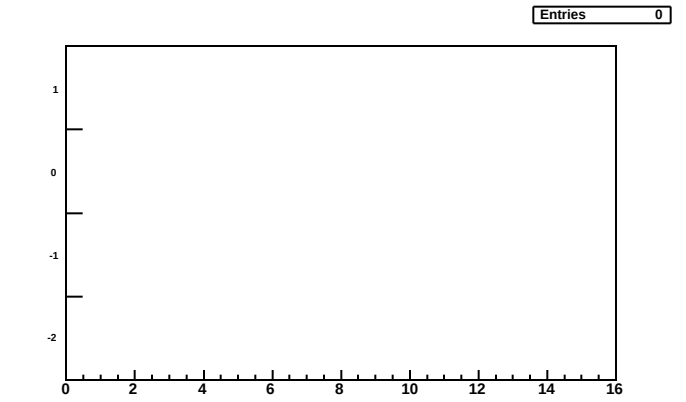


VT201 0-15 (ch1)

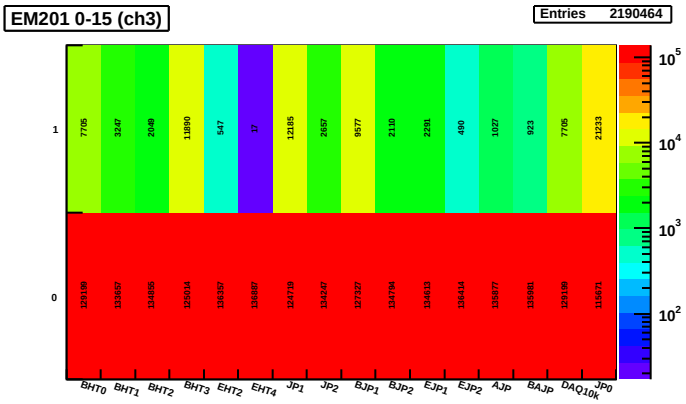


Unused (ch2)

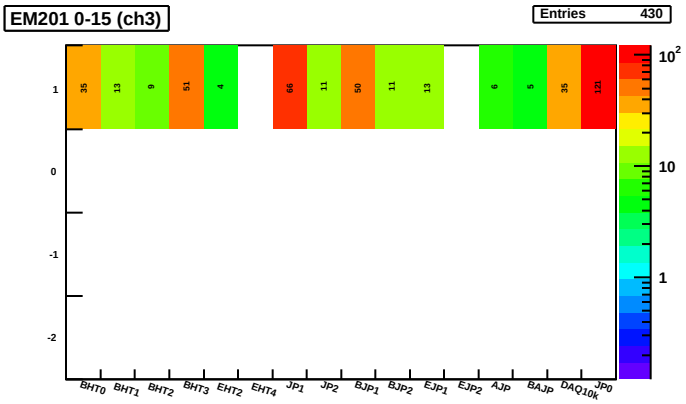




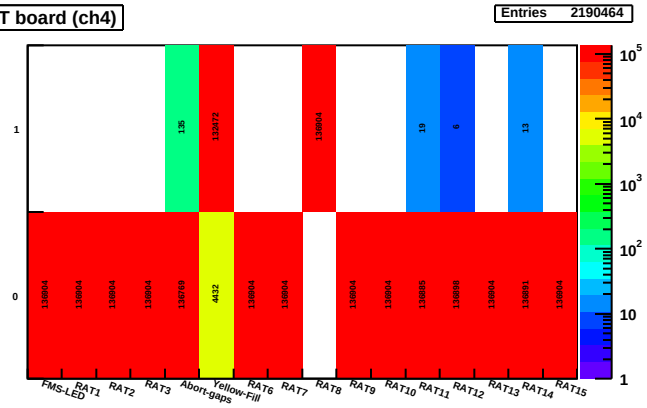
EM201 0-15 (ch3)

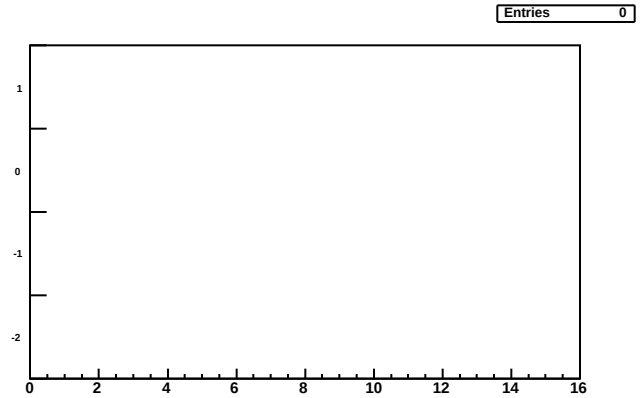


EM201 0-15 (ch3)

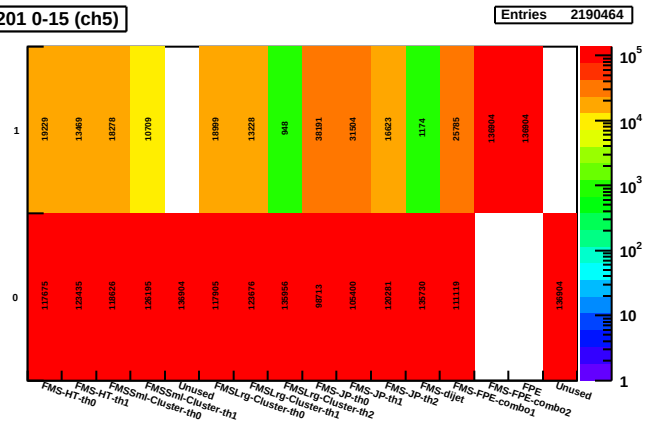


RAT board (ch4)

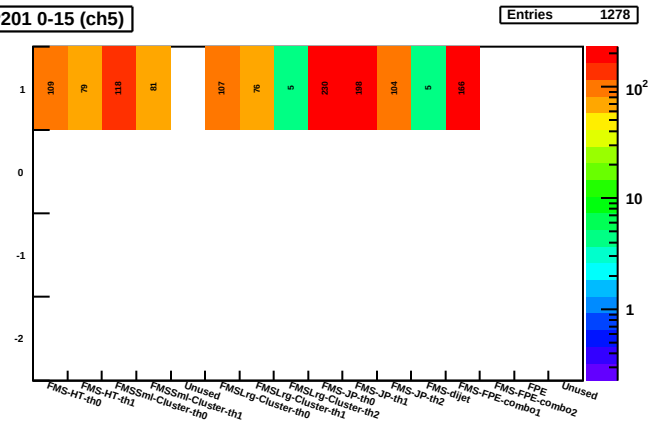




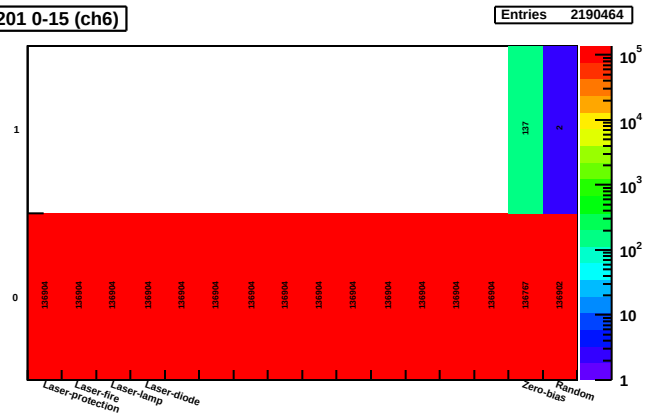
FP201 0-15 (ch5)

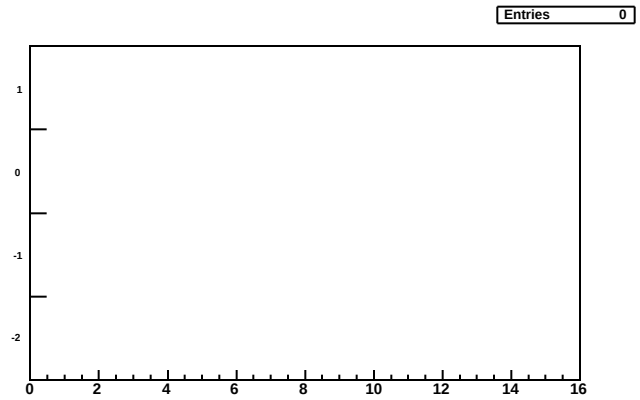
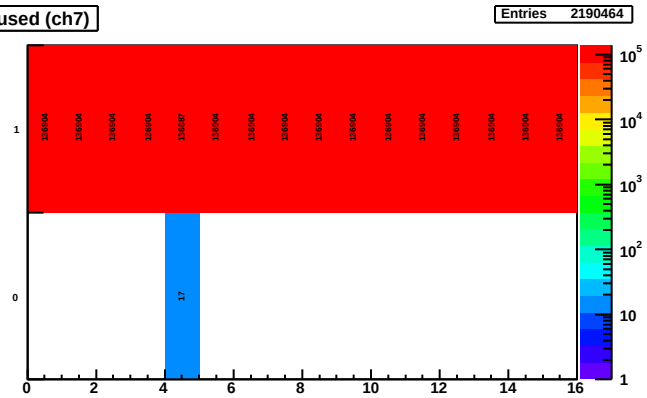
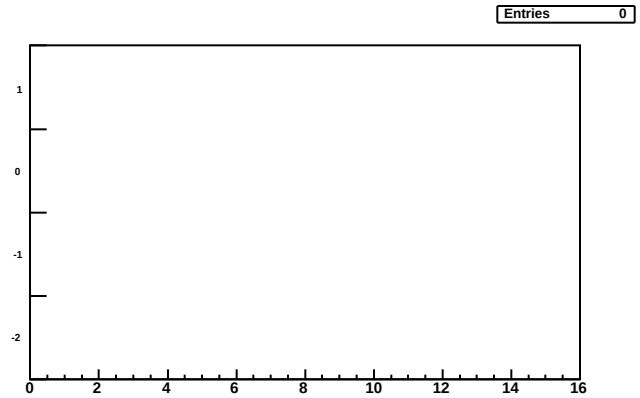


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)