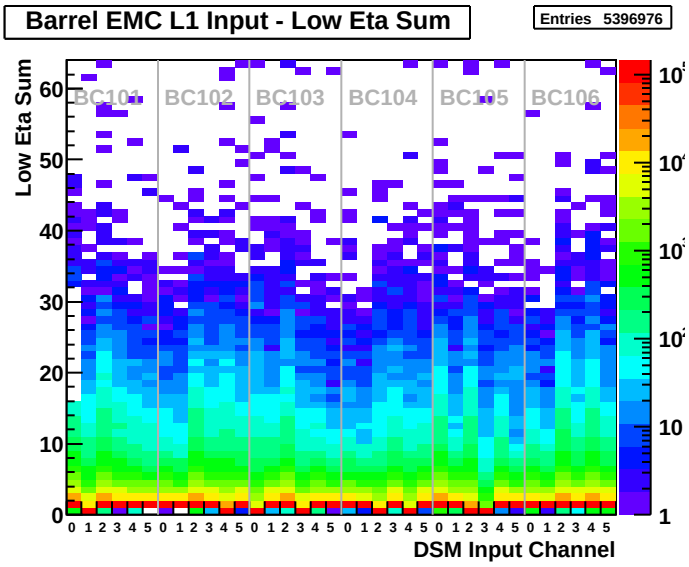
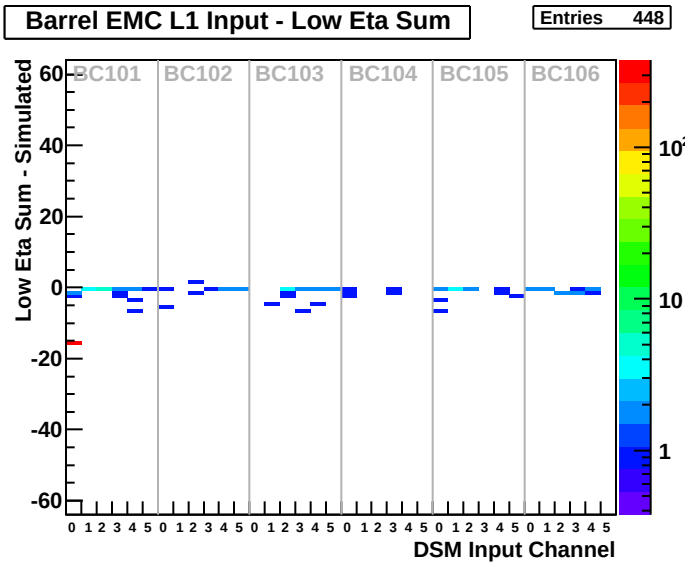
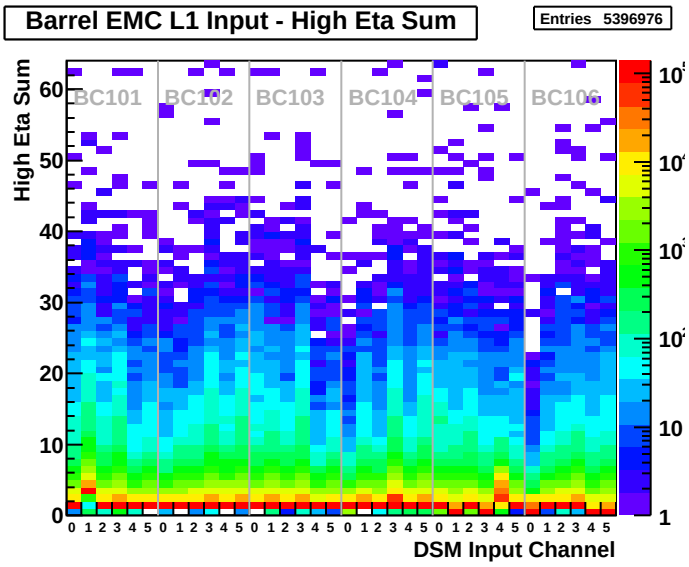
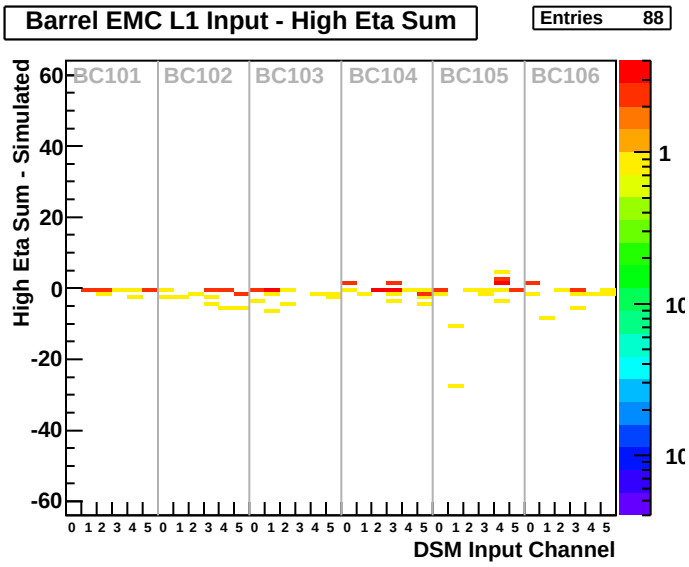
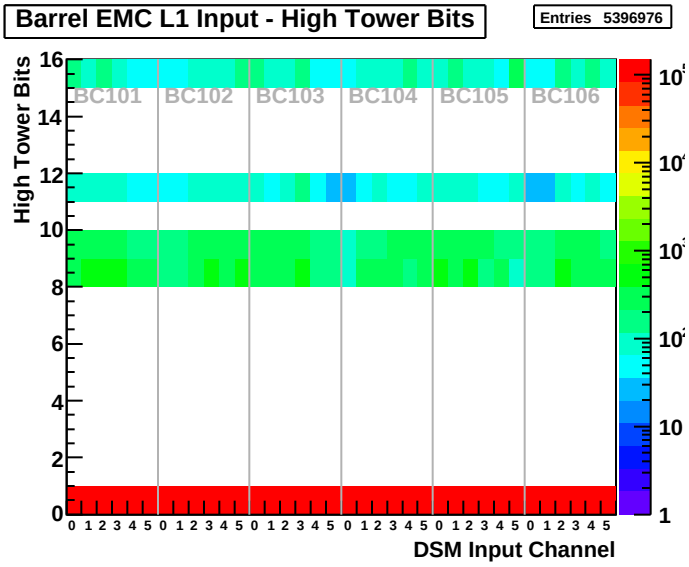
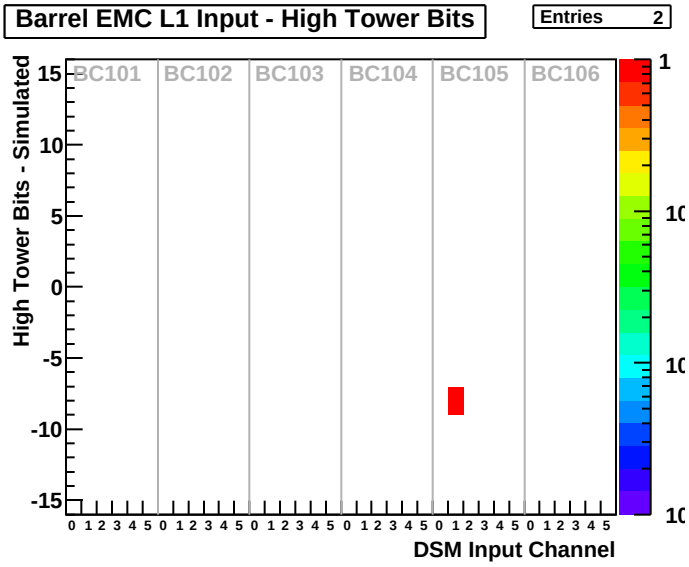

Barrel EMC L1 Input - Low Eta Sum
Entries 5396976
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

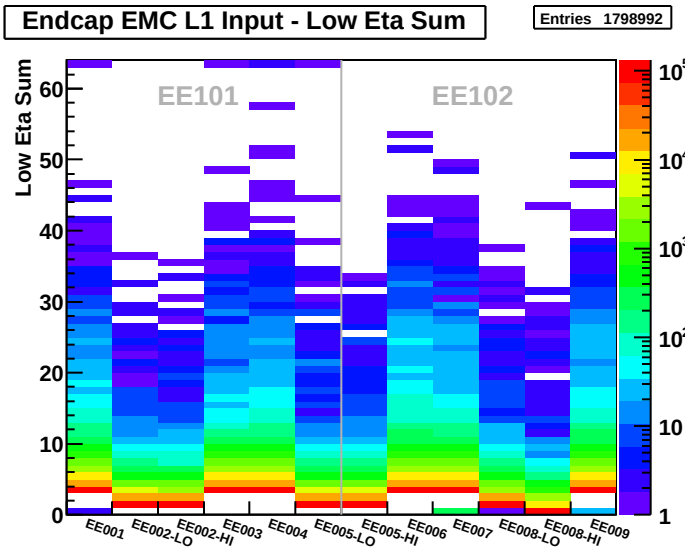

Barrel EMC L1 Input - Low Eta Sum
Entries 448
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

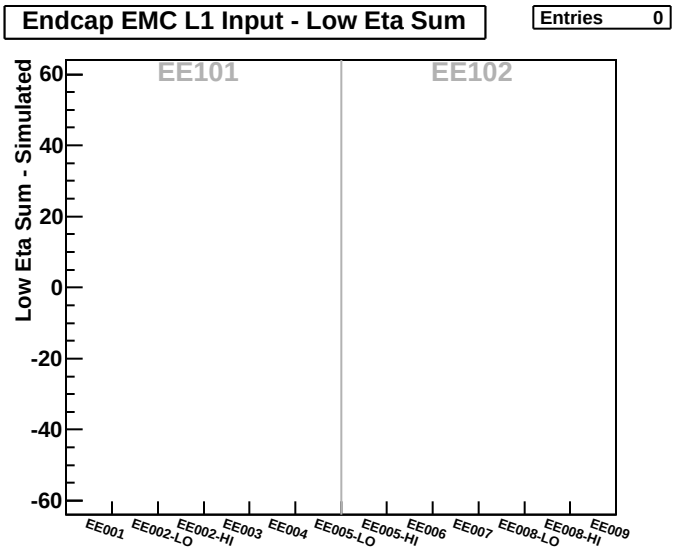

Barrel EMC L1 Input - High Eta Sum
Entries 5396976
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

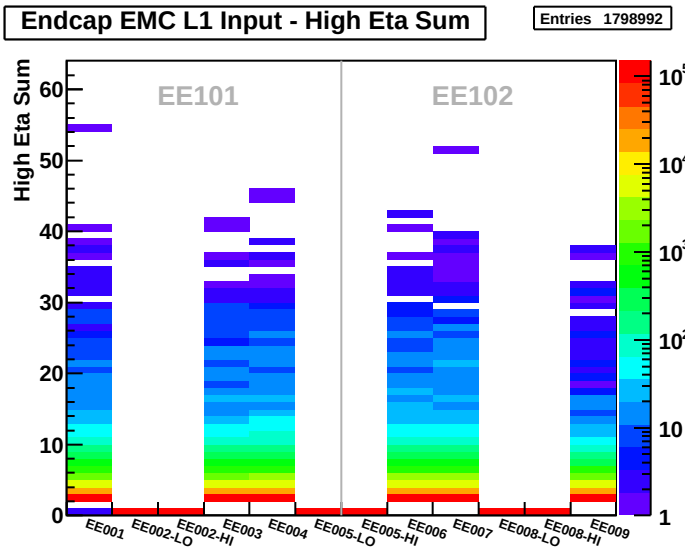

Barrel EMC L1 Input - High Eta Sum
Entries 88
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

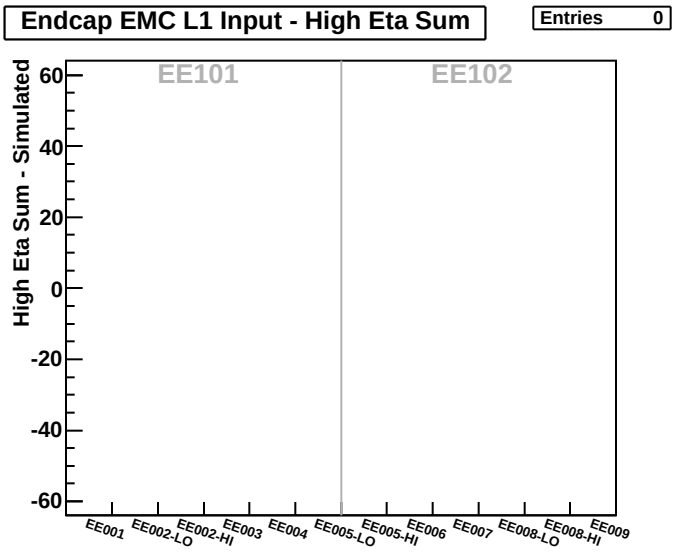

Barrel EMC L1 Input - High Tower Bits
Entries 5396976
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

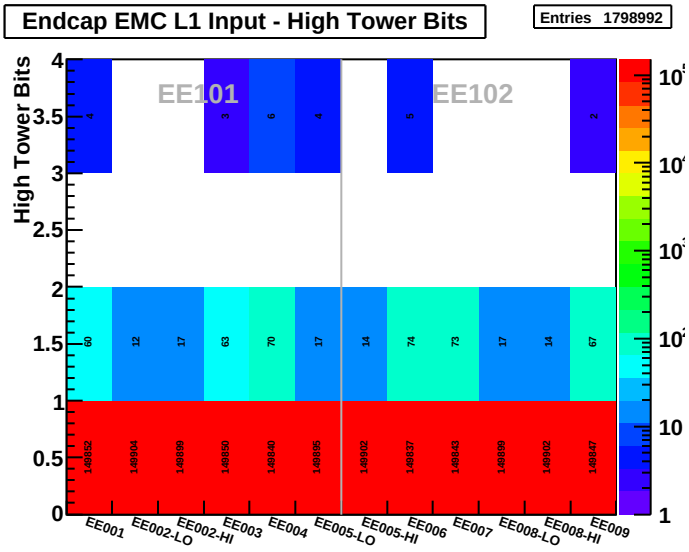

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

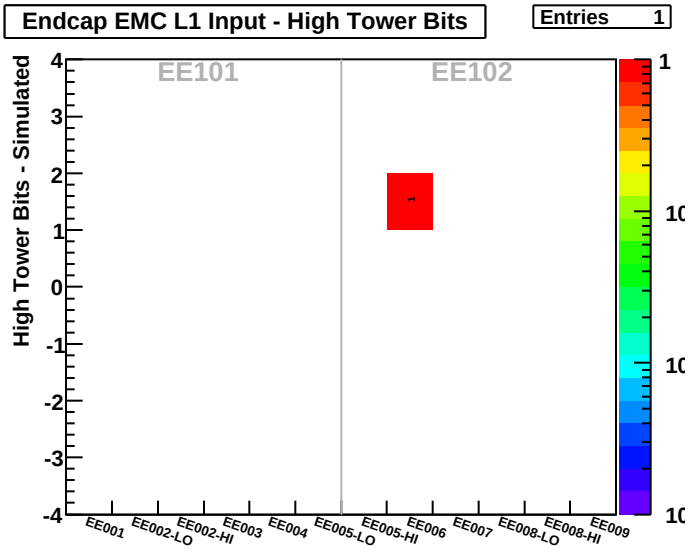

Endcap EMC L1 Input - Low Eta Sum
Entries 1798992
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

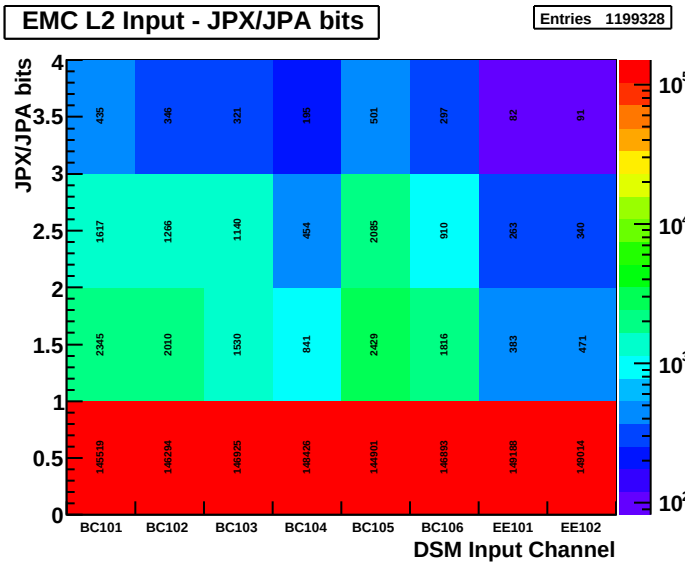

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

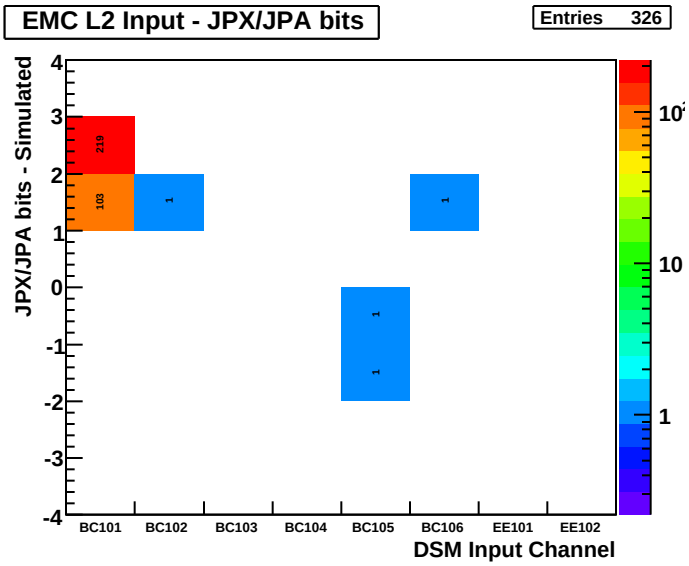

Endcap EMC L1 Input - High Eta Sum
Entries 1798992
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

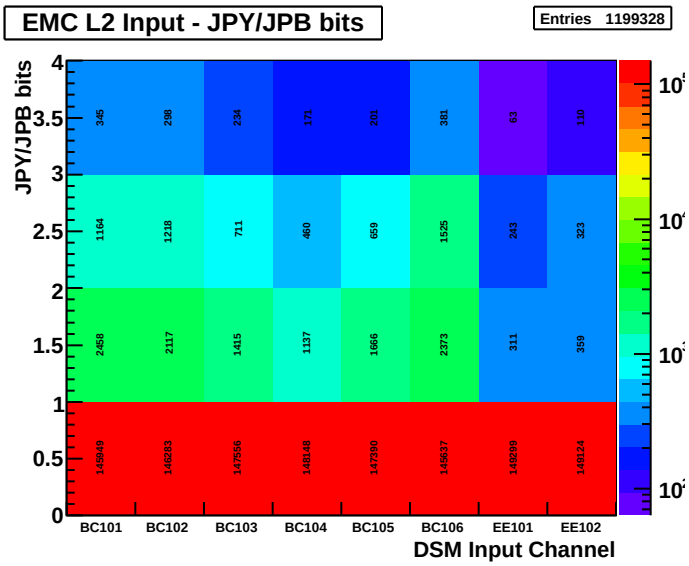

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

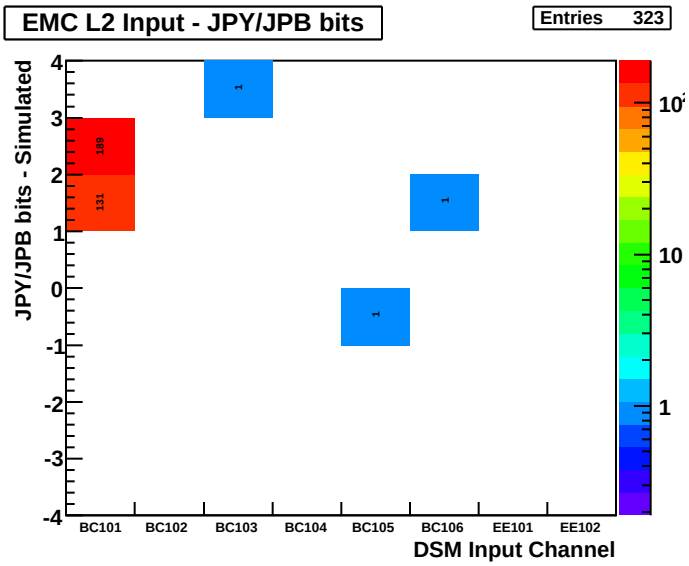

Endcap EMC L1 Input - High Tower Bits
Entries 1798992
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

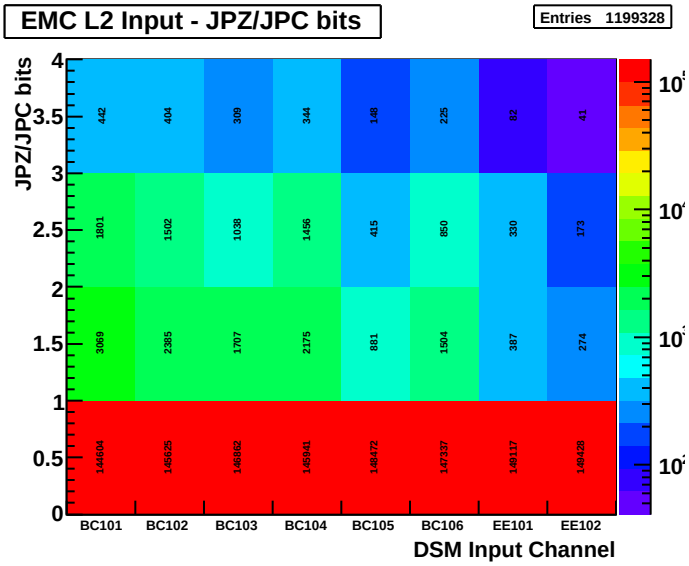

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

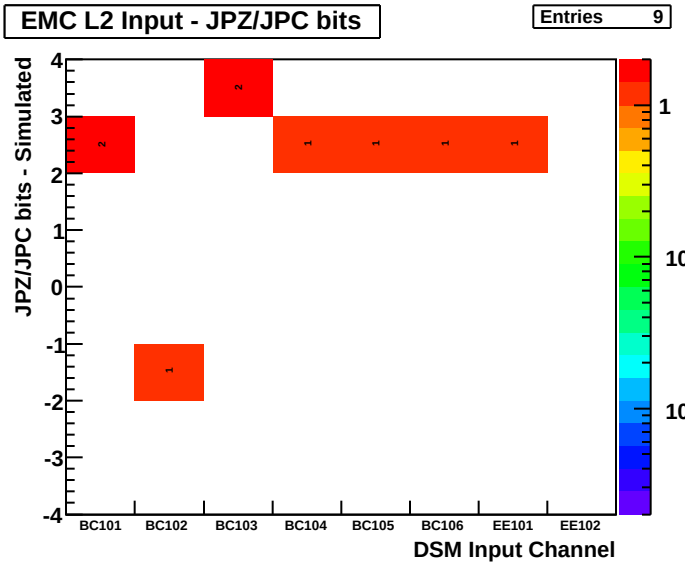

EMC L2 Input - JPX/JPA bits
Entries 1199328
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

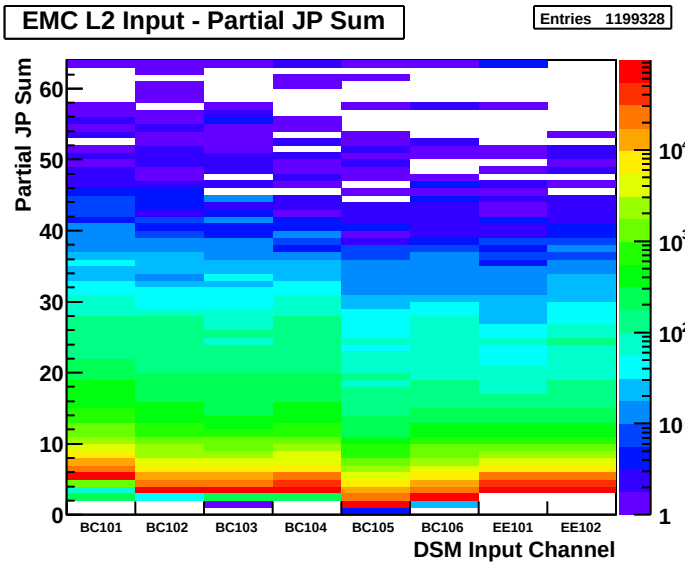

EMC L2 Input - JPX/JPA bits
Entries 326
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

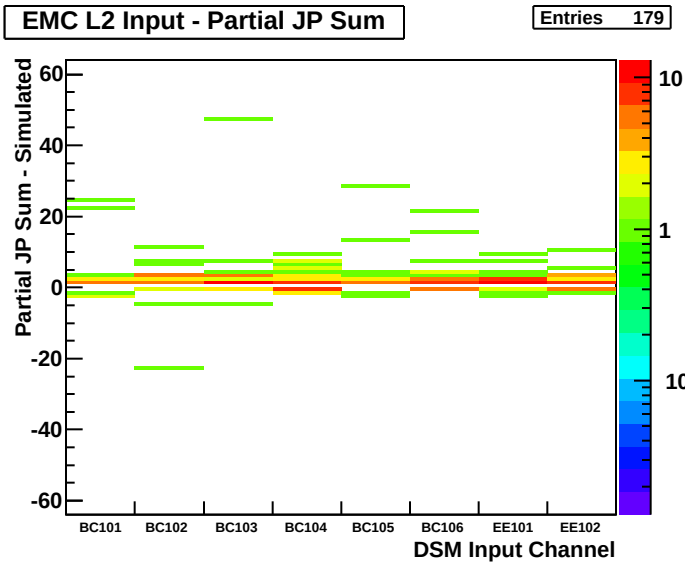

EMC L2 Input - JPY/JPB bits
Entries 1199328
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

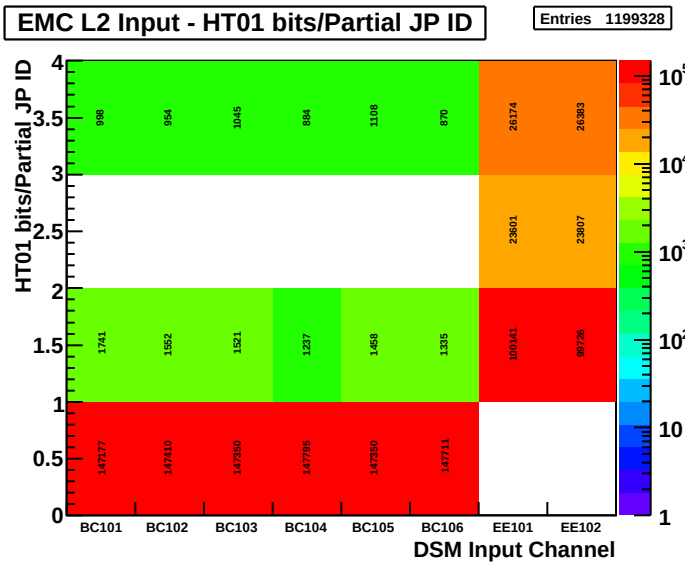

EMC L2 Input - JPY/JPB bits
Entries 323
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

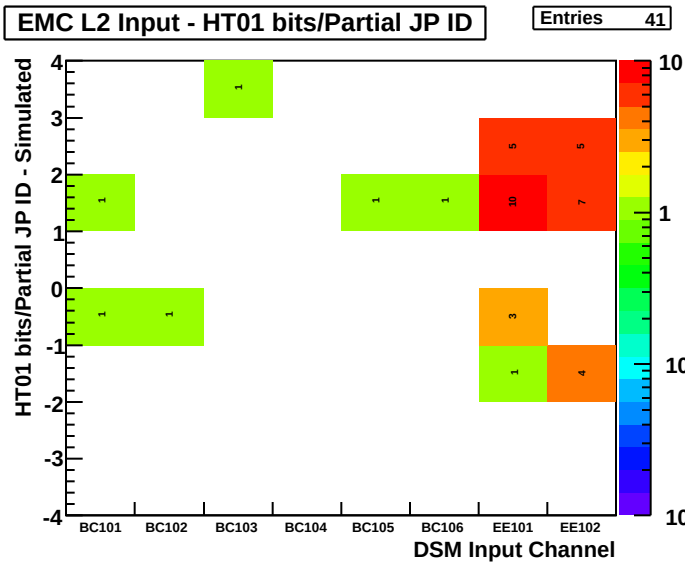

EMC L2 Input - JPZ/JPC bits
Entries 1199328
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

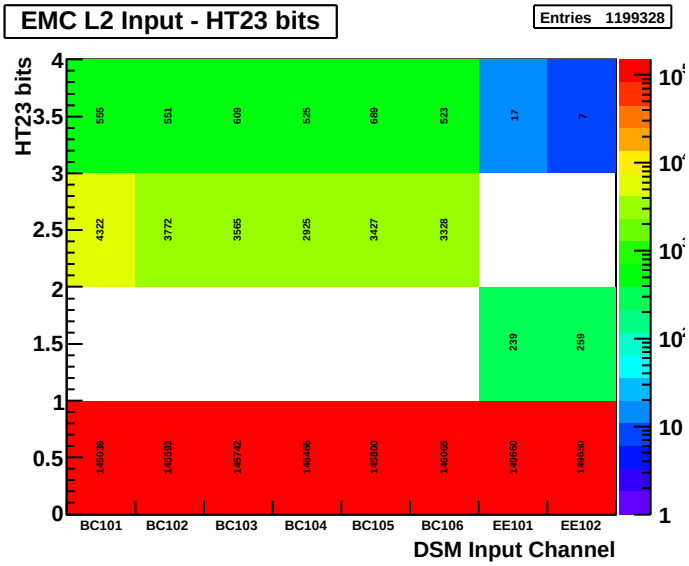

EMC L2 Input - JPZ/JPC bits
Entries 9
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

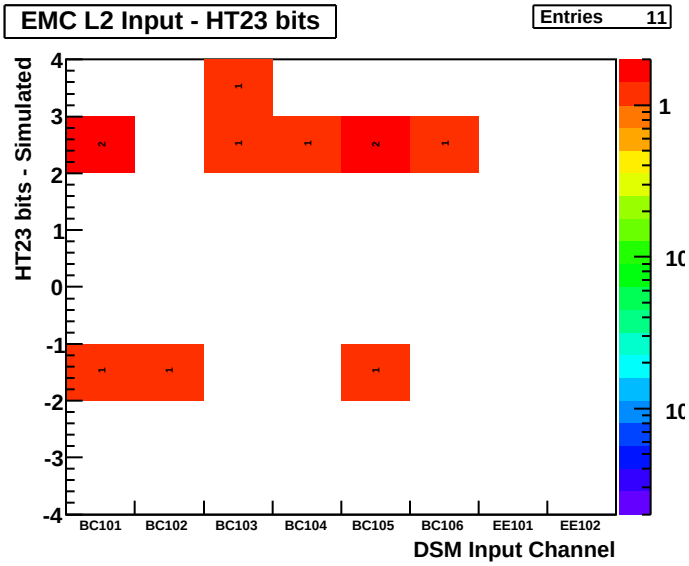

EMC L2 Input - Partial JP Sum
Entries 1199328
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

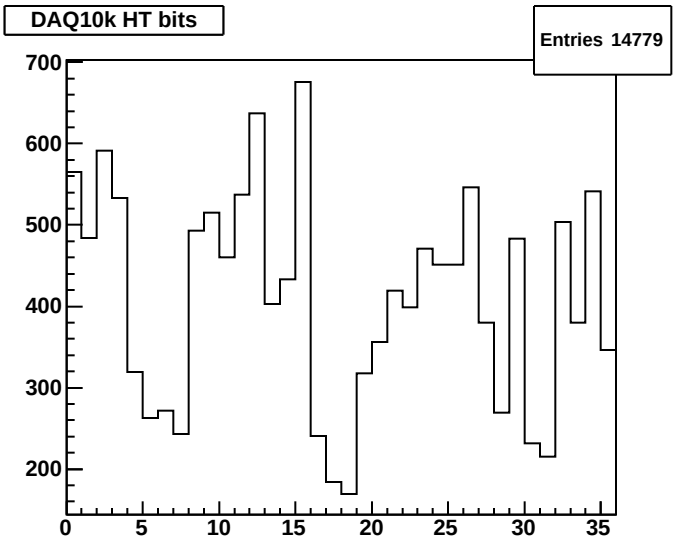

EMC L2 Input - Partial JP Sum
Entries 179
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

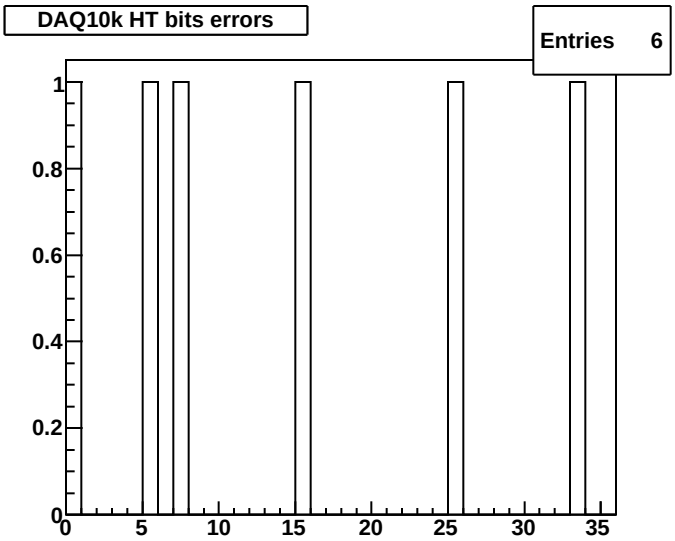

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1199328
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

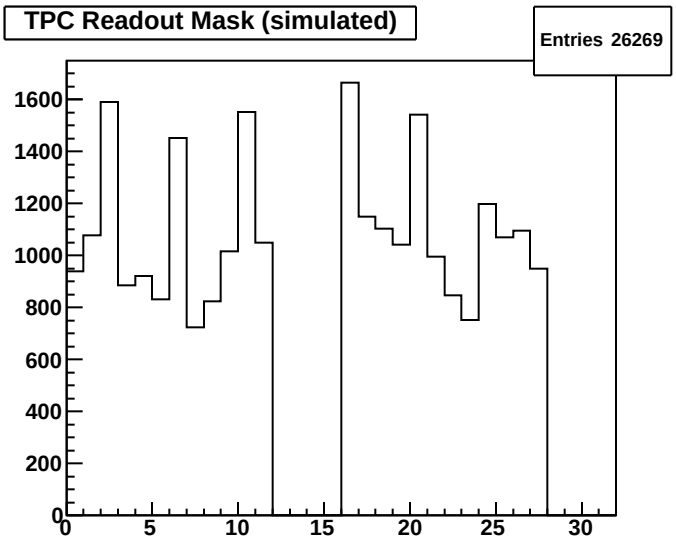

EMC L2 Input - HT01 bits/Partial JP ID
Entries 41
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

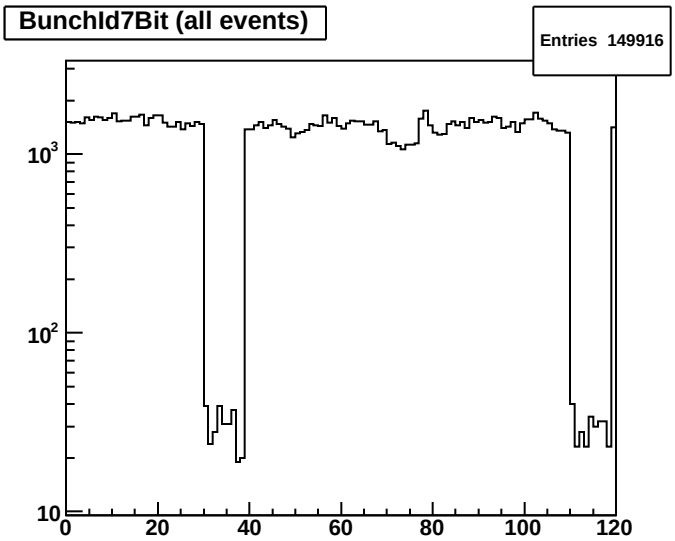

EMC L2 Input - HT23 bits
Entries 1199328
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

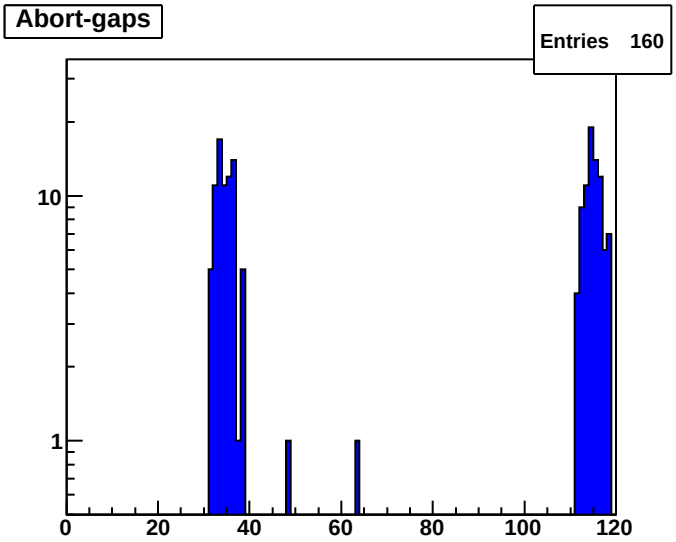

EMC L2 Input - HT23 bits
Entries 11
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

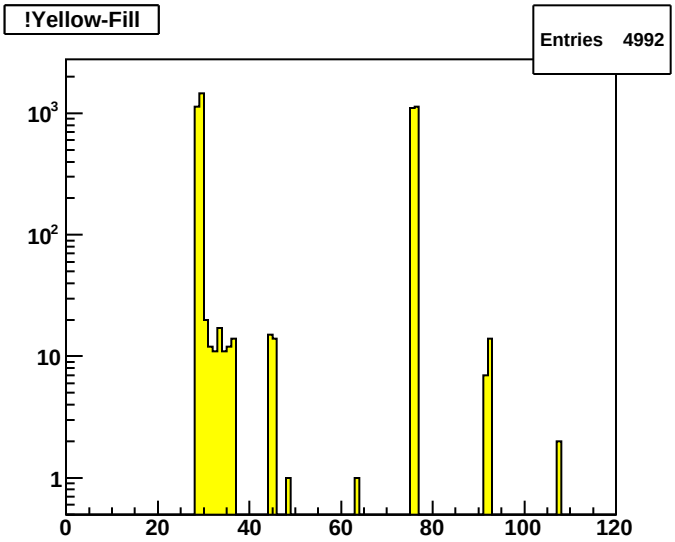

DAQ10k HT bits
Entries 14779

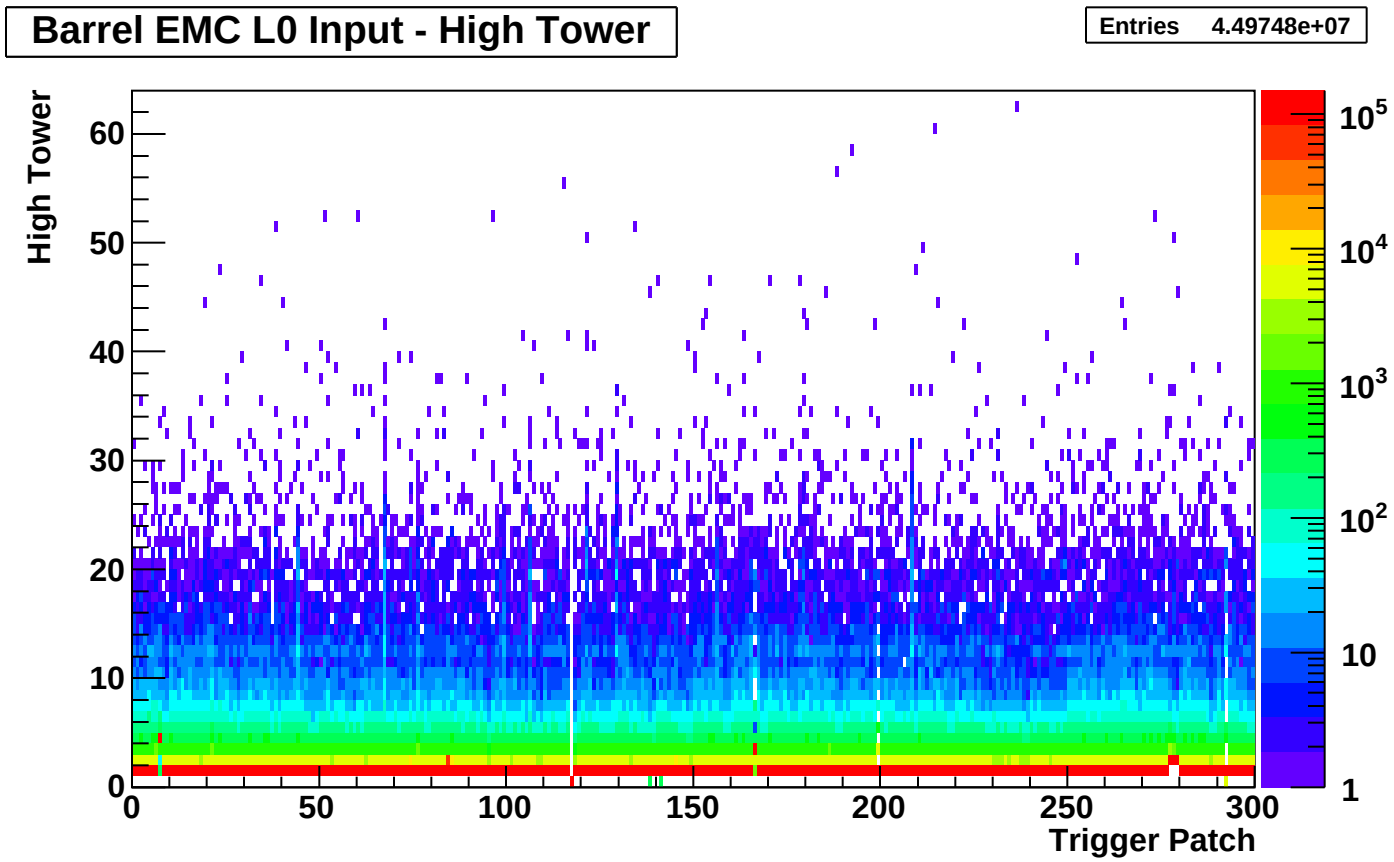

DAQ10k HT bits errors
Entries 6

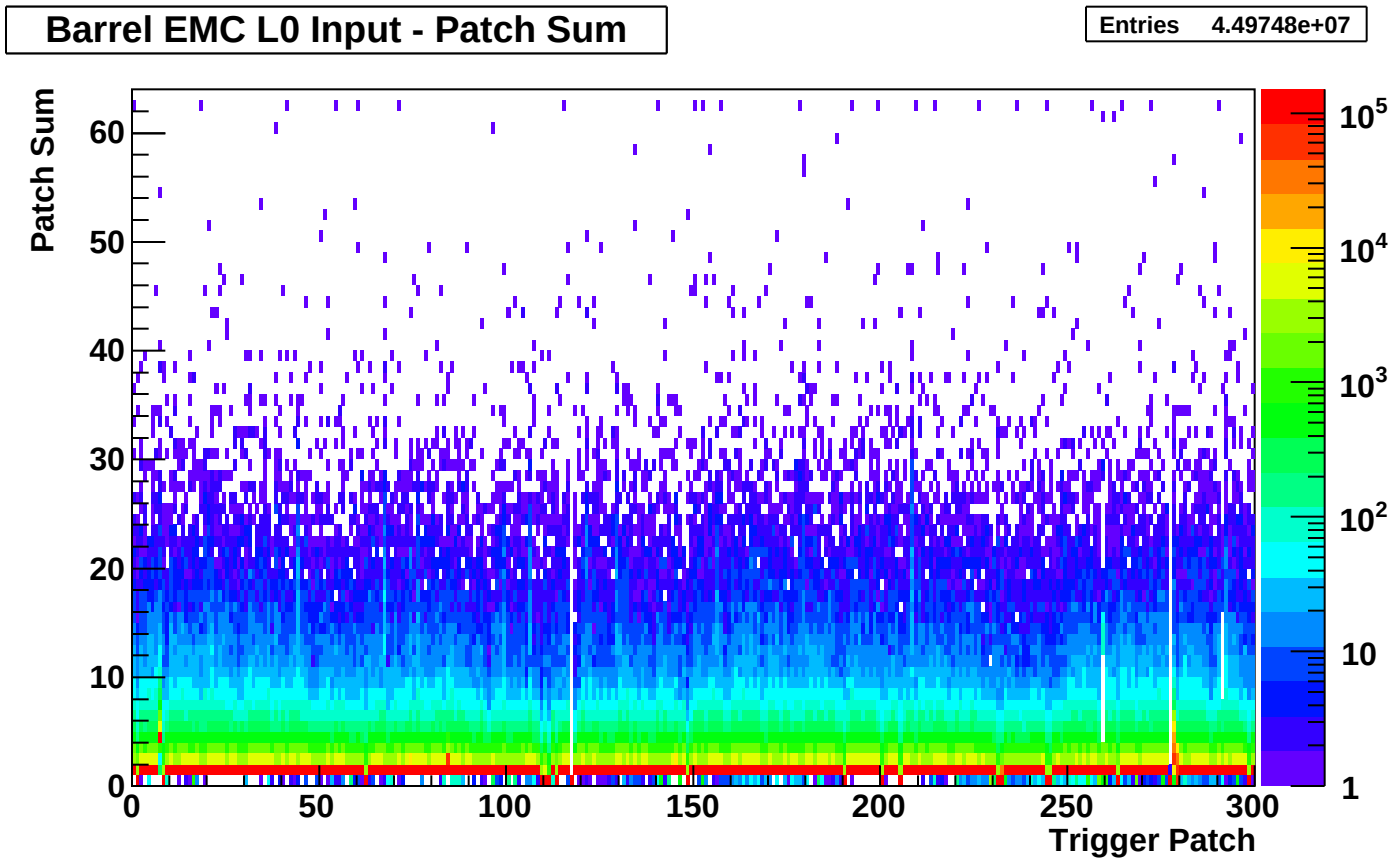

TPC Readout Mask (simulated)
Entries 26269

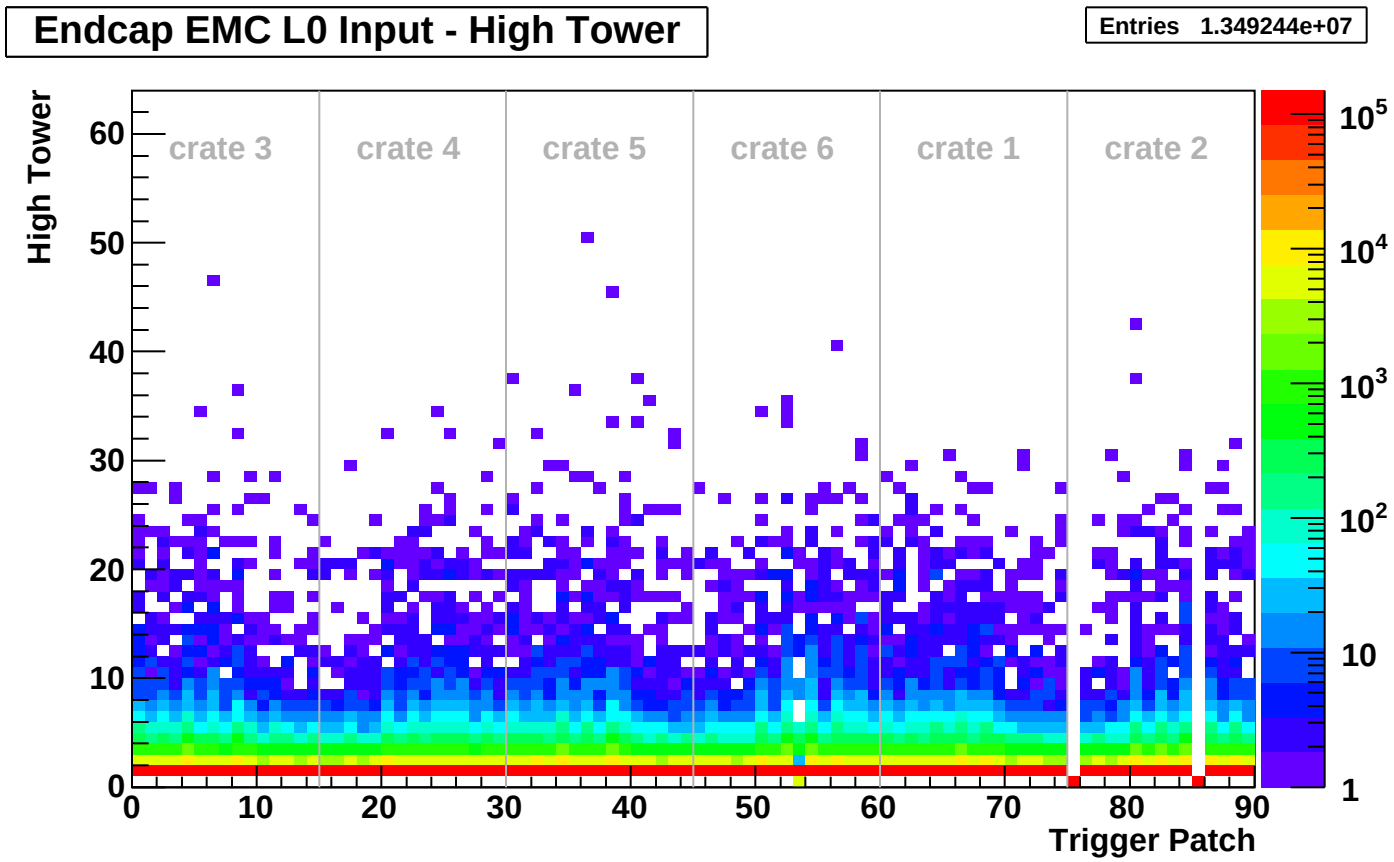

BunchId7Bit (all events)
Entries 149916

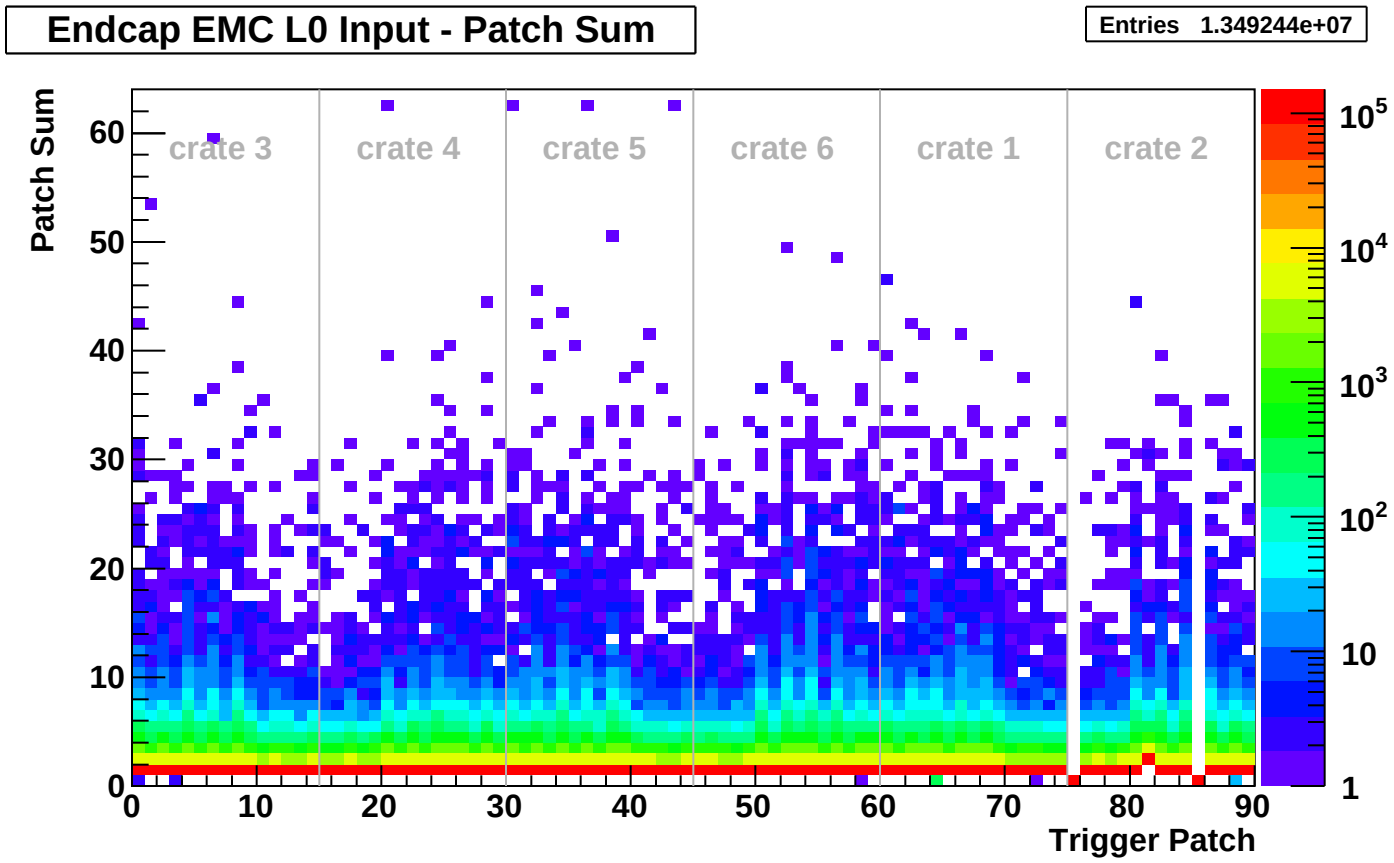

Abort-gaps
Entries 160

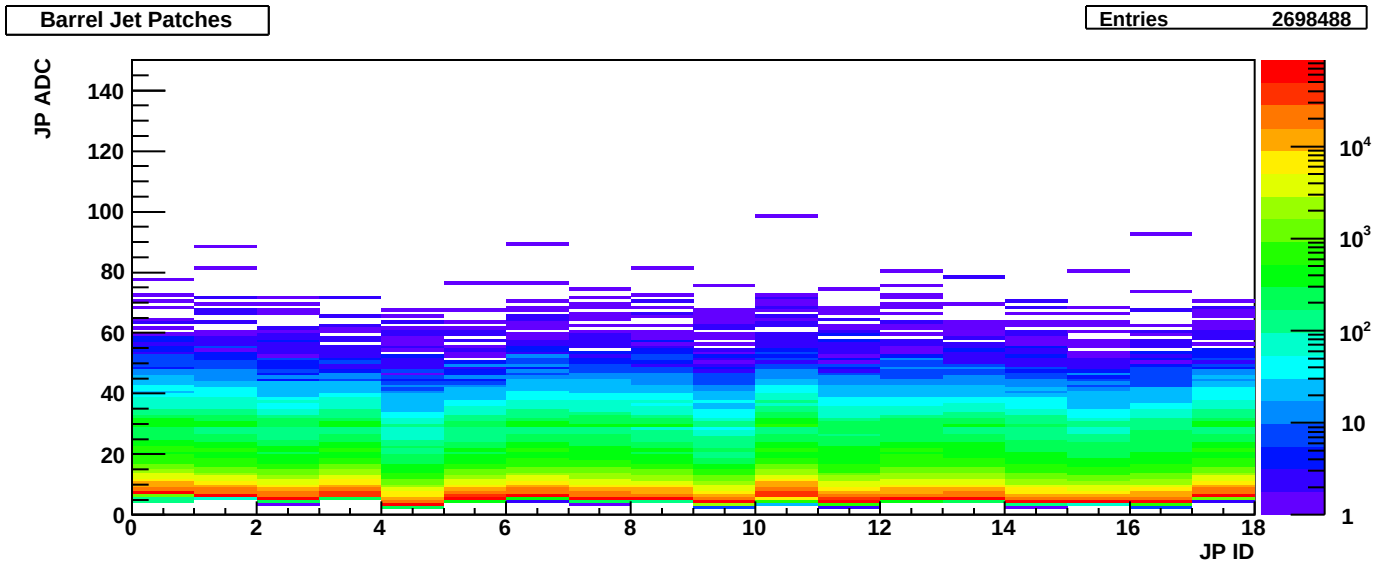

!Yellow-Fill
Entries 4992

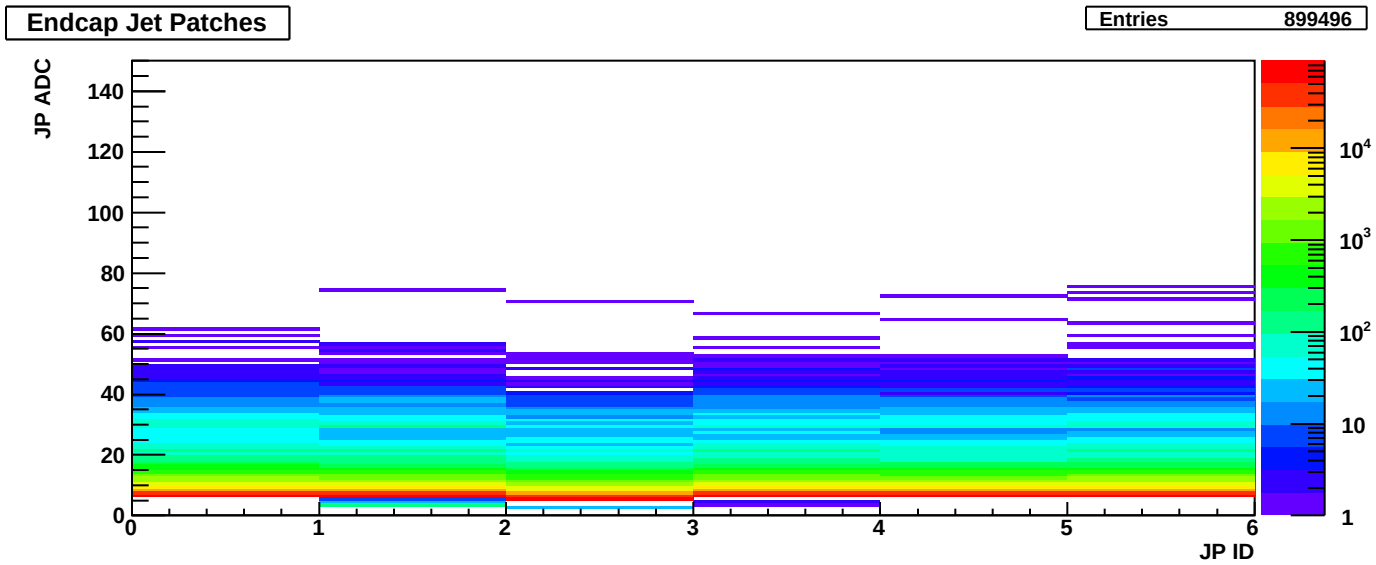

Barrel EMC L0 Input - High Tower
Entries 4.49748e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

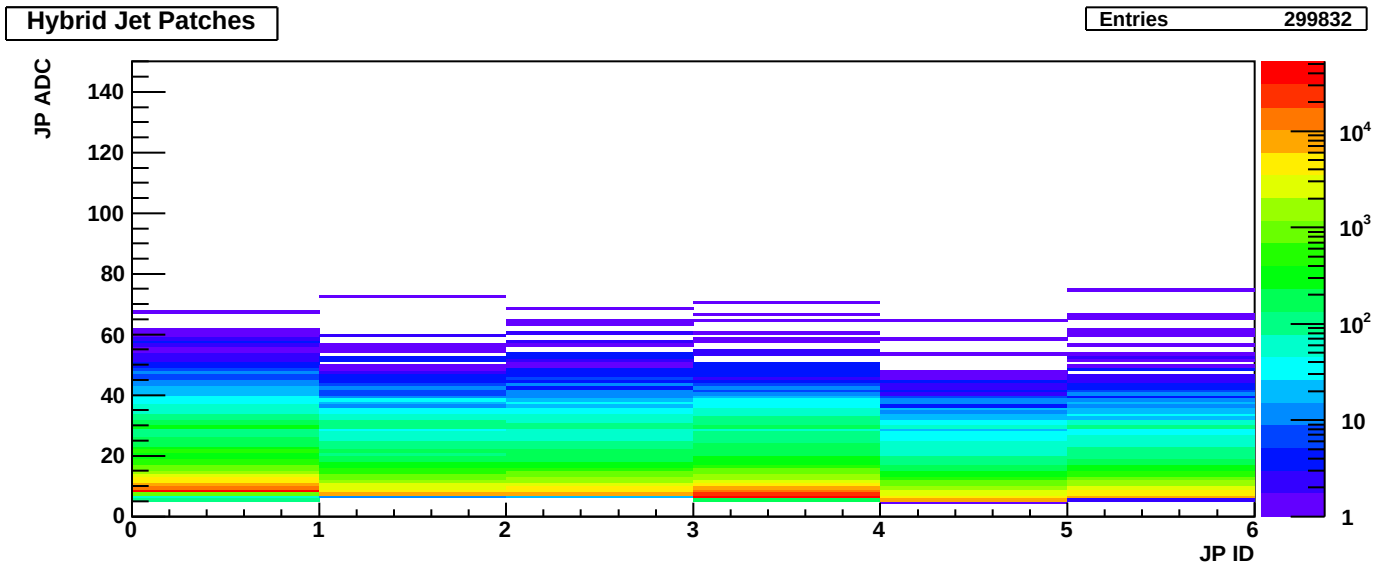

Barrel EMC L0 Input - Patch Sum
Entries 4.49748e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

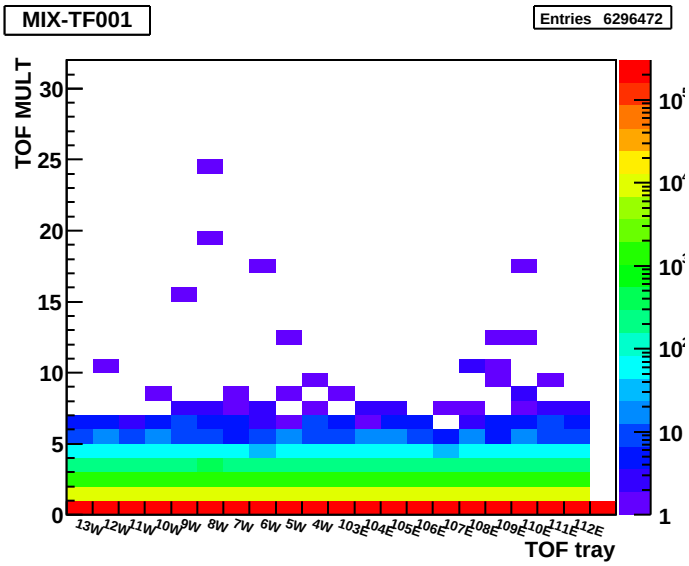

Endcap EMC L0 Input - High Tower
Entries 1.349244e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

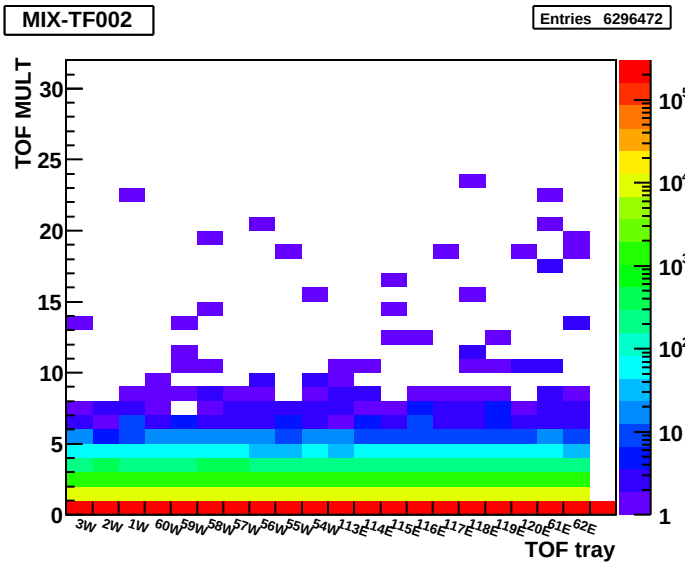

Endcap EMC L0 Input - Patch Sum
Entries 1.349244e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

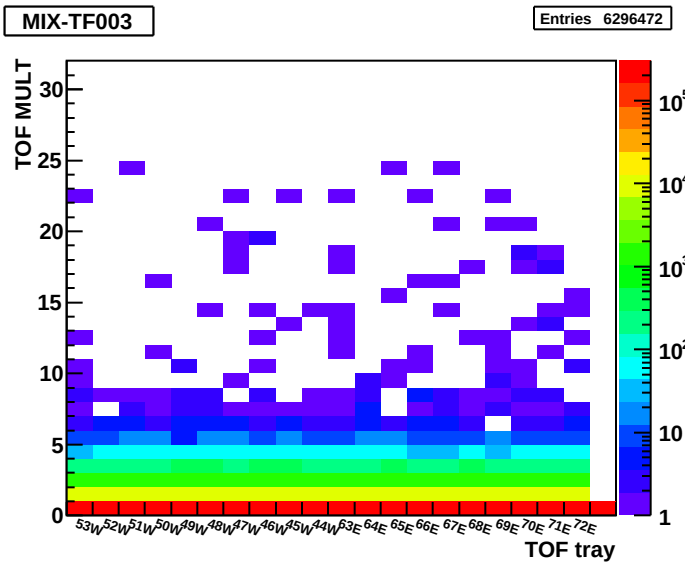

Barrel Jet Patches
Entries 2698488
JP ADC
JP ID

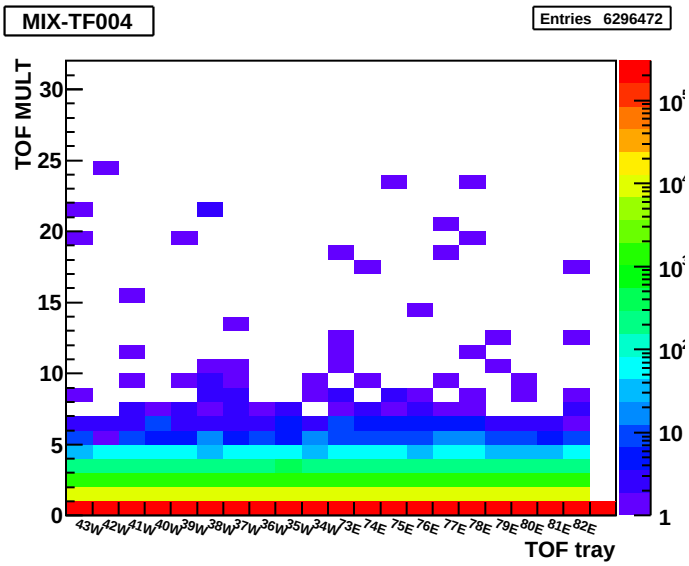

Endcap Jet Patches
Entries 899496
JP ADC
JP ID

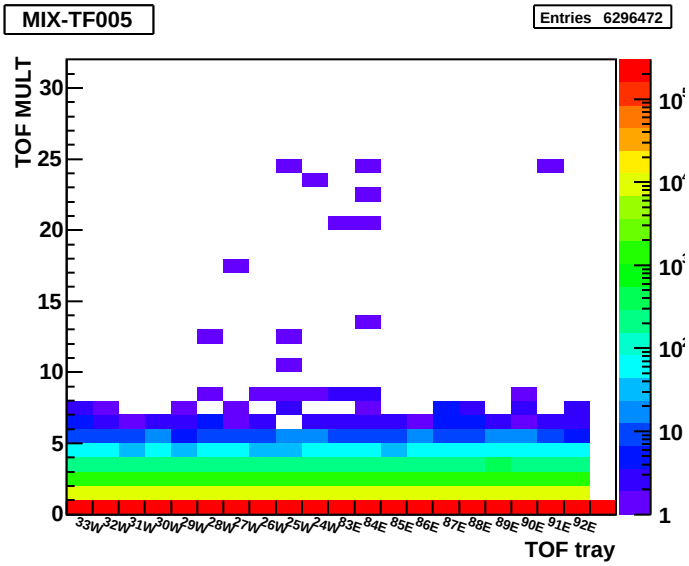

Hybrid Jet Patches
Entries 299832
JP ADC
JP ID

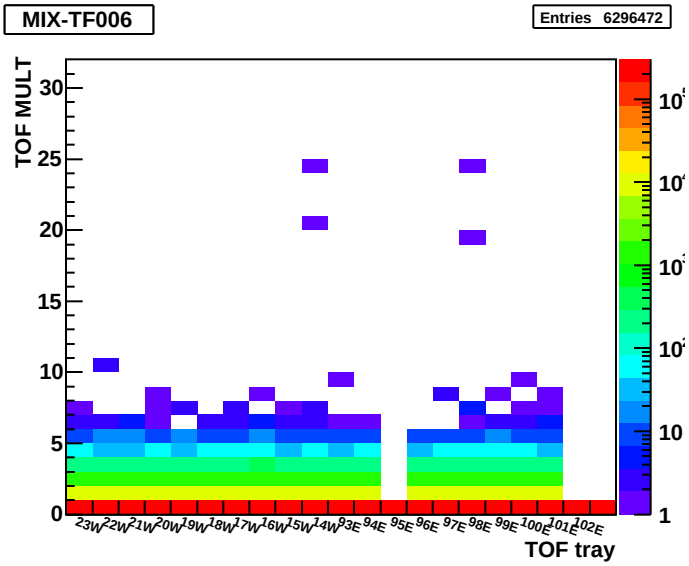

MIX-TF001
Entries 6296472
TOF MULT
TOF tray

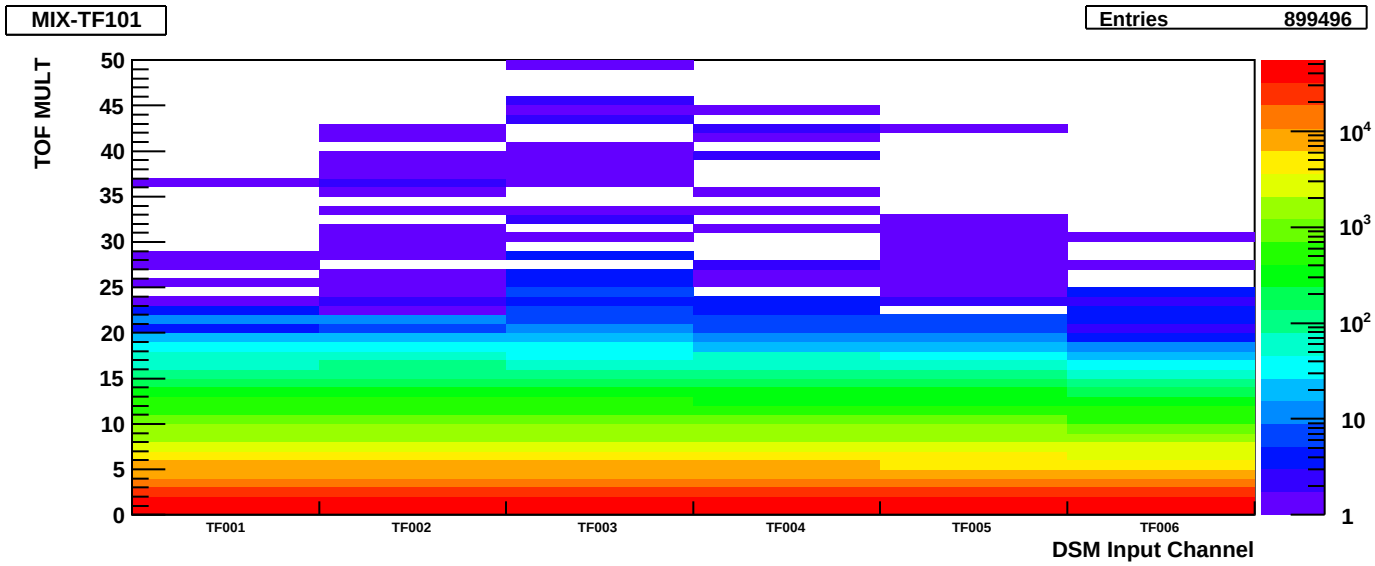

MIX-TF002
Entries 6296472
TOF MULT
TOF tray

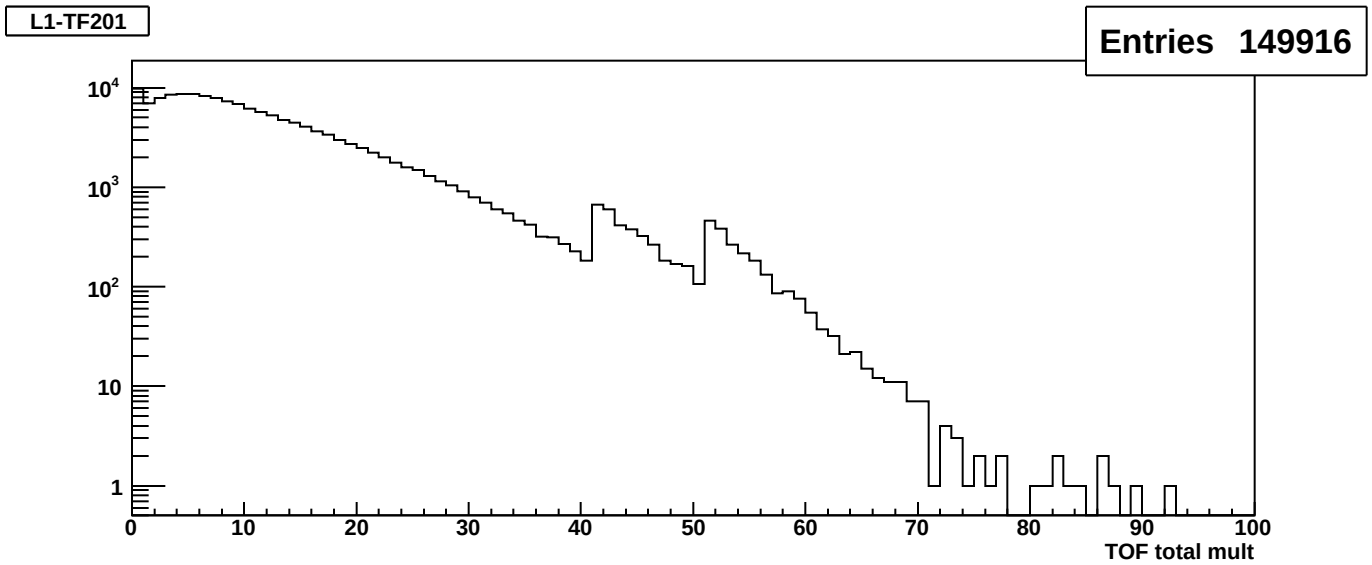

MIX-TF003
Entries 6296472
TOF MULT
TOF tray

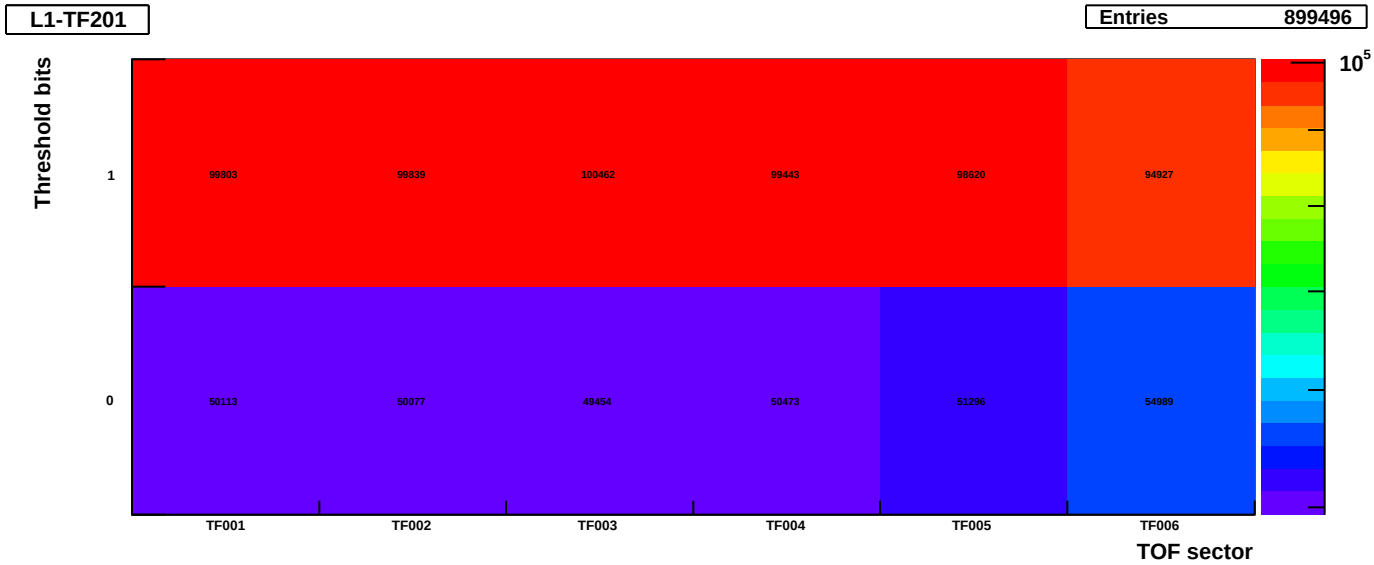

MIX-TF004
Entries 6296472
TOF MULT
TOF tray

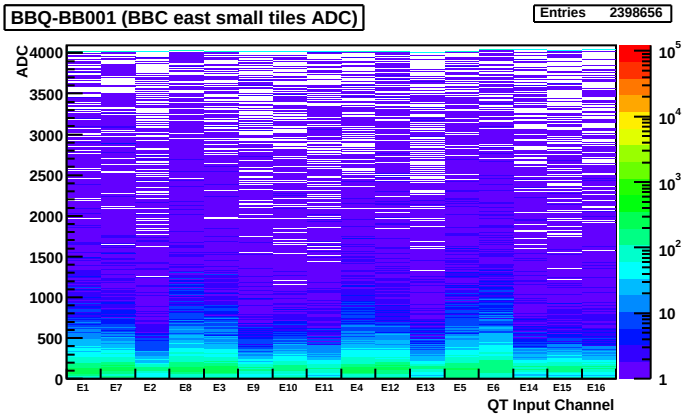

MIX-TF005
Entries 6296472
TOF MULT
TOF tray

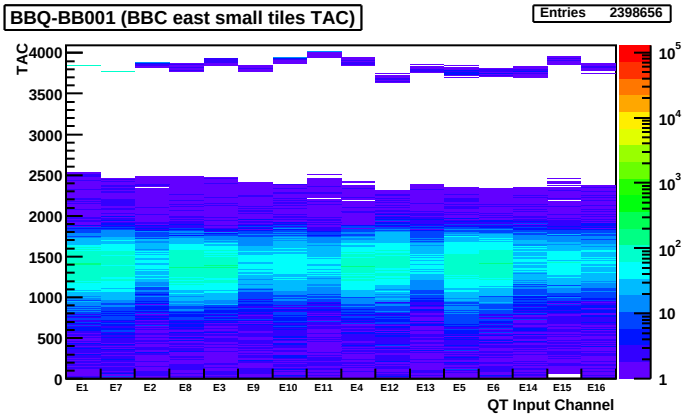

MIX-TF006
Entries 6296472
TOF MULT
TOF tray

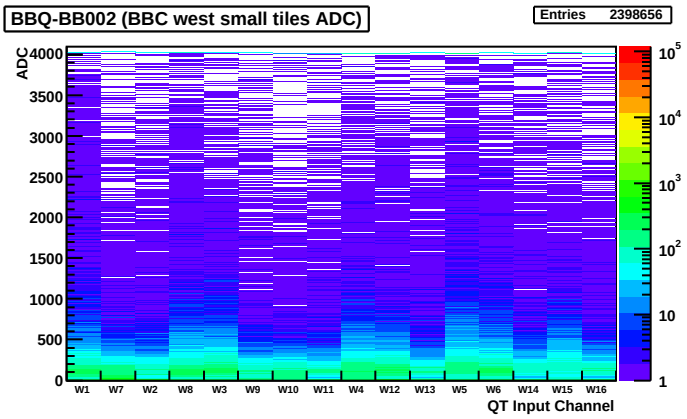

MIX-TF101
Entries 899496
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

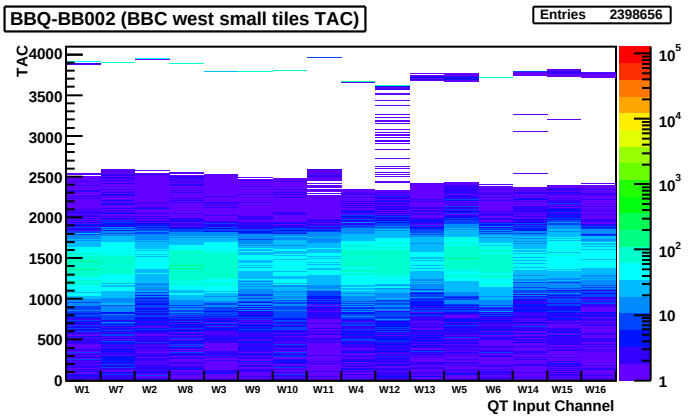

L1-TF201
Entries 149916
TOF total mult

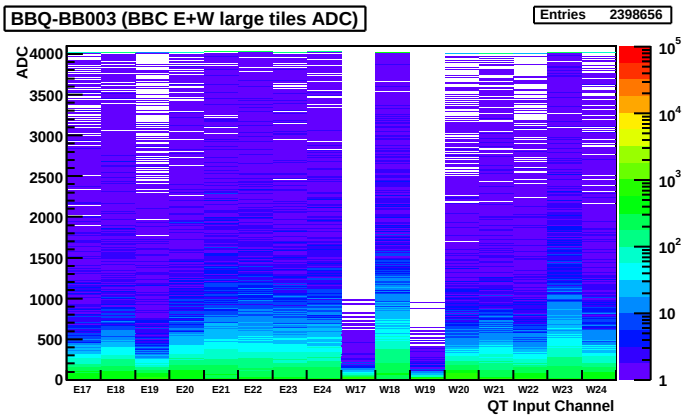

L1-TF201
Entries 899496
Threshold bits
99803
99839
100462
99443
98620
94927
50113
50077
49454
50473
51296
54989
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

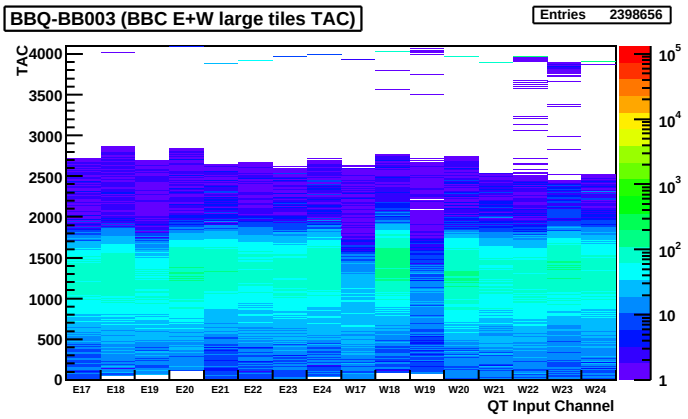

BBQ-BB001 (BBC east small tiles ADC)
Entries 2398656
ADC
QT Input Channel

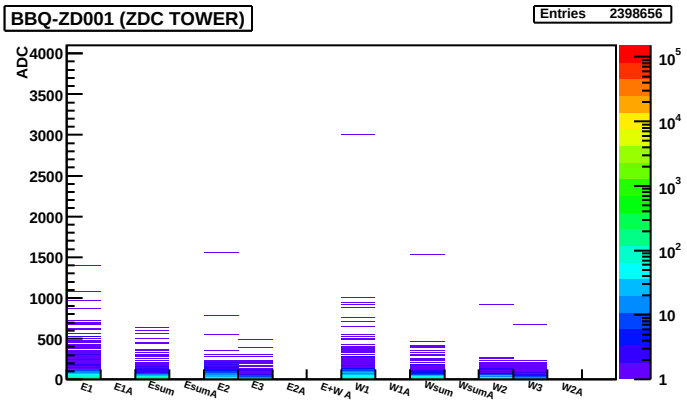

BBQ-BB001 (BBC east small tiles TAC)
Entries 2398656
TAC
QT Input Channel

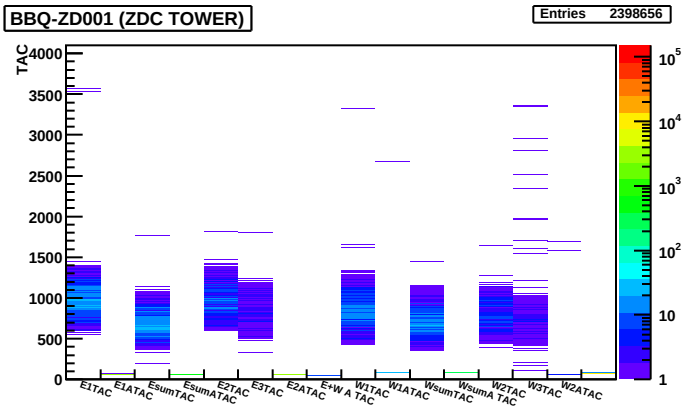

BBQ-BB002 (BBC west small tiles ADC)
Entries 2398656
ADC
QT Input Channel

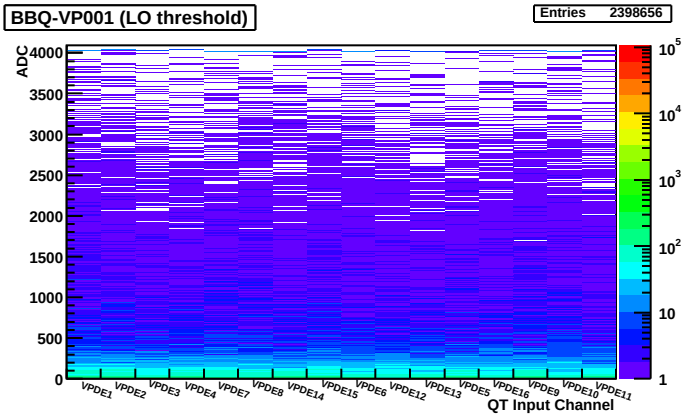

BBQ-BB002 (BBC west small tiles TAC)
Entries 2398656
TAC
QT Input Channel

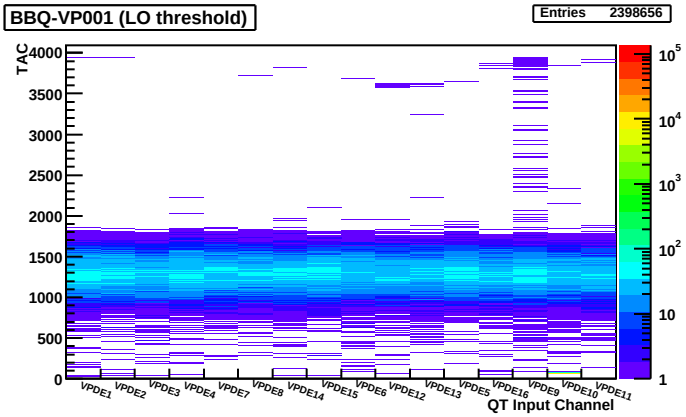

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2398656
ADC
QT Input Channel

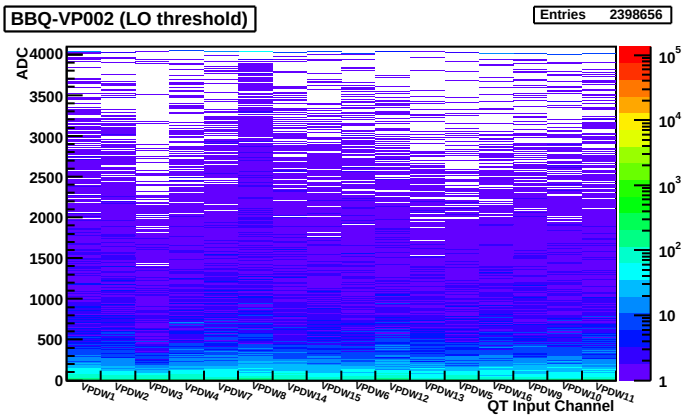

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2398656
TAC
QT Input Channel

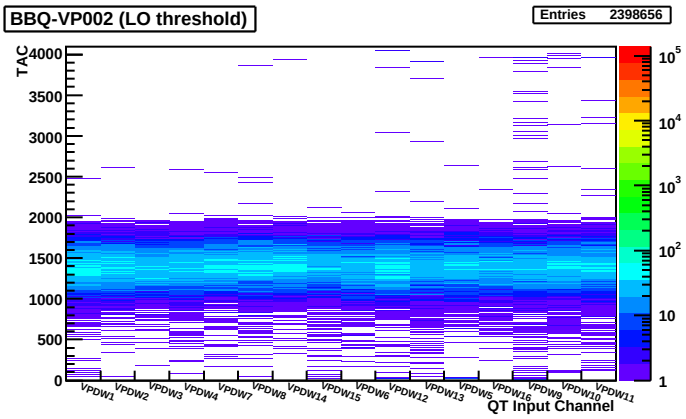

BBQ-ZD001 (ZDC TOWER)
Entries 2398656
ADC

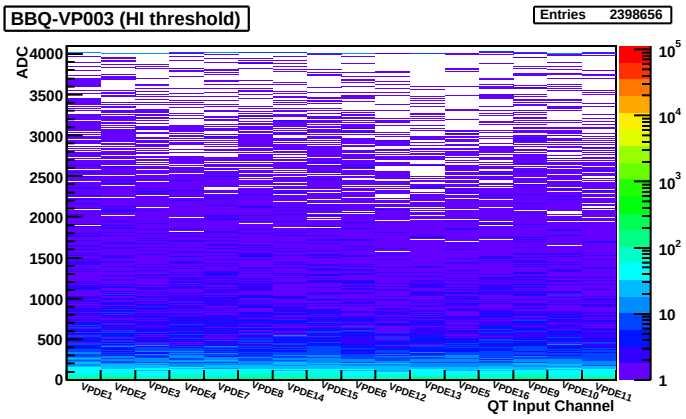

BBQ-ZD001 (ZDC TOWER)
Entries 2398656
TAC

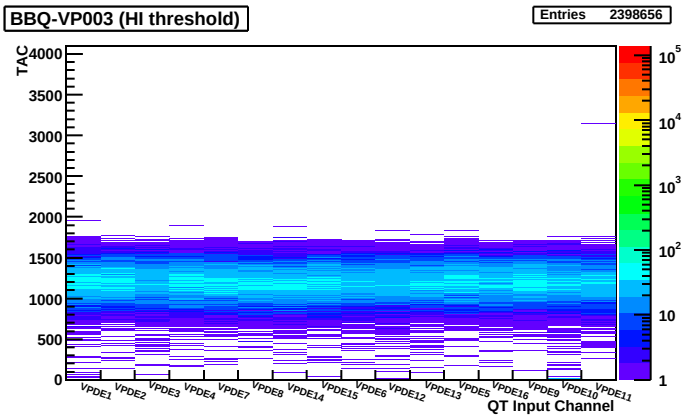

BBQ-VP001 (LO threshold)
Entries 2398656
ADC
QT Input Channel

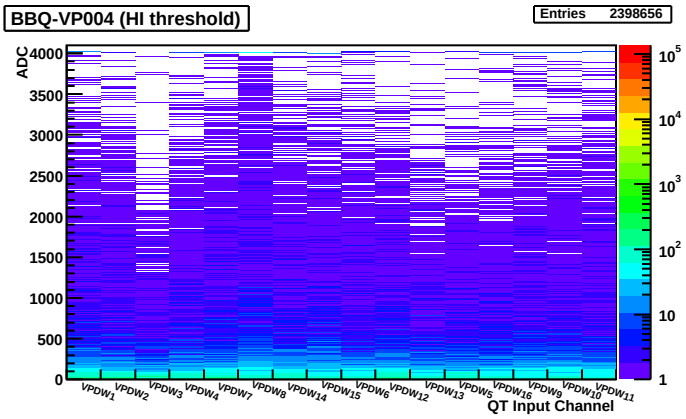

BBQ-VP001 (LO threshold)
Entries 2398656
TAC
QT Input Channel

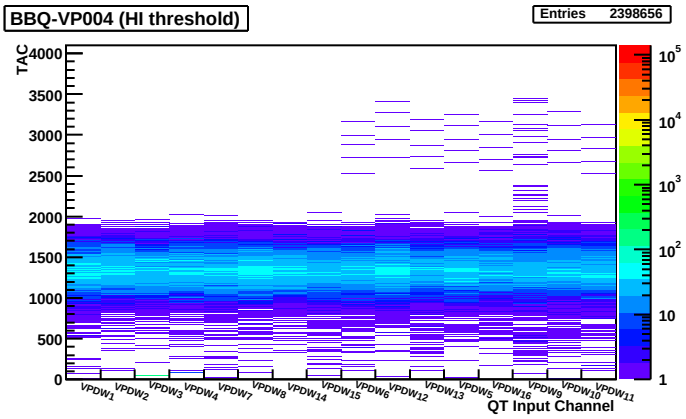

BBQ-VP002 (LO threshold)
Entries 2398656
ADC
QT Input Channel

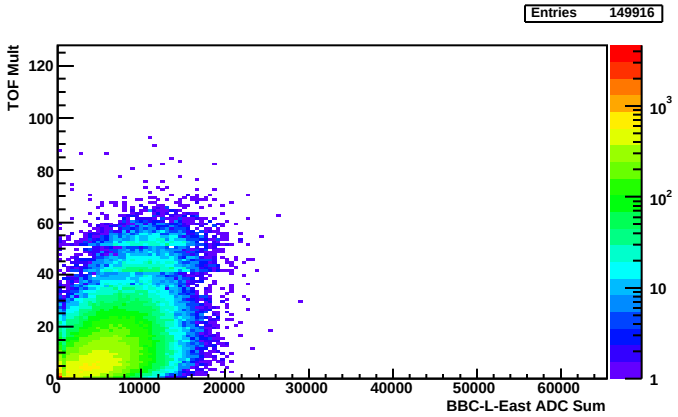

BBQ-VP002 (LO threshold)
Entries 2398656
TAC
QT Input Channel

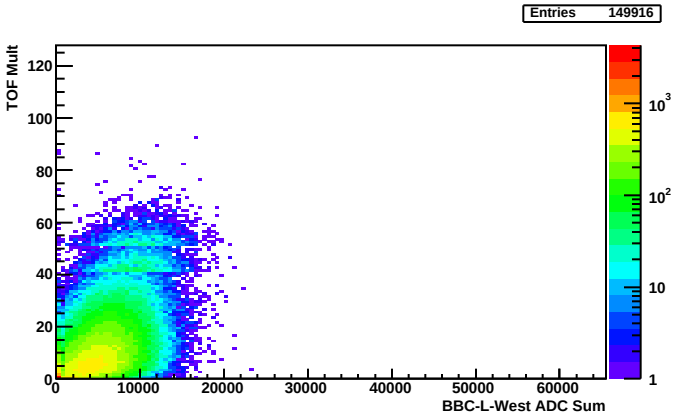

BBQ-VP003 (HI threshold)
Entries 2398656
ADC
QT Input Channel

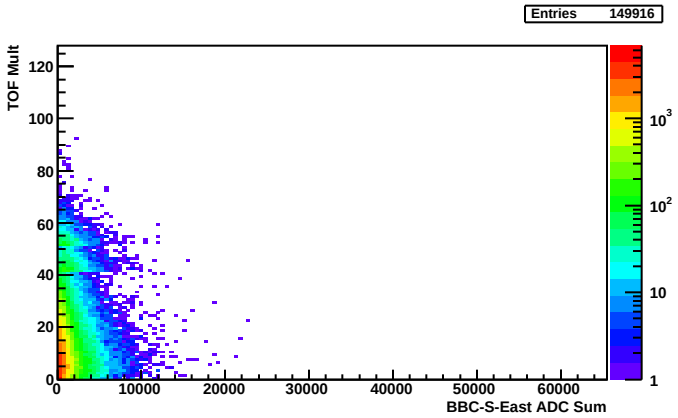

BBQ-VP003 (HI threshold)
Entries 2398656
TAC
QT Input Channel

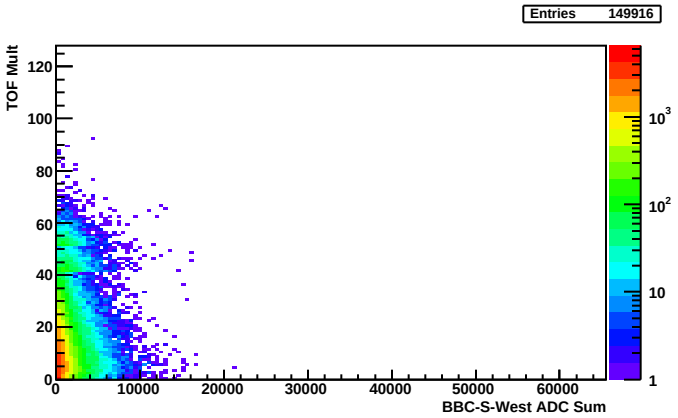

BBQ-VP004 (HI threshold)
Entries 2398656
ADC
QT Input Channel

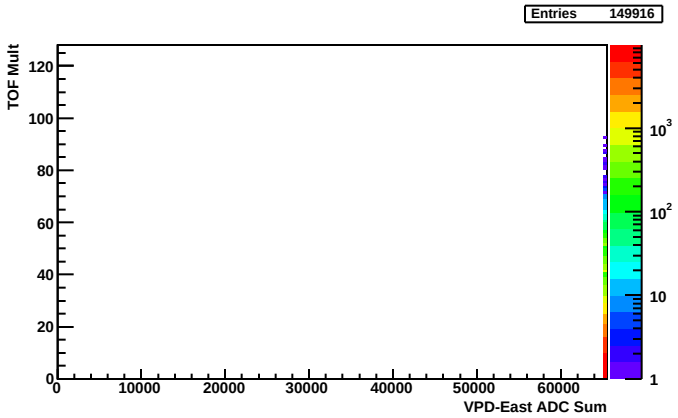

BBQ-VP004 (HI threshold)
Entries 2398656
TAC
QT Input Channel

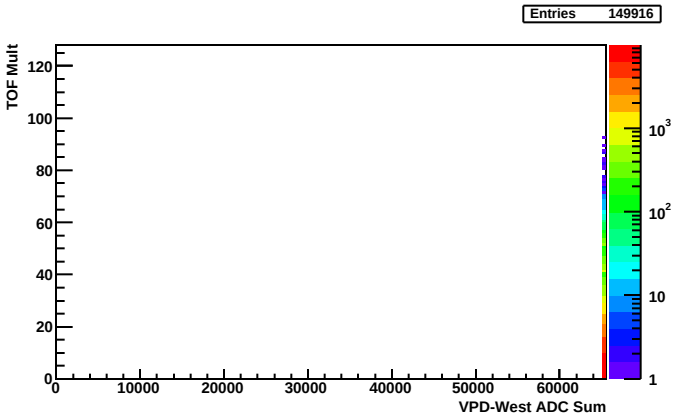

Entries 149916
TOF Mult
BBC-L-East ADC Sum

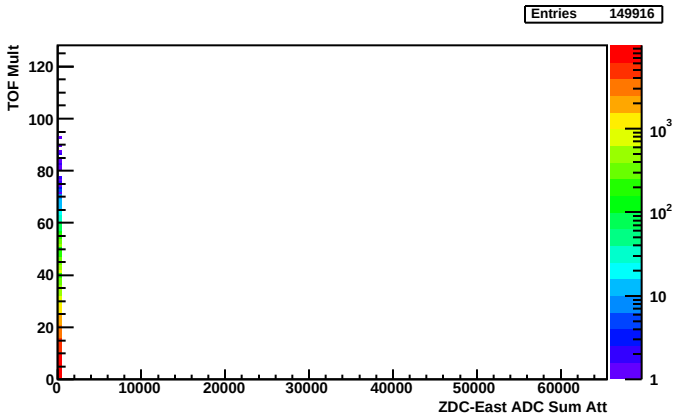

Entries 149916
TOF Mult
BBC-L-West ADC Sum

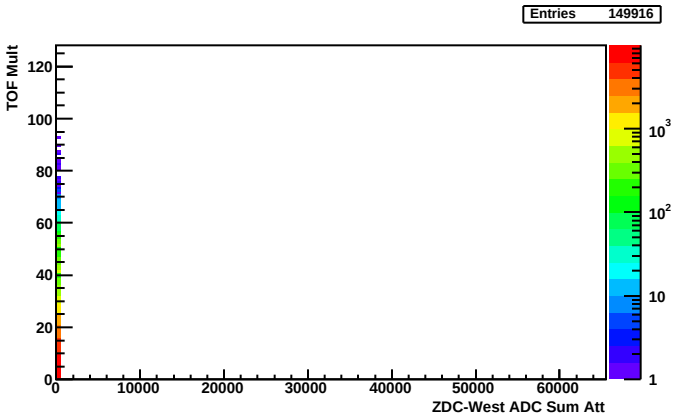

Entries 149916
TOF Mult
BBC-S-East ADC Sum

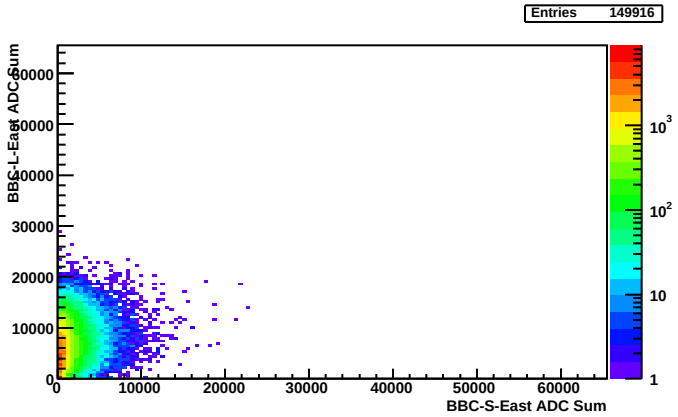

Entries 149916
TOF Mult
BBC-S-West ADC Sum

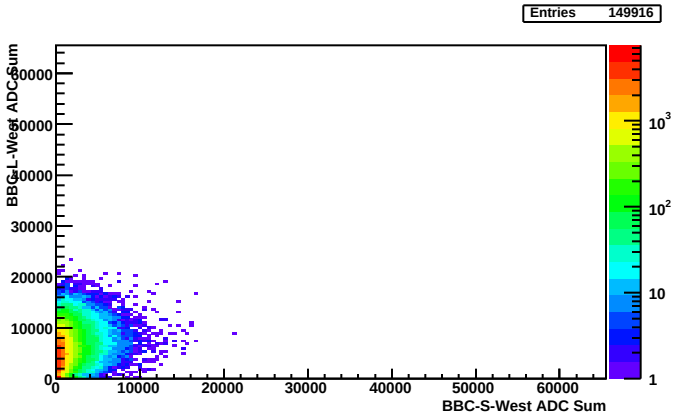

Entries 149916
TOF Mult
VPD-East ADC Sum

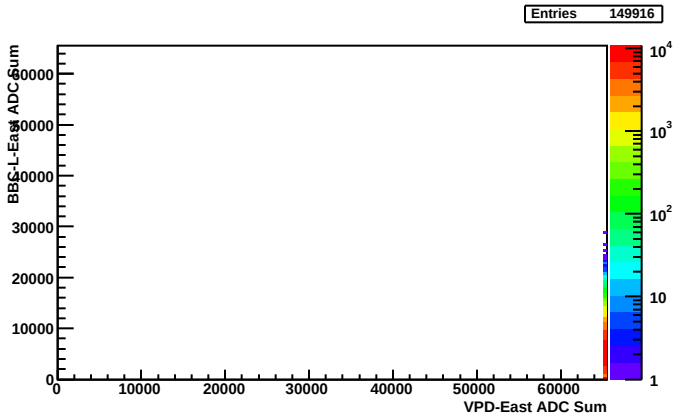

Entries 149916
TOF Mult
VPD-West ADC Sum

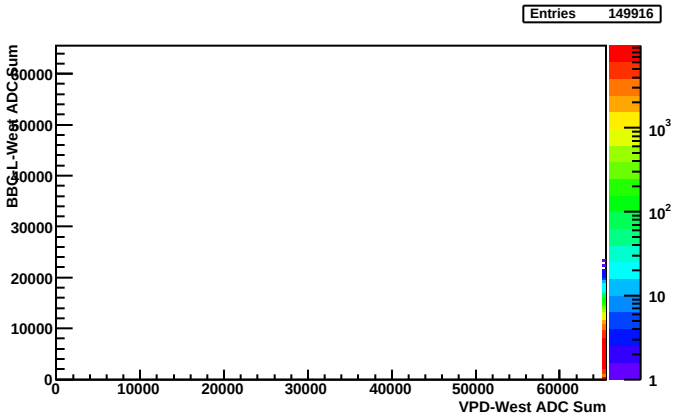

Entries 149916
TOF Mult
ZDC-East ADC Sum Att

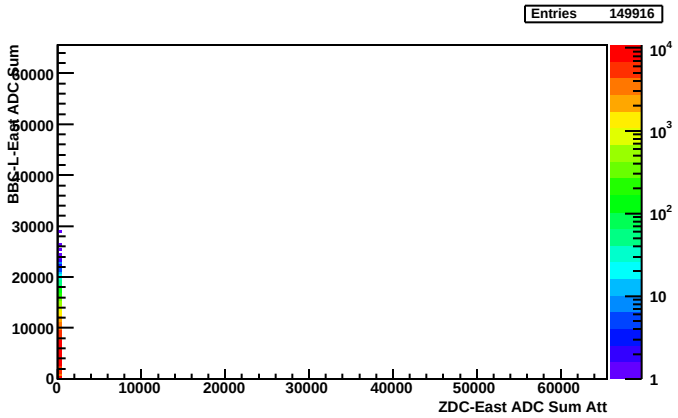

Entries 149916
TOF Mult
ZDC-West ADC Sum Att

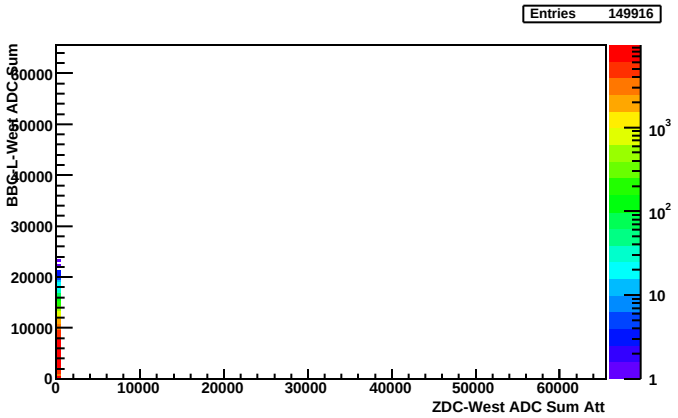

Entries 149916
BBC-L-East ADC Sum
BBC-S-East ADC Sum

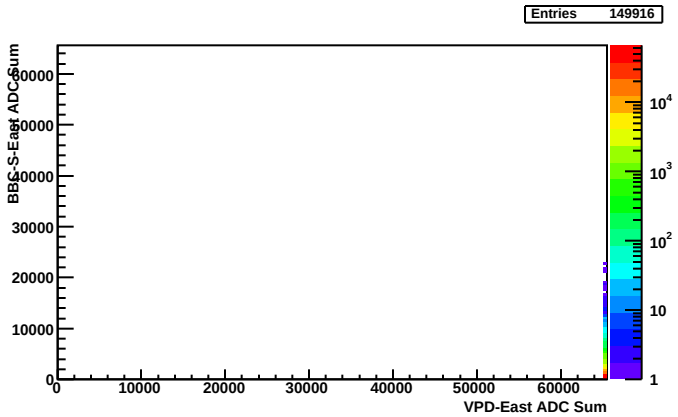

Entries 149916
BBC-L-West ADC Sum
BBC-S-West ADC Sum

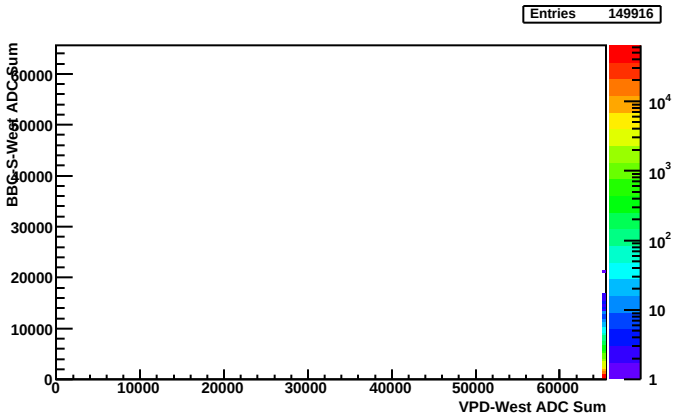

Entries 149916
BBC-L-East ADC Sum
VPD-East ADC Sum

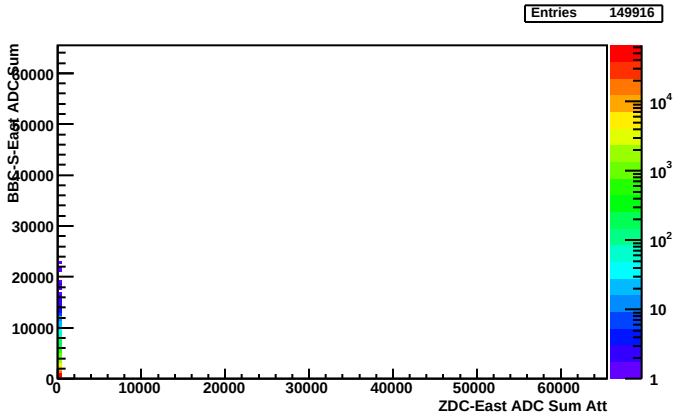

Entries 149916
BBC-L-West ADC Sum
VPD-West ADC Sum

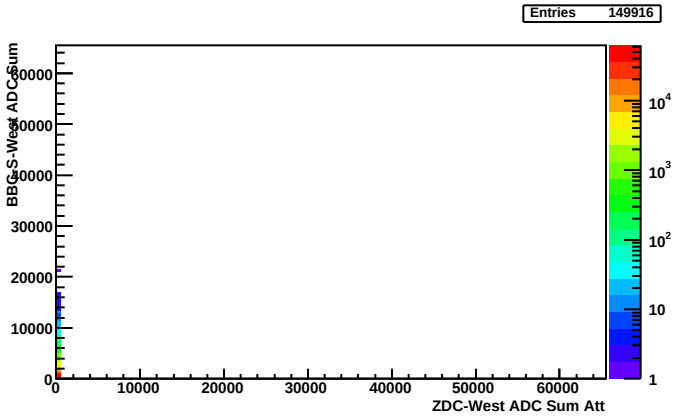

Entries 149916
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

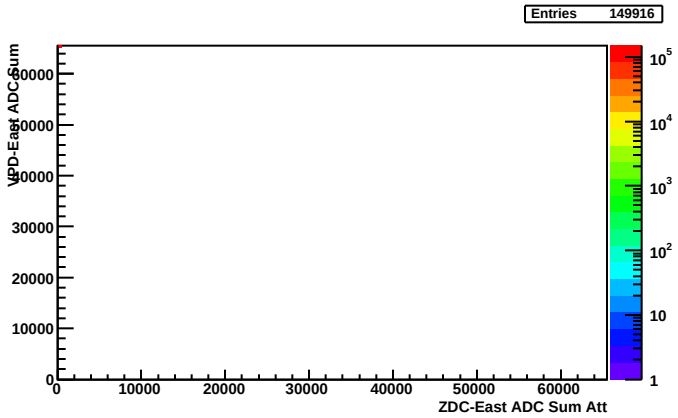

Entries 149916
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

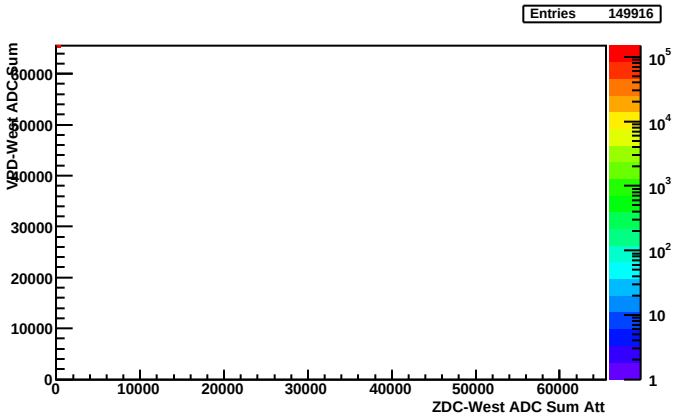

Entries 149916
BBC-S-East ADC Sum
VPD-East ADC Sum

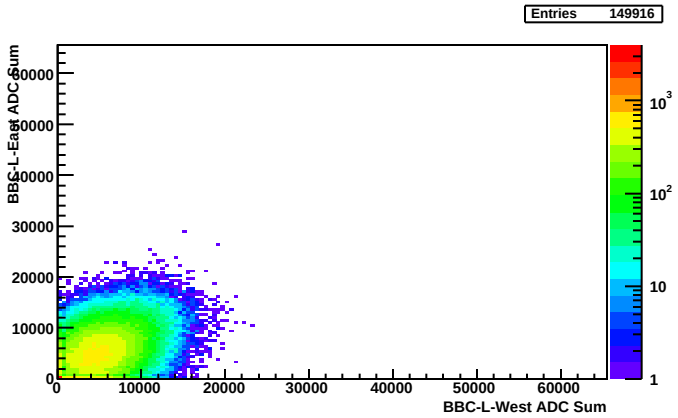

Entries 149916
BBC-S-West ADC Sum
VPD-West ADC Sum

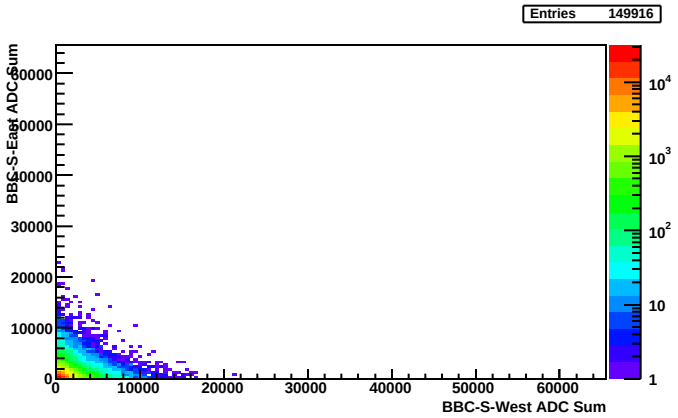

Entries 149916
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

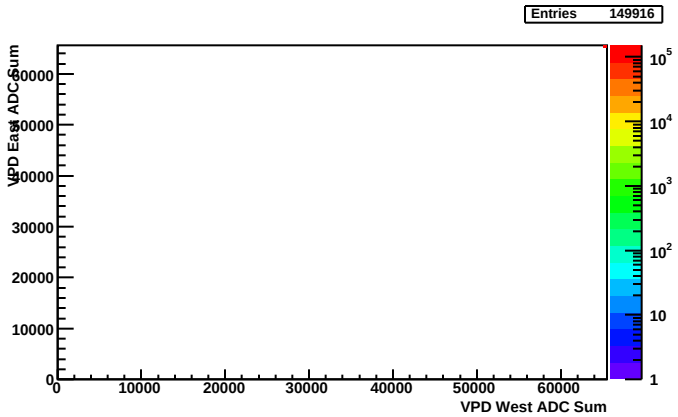

Entries 149916
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

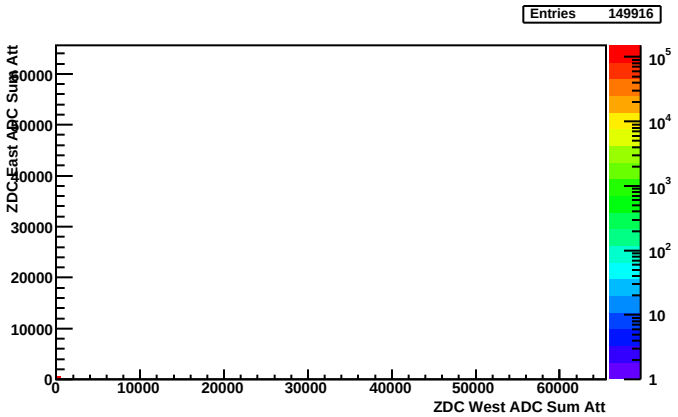

Entries 149916
VPD-East ADC Sum
ZDC-East ADC Sum Att

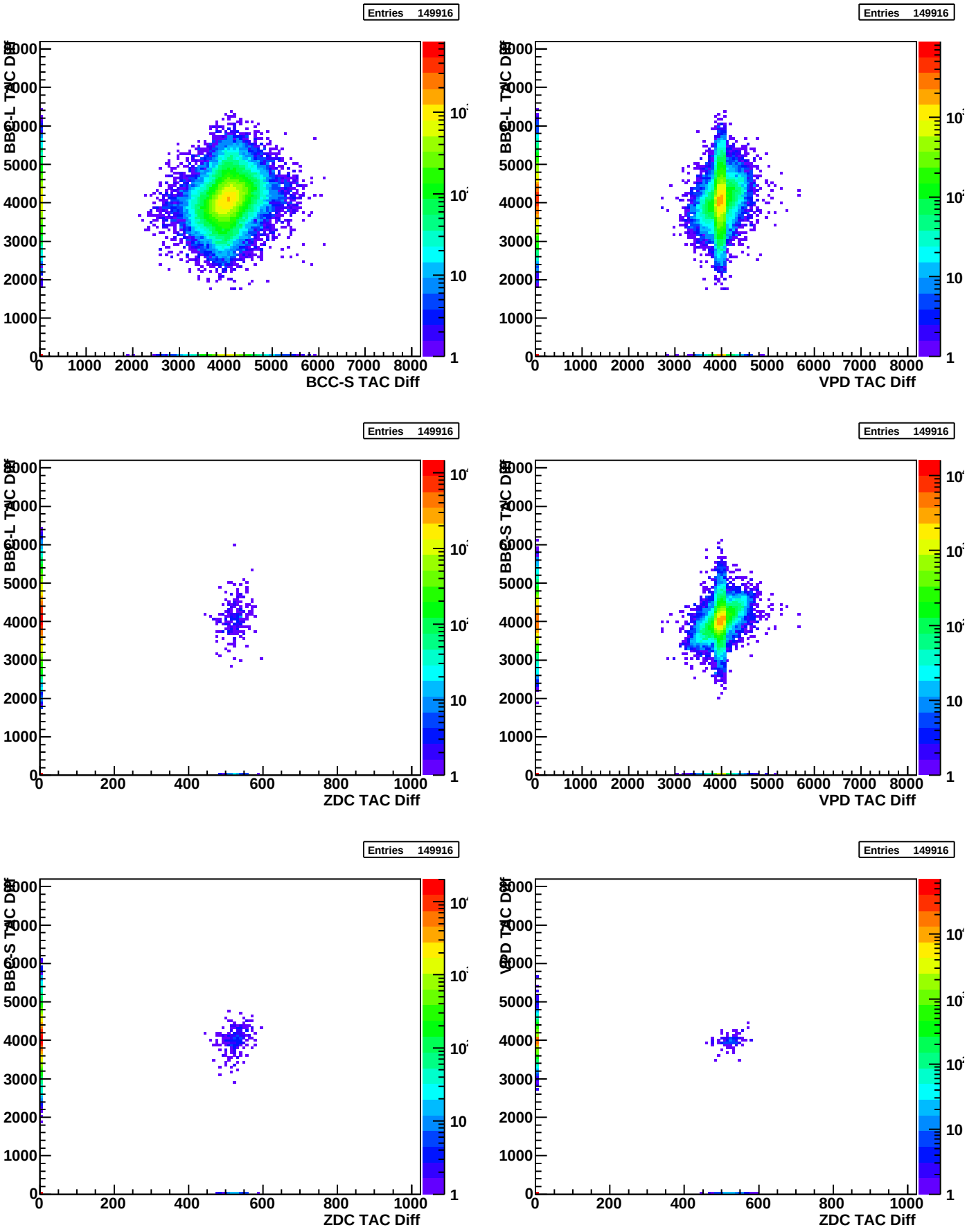

Entries 149916
VPD-West ADC Sum
ZDC-West ADC Sum Att

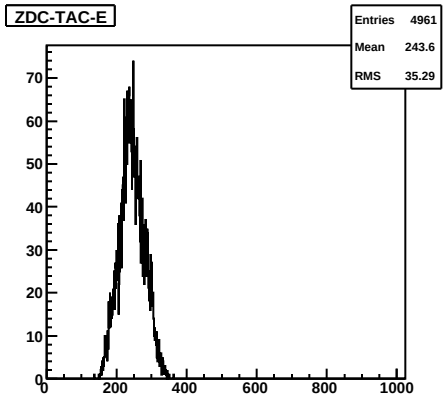

Entries 149916
BBC-L-East ADC Sum
BBC-L-West ADC Sum

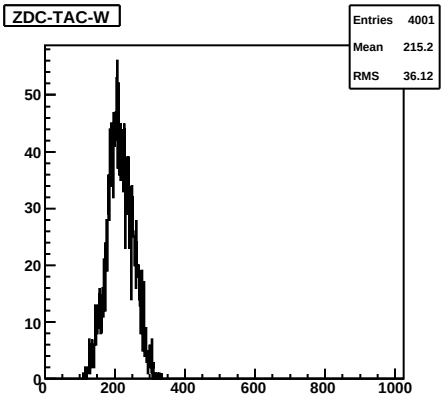

Entries 149916
BBC-S-East ADC Sum
BBC-S-West ADC Sum

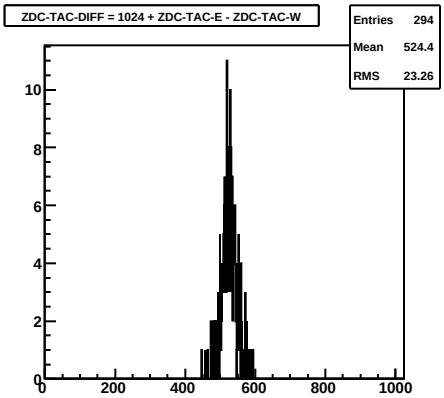

Entries 149916
VPD East ADC Sum
VPD West ADC Sum

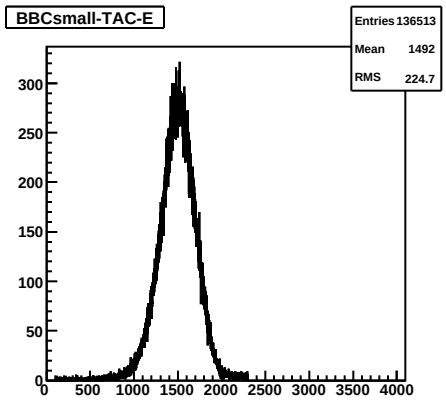

Entries 149916
ZDC East ADC Sum Att
ZDC West ADC Sum Att

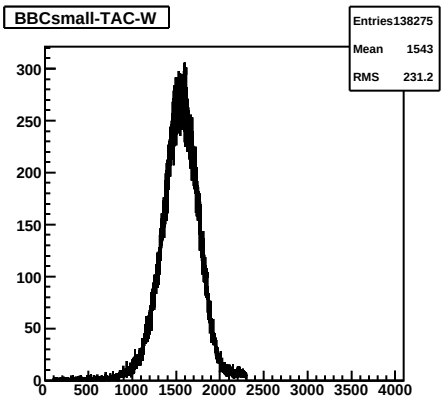

Entries 149916
BBC-L TAC Diff
BCC-S TAC Diff
Entries 149916
BBC-L TAC Diff
VPD TAC Diff
Entries 149916
BBC-L TAC Diff
ZDC TAC Diff
Entries 149916
BBC-S TAC Diff
VPD TAC Diff
Entries 149916
BBC-S TAC Diff
ZDC TAC Diff
Entries 149916
VPD TAC Diff
ZDC TAC Diff

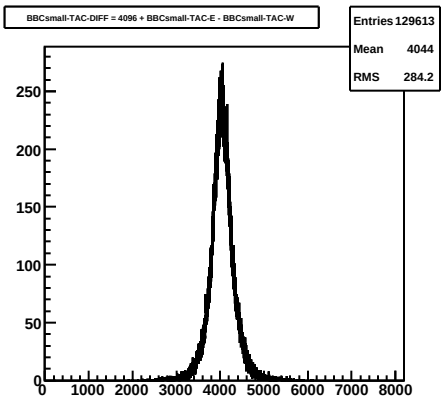

ZDC-TAC-E
Entries 4961
Mean 243.6
RMS 35.29

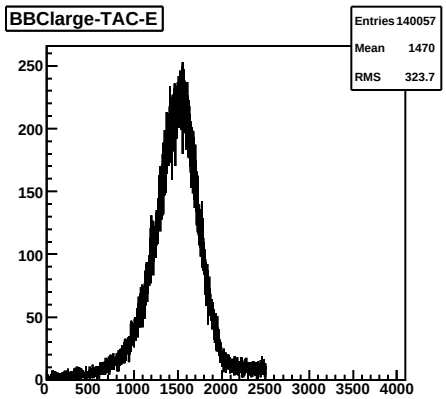

ZDC-TAC-W
Entries 4001
Mean 215.2
RMS 36.12

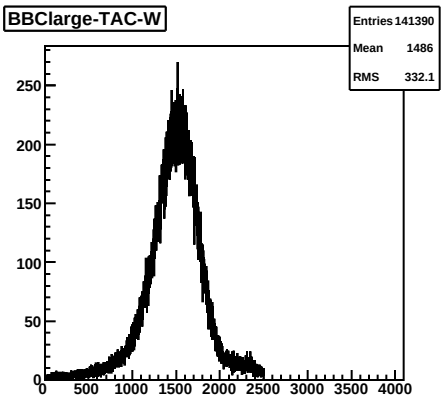

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 294
Mean 524.4
RMS 23.26

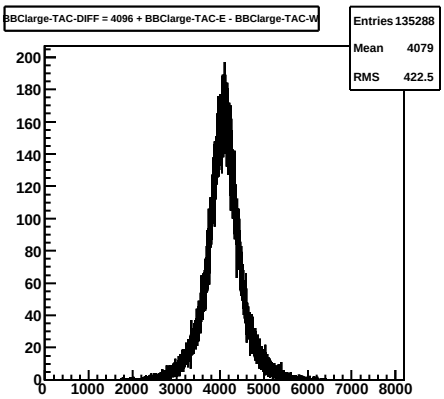

BBCsmall-TAC-E
Entries 136513
Mean 1492
RMS 224.7

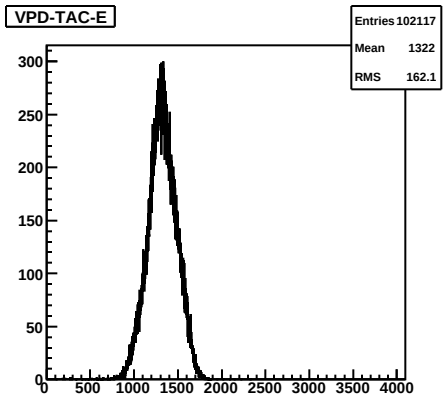

BBCsmall-TAC-W
Entries 138275
Mean 1543
RMS 231.2

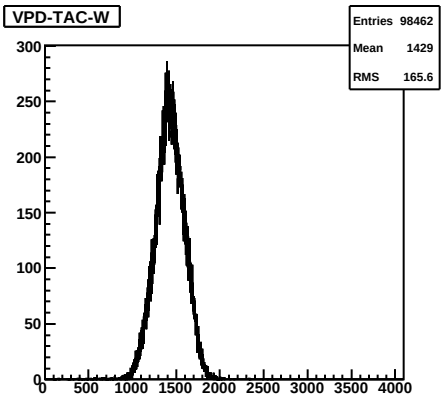

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 129613
Mean 4044
RMS 284.2

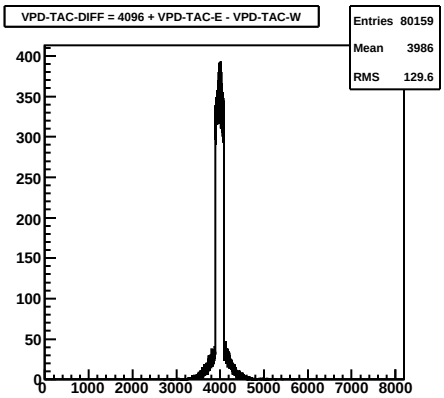

BBClarge-TAC-E
Entries 140057
Mean 1470
RMS 323.7

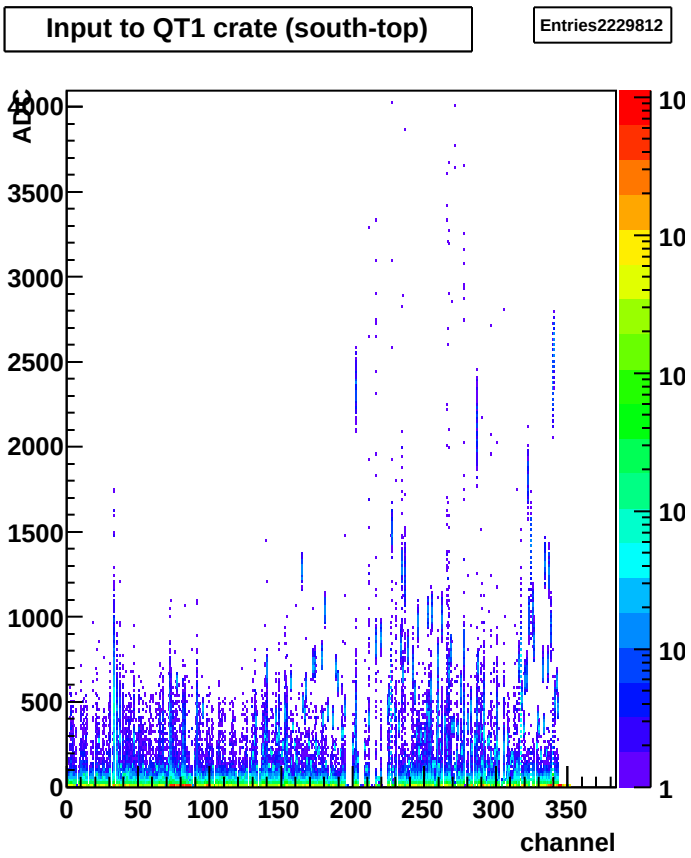

BBClarge-TAC-W
Entries 141390
Mean 1486
RMS 332.1

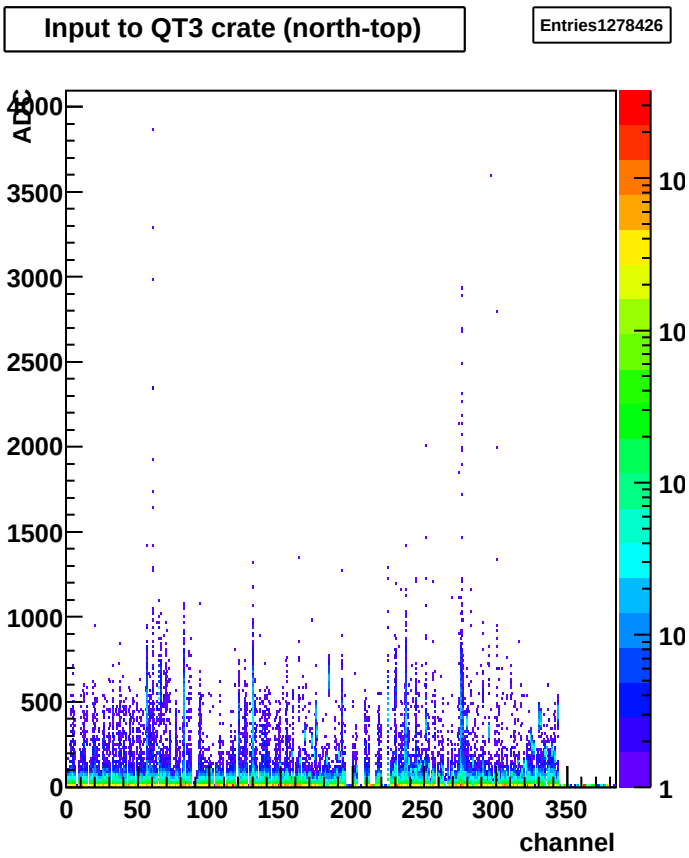

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 135288
Mean 4079
RMS 422.5

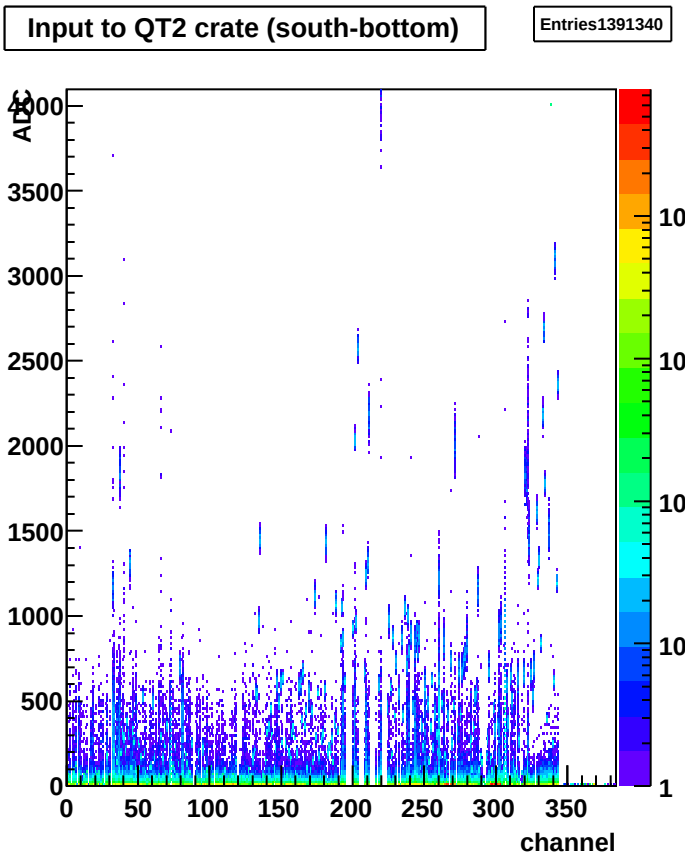

VPD-TAC-E
Entries 102117
Mean 1322
RMS 162.1

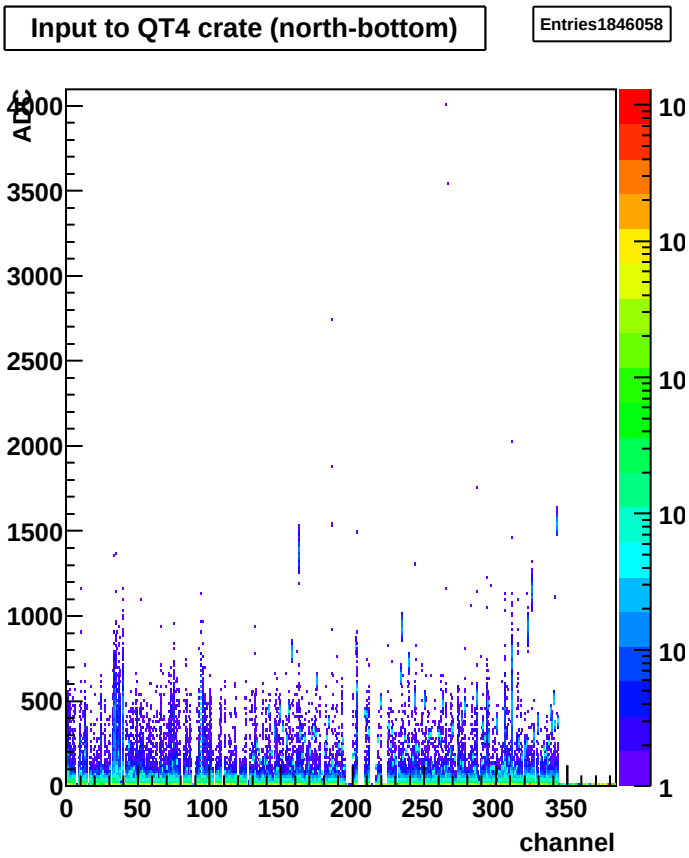

VPD-TAC-W
Entries 98462
Mean 1429
RMS 165.6

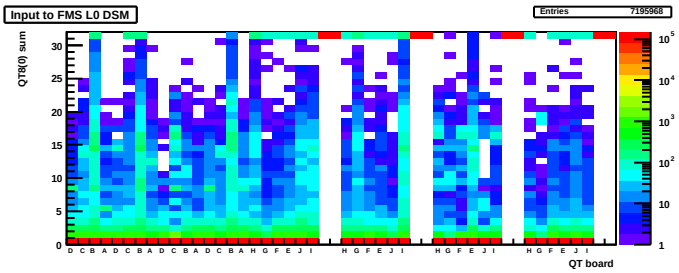

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 80159
Mean 3986
RMS 129.6

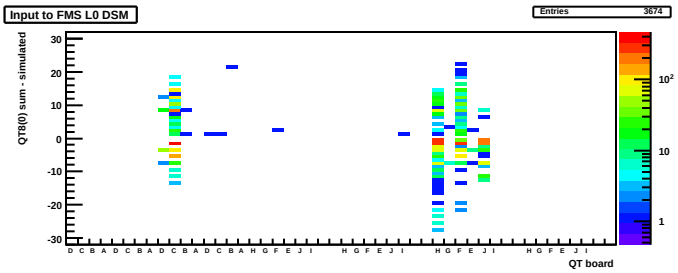

Input to QT1 crate (south-top)
Entries2229812
ADC
channel

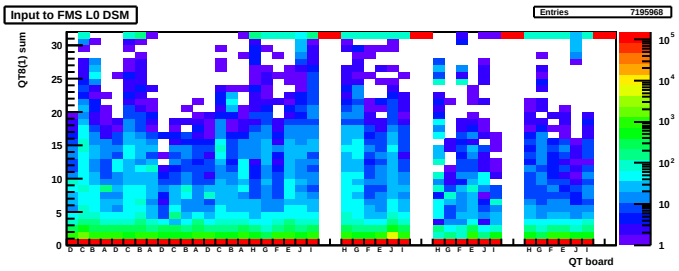

Input to QT3 crate (north-top)
Entries1278426
ADC
channel

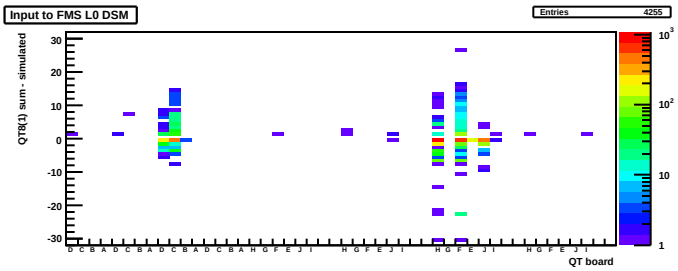

Input to QT2 crate (south-bottom)
Entries1391340
ADC
channel

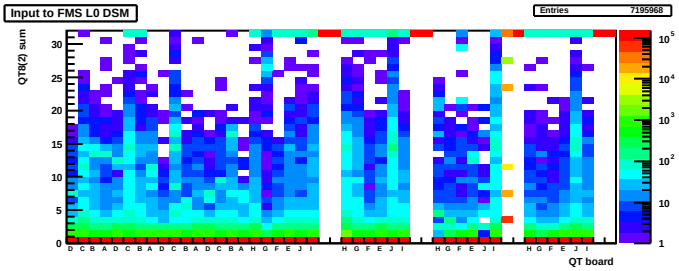

Input to QT4 crate (north-bottom)
Entries1846058
ADC
channel

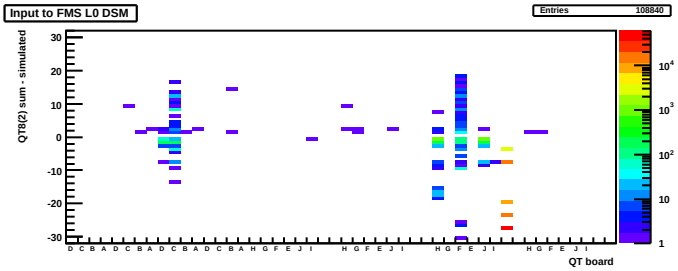

Input to FMS L0 DSM
Entries 7195968
QT8(0) sum
QT board

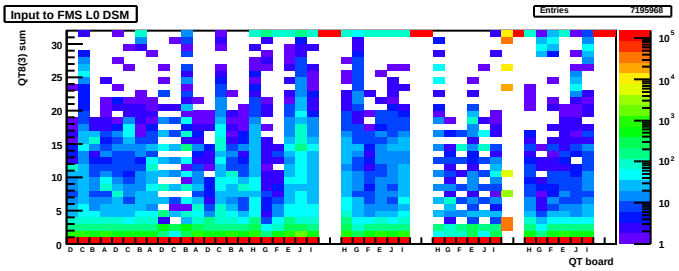

Input to FMS L0 DSM
Entries 3674
QT8(0) sum - simulated
QT board

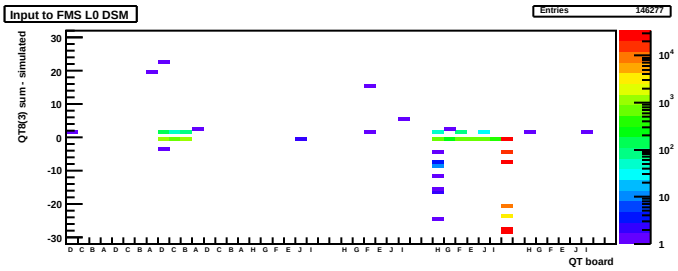

Input to FMS L0 DSM
Entries 7195968
QT8(1) sum
QT board

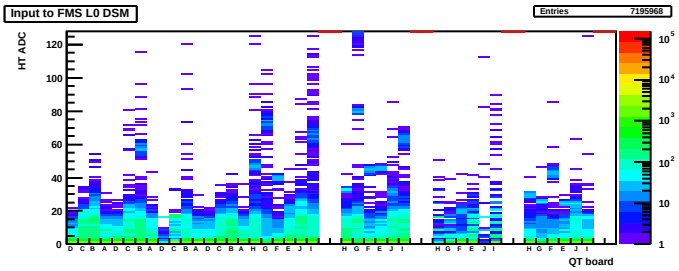

Input to FMS L0 DSM
Entries 4255
QT8(1) sum - simulated
QT board

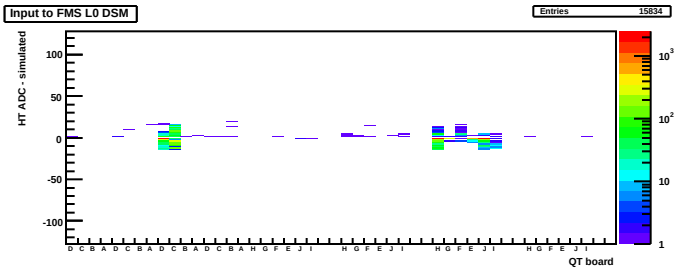

Input to FMS L0 DSM
Entries 7195968
QT8(2) sum
QT board

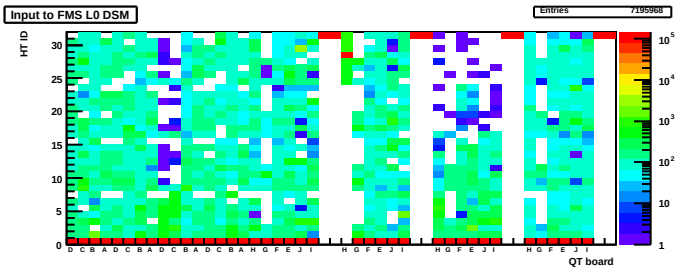

Input to FMS L0 DSM
Entries 108840
QT8(2) sum - simulated
QT board

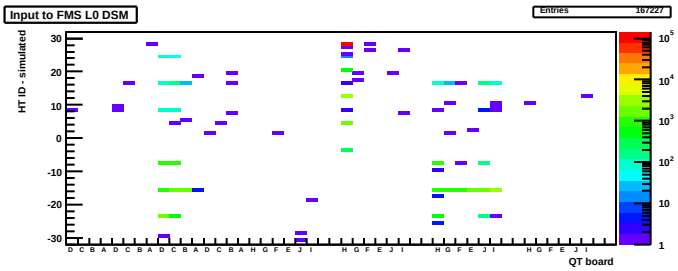

Input to FMS L0 DSM
Entries 7195968
QT8(3) sum
QT board

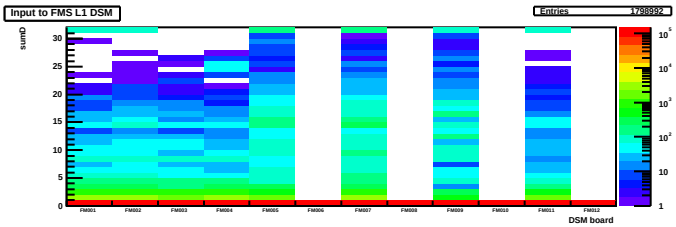

Input to FMS L0 DSM
Entries 146277
QT8(3) sum - simulated
QT board

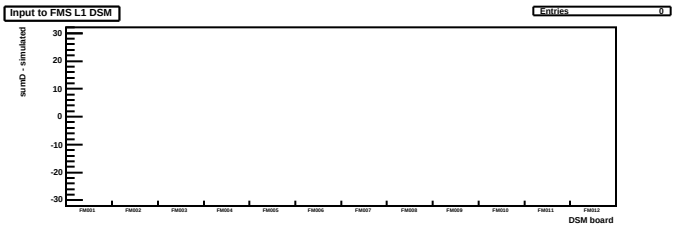

Input to FMS L0 DSM
Entries 7195968
HT ADC
QT board

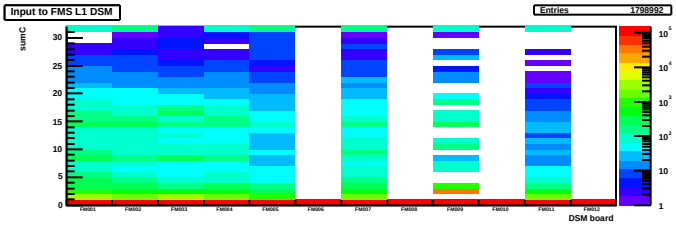

Input to FMS L0 DSM
Entries 15834
HT ADC - simulated
QT board

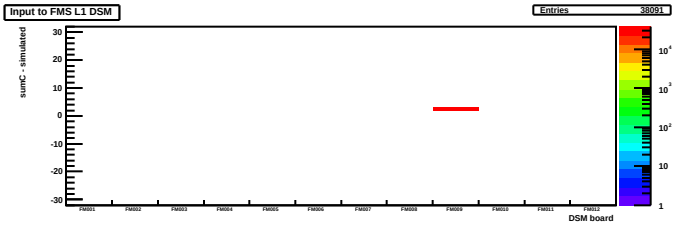

Input to FMS L0 DSM
Entries 7195968
HT ID
QT board

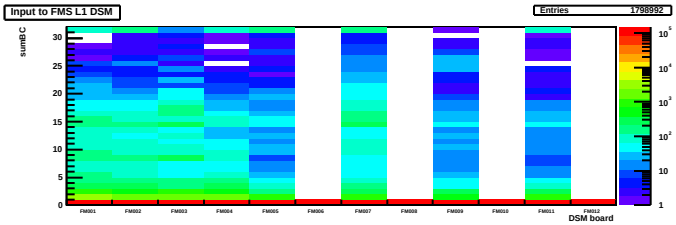

Input to FMS L0 DSM
Entries 167227
HT ID - simulated
QT board

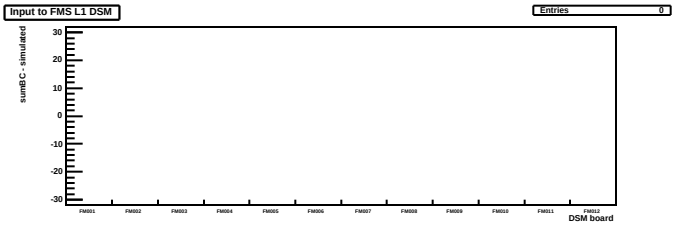

Input to FMS L1 DSM
Entries 1798992
sumD
DSM board

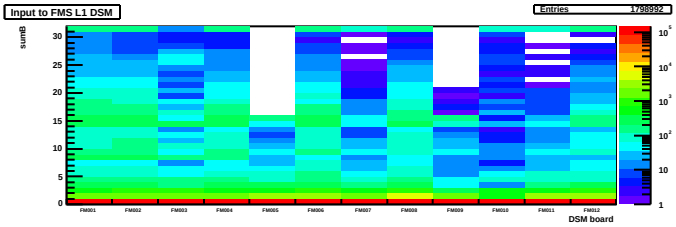

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

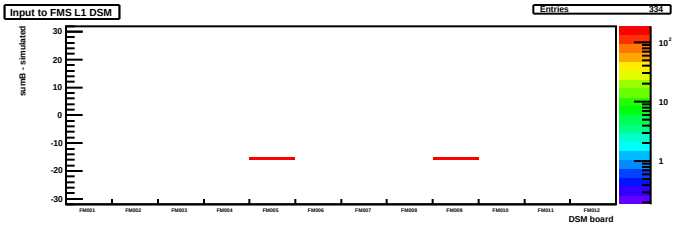

Input to FMS L1 DSM
Entries 1798992
sumC
DSM board

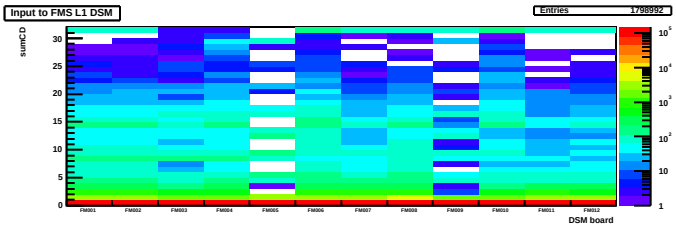

Input to FMS L1 DSM
Entries 38091
sumC - simulated
DSM board

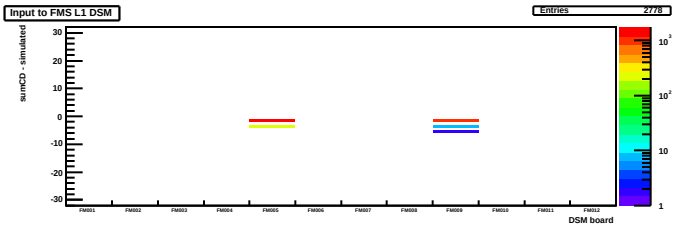

Input to FMS L1 DSM
Entries 1798992
sumBC
DSM board

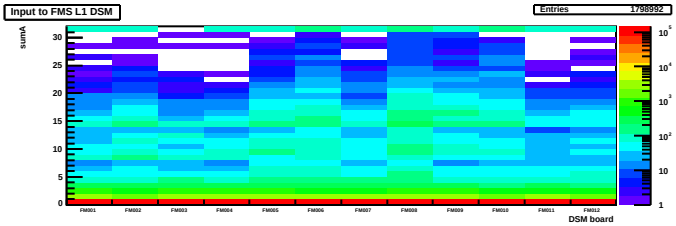

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

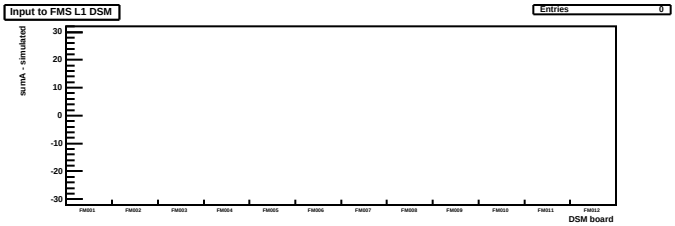

Input to FMS L1 DSM
Entries 1798992
sumB
DSM board

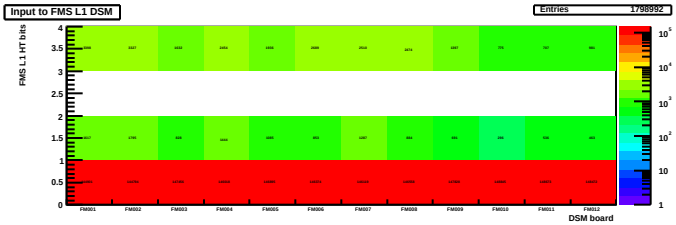

Input to FMS L1 DSM
Entries 334
sumB - simulated
DSM board

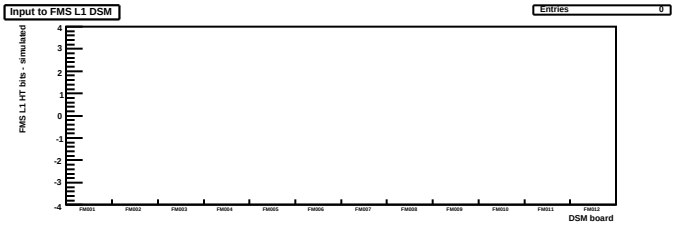

Input to FMS L1 DSM
Entries 1798992
sumCD
DSM board

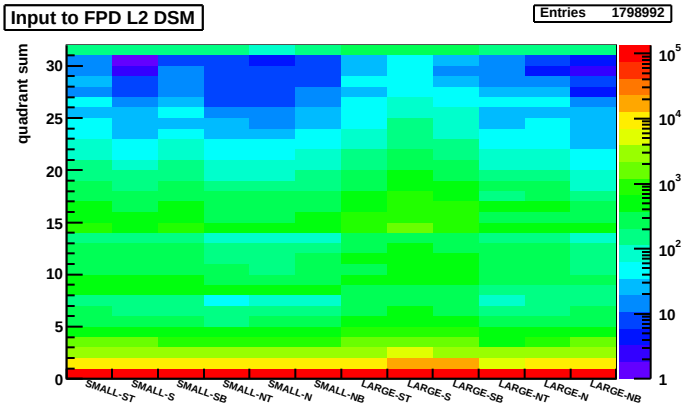

Input to FMS L1 DSM
Entries 2778
sumCD - simulated
DSM board

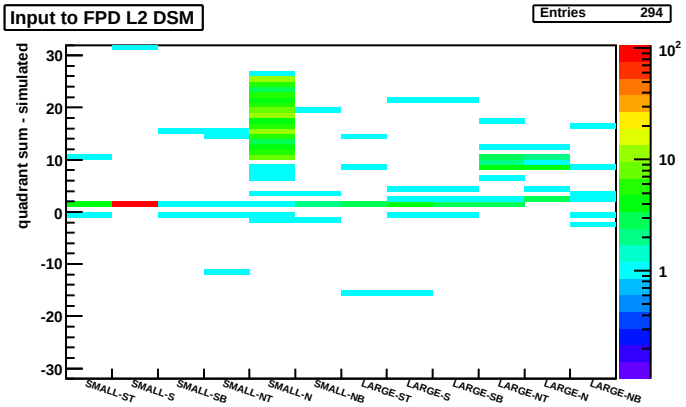

Input to FMS L1 DSM
Entries 1798992
sumA
DSM board

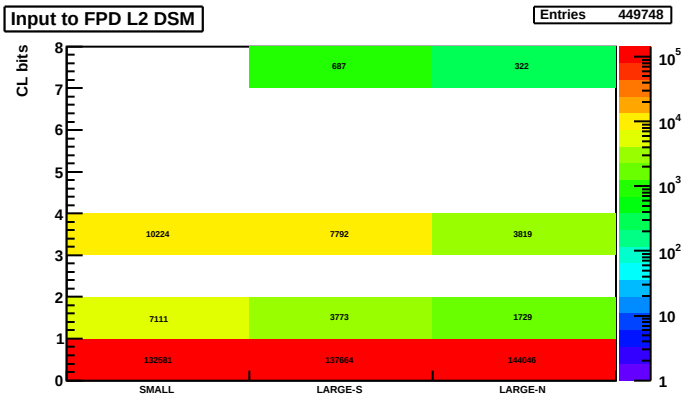

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

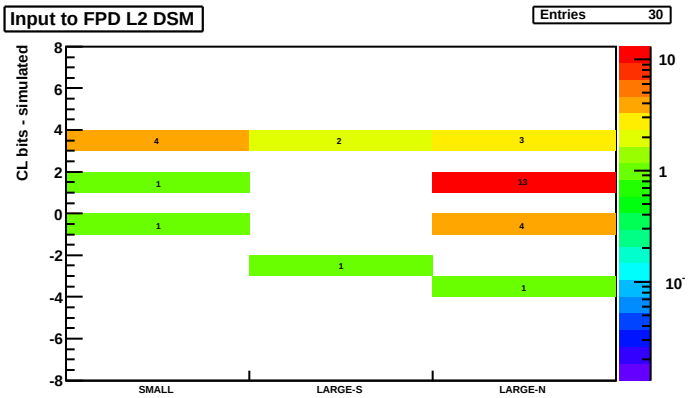

Input to FMS L1 DSM
Entries 1798992
FMS L1 HT bits
DSM board

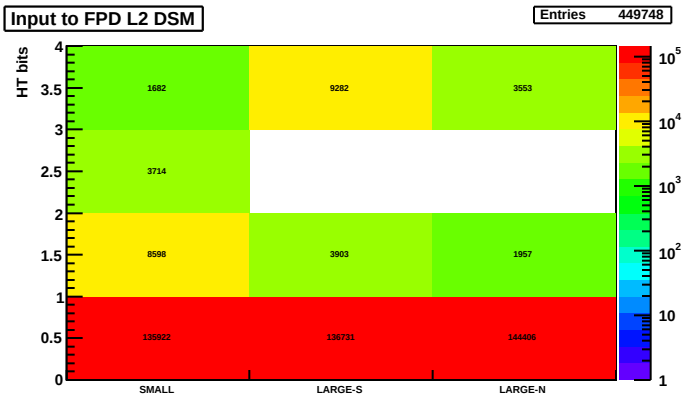

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

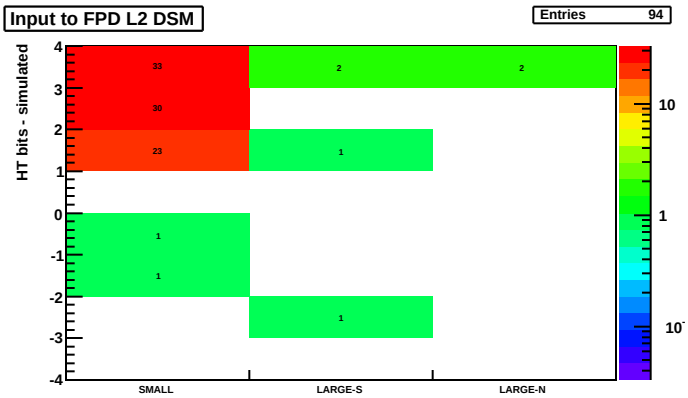

Input to FPD L2 DSM
Entries 1798992
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

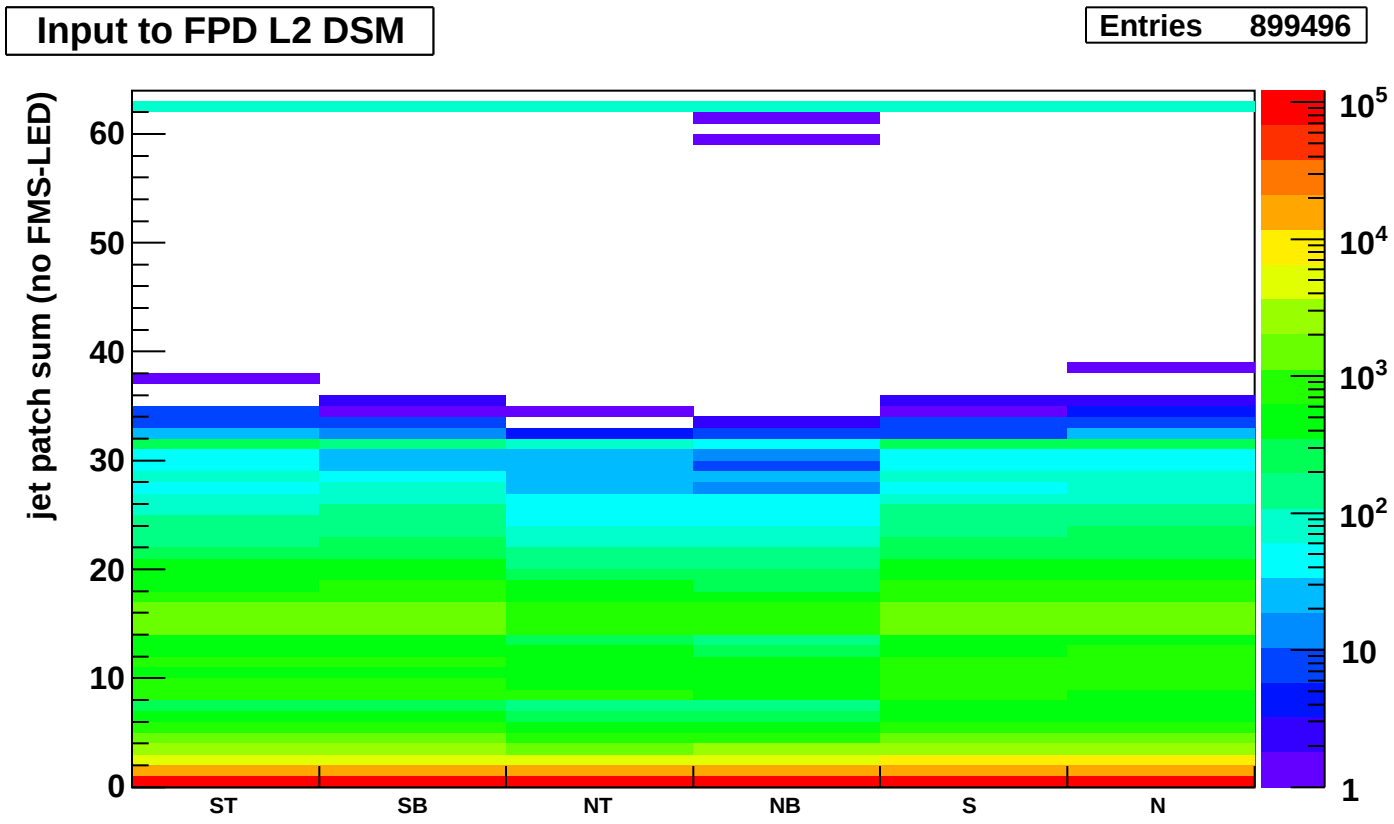

Input to FPD L2 DSM
Entries 294
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

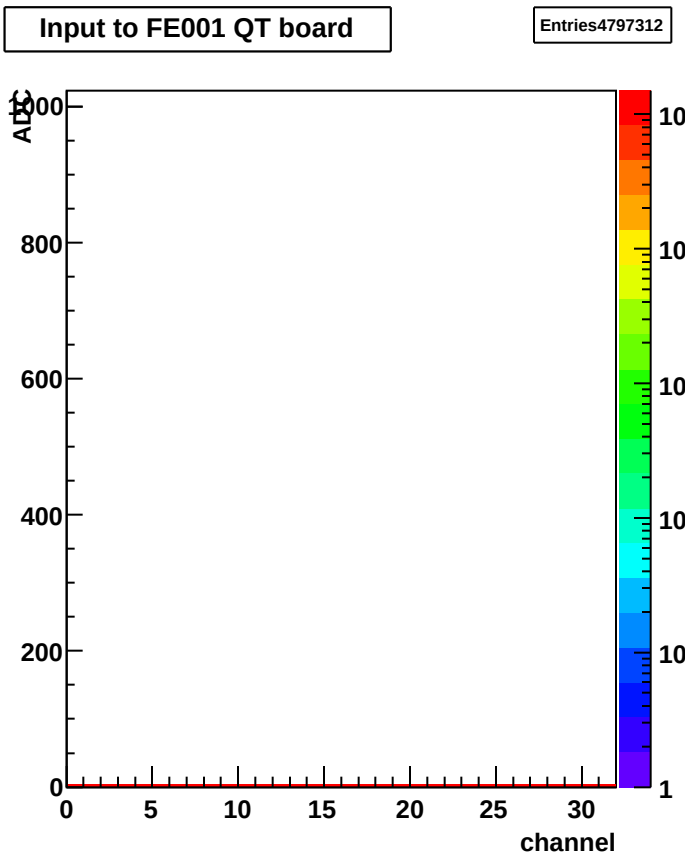

Input to FPD L2 DSM
Entries 449748
CL bits
687
322
10224
7792
3819
7111
3773
1729
132581
137664
144046
SMALL
LARGE-S
LARGE-N

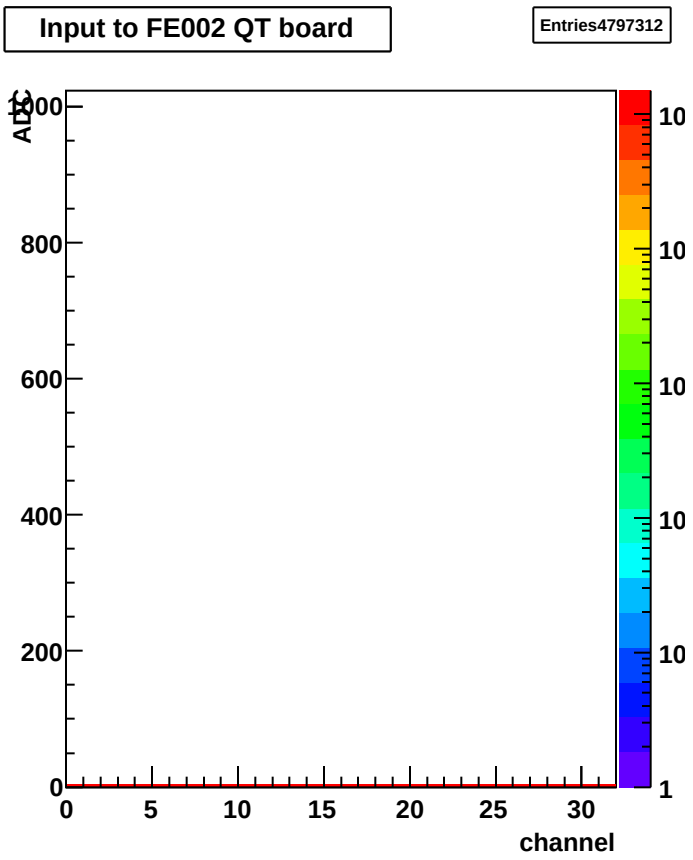

Input to FPD L2 DSM
Entries 30
CL bits - simulated
4
2
3
1
13
1
4
1
1
SMALL
LARGE-S
LARGE-N

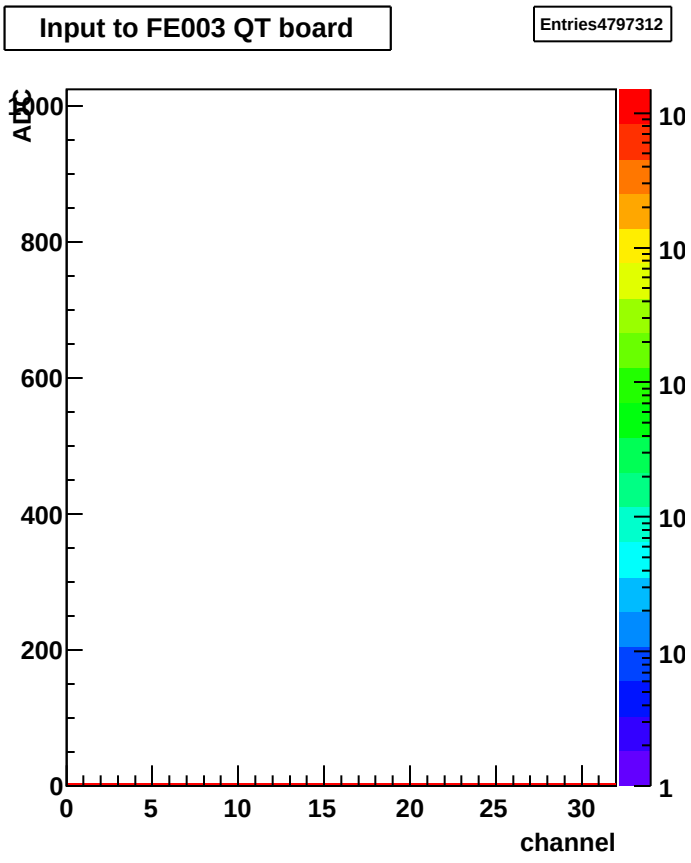

Input to FPD L2 DSM
Entries 449748
HT bits
1682
9282
3553
3714
8598
3903
1957
135922
136731
144406
SMALL
LARGE-S
LARGE-N

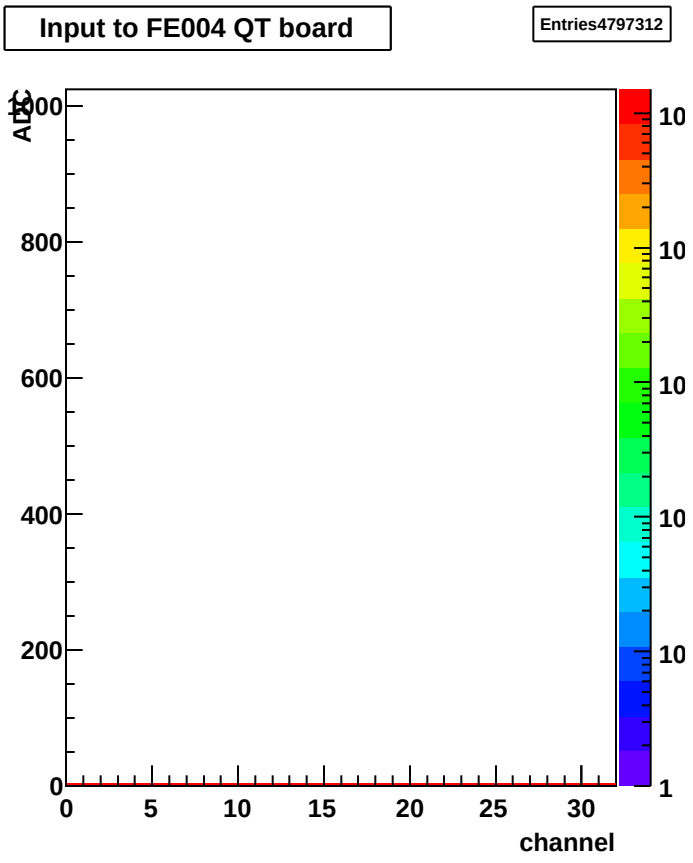

Input to FPD L2 DSM
Entries 94
HT bits - simulated
33
2
2
30
23
1
1
1
1
SMALL
LARGE-S
LARGE-N

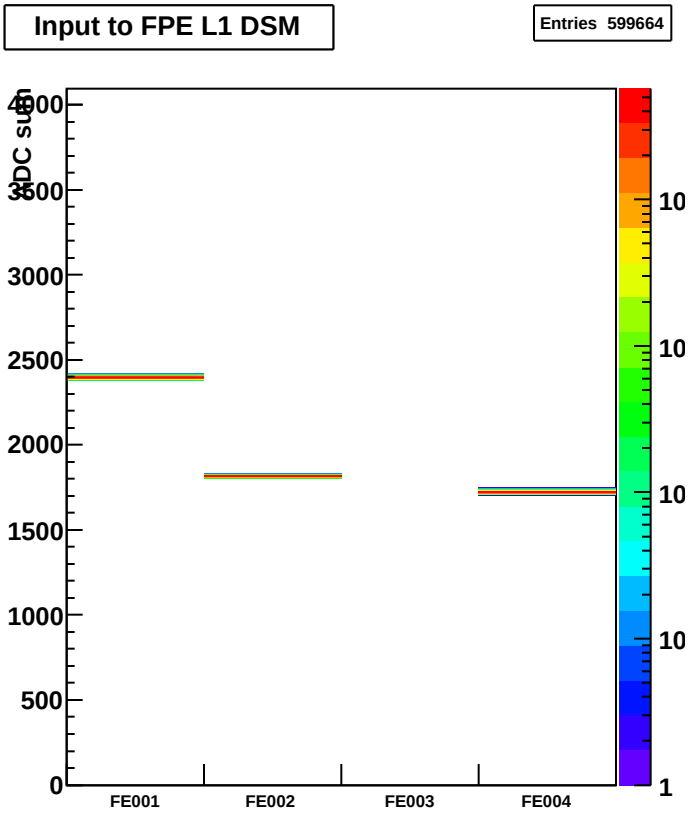

Input to FPD L2 DSM
Entries 899496
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

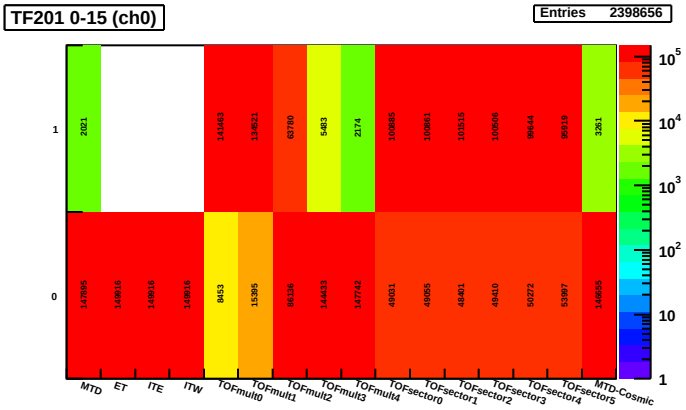

Input to FE001 QT board
Entries4797312
ADC
channel

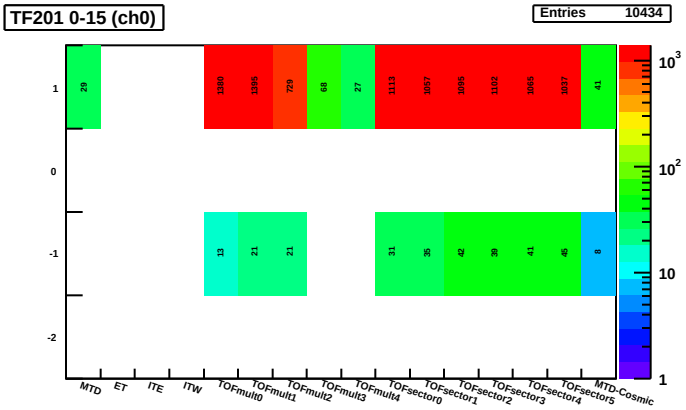

Input to FE002 QT board
Entries4797312
ADC
channel

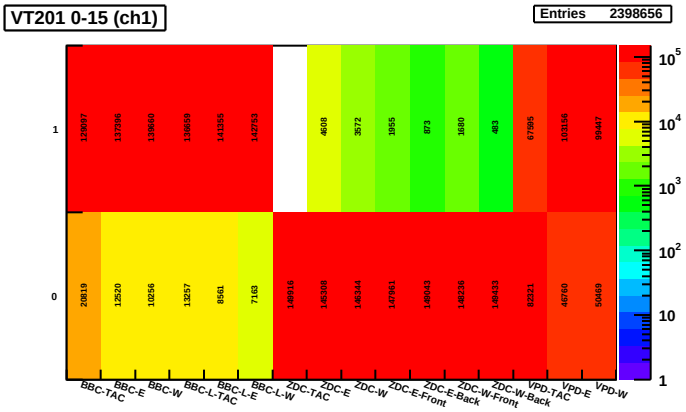

Input to FE003 QT board
Entries4797312
ADC
channel

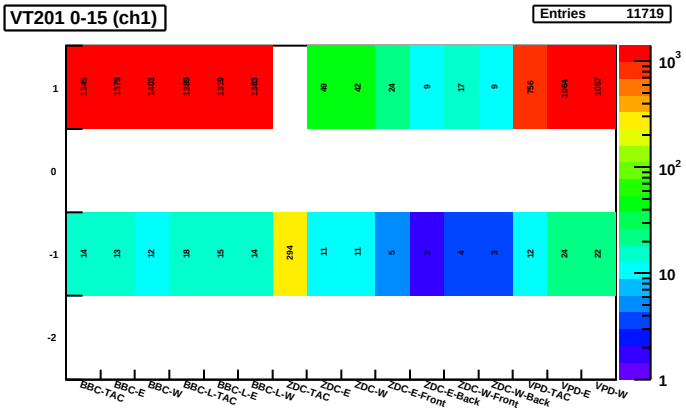

Input to FE004 QT board
Entries4797312
ADC
channel

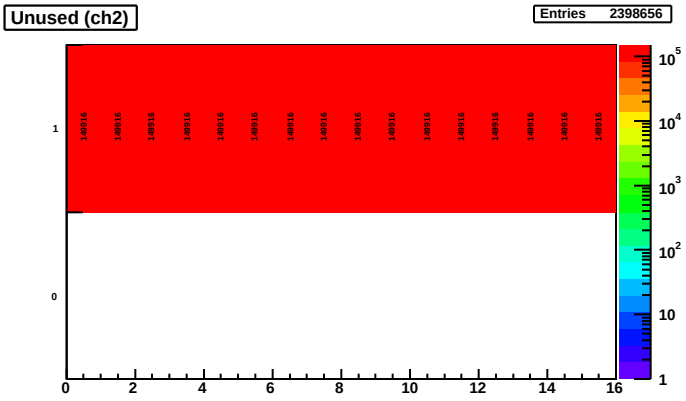

Input to FPE L1 DSM
Entries 599664
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

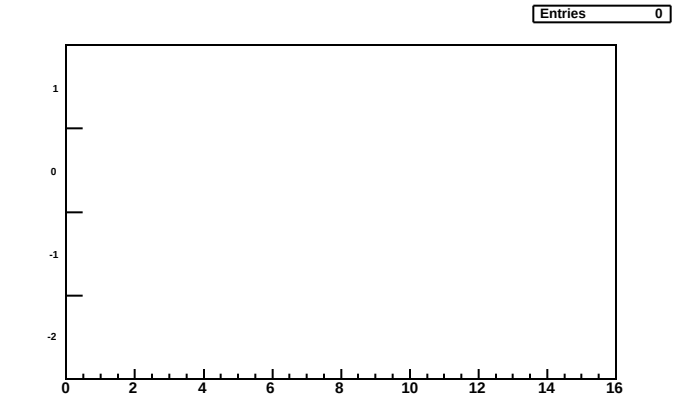

TF201 0-15 (ch0)
Entries 2398656
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

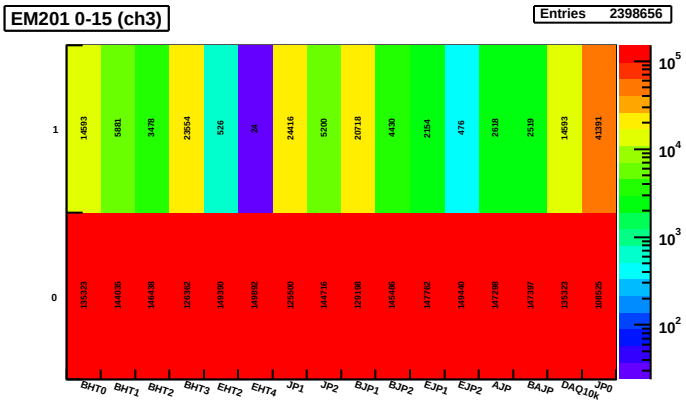

TF201 0-15 (ch0)
Entries 10434
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

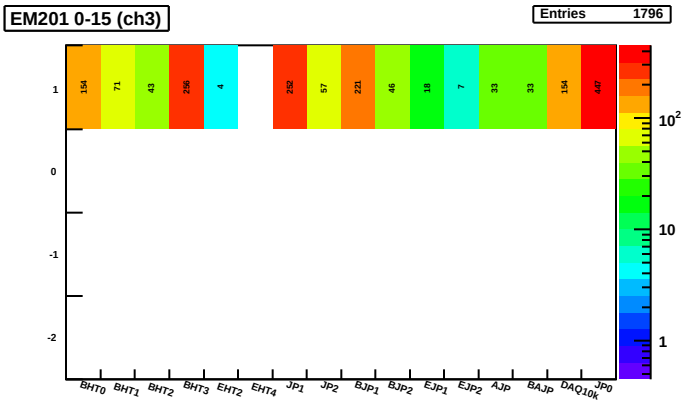

VT201 0-15 (ch1)
Entries 2398656
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 11719
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

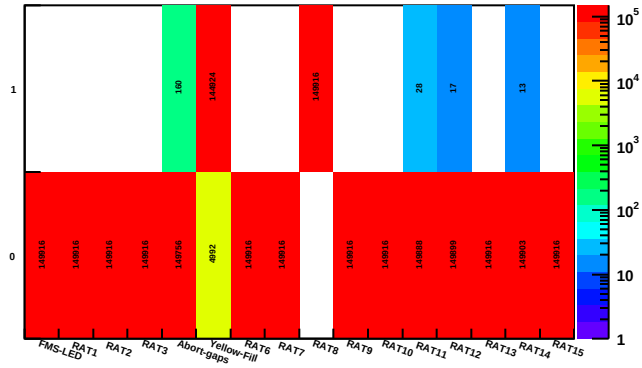

Unused (ch2)
Entries 2398656

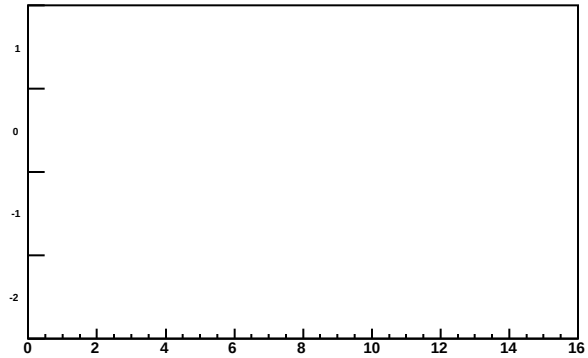

Entries 0


EM201 0-15 (ch3)
Entries 2398656
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

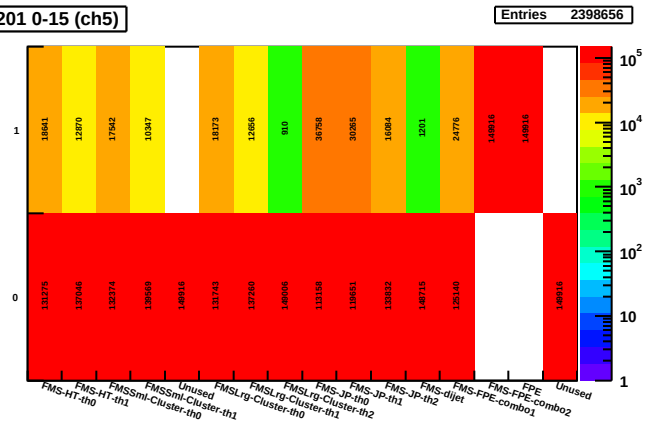

EM201 0-15 (ch3)
Entries 1796
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

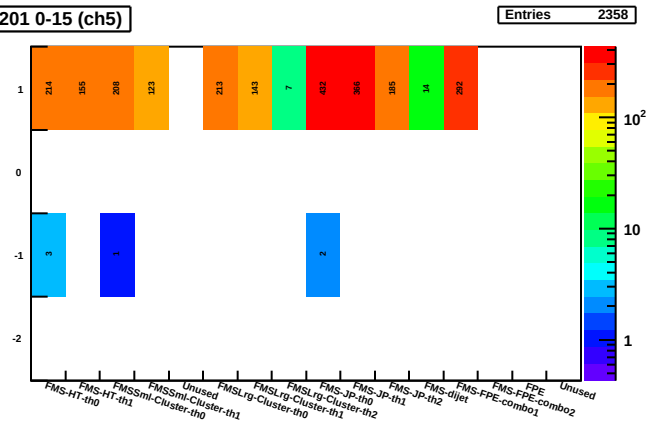




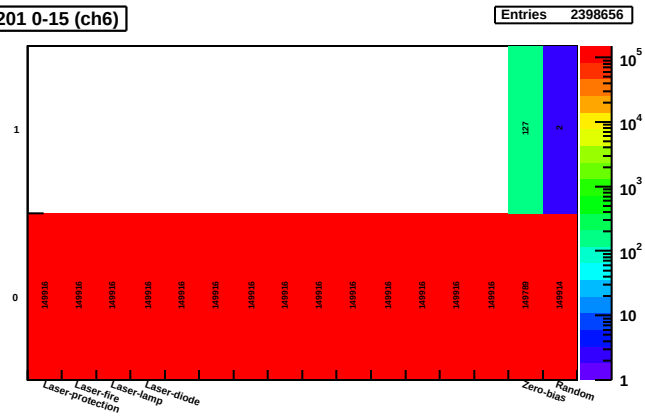
FP201 0-15 (ch5)

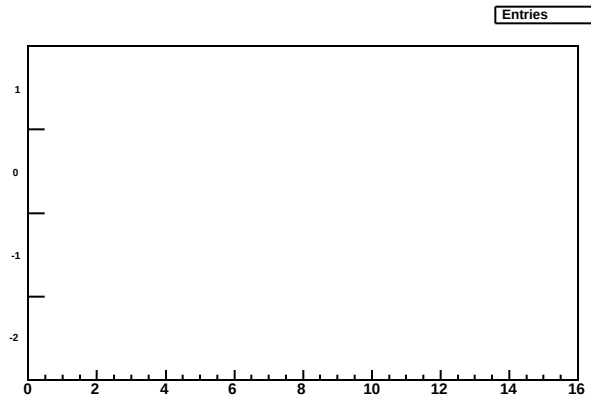
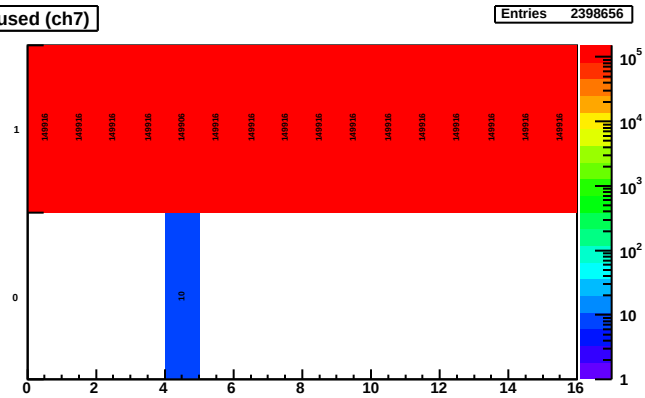
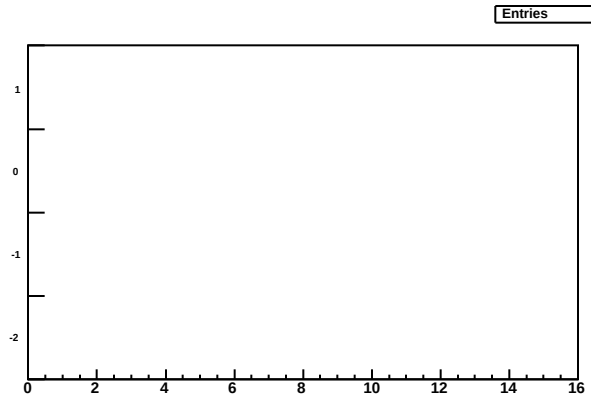


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)