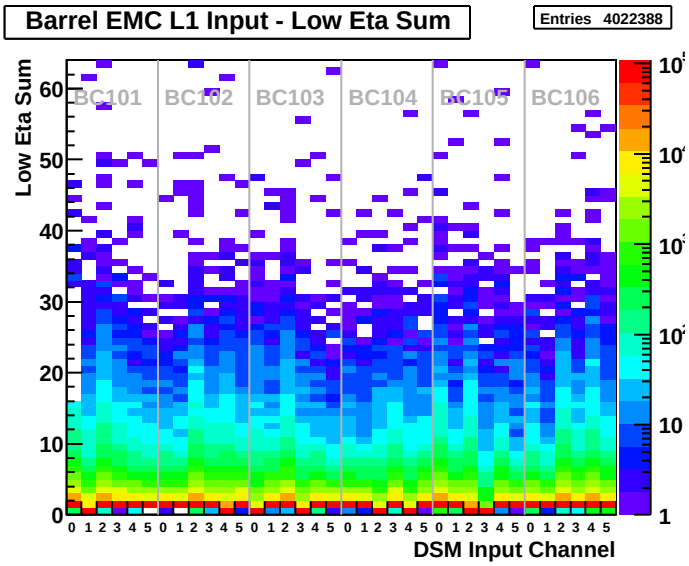
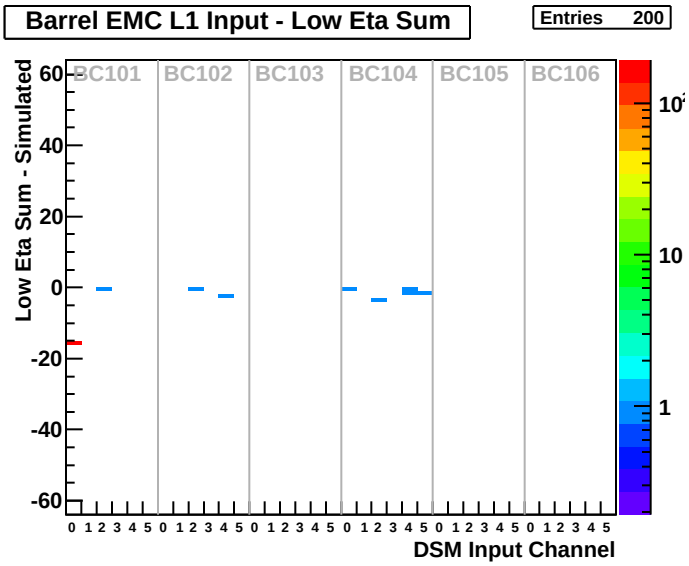
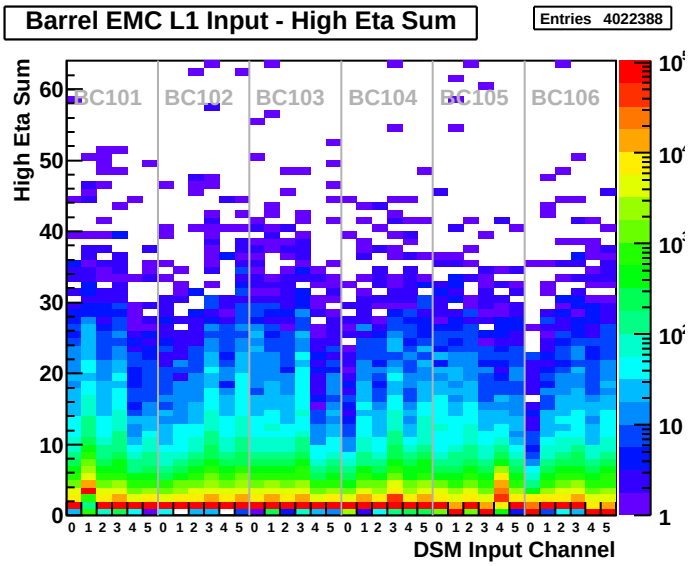
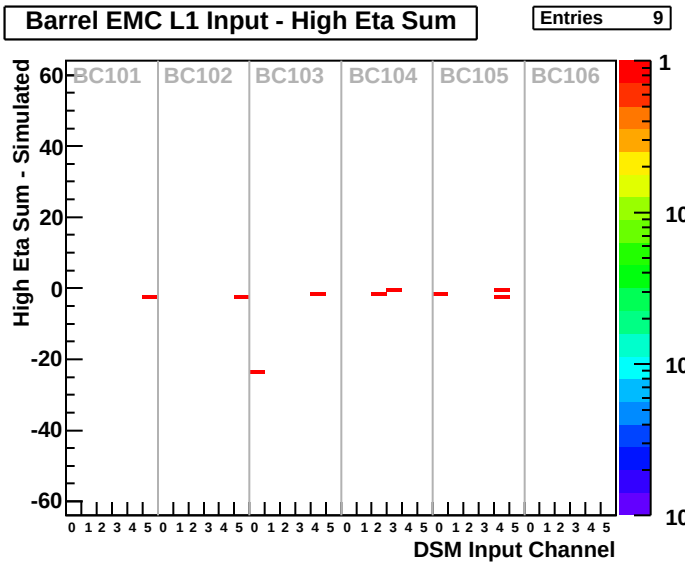
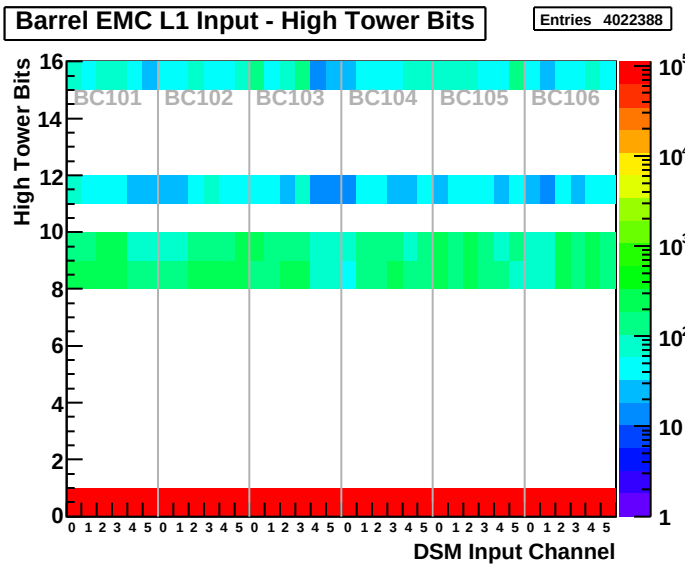
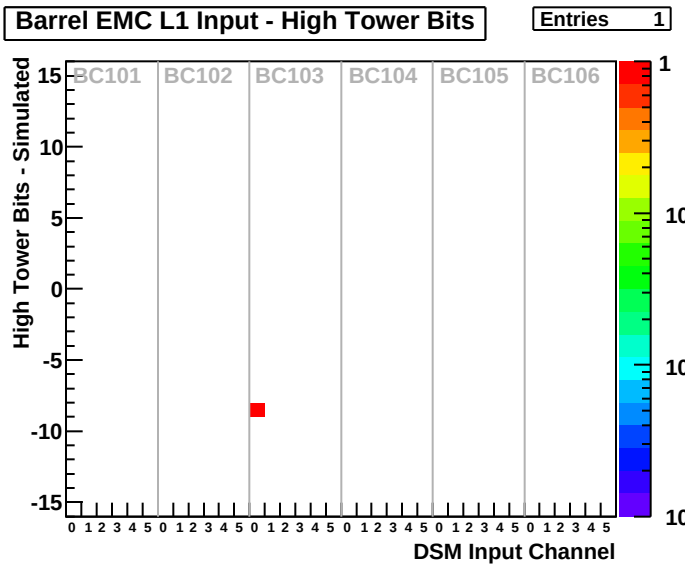

Barrel EMC L1 Input - Low Eta Sum
Entries 4022388
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

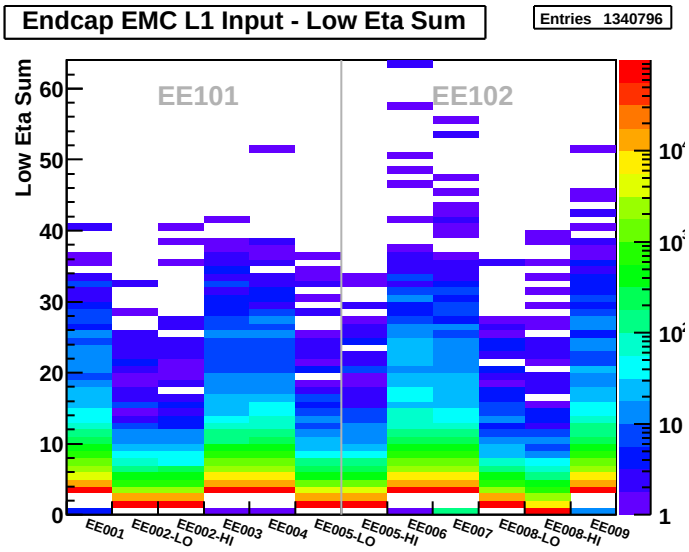

Barrel EMC L1 Input - Low Eta Sum
Entries 200
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

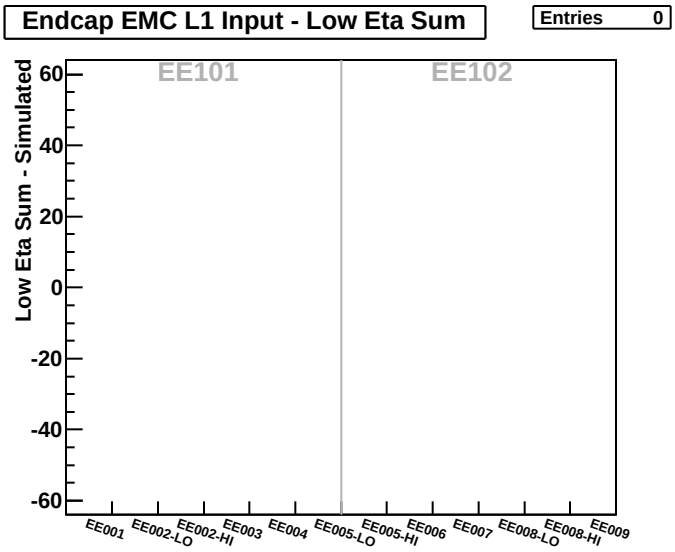

Barrel EMC L1 Input - High Eta Sum
Entries 4022388
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

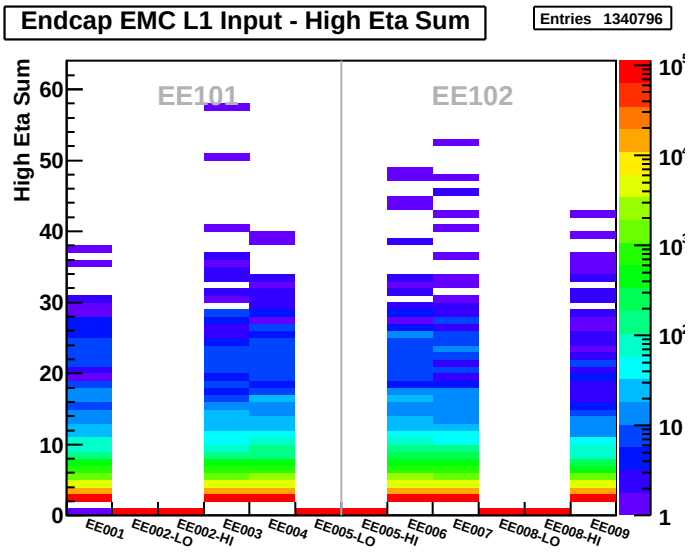

Barrel EMC L1 Input - High Eta Sum
Entries 9
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

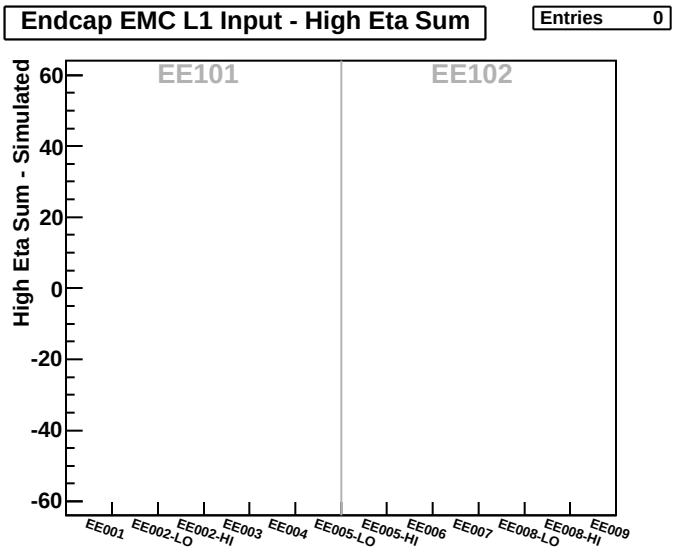

Barrel EMC L1 Input - High Tower Bits
Entries 4022388
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

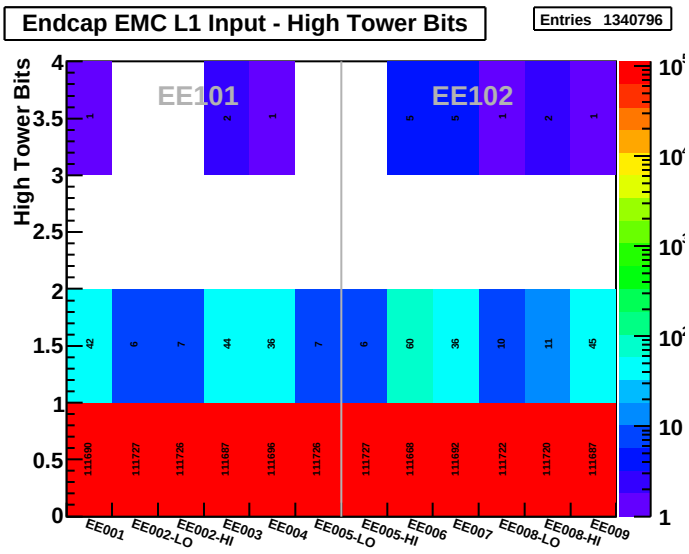

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

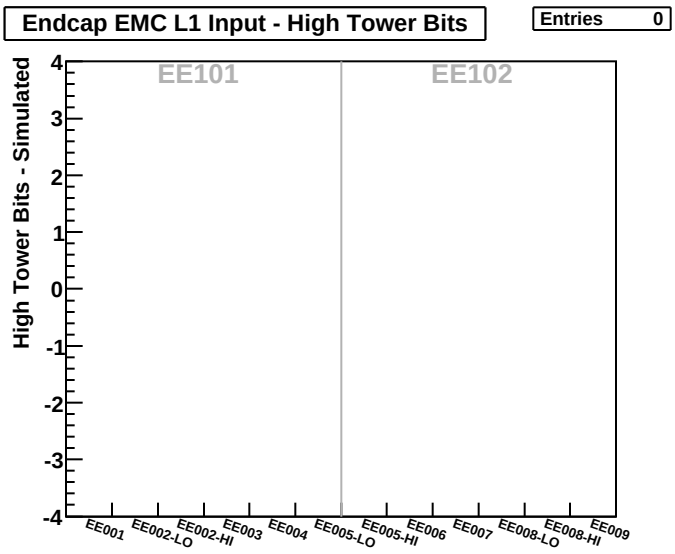

Endcap EMC L1 Input - Low Eta Sum
Entries 1340796
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

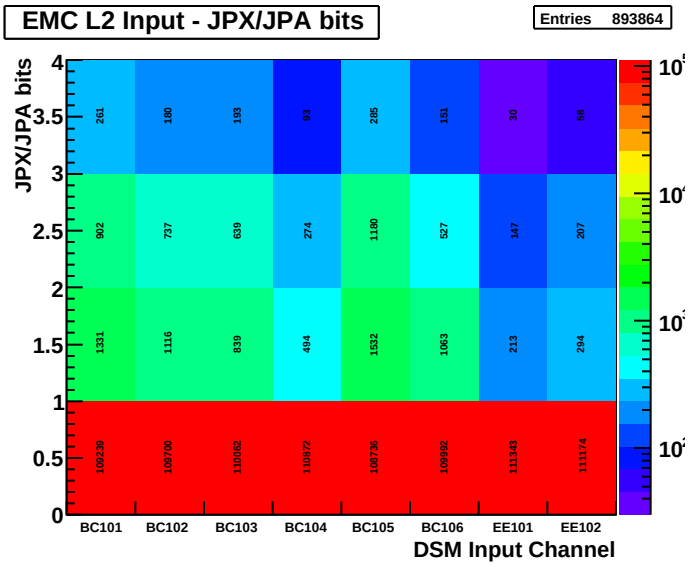

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

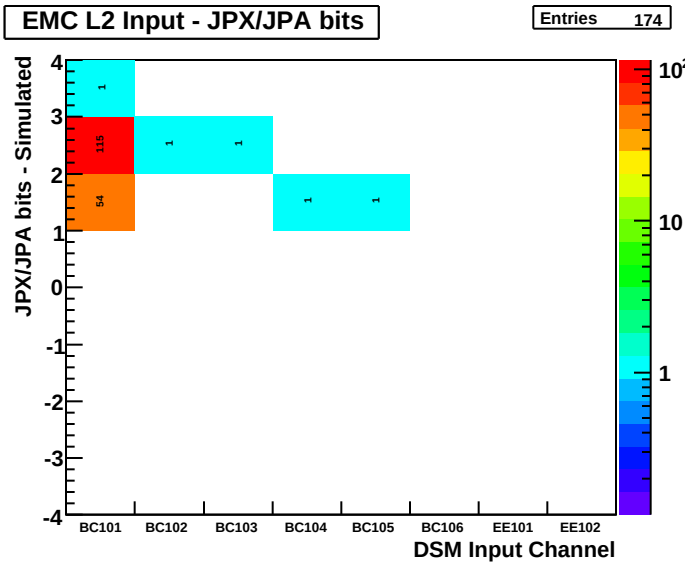

Endcap EMC L1 Input - High Eta Sum
Entries 1340796
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

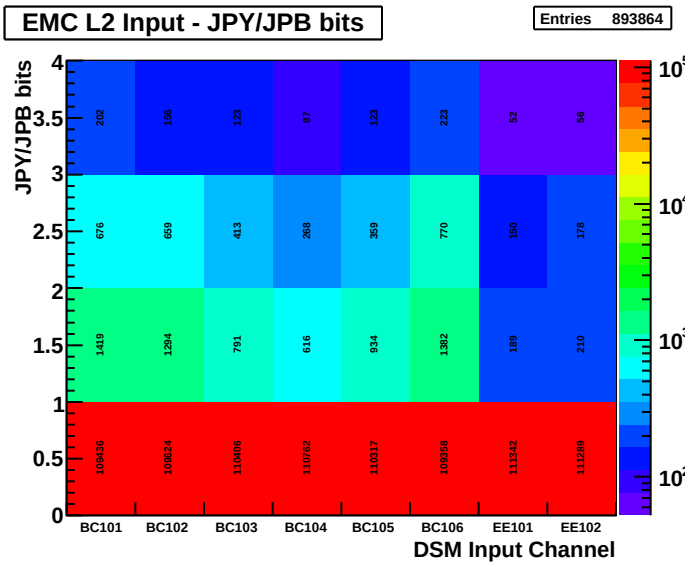

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

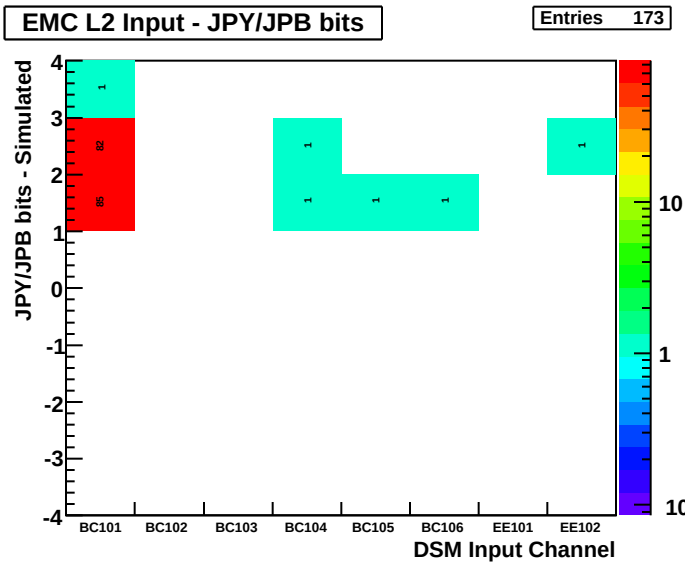

Endcap EMC L1 Input - High Tower Bits
Entries 1340796
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

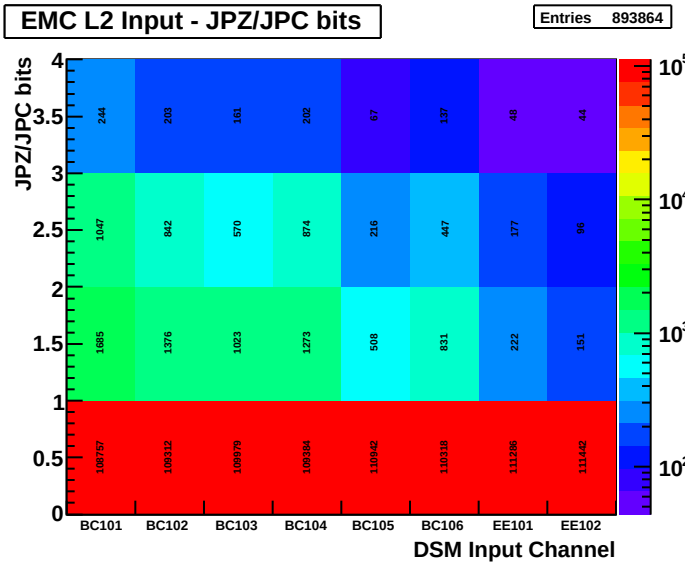

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

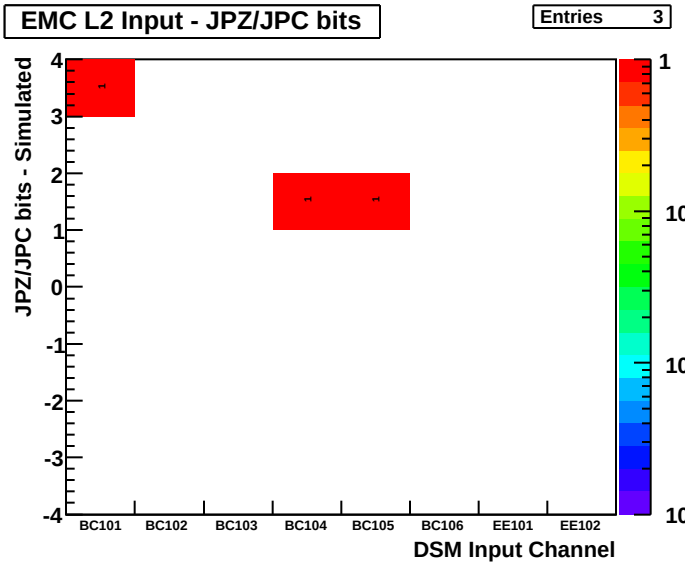

EMC L2 Input - JPX/JPA bits
Entries 893864
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
261 180 193 93 285 151 30 58
902 737 639 274 1180 527 147 207
1331 1116 839 494 1532 1063 213 294
109239 109700 110062 110872 108736 109992 111343 111174

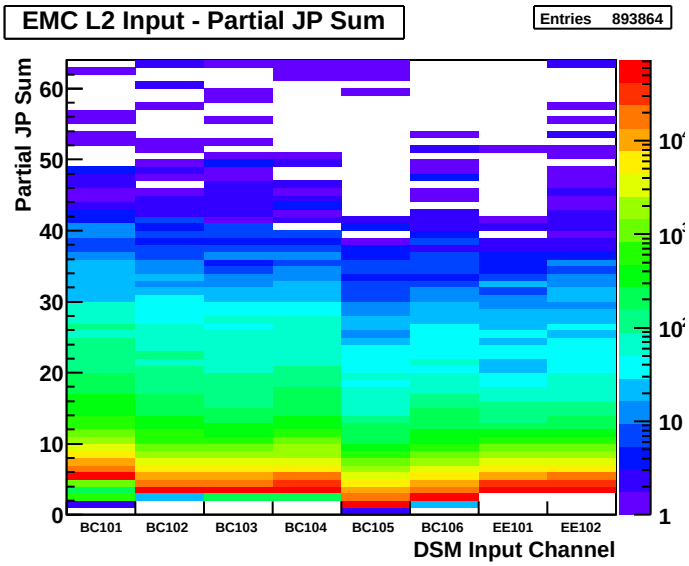

EMC L2 Input - JPX/JPA bits
Entries 174
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
115 1 1
54 1 1

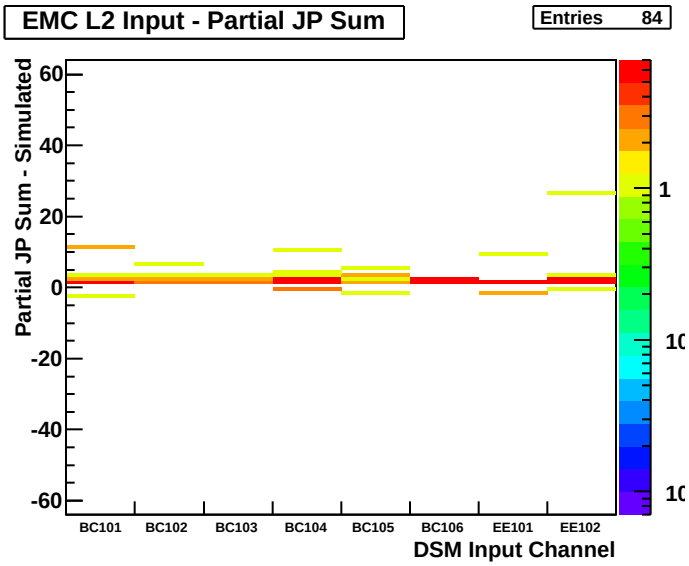

EMC L2 Input - JPY/JPB bits
Entries 893864
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
202 156 123 87 123 223 52 56
676 659 413 268 359 770 150 178
1419 1294 791 616 934 1382 189 210
109436 109624 110406 110762 110317 109358 111342 111289

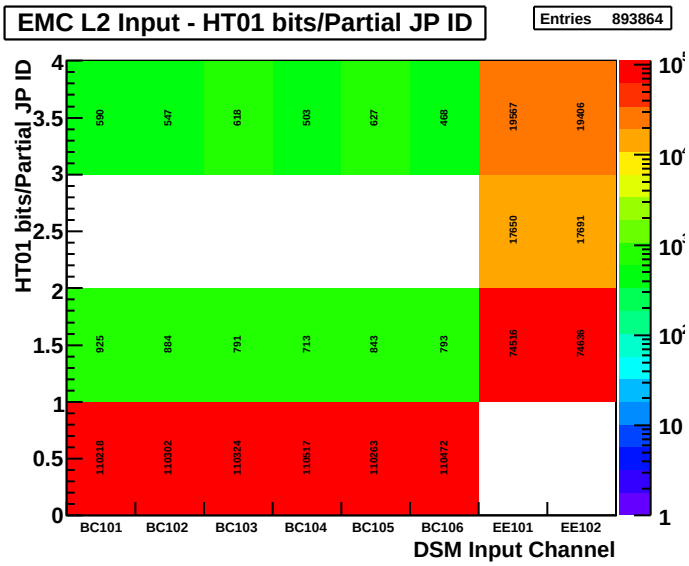

EMC L2 Input - JPY/JPB bits
Entries 173
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
82 1
85 1 1 1 1

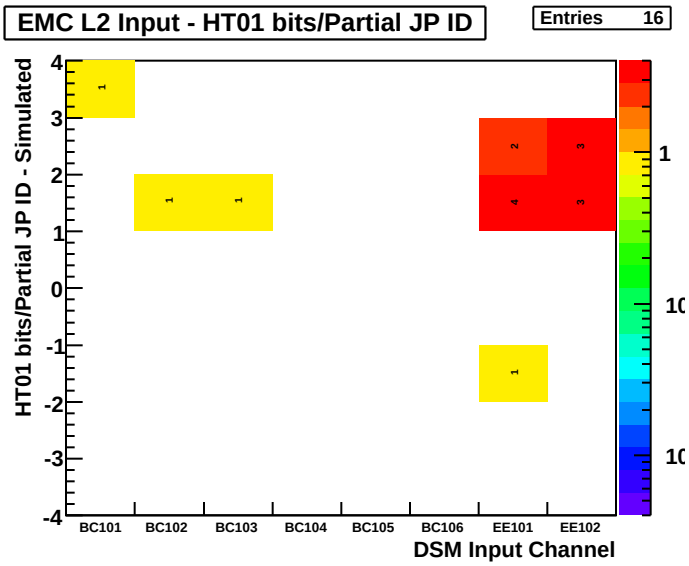

EMC L2 Input - JPZ/JPC bits
Entries 893864
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
244 203 161 202 67 137 48 44
1047 842 570 874 216 447 177 96
1685 1376 1023 1273 508 831 222 151
108757 109312 109979 109384 110942 110318 111286 111442

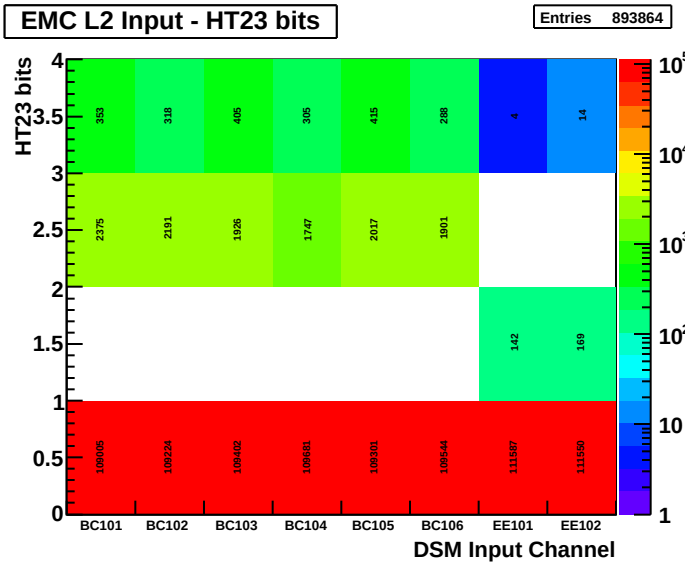

EMC L2 Input - JPZ/JPC bits
Entries 3
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1 1

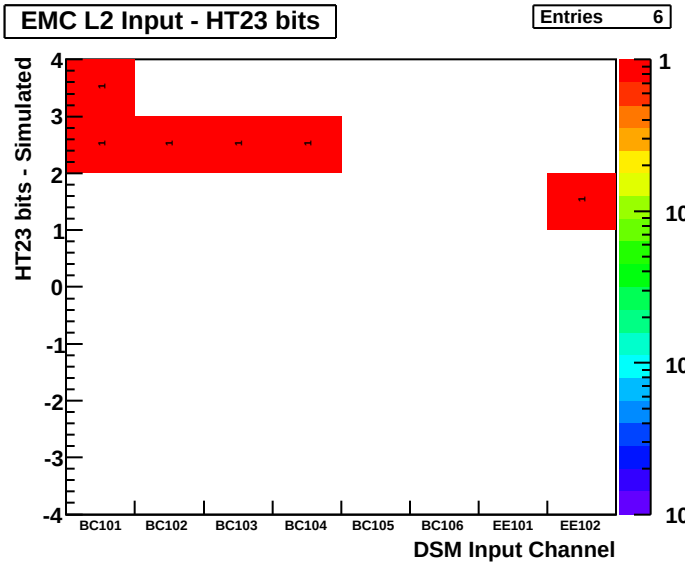

EMC L2 Input - Partial JP Sum
Entries 893864
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

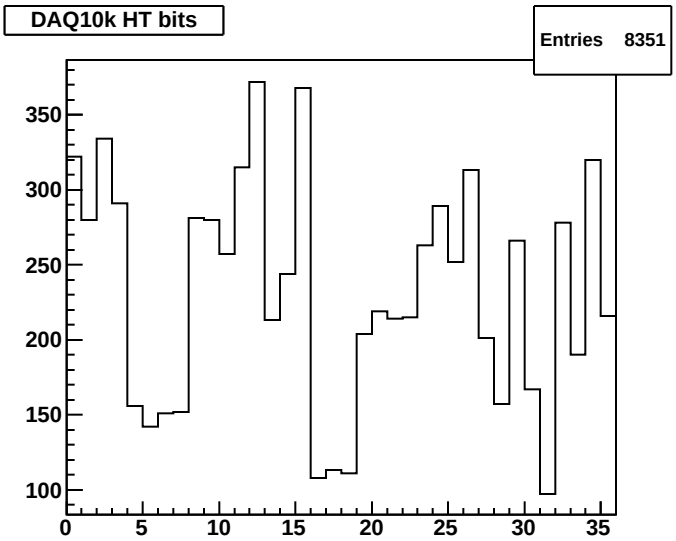

EMC L2 Input - Partial JP Sum
Entries 84
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

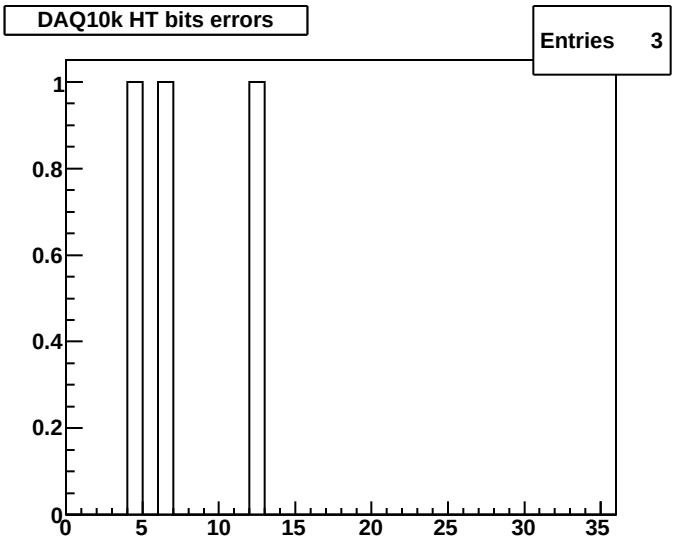

EMC L2 Input - HT01 bits/Partial JP ID
Entries 893864
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

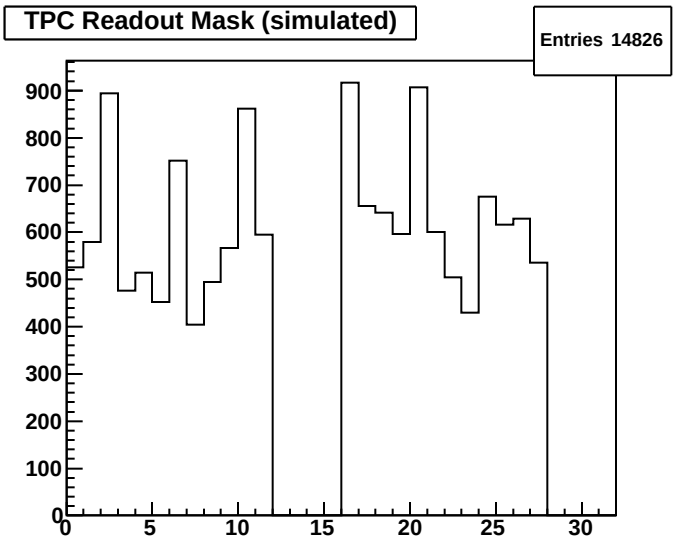

EMC L2 Input - HT01 bits/Partial JP ID
Entries 16
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

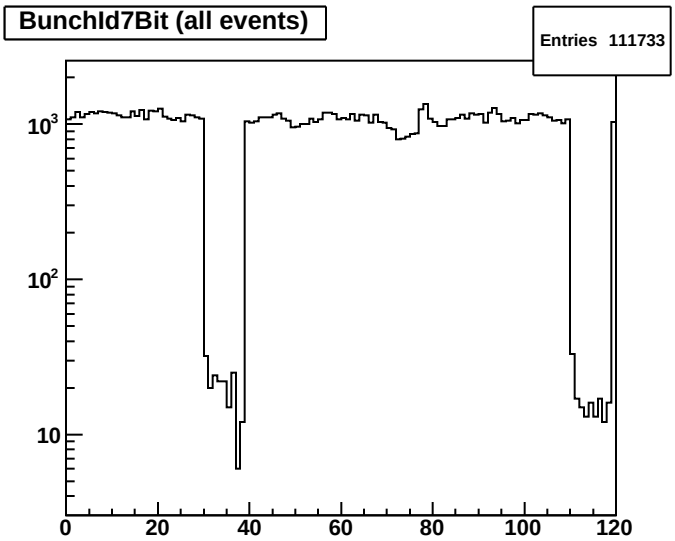

EMC L2 Input - HT23 bits
Entries 893864
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

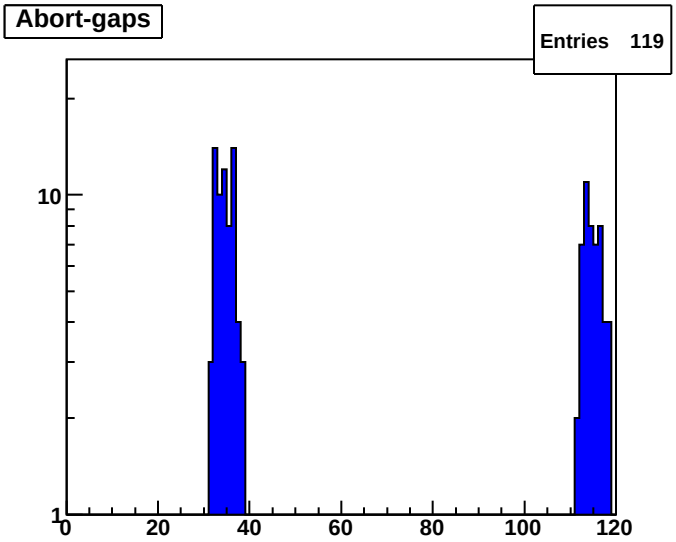

EMC L2 Input - HT23 bits
Entries 6
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

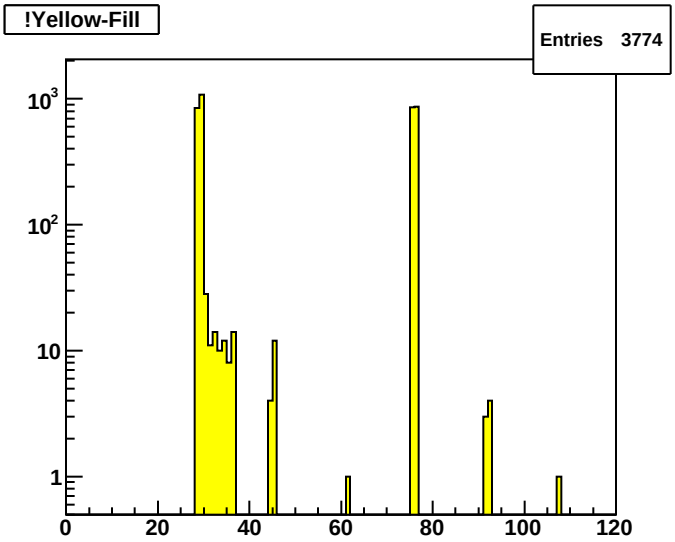

DAQ10k HT bits
Entries 8351

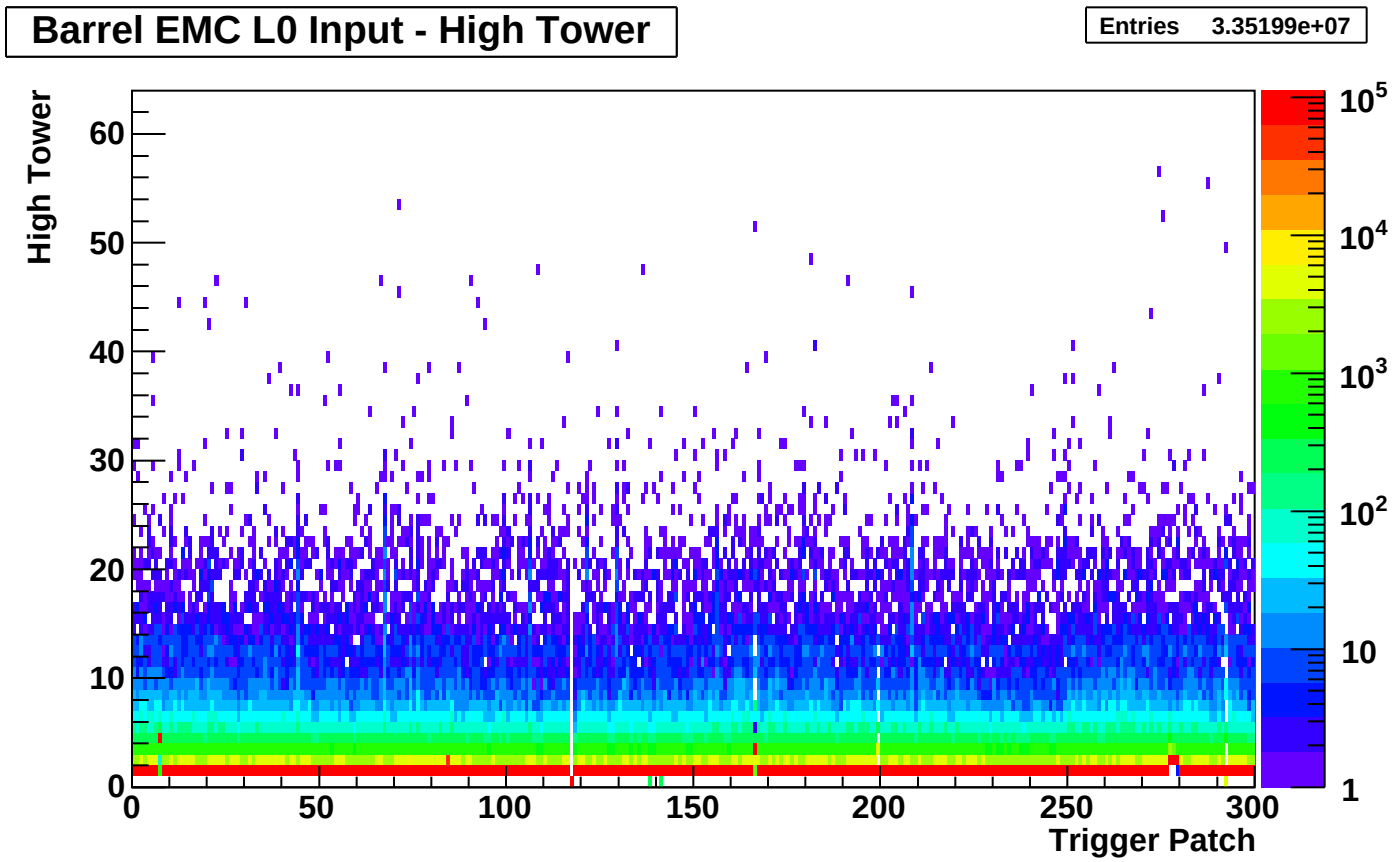

DAQ10k HT bits errors
Entries 3

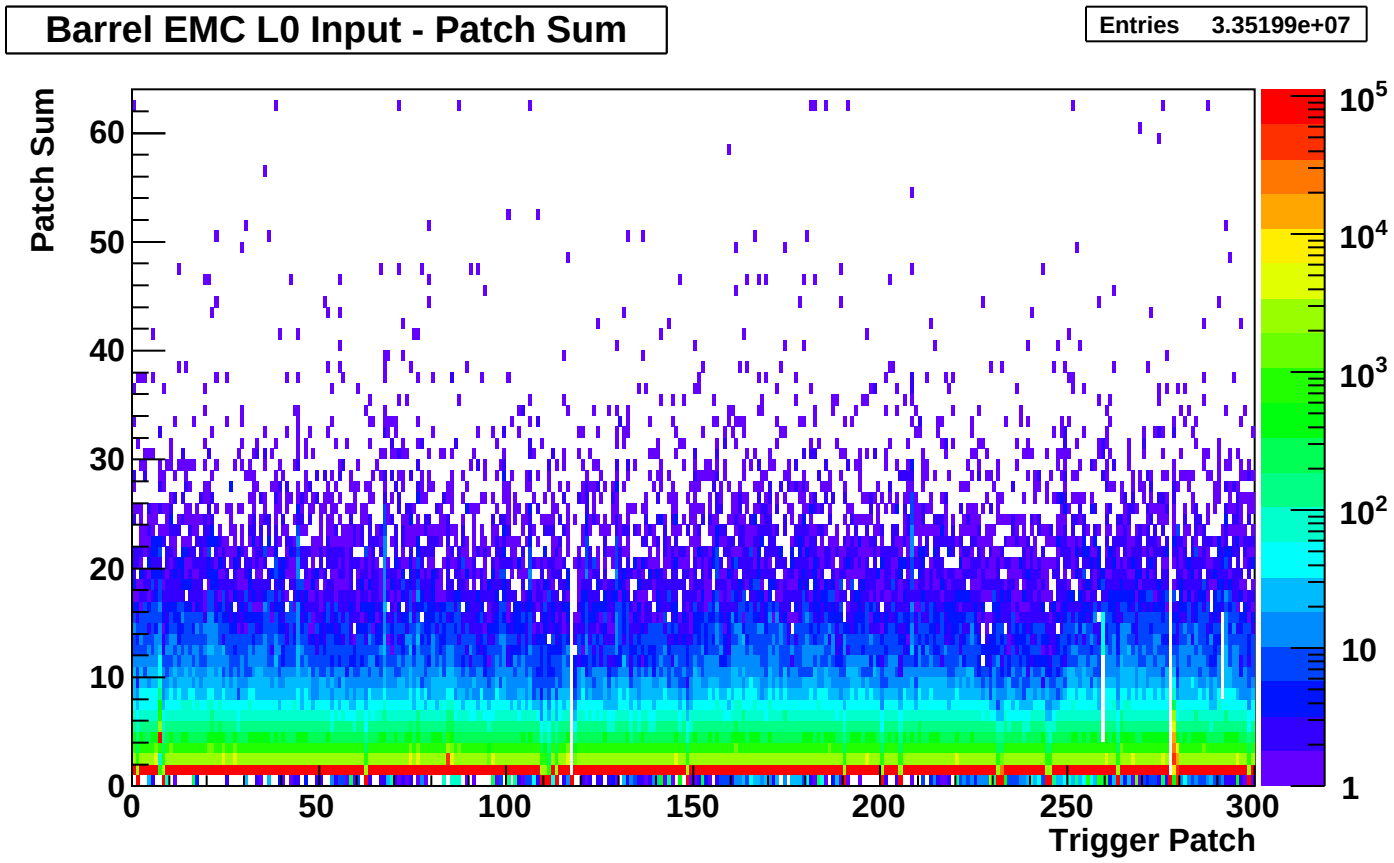

TPC Readout Mask (simulated)
Entries 14826

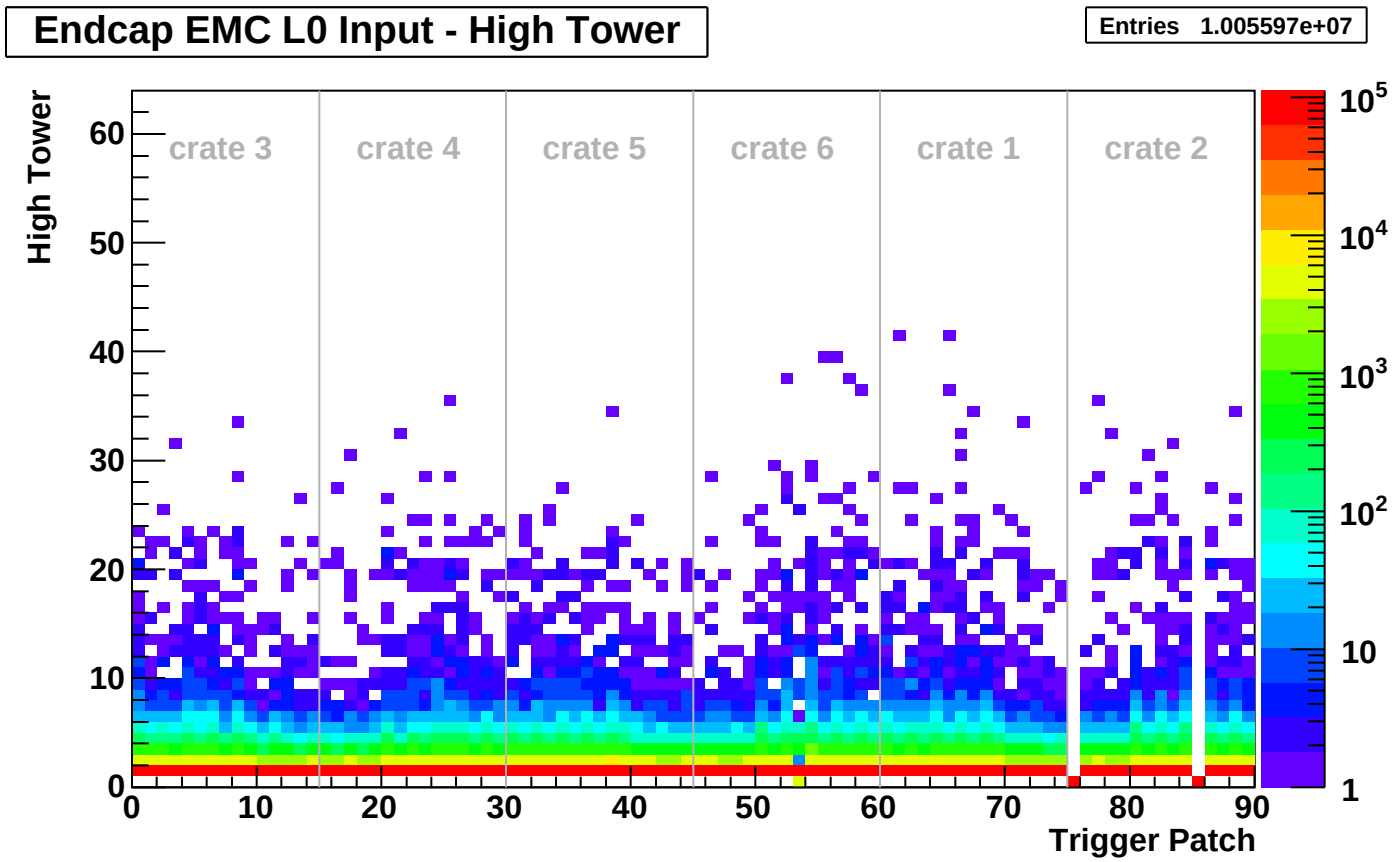

BunchId7Bit (all events)
Entries 111733

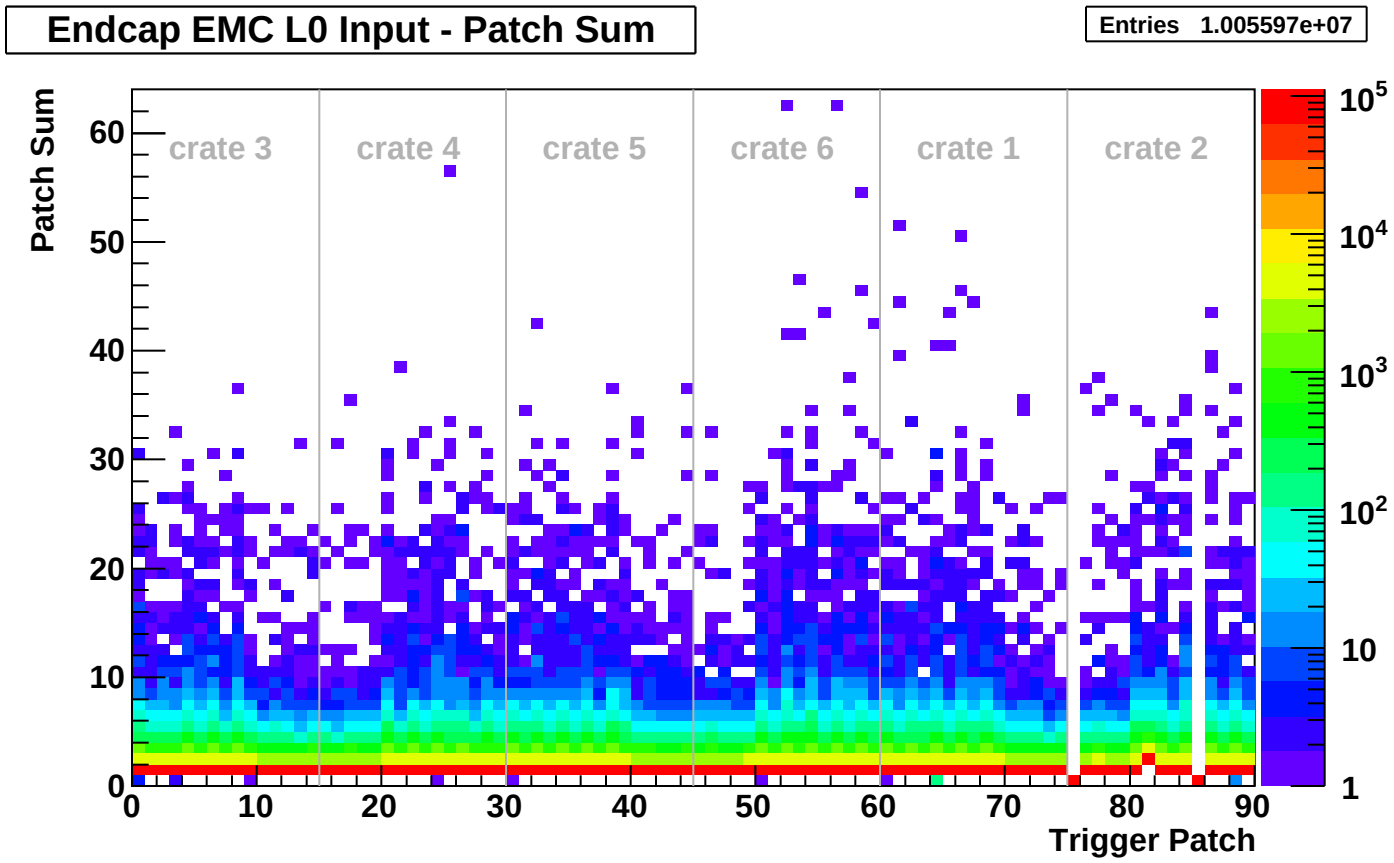

Abort-gaps
Entries 119

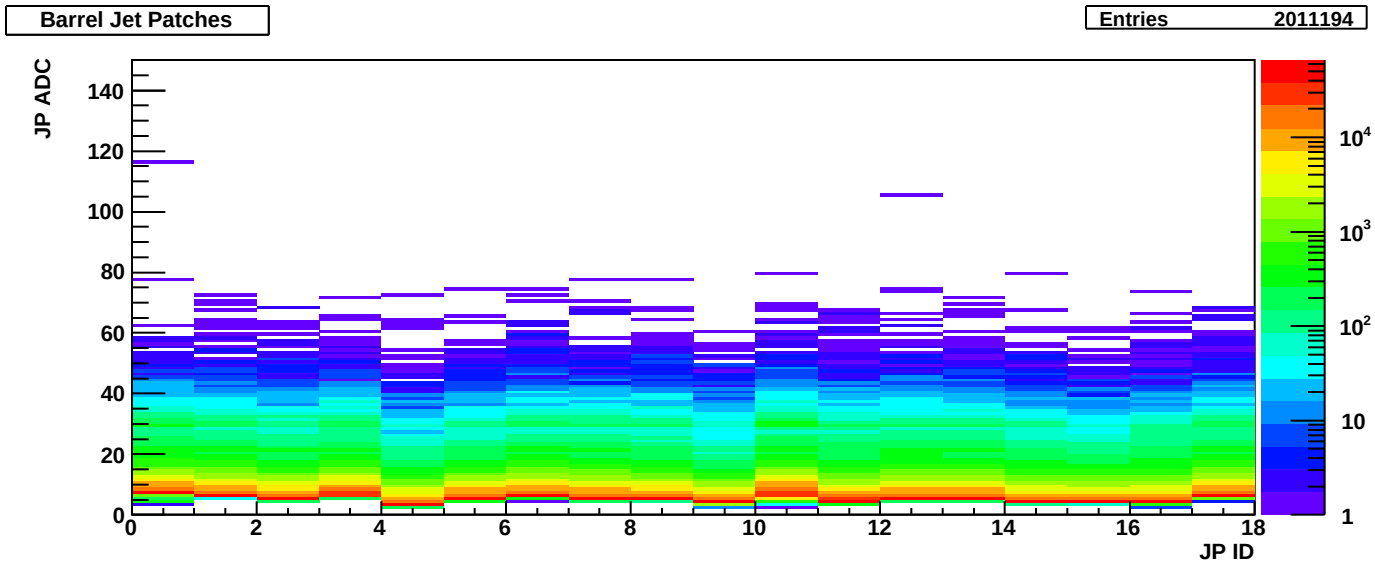

!Yellow-Fill
Entries 3774

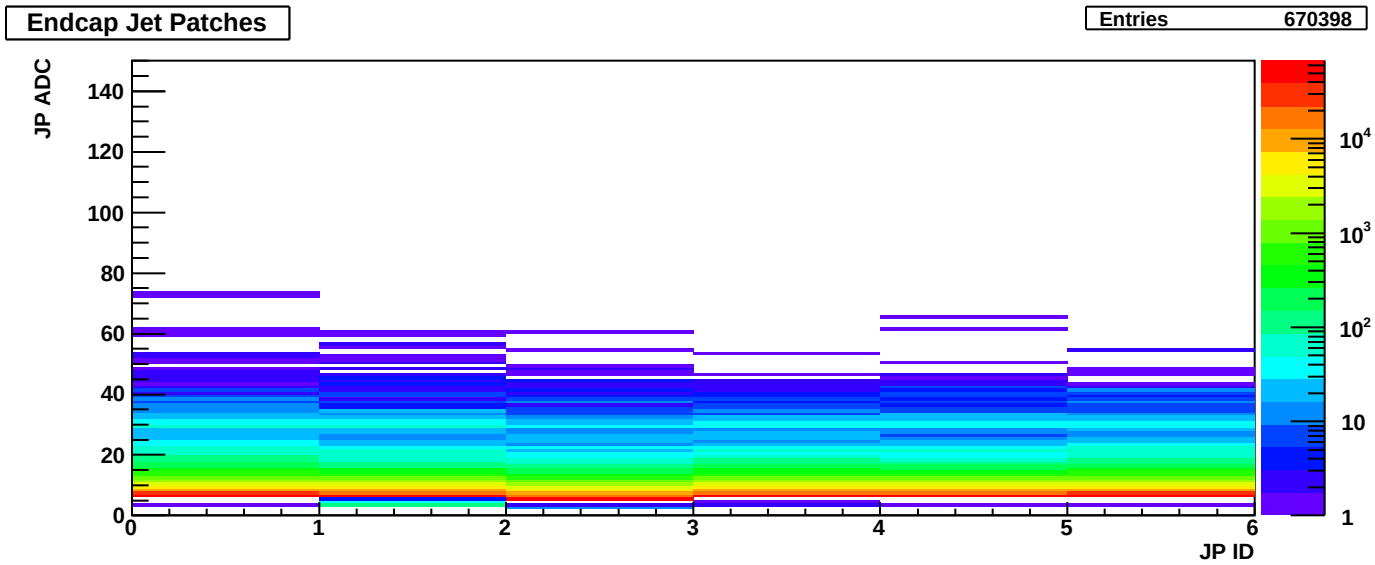

Barrel EMC L0 Input - High Tower
Entries 3.35199e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

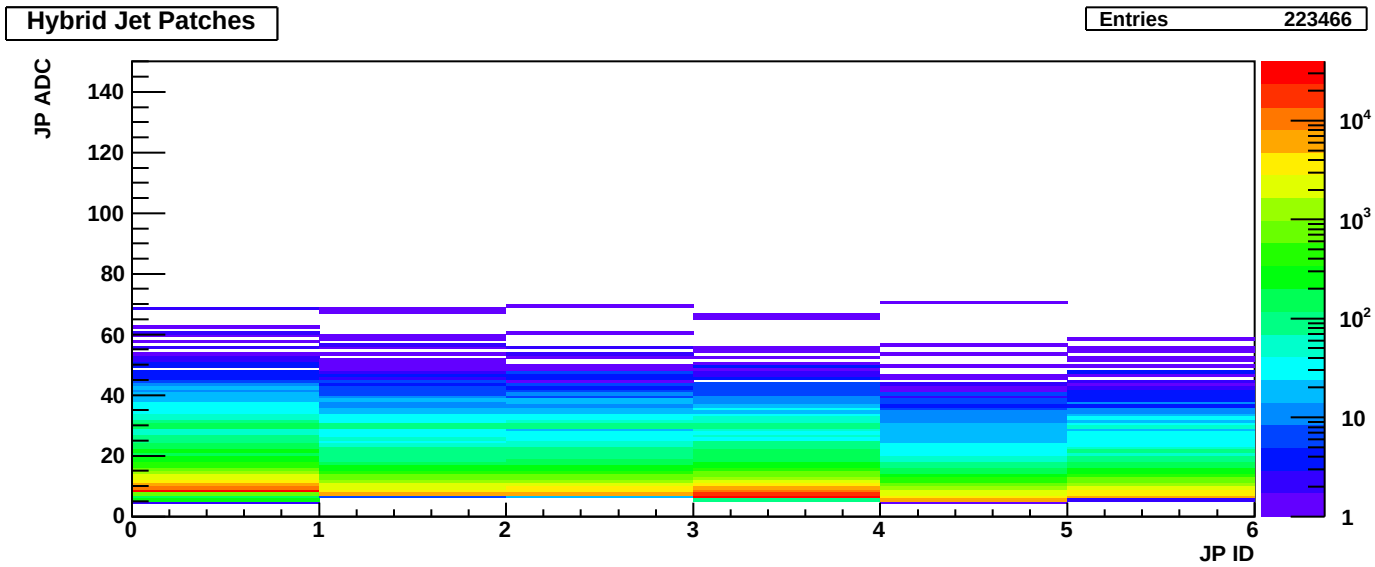

Barrel EMC L0 Input - Patch Sum
Entries 3.35199e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

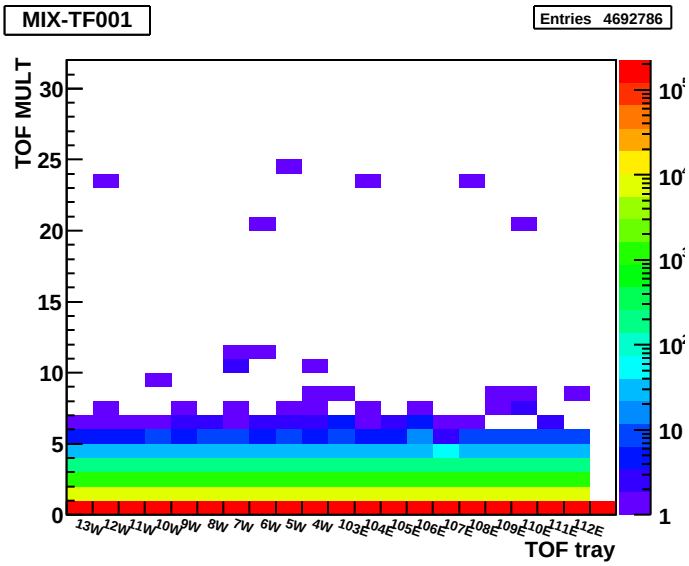

Endcap EMC L0 Input - High Tower
Entries 1.005597e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

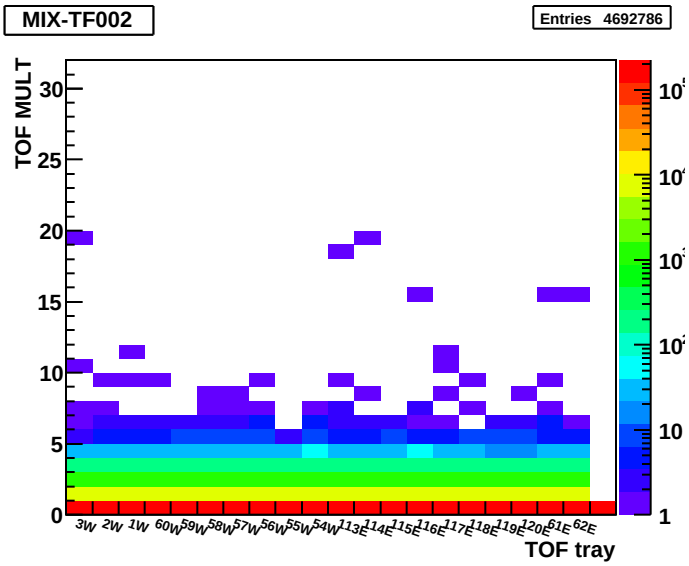

Endcap EMC L0 Input - Patch Sum
Entries 1.005597e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

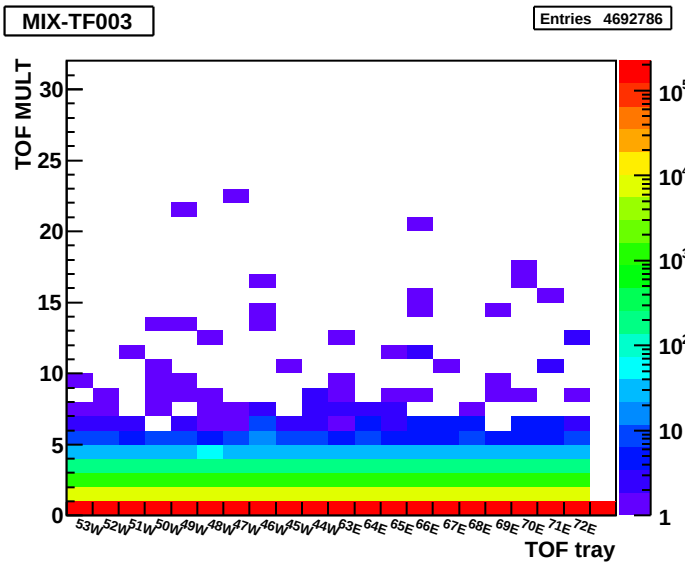

Barrel Jet Patches
Entries 2011194
JP ADC
JP ID

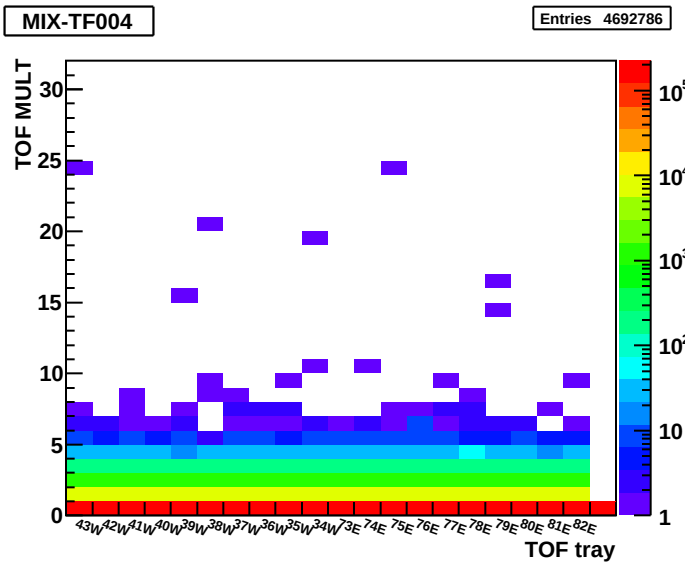

Endcap Jet Patches
Entries 670398
JP ADC
JP ID

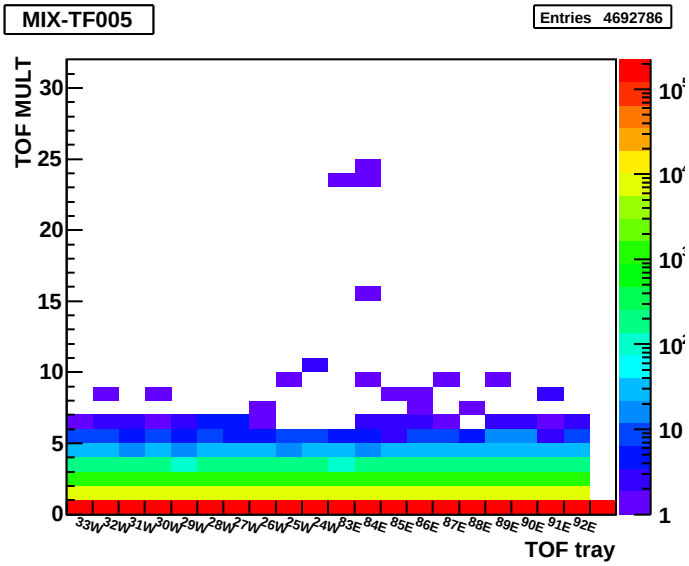

Hybrid Jet Patches
Entries 223466
JP ADC
JP ID

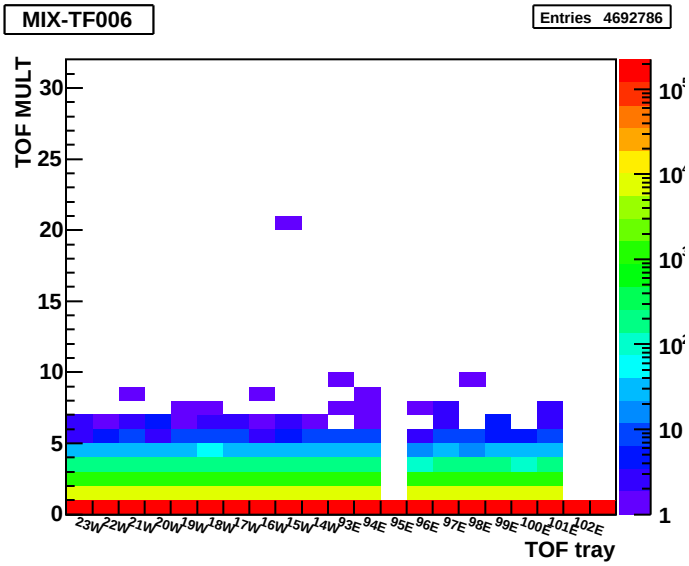

MIX-TF001
Entries 4692786
TOF MULT
TOF tray

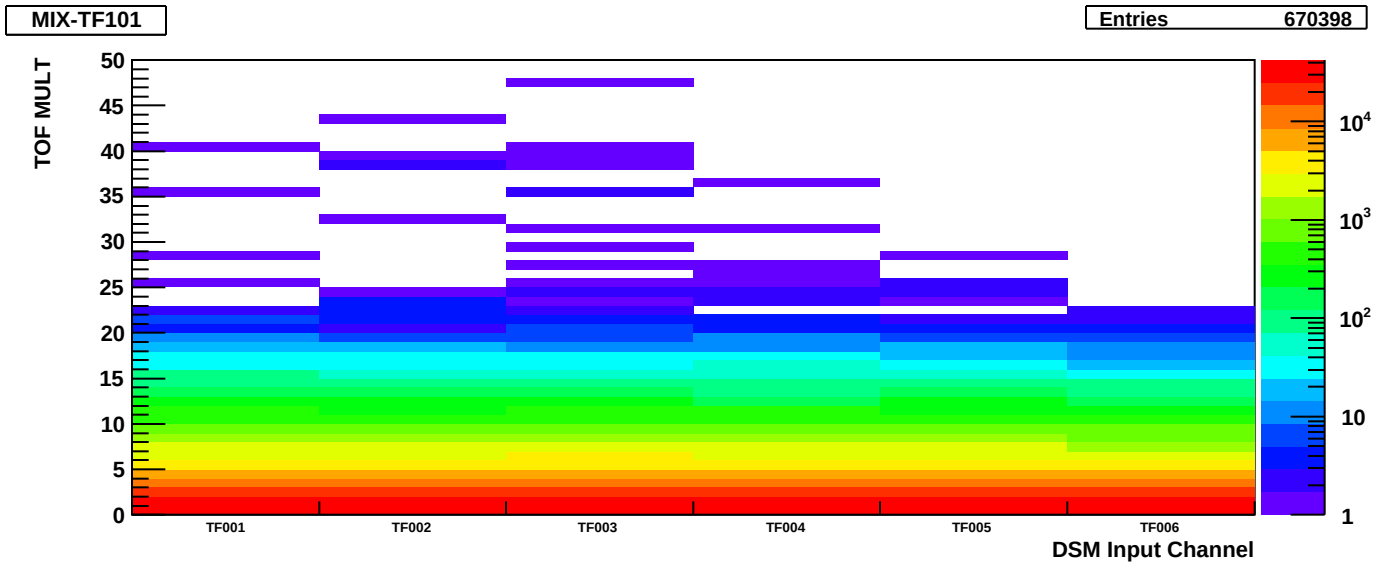

MIX-TF002
Entries 4692786
TOF MULT
TOF tray

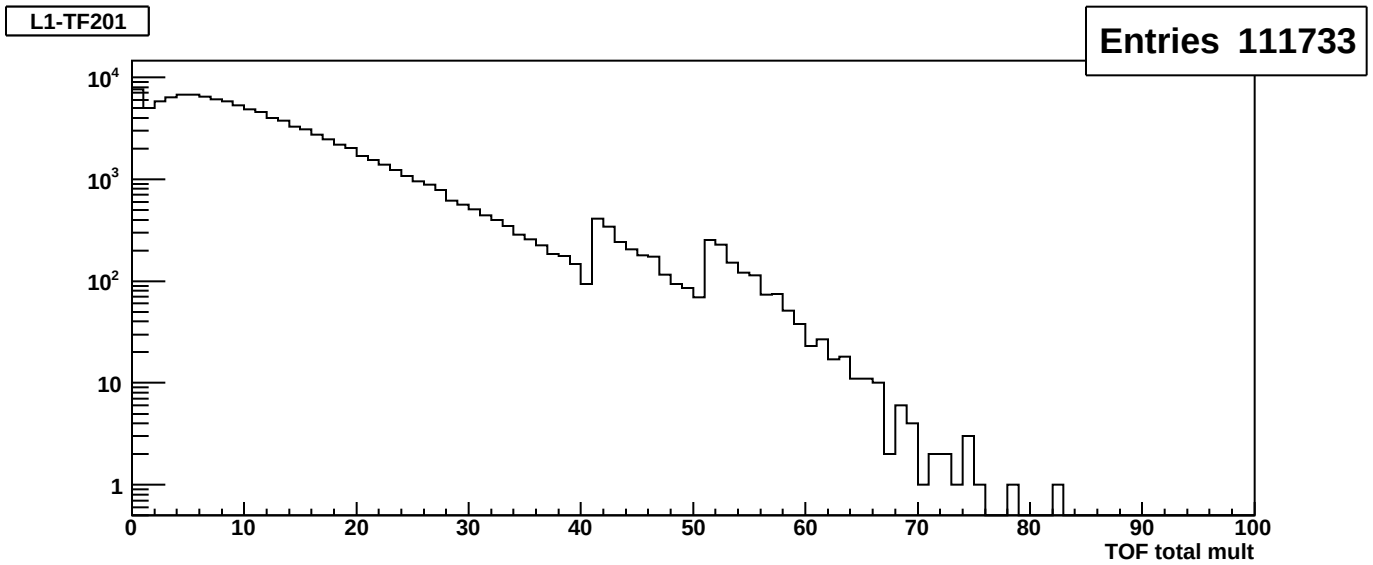

MIX-TF003
Entries 4692786
TOF MULT
TOF tray

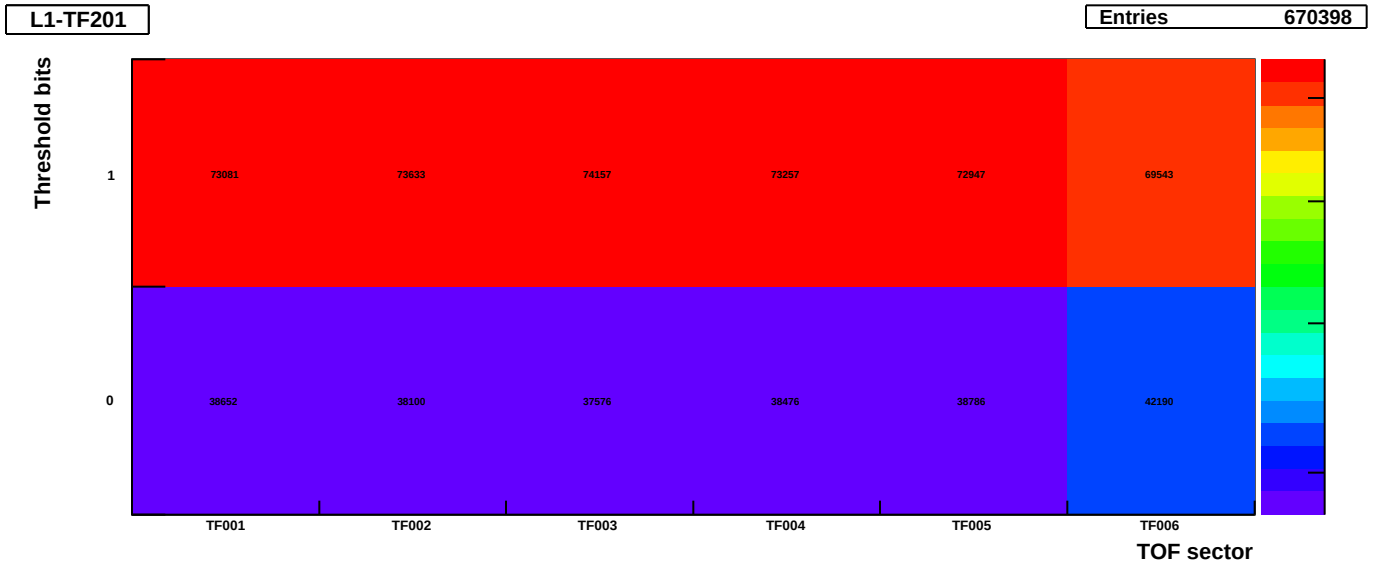

MIX-TF004
Entries 4692786
TOF MULT
TOF tray

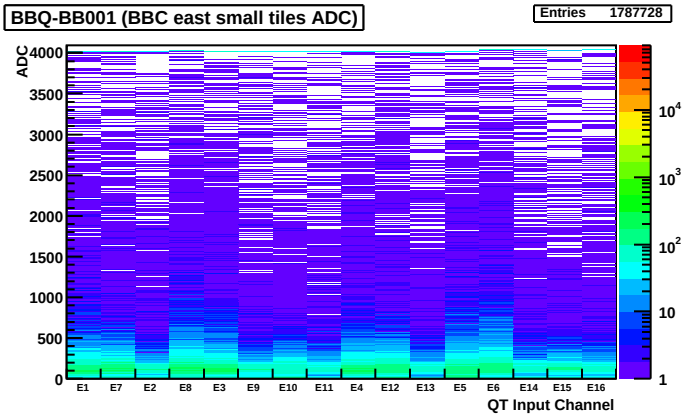

MIX-TF005
Entries 4692786
TOF MULT
TOF tray

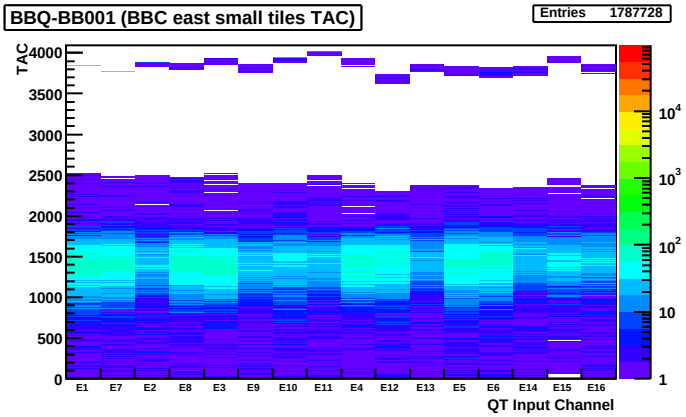

MIX-TF006
Entries 4692786
TOF MULT
TOF tray

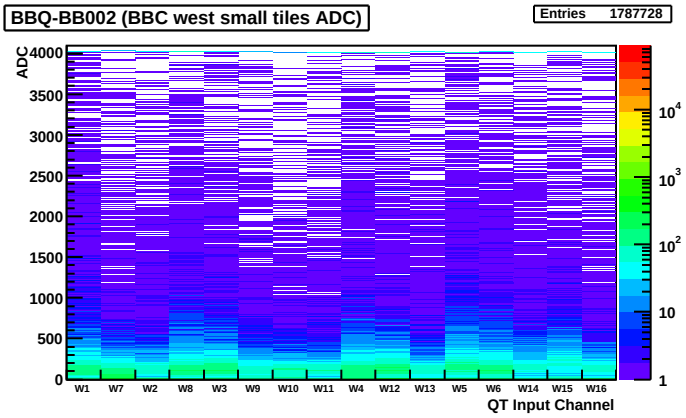

MIX-TF101
Entries 670398
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

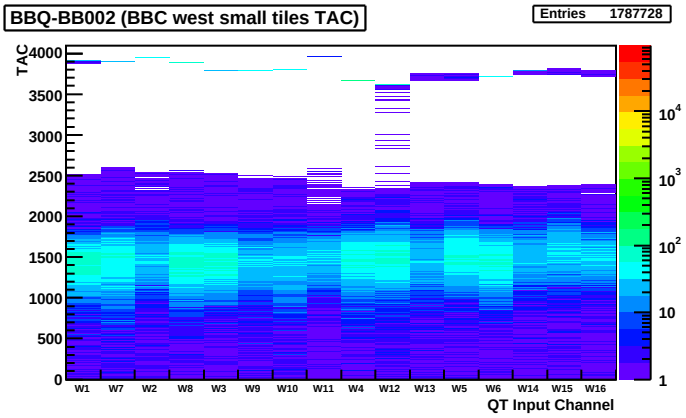

L1-TF201
Entries 111733
TOF total mult

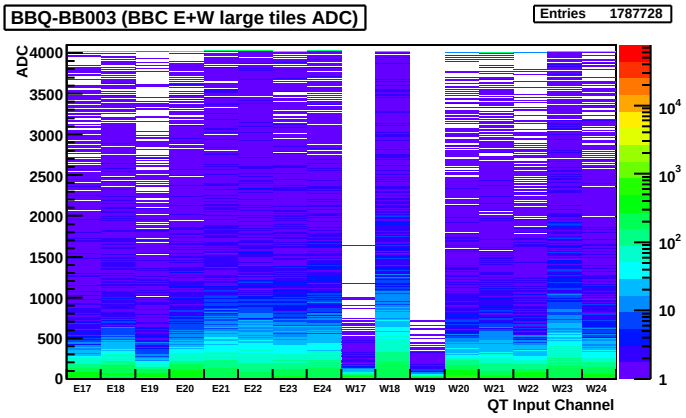

L1-TF201
Entries 670398
Threshold bits
73081
73633
74157
73257
72947
69543
38652
38100
37576
38476
38786
42190
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

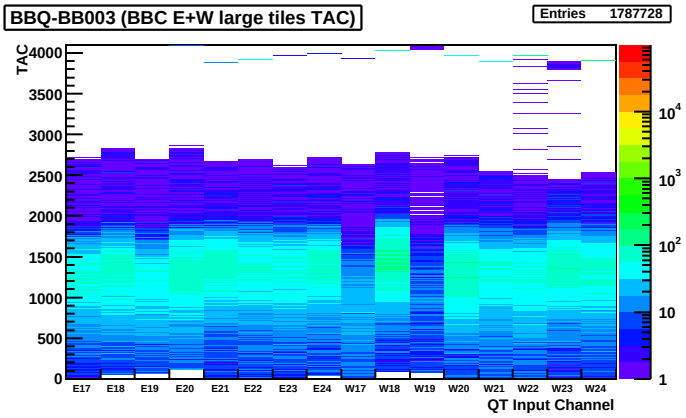

BBQ-BB001 (BBC east small tiles ADC)
Entries 1787728
ADC
QT Input Channel

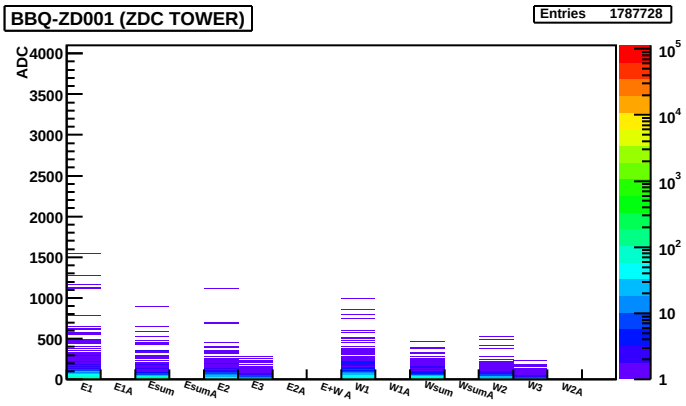

BBQ-BB001 (BBC east small tiles TAC)
Entries 1787728
TAC
QT Input Channel

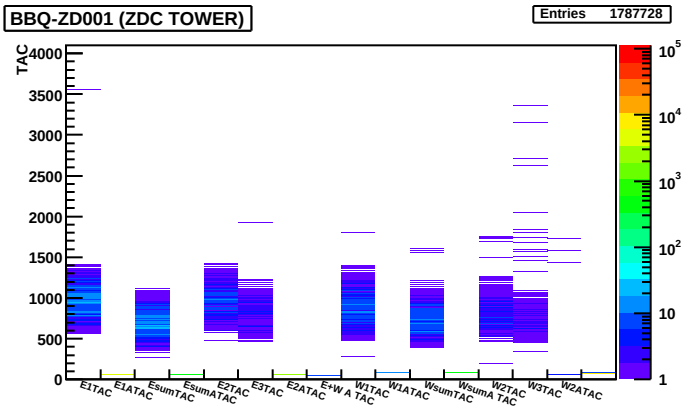

BBQ-BB002 (BBC west small tiles ADC)
Entries 1787728
ADC
QT Input Channel

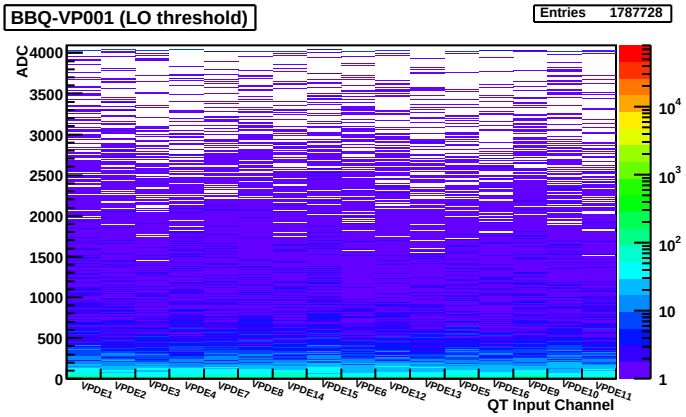

BBQ-BB002 (BBC west small tiles TAC)
Entries 1787728
TAC
QT Input Channel

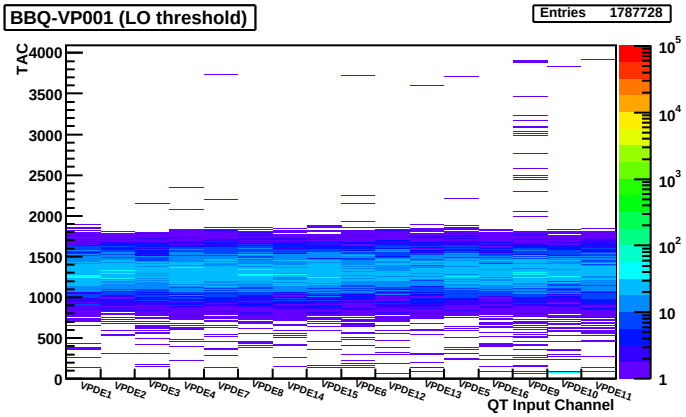

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1787728
ADC
QT Input Channel

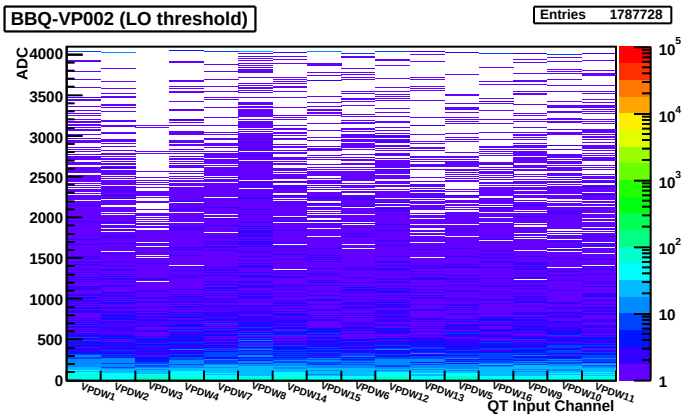

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1787728
TAC
QT Input Channel

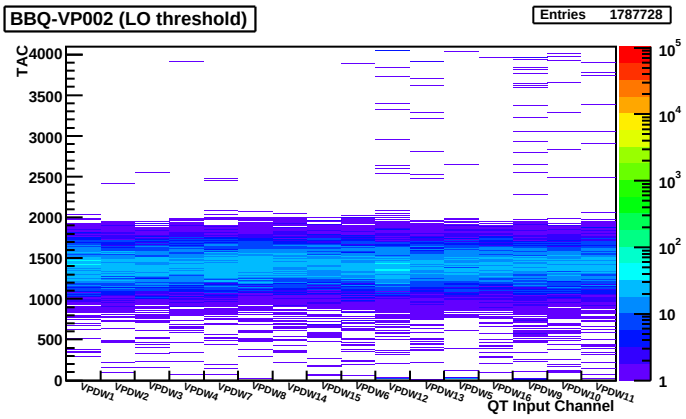

BBQ-ZD001 (ZDC TOWER)
Entries 1787728
ADC

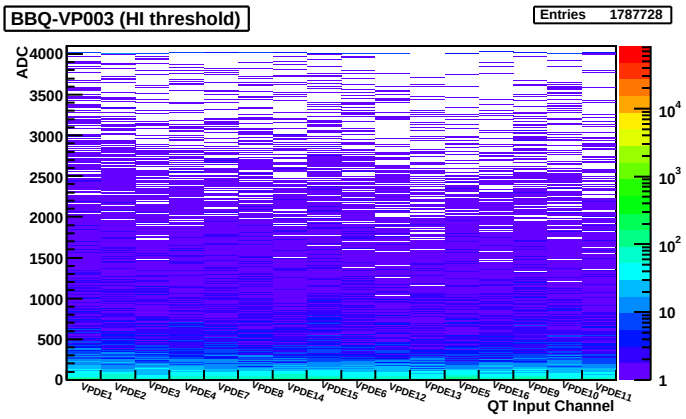

BBQ-ZD001 (ZDC TOWER)
Entries 1787728
TAC

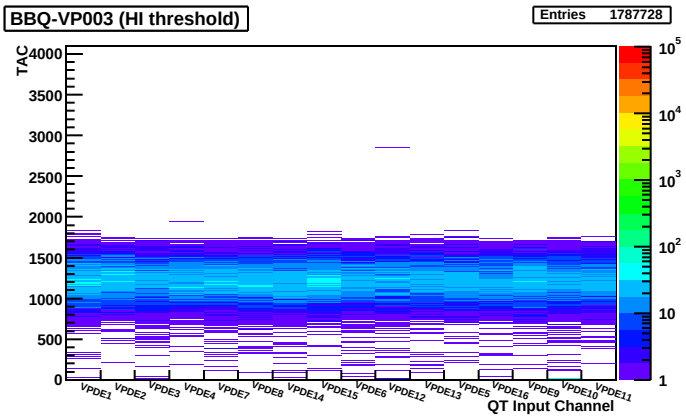

BBQ-VP001 (LO threshold)
Entries 1787728
ADC
QT Input Channel

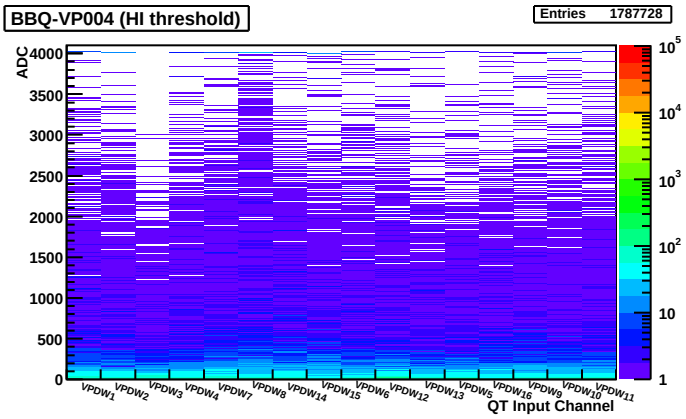

BBQ-VP001 (LO threshold)
Entries 1787728
TAC
QT Input Channel

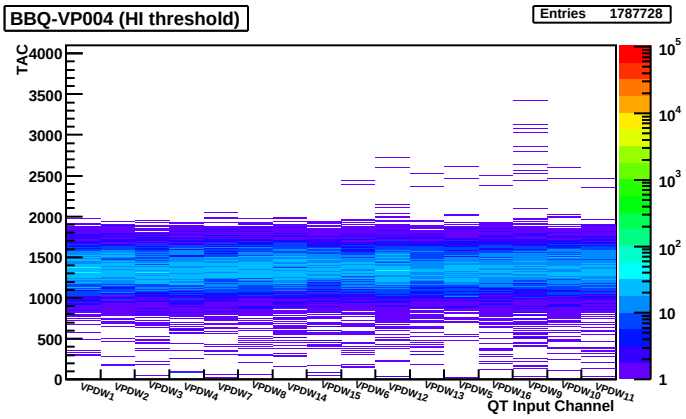

BBQ-VP002 (LO threshold)
Entries 1787728
ADC
QT Input Channel

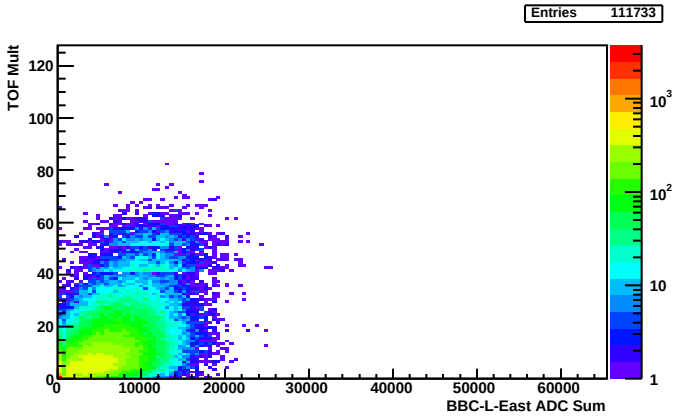

BBQ-VP002 (LO threshold)
Entries 1787728
TAC
QT Input Channel

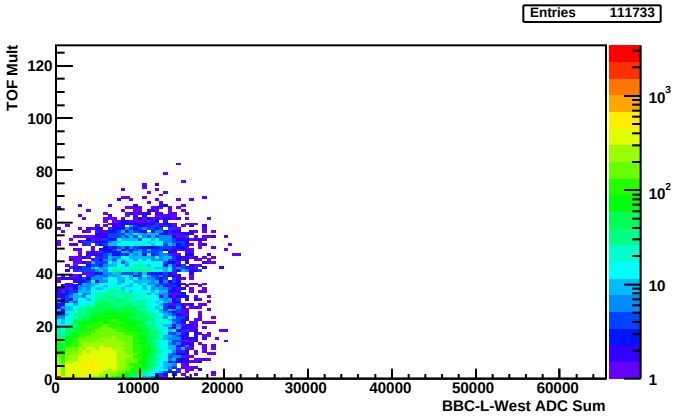

BBQ-VP003 (HI threshold)
Entries 1787728
ADC
QT Input Channel

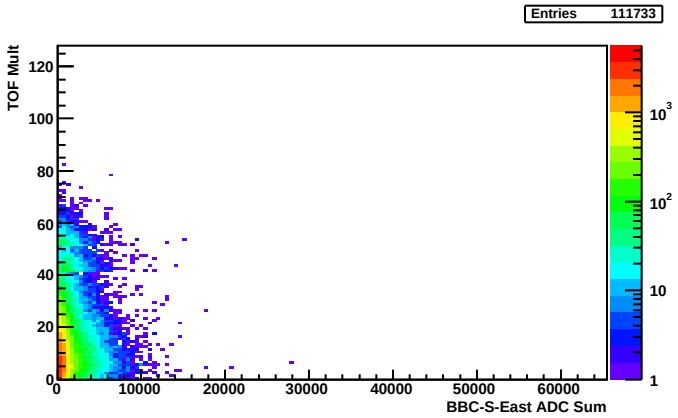

BBQ-VP003 (HI threshold)
Entries 1787728
TAC
QT Input Channel

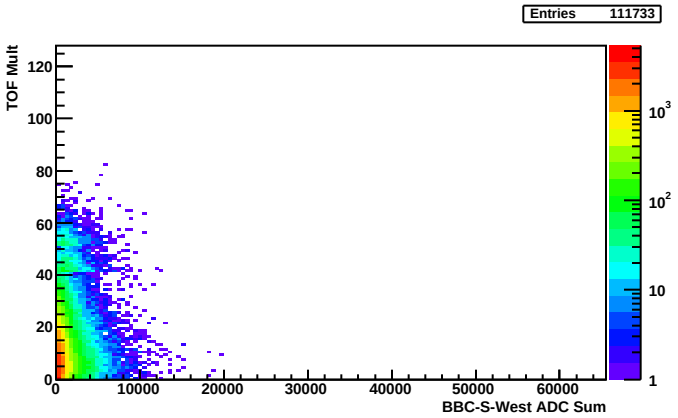

BBQ-VP004 (HI threshold)
Entries 1787728
ADC
QT Input Channel

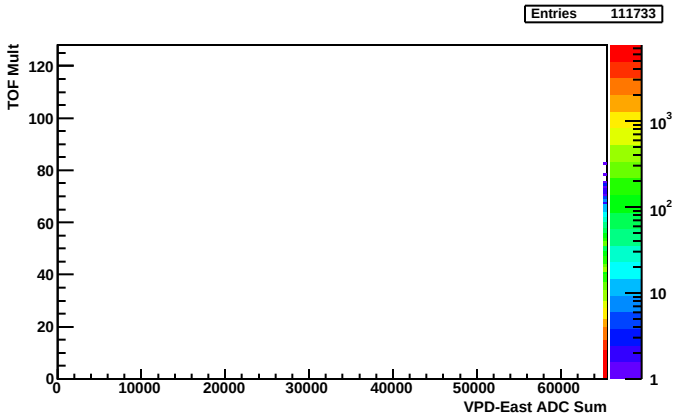

BBQ-VP004 (HI threshold)
Entries 1787728
TAC
QT Input Channel

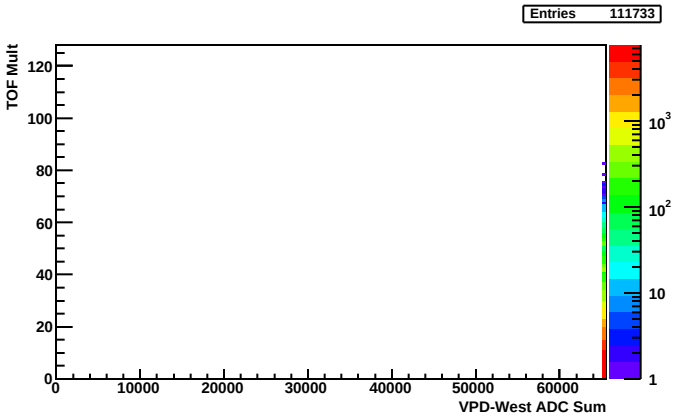

Entries 111733
TOF Mult
BBC-L-East ADC Sum

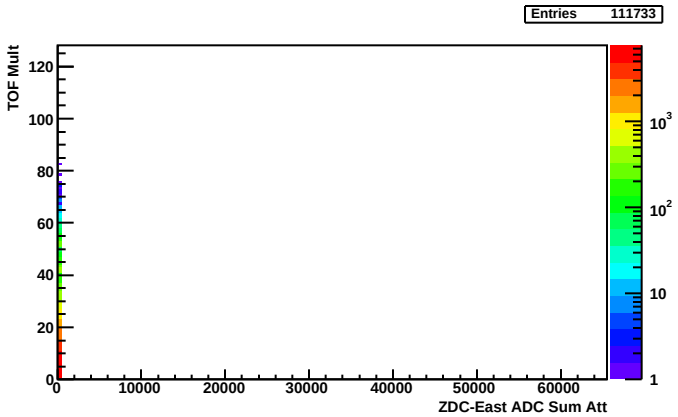

Entries 111733
TOF Mult
BBC-L-West ADC Sum

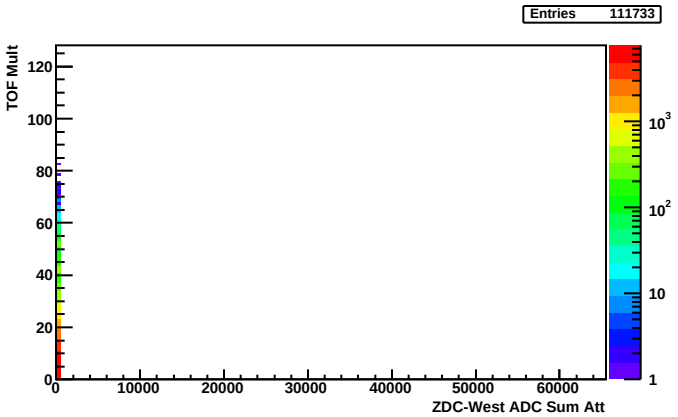

Entries 111733
TOF Mult
BBC-S-East ADC Sum

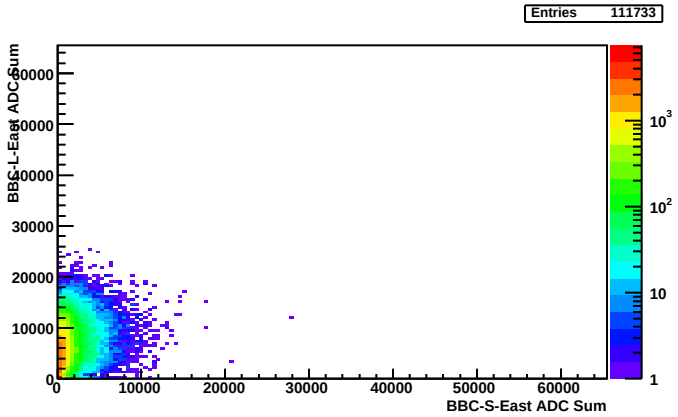

Entries 111733
TOF Mult
BBC-S-West ADC Sum

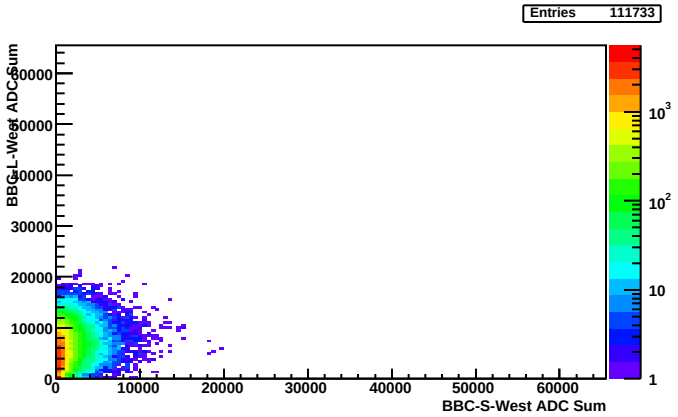

Entries 111733
TOF Mult
VPD-East ADC Sum

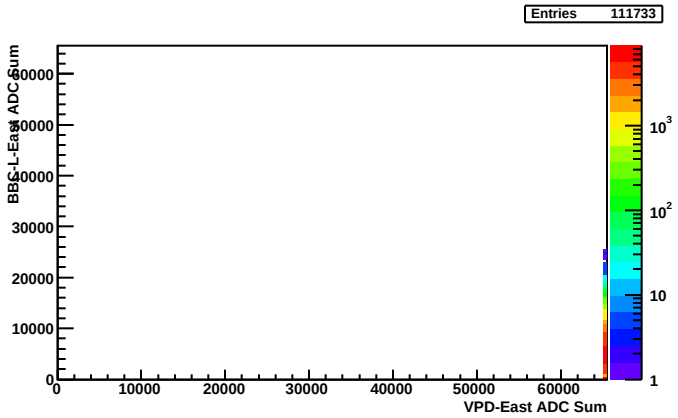

Entries 111733
TOF Mult
VPD-West ADC Sum

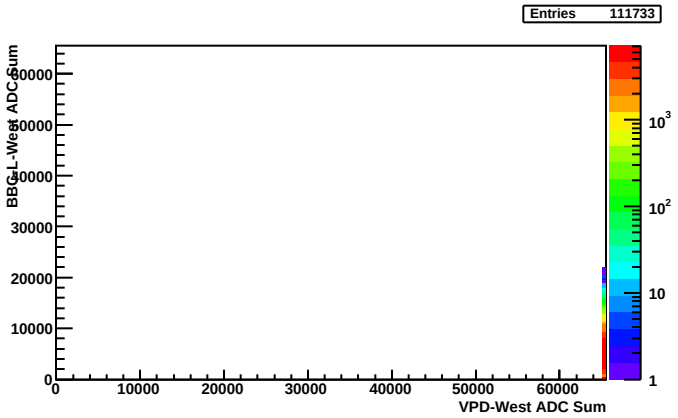

Entries 111733
TOF Mult
ZDC-East ADC Sum Att

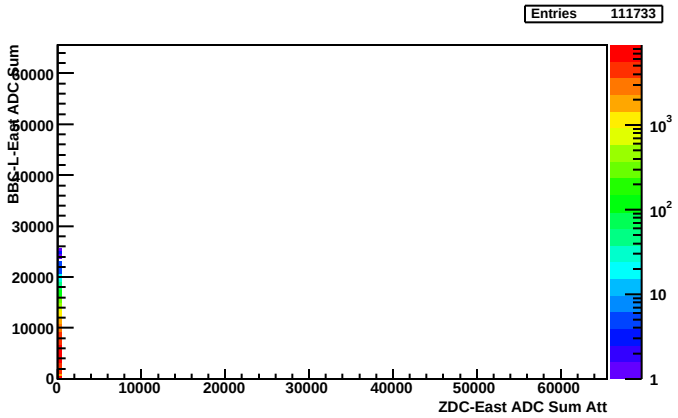

Entries 111733
TOF Mult
ZDC-West ADC Sum Att

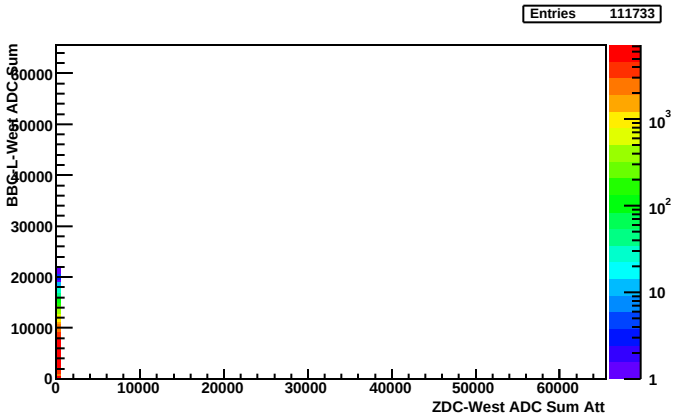

Entries 111733
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 111733
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 111733
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 111733
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 111733
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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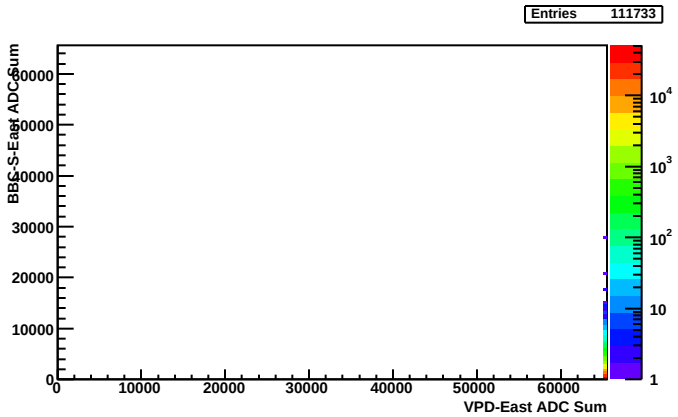
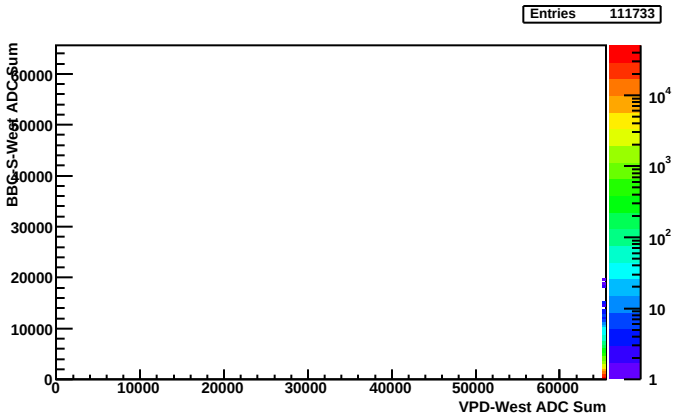
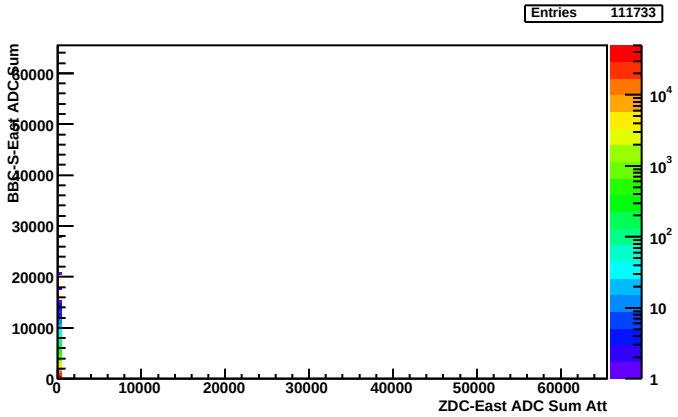
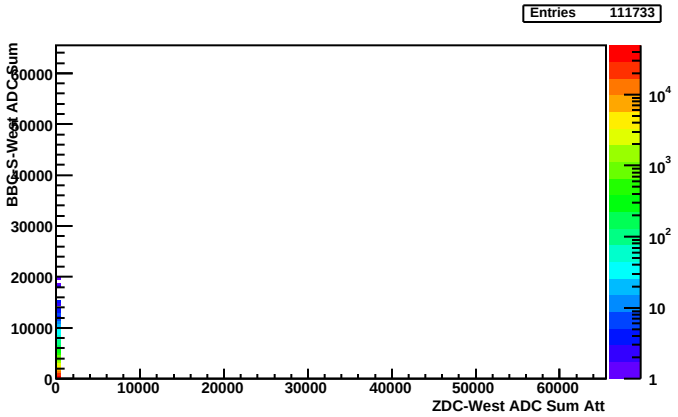
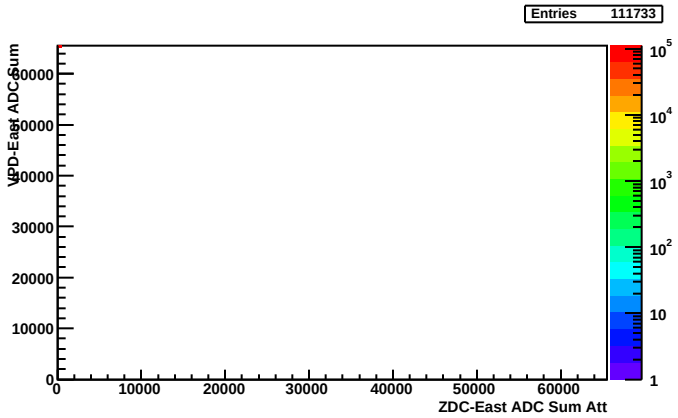
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

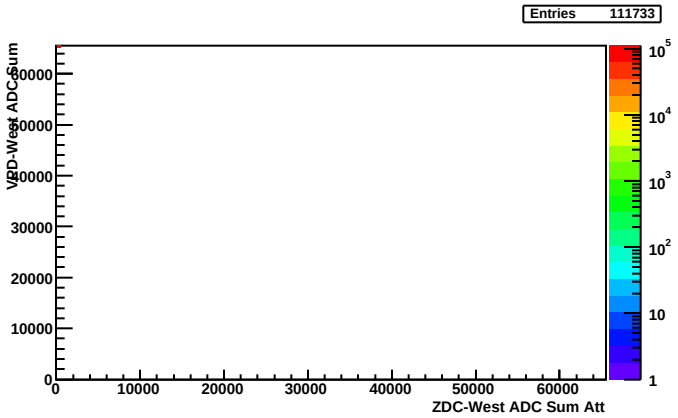

Entries 111733
BBC-S-East ADC Sum
VPD-East ADC Sum

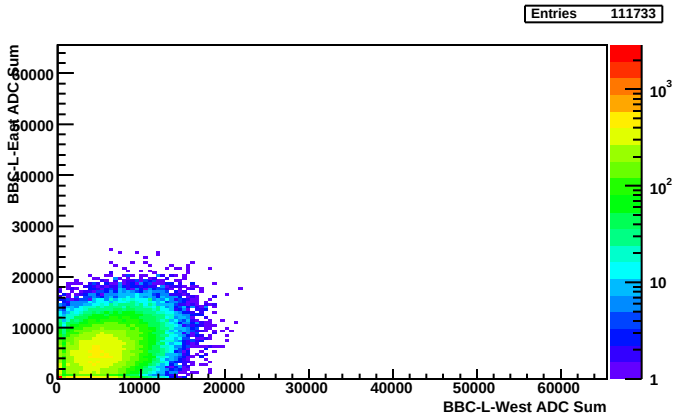

Entries 111733
BBC-S-West ADC Sum
VPD-West ADC Sum

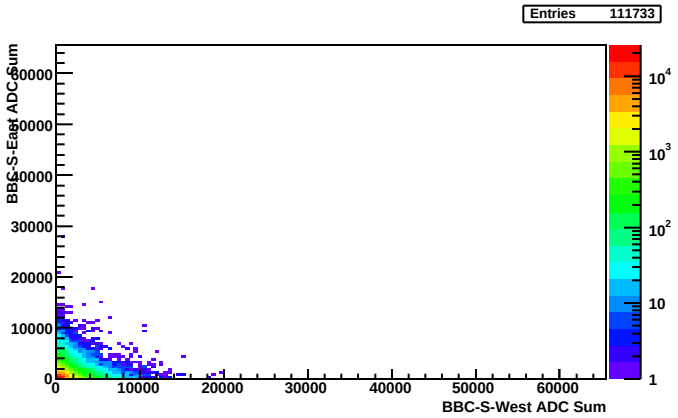

Entries 111733
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

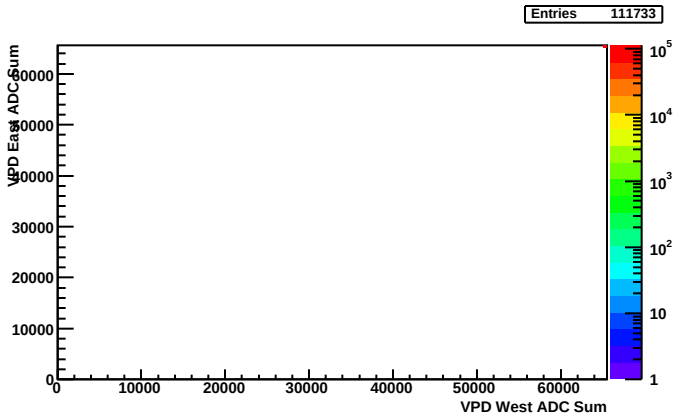

Entries 111733
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

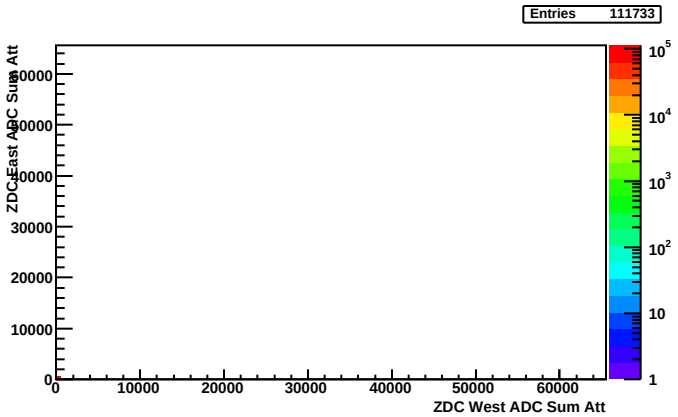

Entries 111733
VPD-East ADC Sum
ZDC-East ADC Sum Att

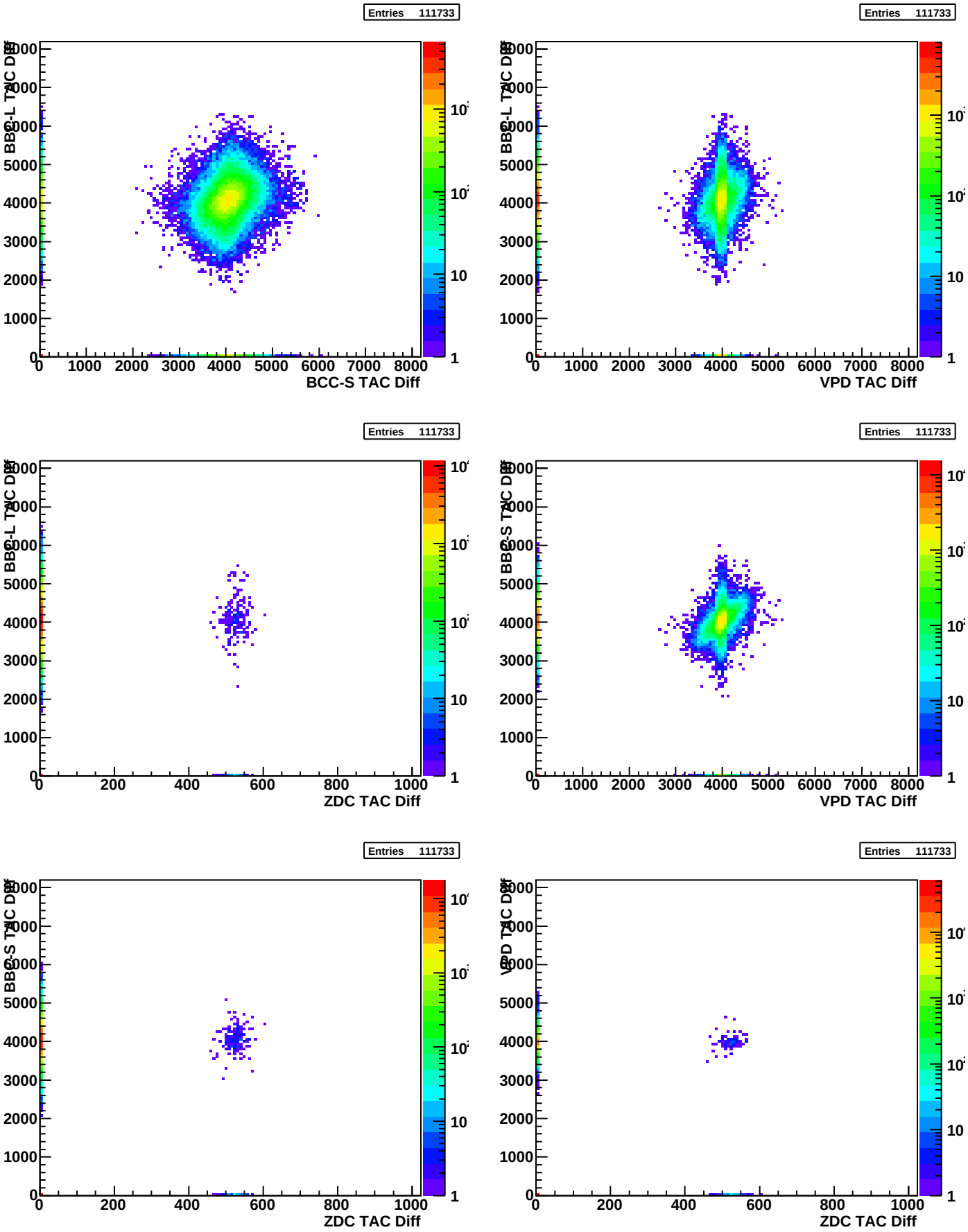

Entries 111733
VPD-West ADC Sum
ZDC-West ADC Sum Att

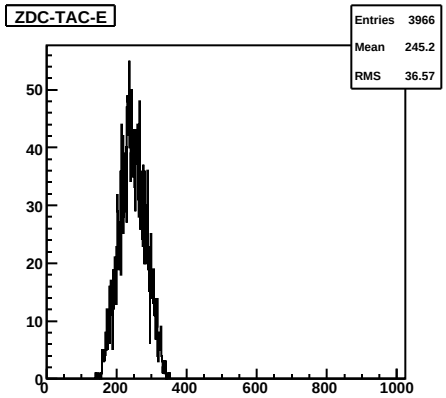

Entries 111733
BBC-L-East ADC Sum
BBC-L-West ADC Sum

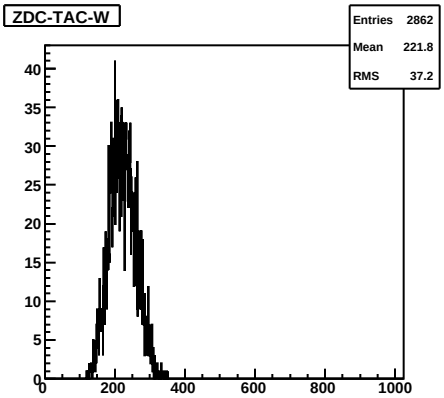

Entries 111733
BBC-S-East ADC Sum
BBC-S-West ADC Sum

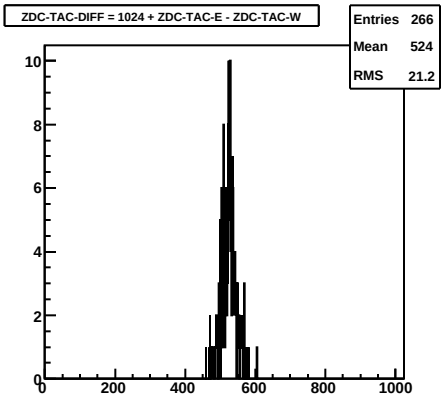

Entries 111733
VPD East ADC Sum
VPD West ADC Sum

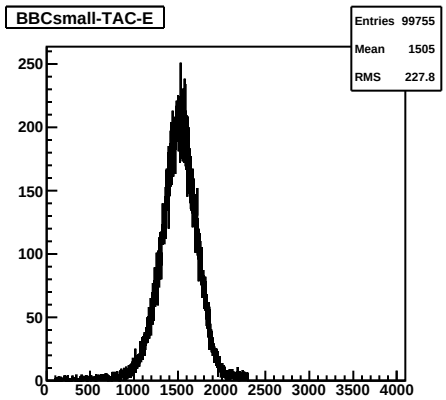

Entries 111733
ZDC East ADC Sum Att
ZDC West ADC Sum Att

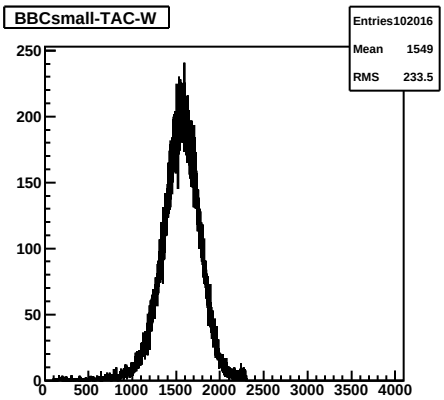

Entries 111733
BBC-L TAC Diff
BCC-S TAC Diff
Entries 111733
BBC-L TAC Diff
VPD TAC Diff
Entries 111733
BBC-L TAC Diff
ZDC TAC Diff
Entries 111733
BBC-S TAC Diff
VPD TAC Diff
Entries 111733
BBC-S TAC Diff
ZDC TAC Diff
Entries 111733
VPD TAC Diff
ZDC TAC Diff

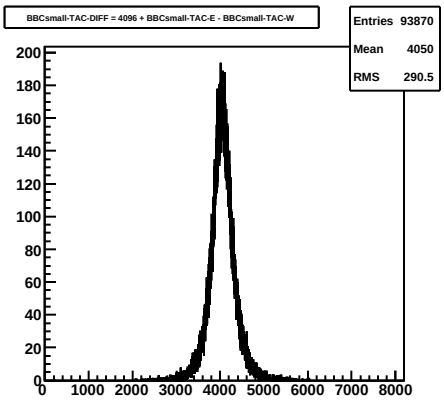

ZDC-TAC-E
Entries 3966
Mean 245.2
RMS 36.57

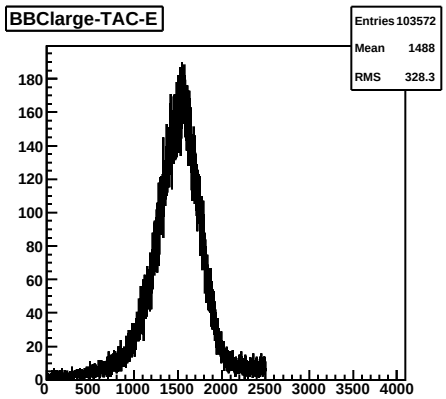

ZDC-TAC-W
Entries 2862
Mean 221.8
RMS 37.2

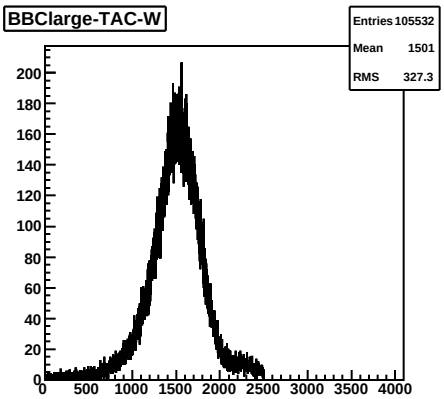

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 266
Mean 524
RMS 21.2

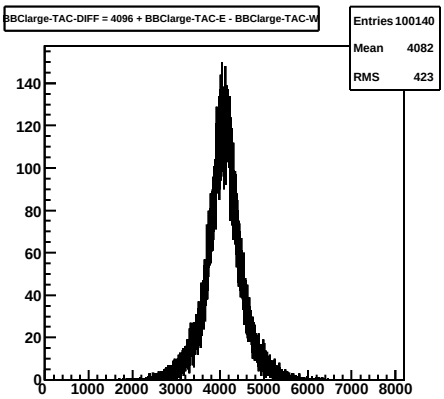

BBCsmall-TAC-E
Entries 99755
Mean 1505
RMS 227.8

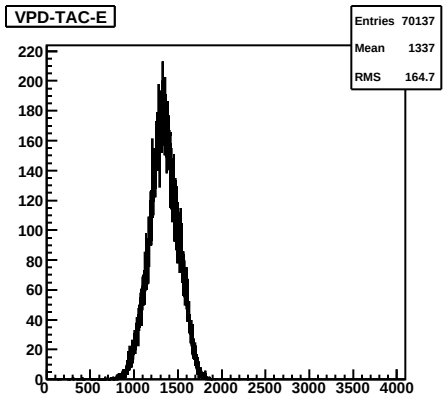

BBCsmall-TAC-W
Entries 102016
Mean 1549
RMS 233.5

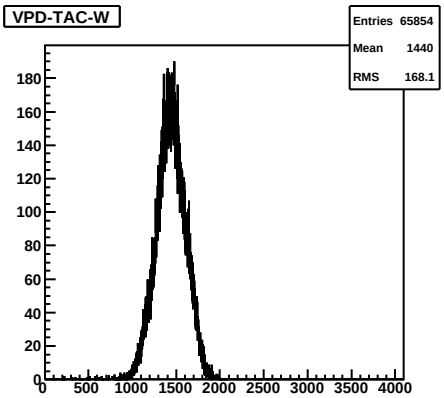

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 93870
Mean 4050
RMS 290.5

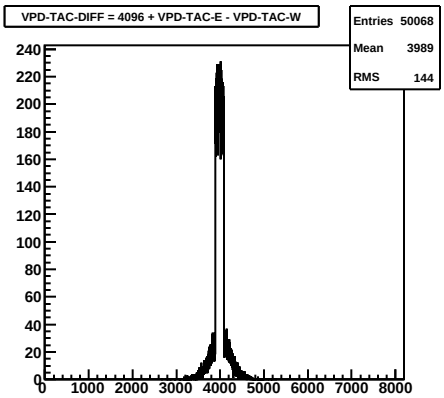

BBClarge-TAC-E
Entries 103572
Mean 1488
RMS 328.3

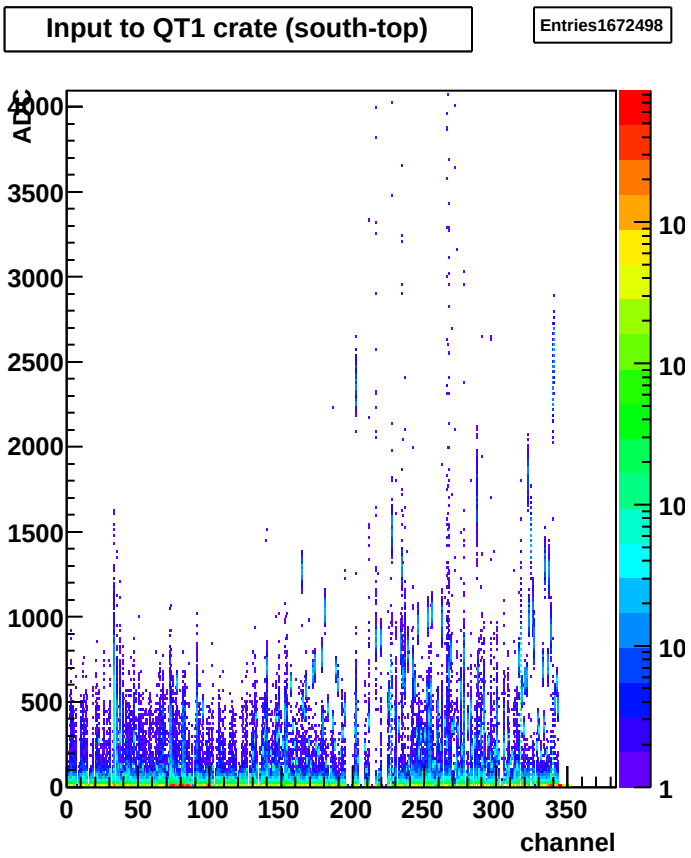

BBClarge-TAC-W
Entries 105532
Mean 1501
RMS 327.3

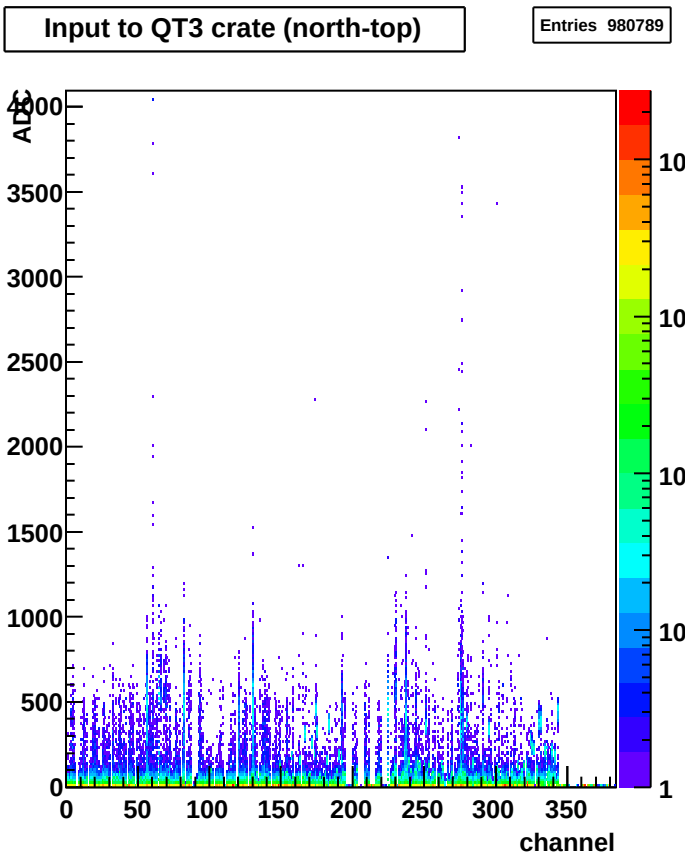

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 100140
Mean 4082
RMS 423

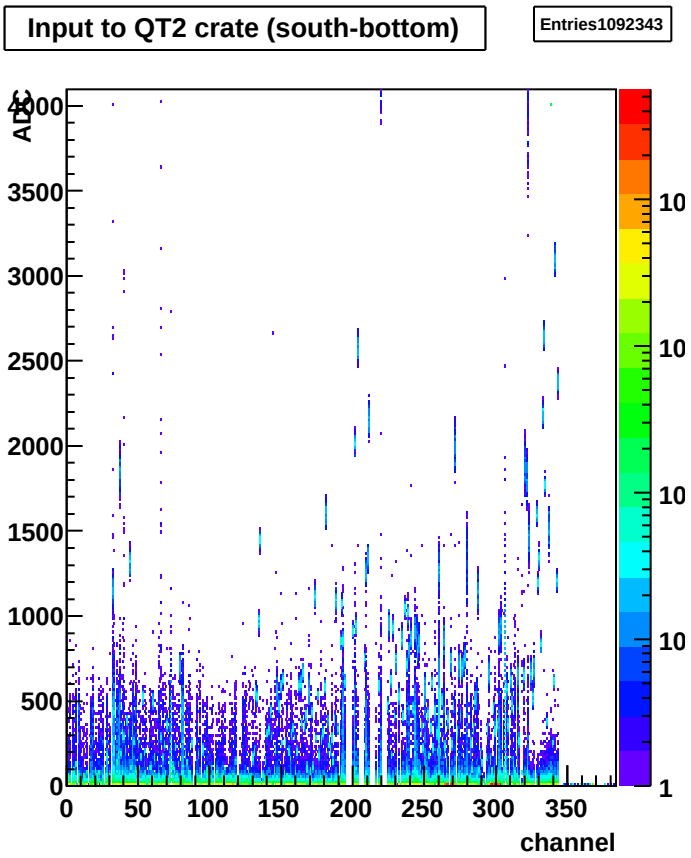

VPD-TAC-E
Entries 70137
Mean 1337
RMS 164.7

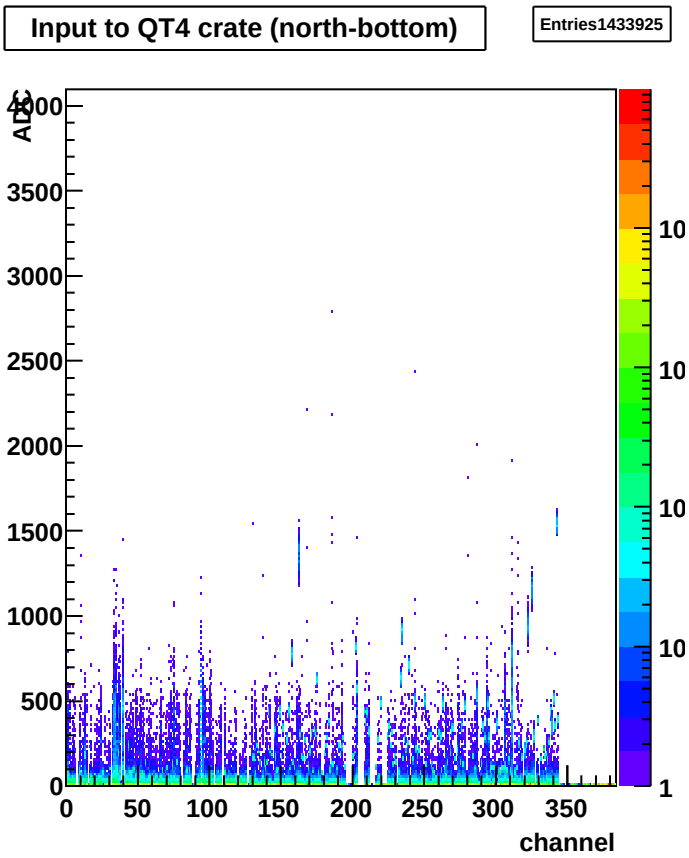

VPD-TAC-W
Entries 65854
Mean 1440
RMS 168.1

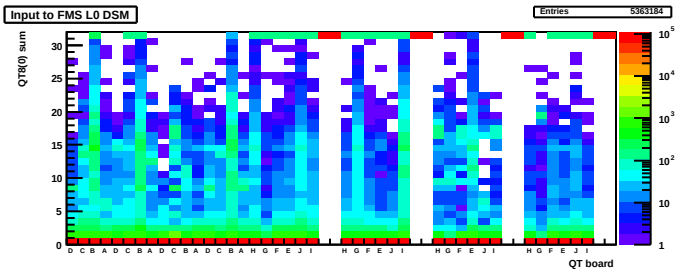

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 50068
Mean 3989
RMS 144

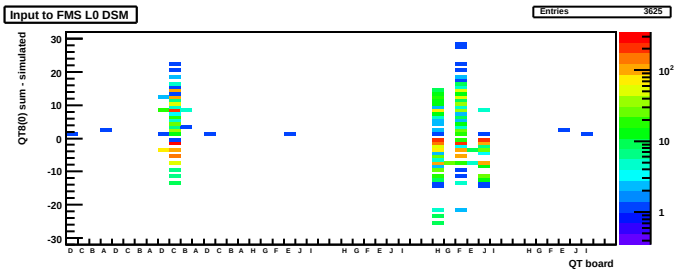

Input to QT1 crate (south-top)
Entries1672498
ADC
channel

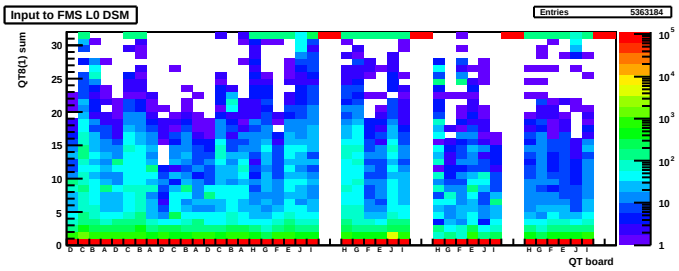

Input to QT3 crate (north-top)
Entries 980789
ADC
channel

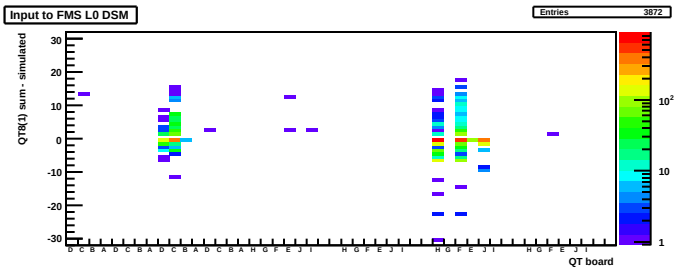

Input to QT2 crate (south-bottom)
Entries1092343
ADC
channel

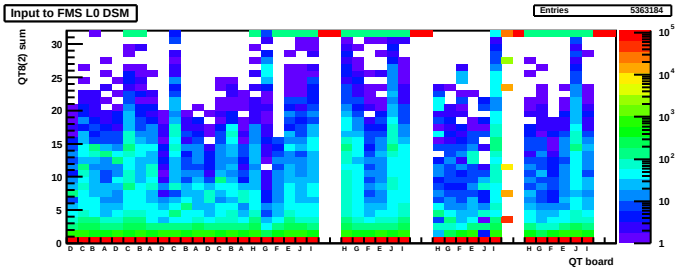

Input to QT4 crate (north-bottom)
Entries1433925
ADC
channel

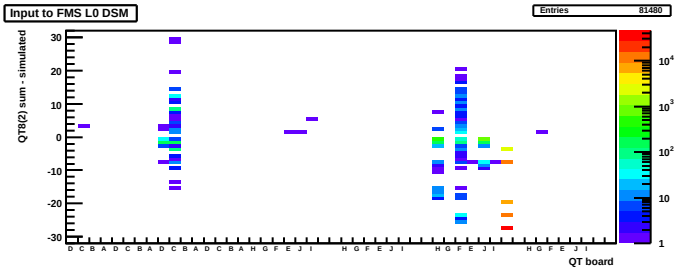

Input to FMS L0 DSM
Entries 5363184
QT8(0) sum
QT board

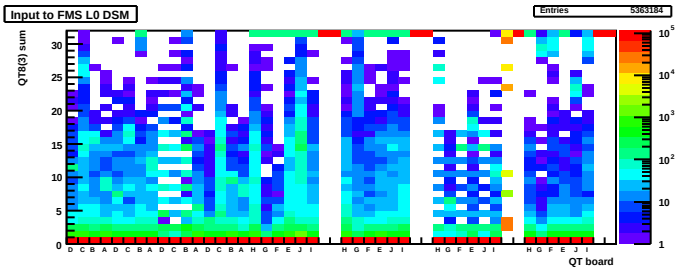

Input to FMS L0 DSM
Entries 3625
QT8(0) sum - simulated
QT board

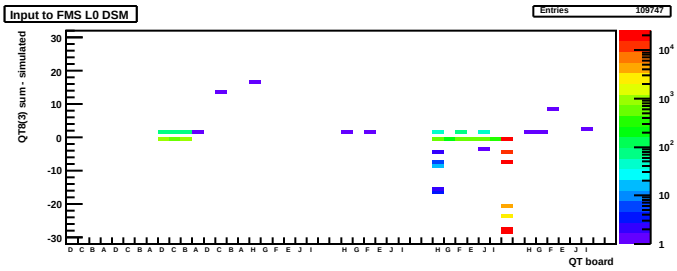

Input to FMS L0 DSM
Entries 5363184
QT8(1) sum
QT board

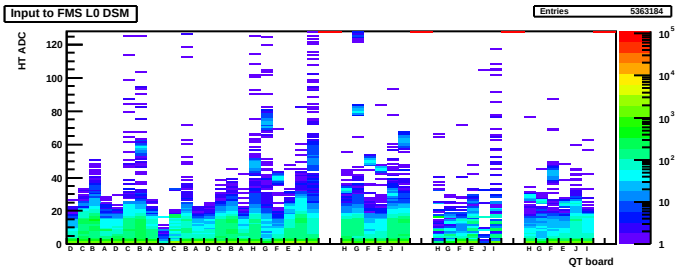

Input to FMS L0 DSM
Entries 3872
QT8(1) sum - simulated
QT board

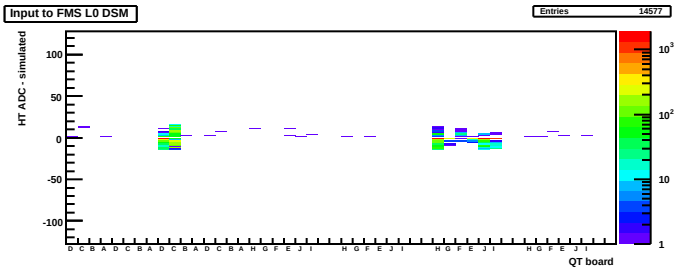

Input to FMS L0 DSM
Entries 5363184
QT8(2) sum
QT board

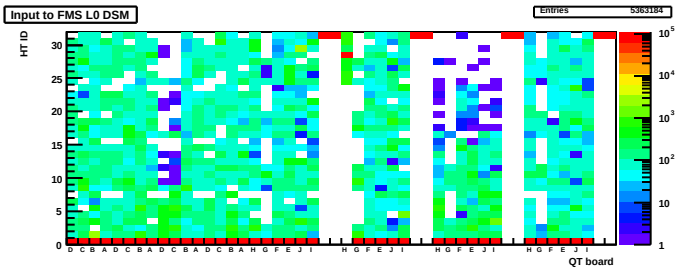

Input to FMS L0 DSM
Entries 81480
QT8(2) sum - simulated
QT board

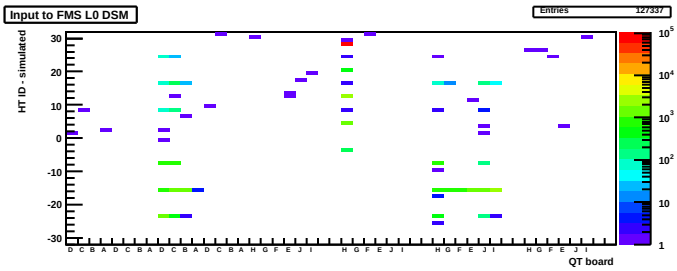

Input to FMS L0 DSM
Entries 5363184
QT8(3) sum
QT board

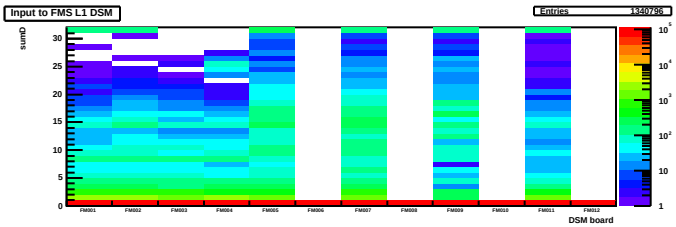

Input to FMS L0 DSM
Entries 109747
QT8(3) sum - simulated
QT board

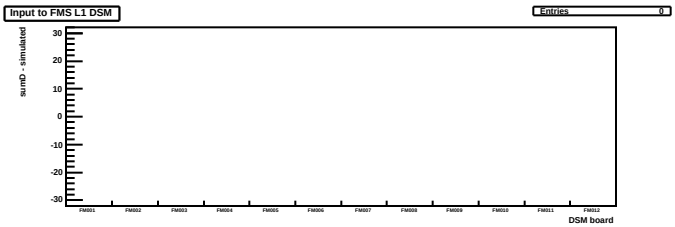

Input to FMS L0 DSM
Entries 5363184
HT ADC
QT board

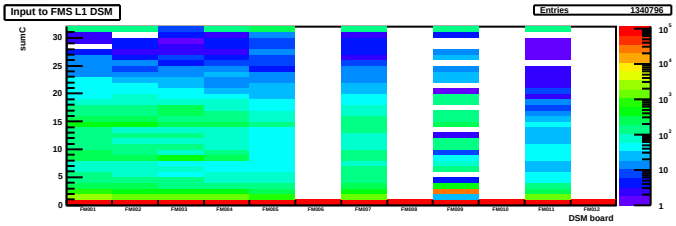

Input to FMS L0 DSM
Entries 14577
HT ADC - simulated
QT board

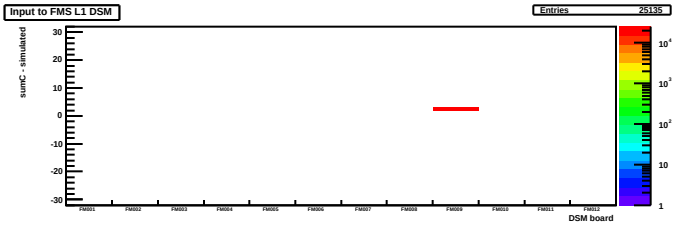

Input to FMS L0 DSM
Entries 5363184
HT ID
QT board

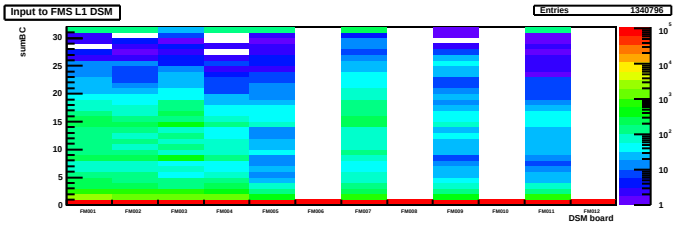

Input to FMS L0 DSM
Entries 127337
HT ID - simulated
QT board

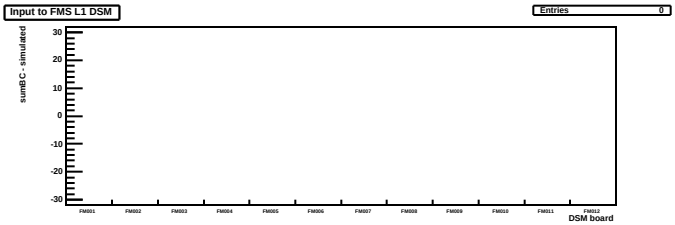

Input to FMS L1 DSM
Entries 1340796
sumD
DSM board

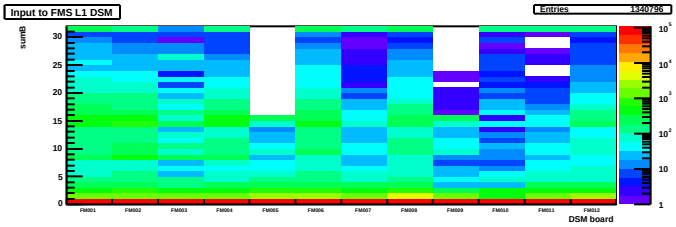

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

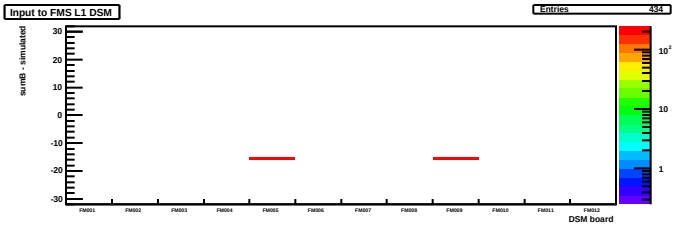

Input to FMS L1 DSM
Entries 1340796
sumC
DSM board

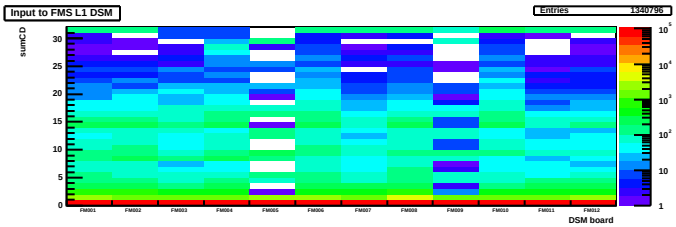

Input to FMS L1 DSM
Entries 25135
sumC - simulated
DSM board

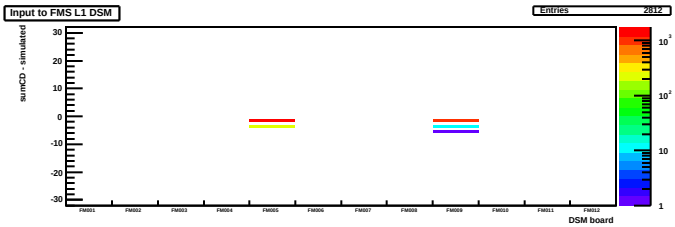

Input to FMS L1 DSM
Entries 1340796
sumBC
DSM board

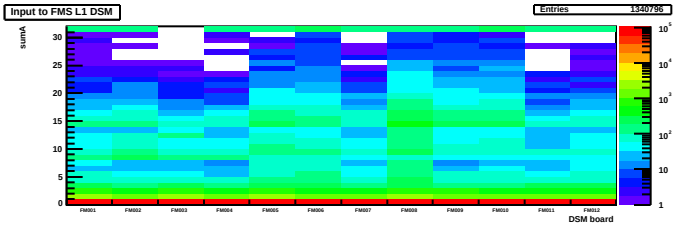

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

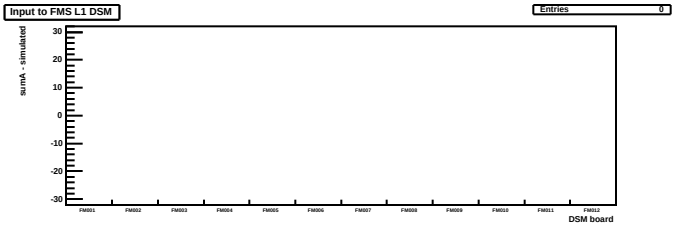

Input to FMS L1 DSM
Entries 1340796
sumB
DSM board

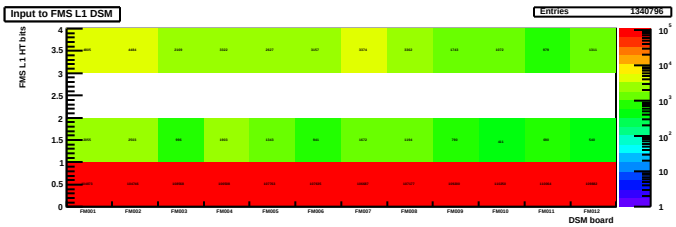

Input to FMS L1 DSM
Entries 434
sumB - simulated
DSM board

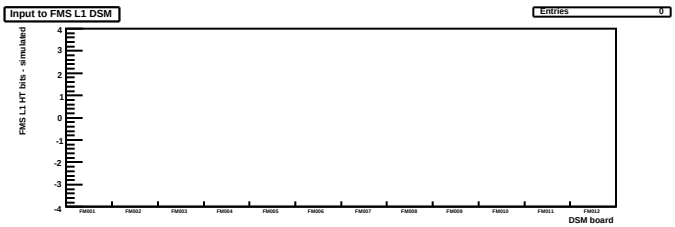

Input to FMS L1 DSM
Entries 1340796
sumCD
DSM board

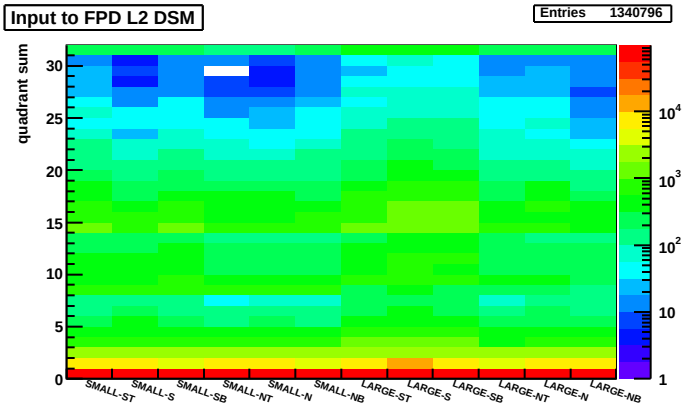

Input to FMS L1 DSM
Entries 2812
sumCD - simulated
DSM board

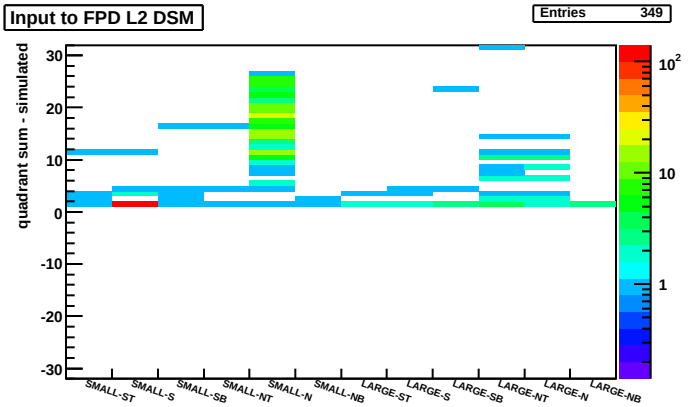

Input to FMS L1 DSM
Entries 1340796
sumA
DSM board

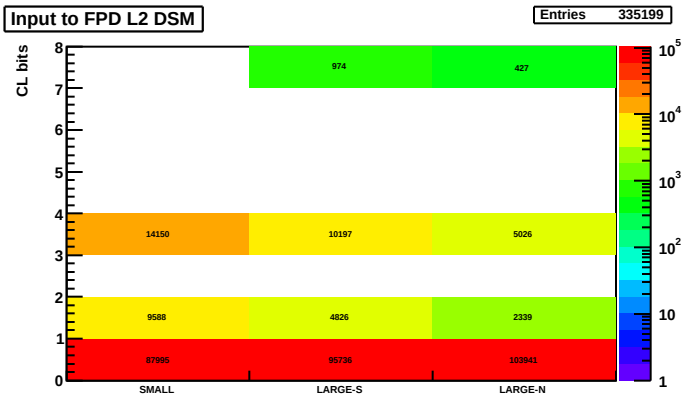

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

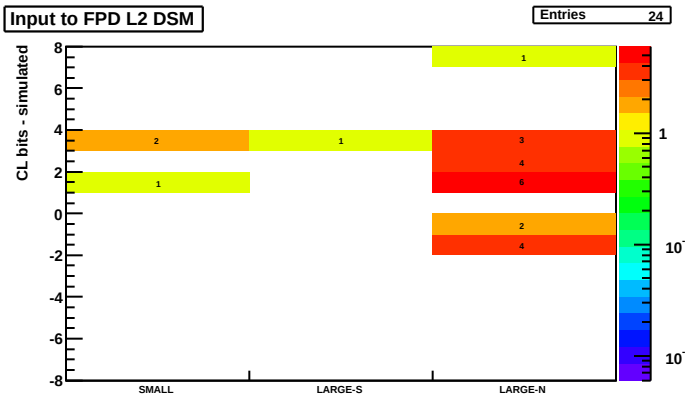

Input to FMS L1 DSM
Entries 1340796
FMS L1 HT bits
DSM board

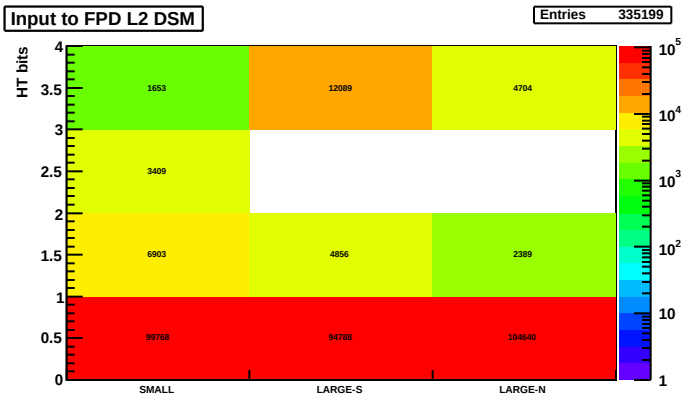

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

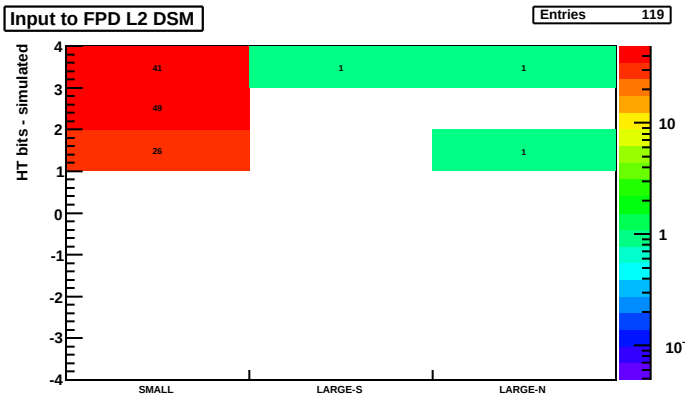

Input to FPD L2 DSM
Entries 1340796
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

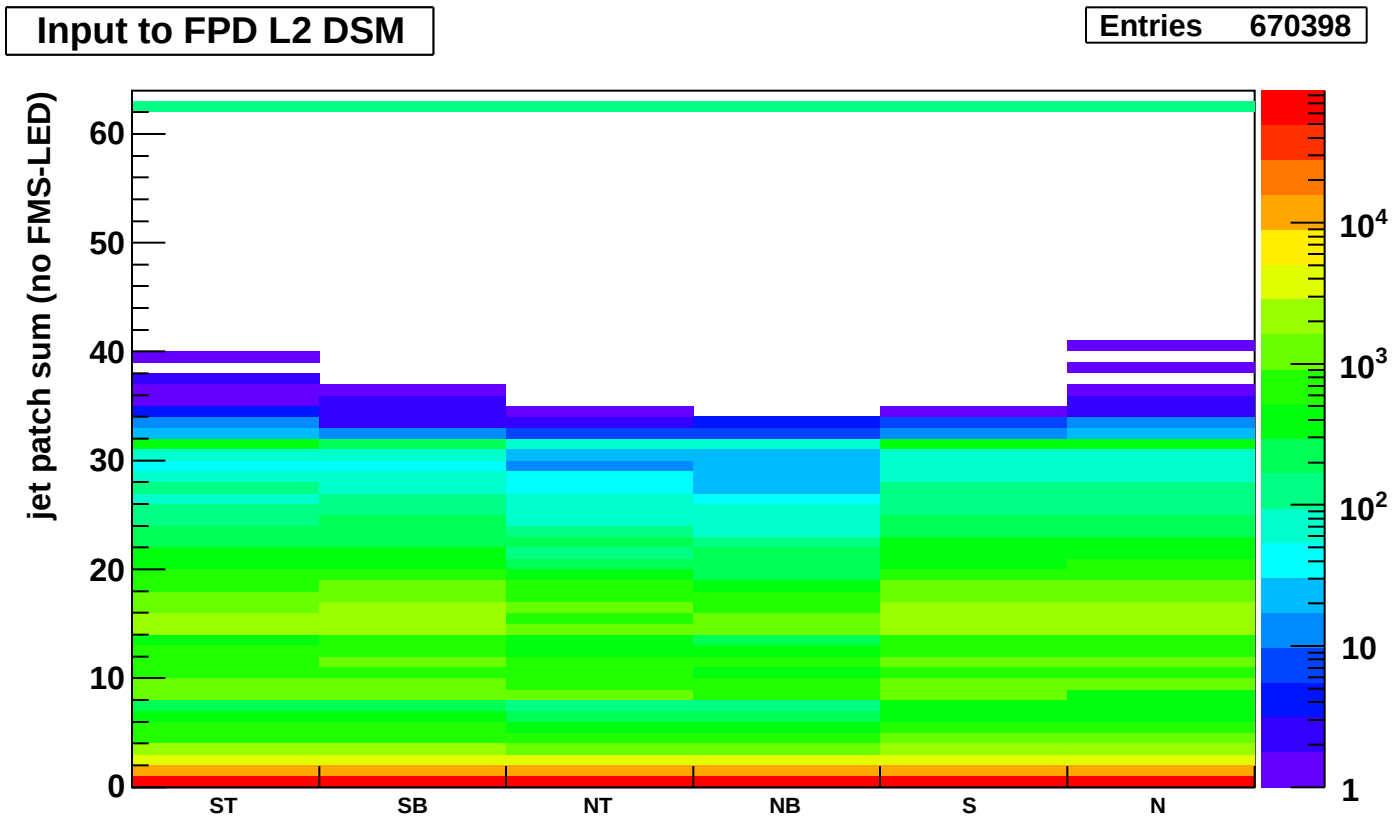

Input to FPD L2 DSM
Entries 349
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

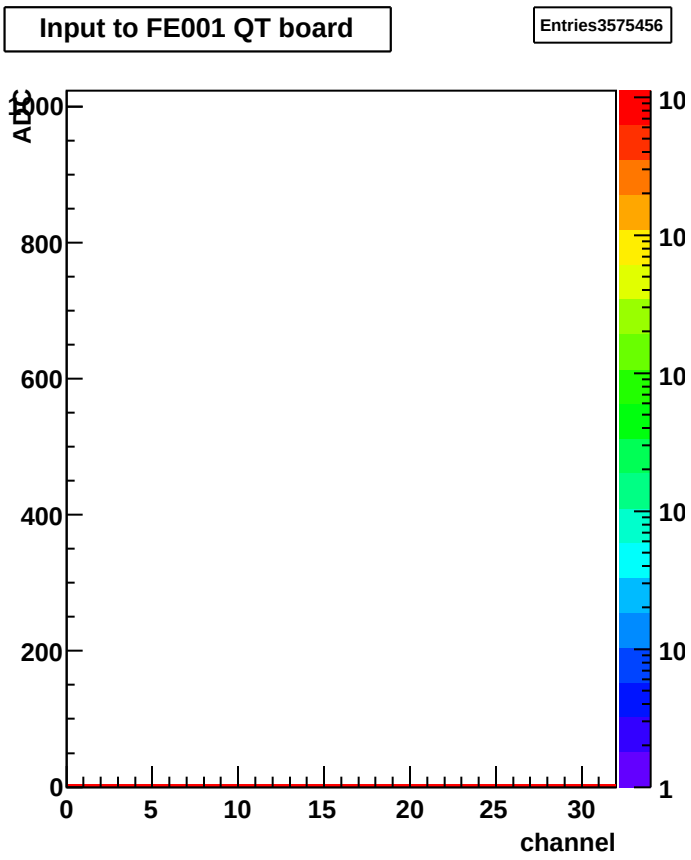

Input to FPD L2 DSM
Entries 335199
CL bits
974
427
14150
10197
5026
9588
4826
2339
87995
95736
103941
SMALL
LARGE-S
LARGE-N

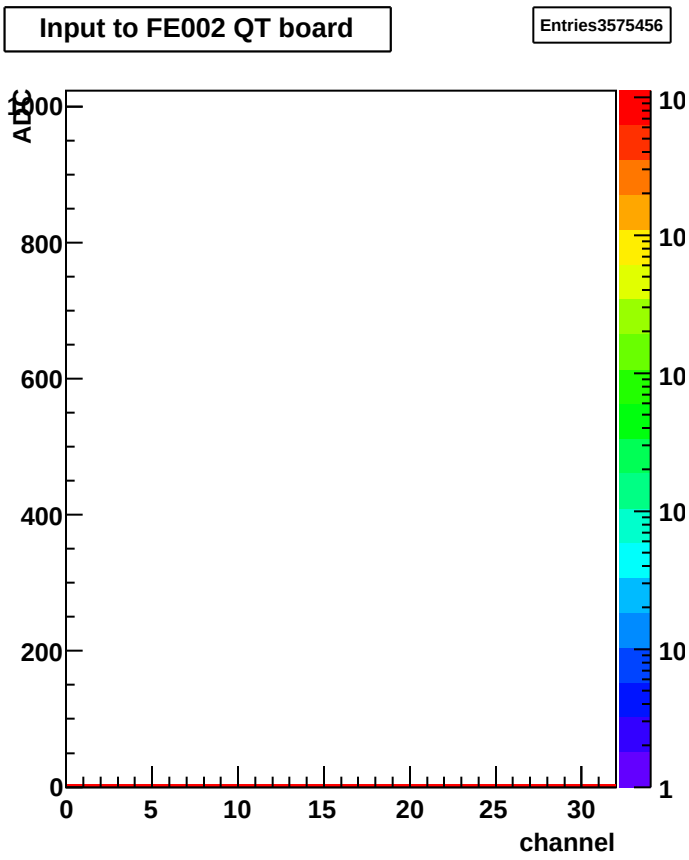

Input to FPD L2 DSM
Entries 24
CL bits - simulated
SMALL
LARGE-S
LARGE-N

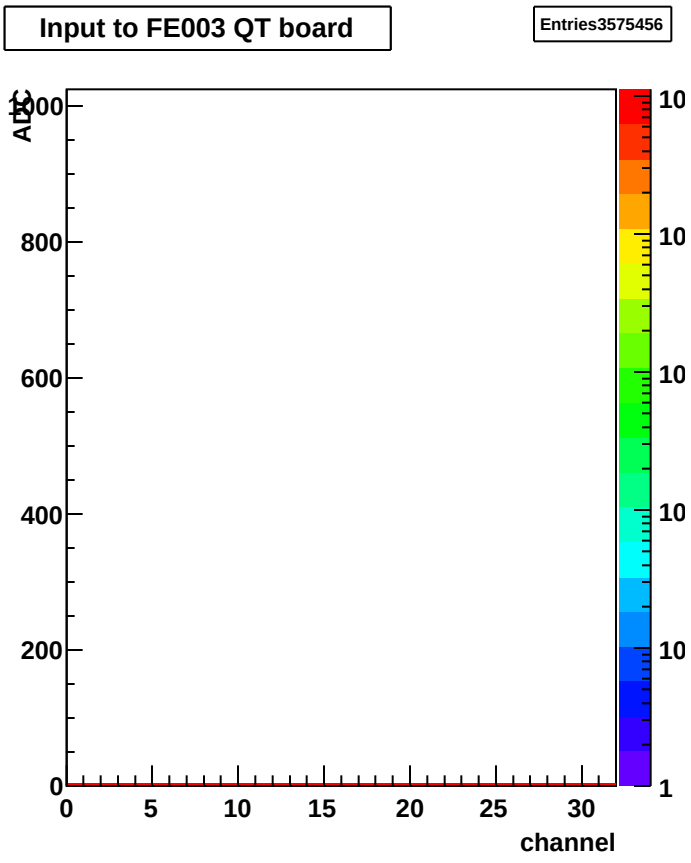

Input to FPD L2 DSM
Entries 335199
HT bits
1653
12089
4704
3409
6903
4856
2389
99768
94788
104640
SMALL
LARGE-S
LARGE-N

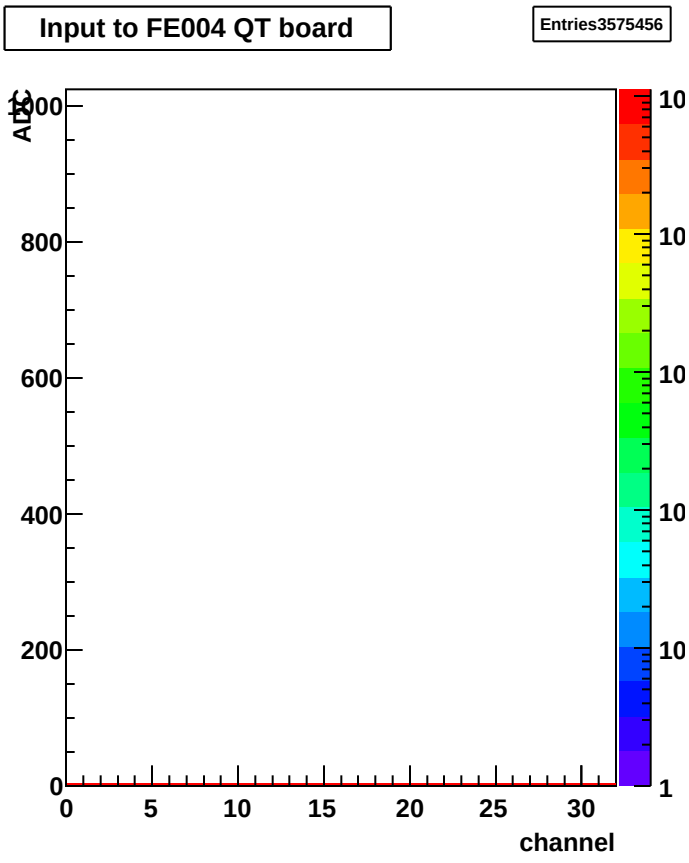

Input to FPD L2 DSM
Entries 119
HT bits - simulated
41
1
1
49
26
1
SMALL
LARGE-S
LARGE-N

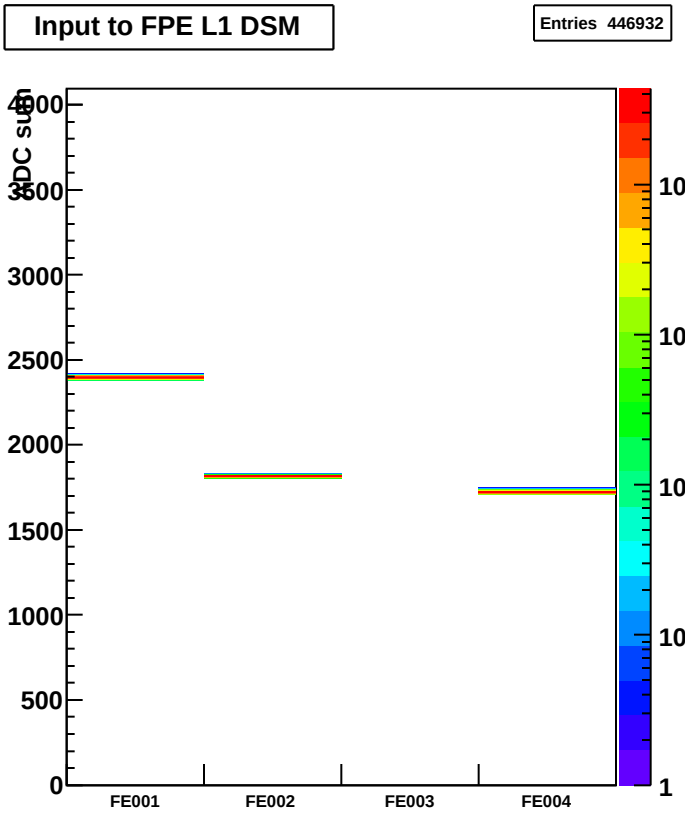

Input to FPD L2 DSM
Entries 670398
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

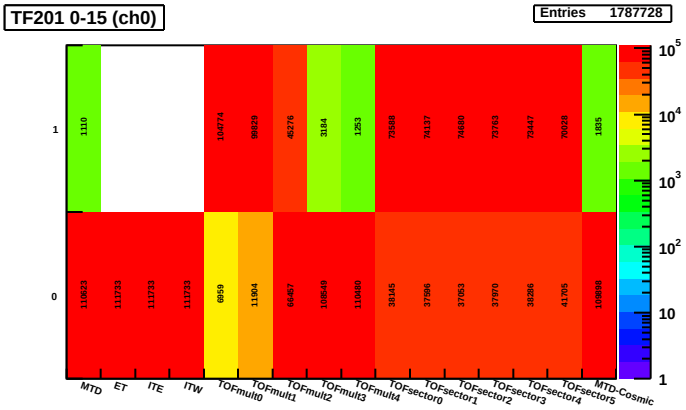

Input to FE001 QT board
Entries3575456
ADC
channel

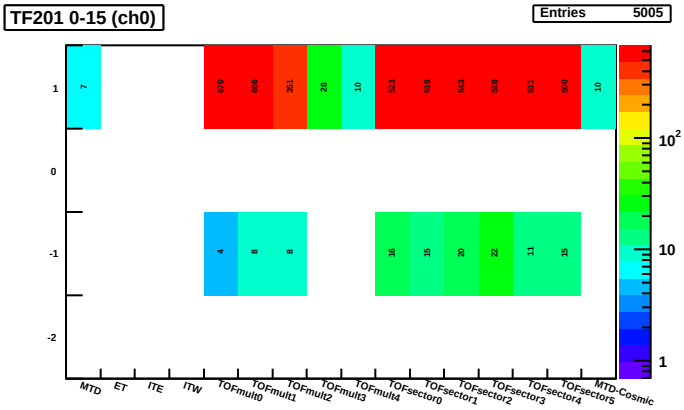

Input to FE002 QT board
Entries3575456
ADC
channel

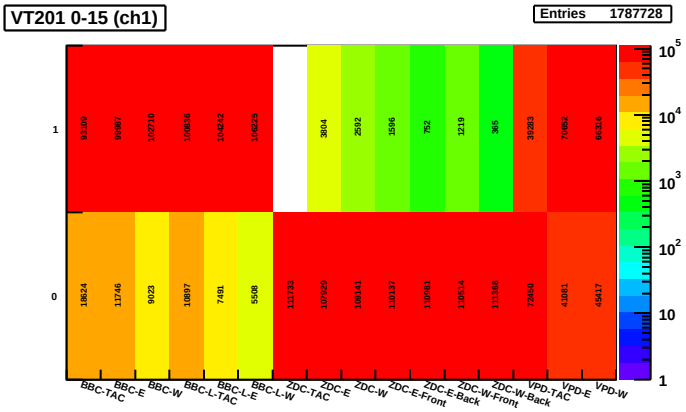

Input to FE003 QT board
Entries3575456
ADC
channel

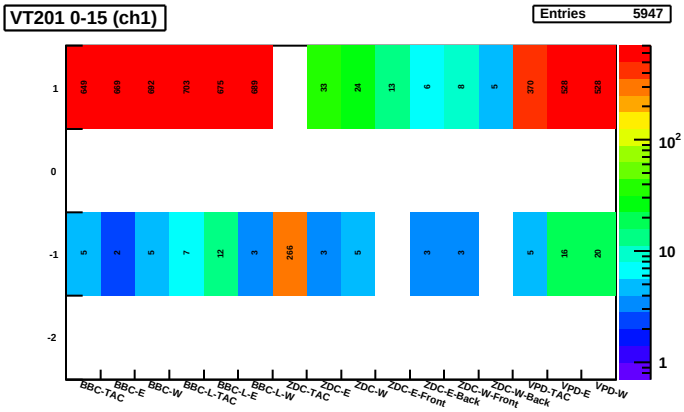

Input to FE004 QT board
Entries3575456
ADC
channel

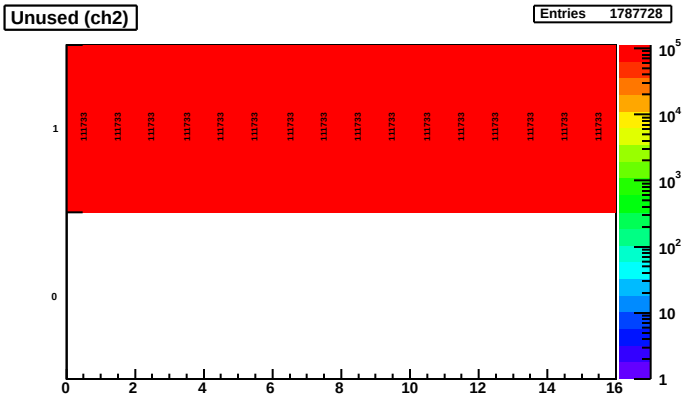

Input to FPE L1 DSM
Entries 446932
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

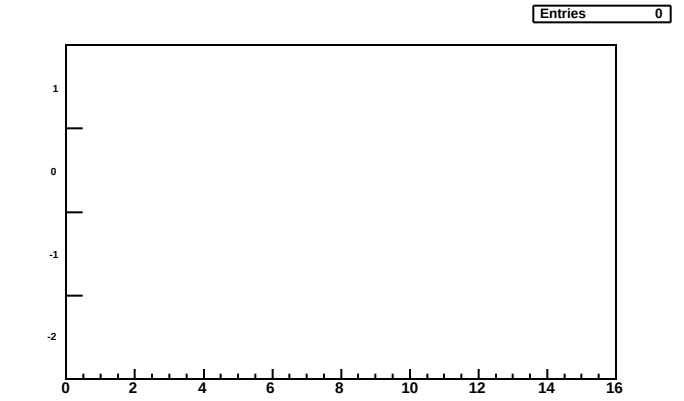

TF201 0-15 (ch0)
Entries 1787728
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

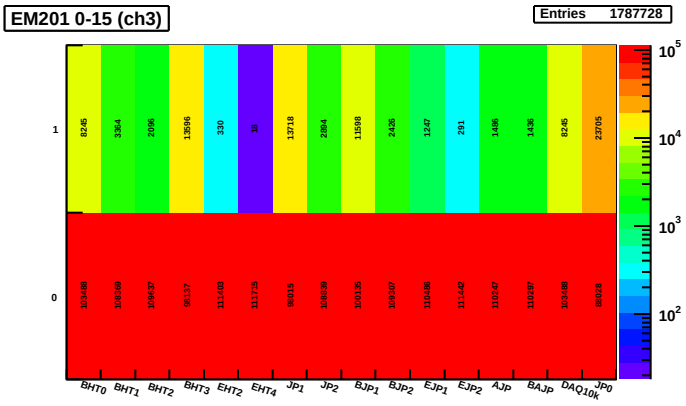

TF201 0-15 (ch0)
Entries 5005
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1787728
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 5947
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1787728


Entries 0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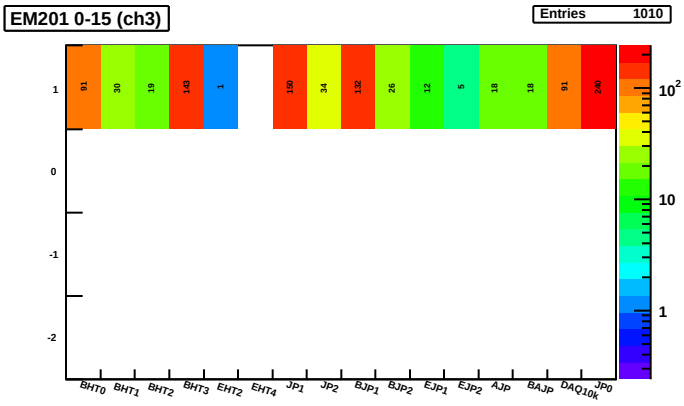
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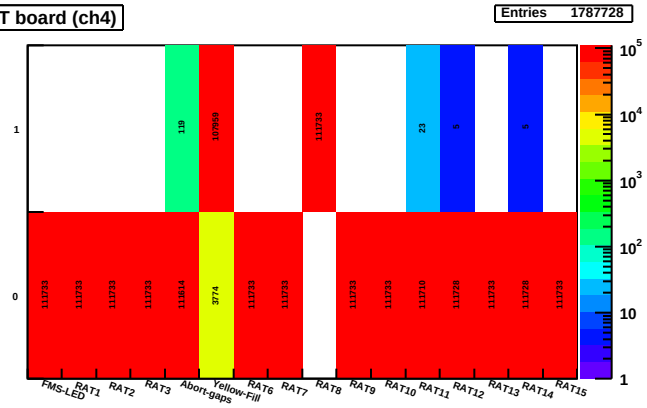
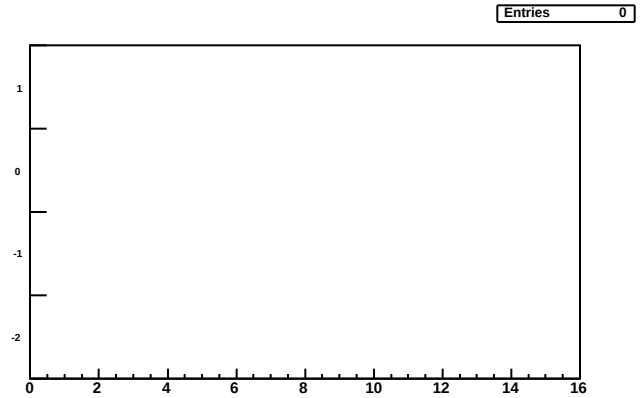
RAT board (ch4)





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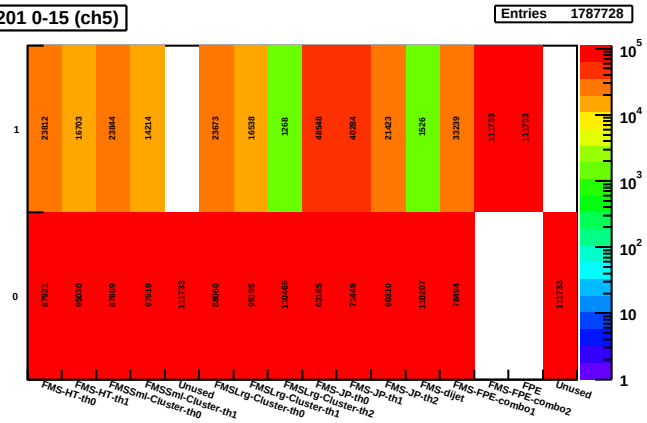

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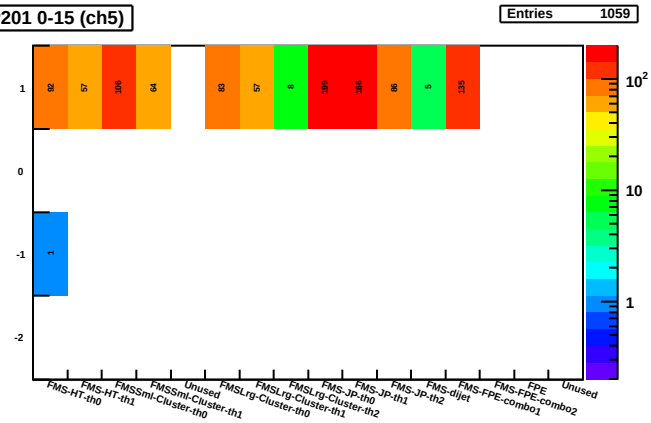

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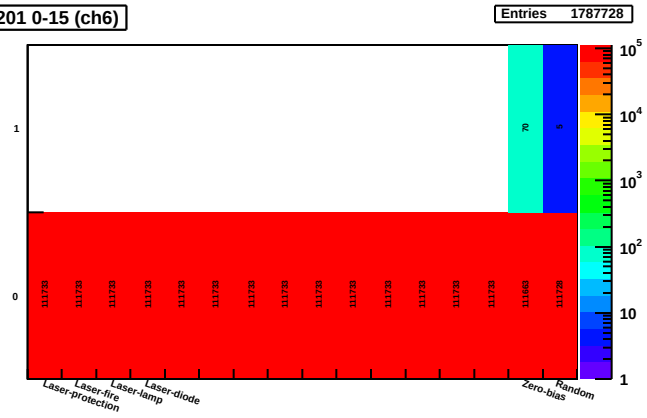

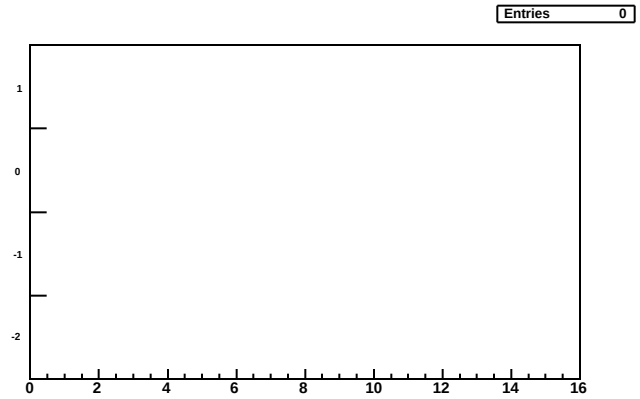
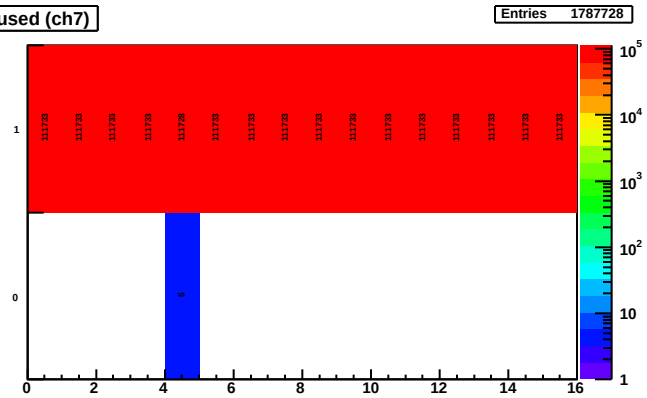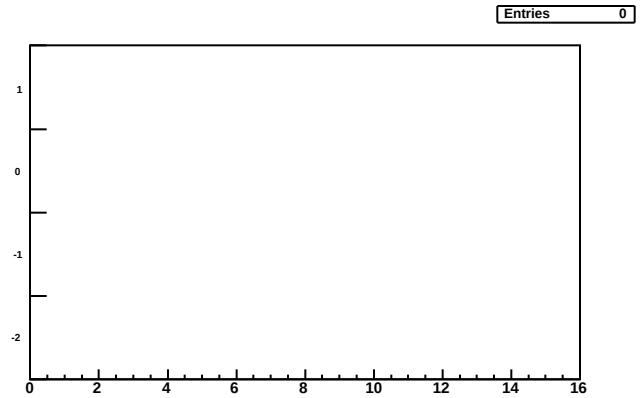
Unused (ch7)