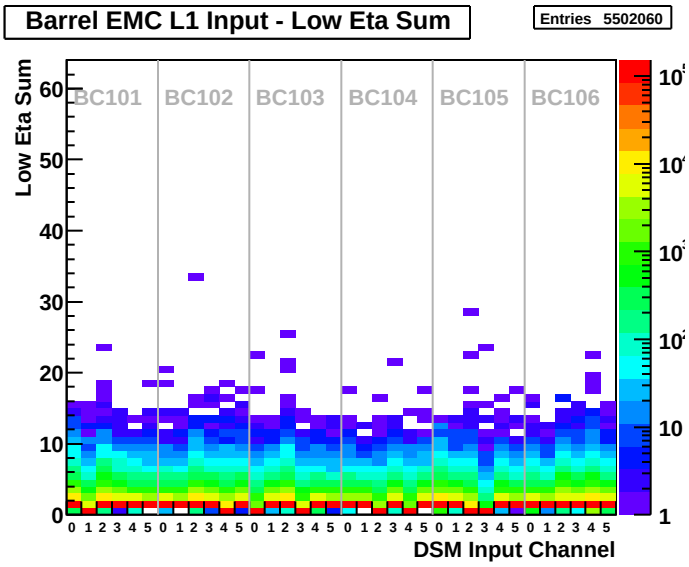
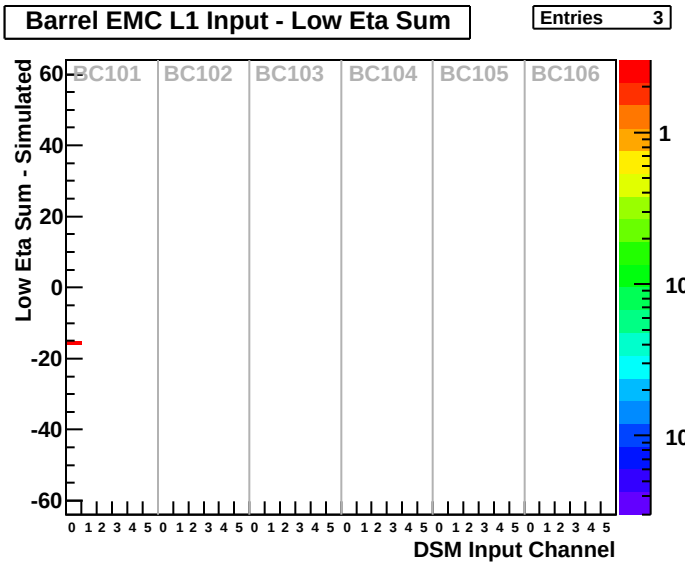
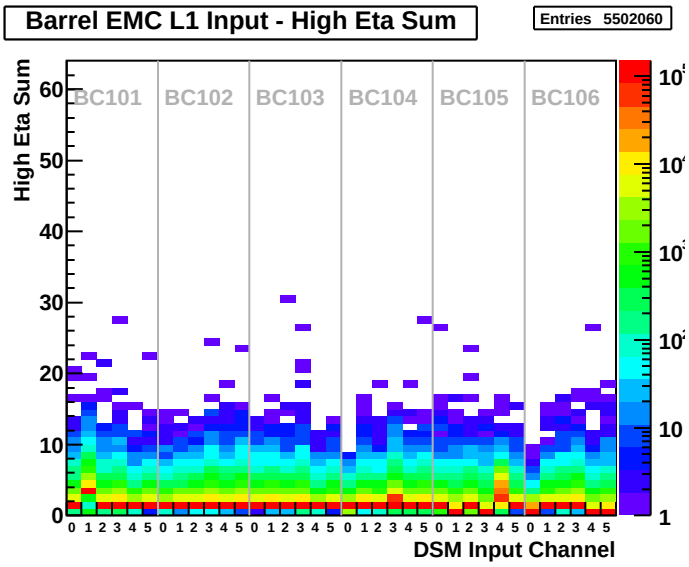
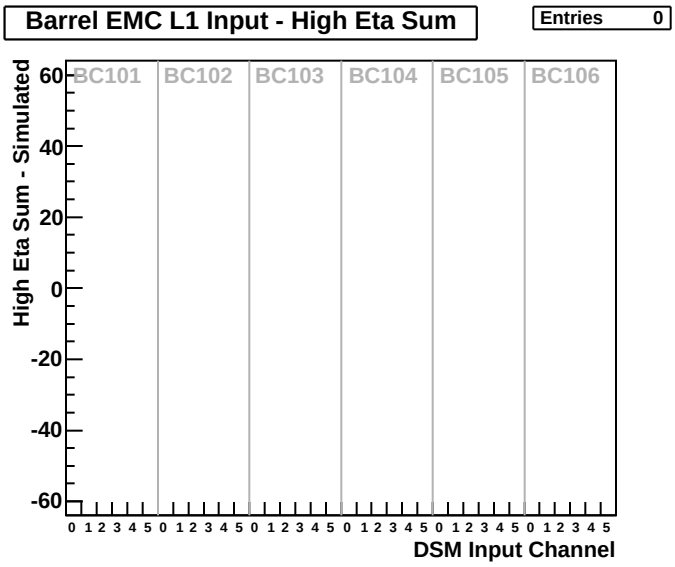
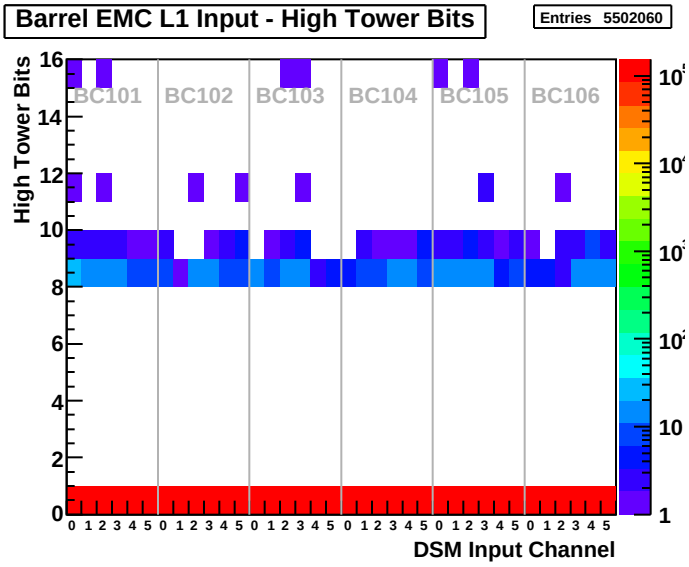
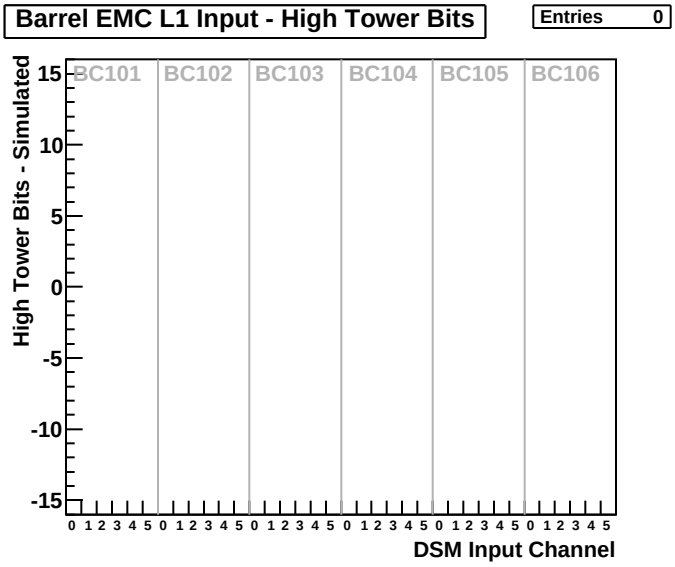

Barrel EMC L1 Input - Low Eta Sum
Entries 5502060
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

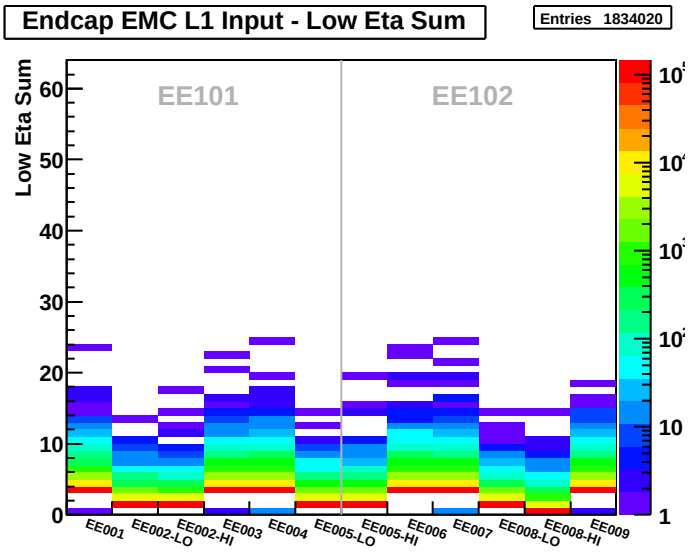

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

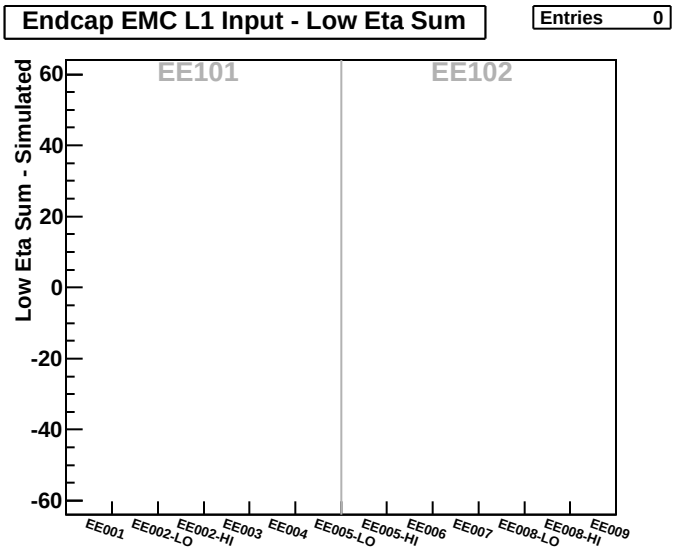

Barrel EMC L1 Input - High Eta Sum
Entries 5502060
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

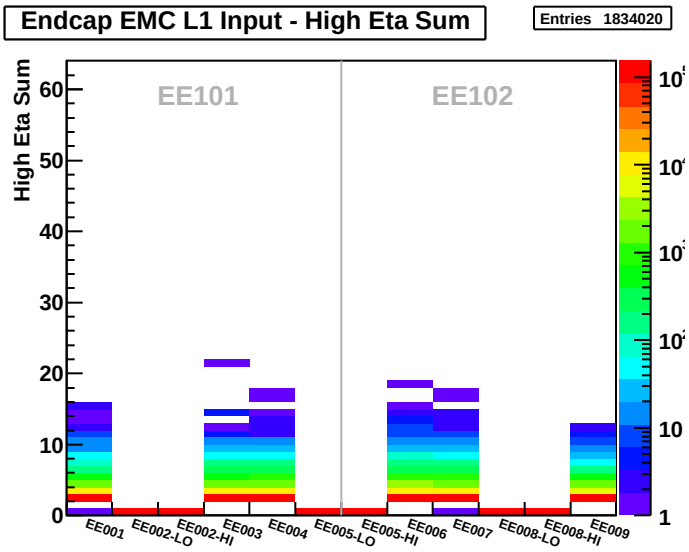

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

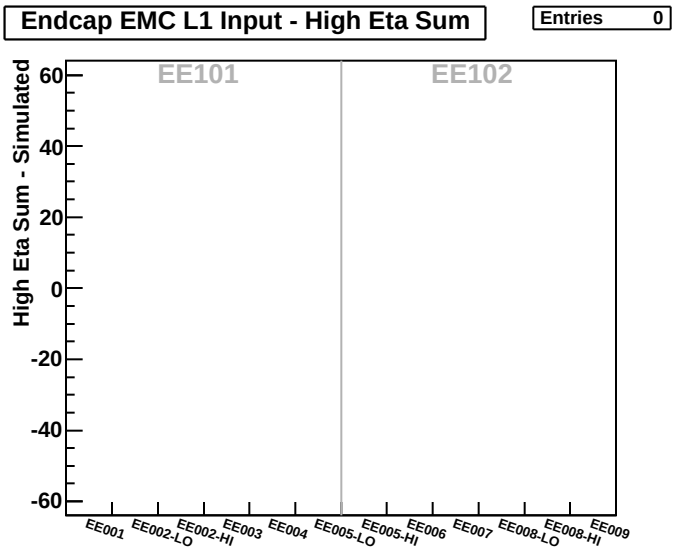

Barrel EMC L1 Input - High Tower Bits
Entries 5502060
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

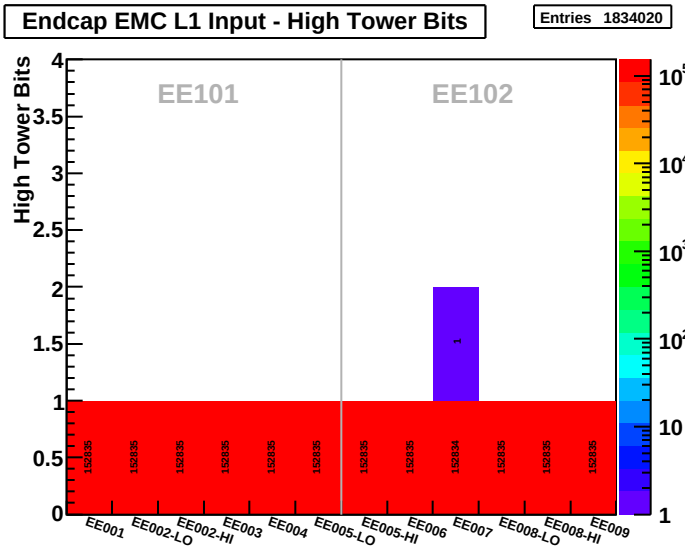

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

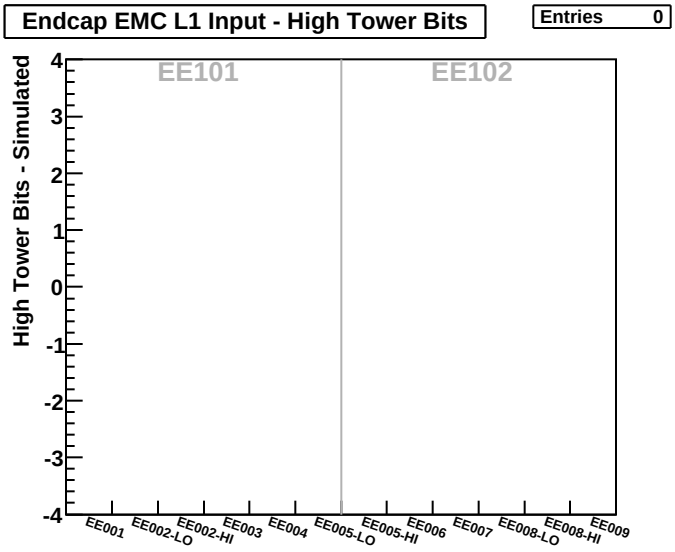

Endcap EMC L1 Input - Low Eta Sum
Entries 1834020
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

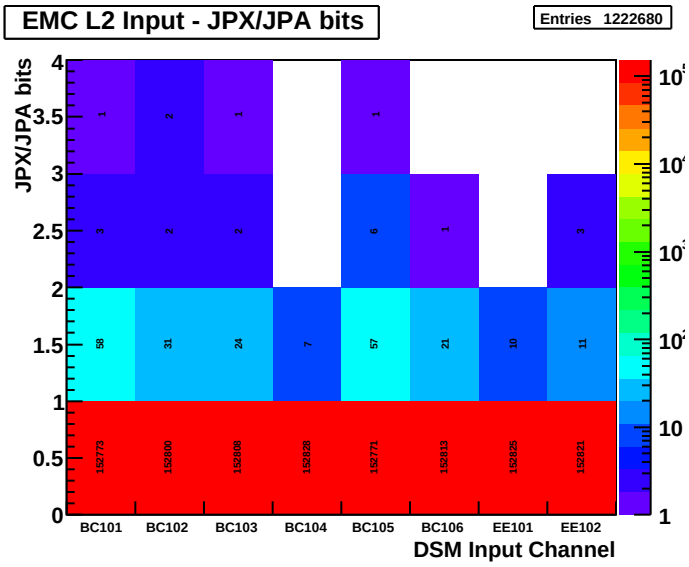

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

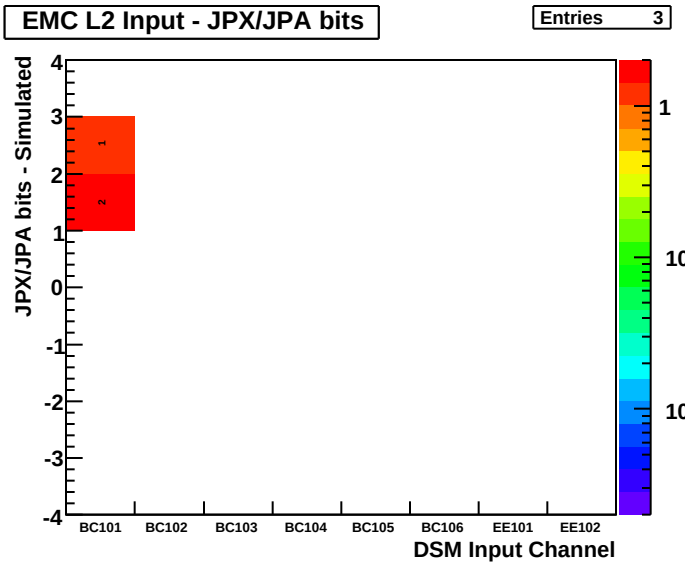

Endcap EMC L1 Input - High Eta Sum
Entries 1834020
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

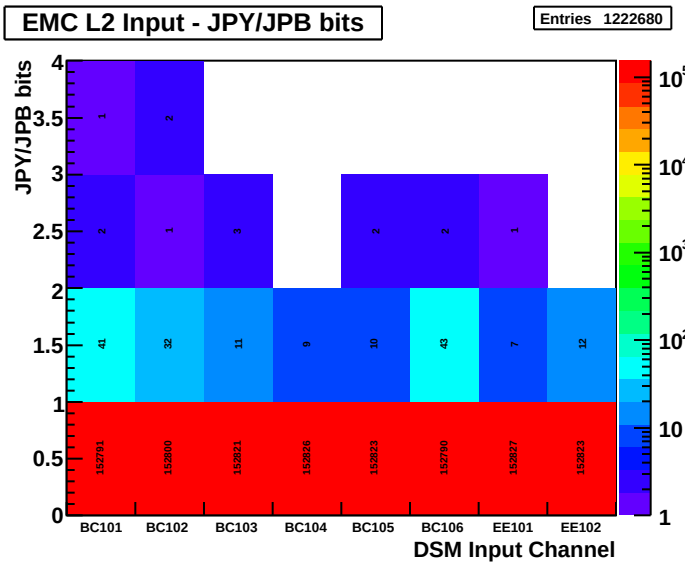

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

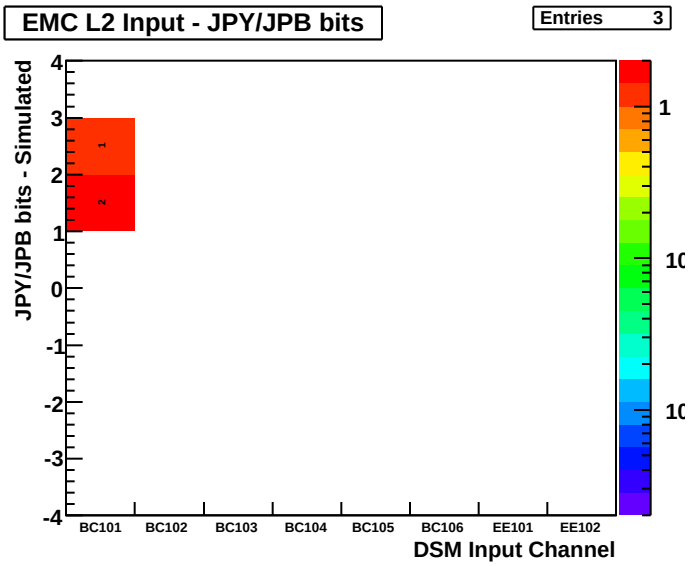

Endcap EMC L1 Input - High Tower Bits
Entries 1834020
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

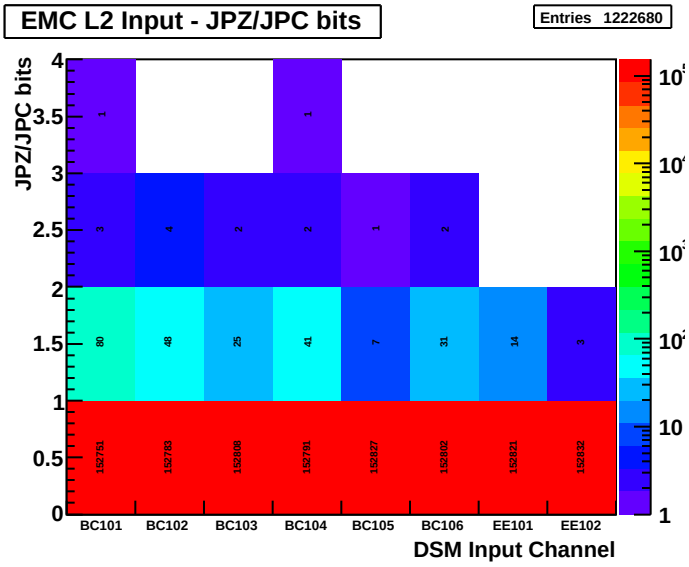

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

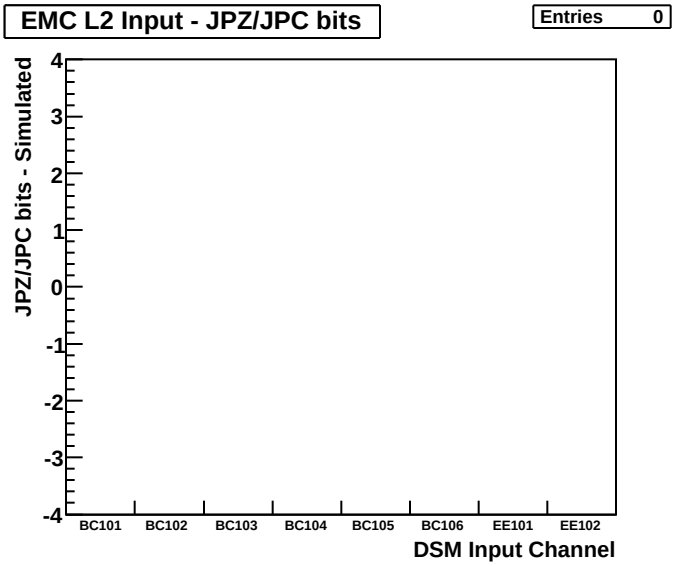

EMC L2 Input - JPX/JPA bits
Entries 1222680
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

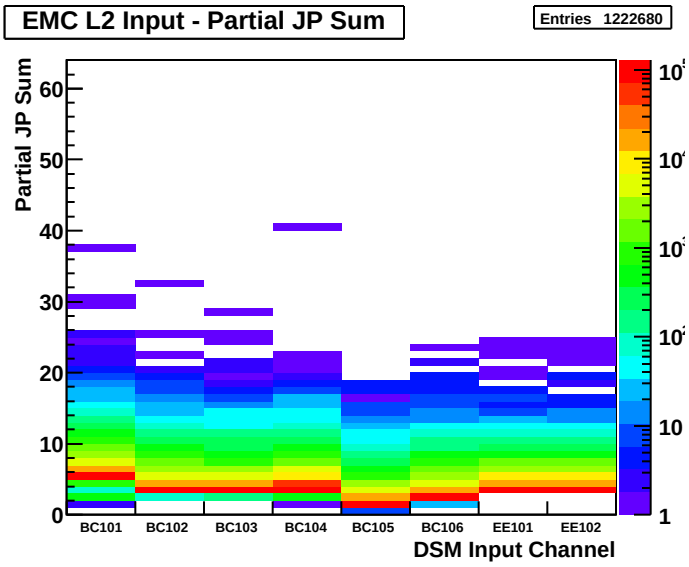

EMC L2 Input - JPX/JPA bits
Entries 3
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

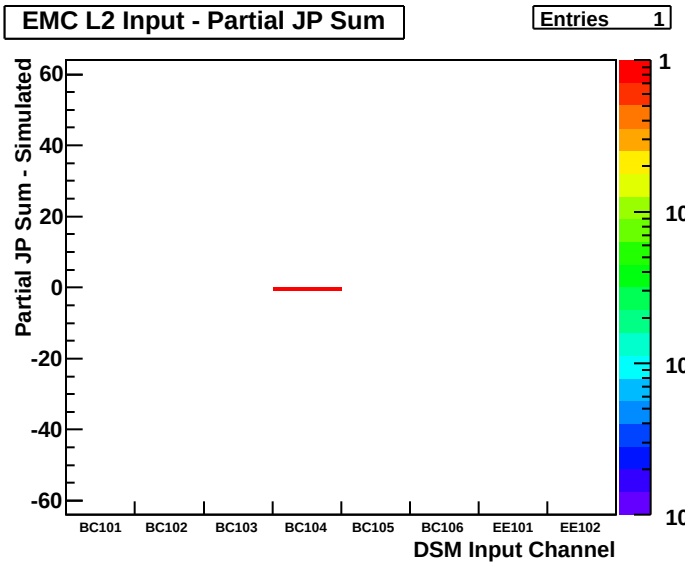

EMC L2 Input - JPY/JPB bits
Entries 1222680
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

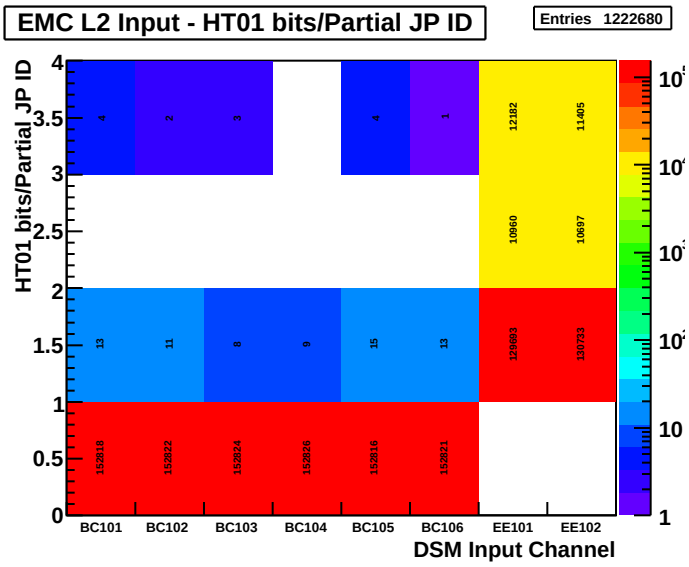

EMC L2 Input - JPY/JPB bits
Entries 3
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

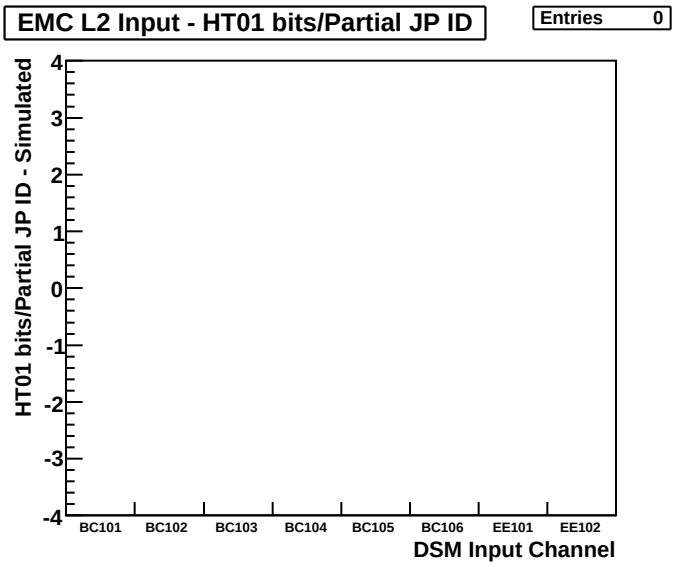

EMC L2 Input - JPZ/JPC bits
Entries 1222680
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

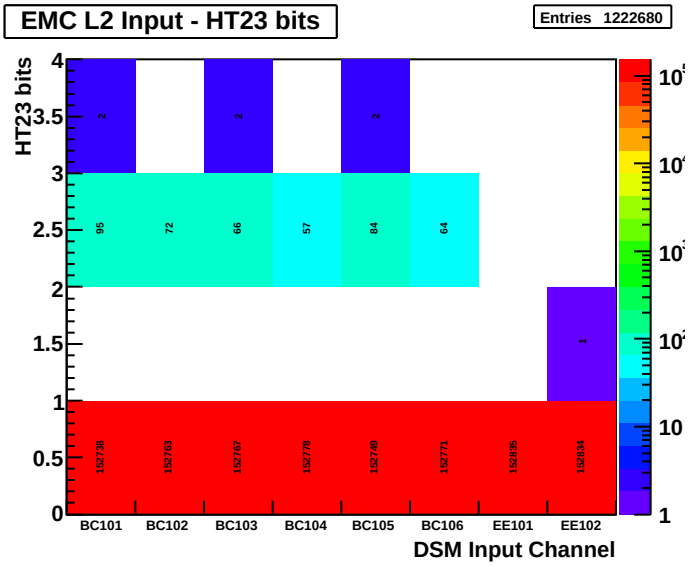

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

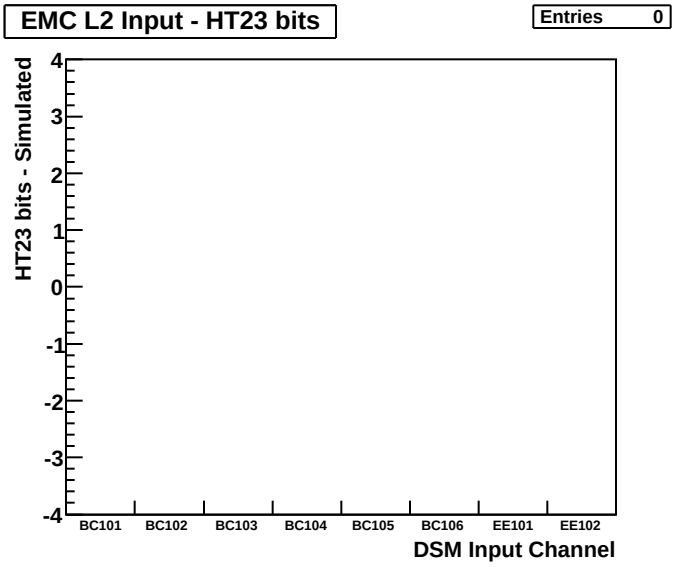

EMC L2 Input - Partial JP Sum
Entries 1222680
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

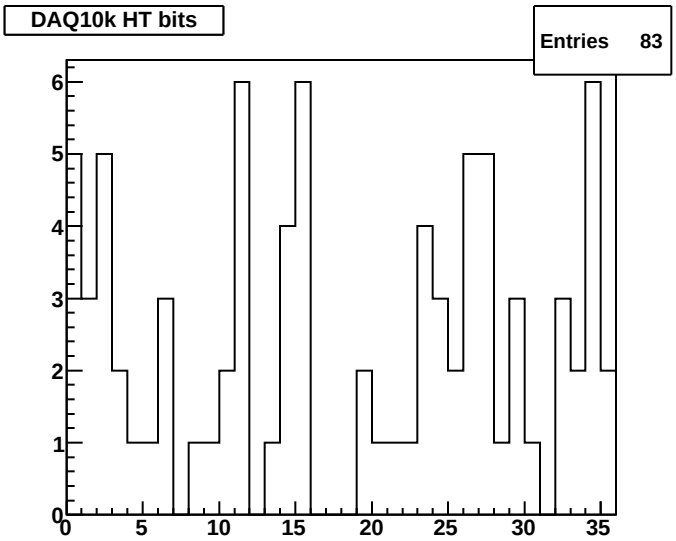

EMC L2 Input - Partial JP Sum
Entries 1
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

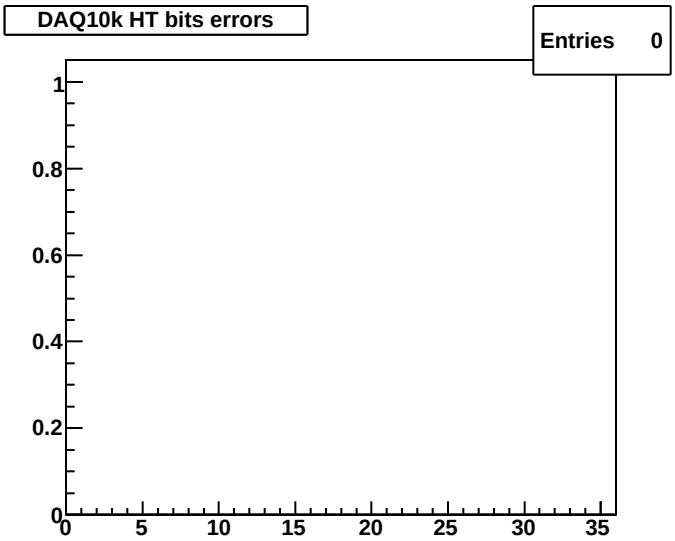

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1222680
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

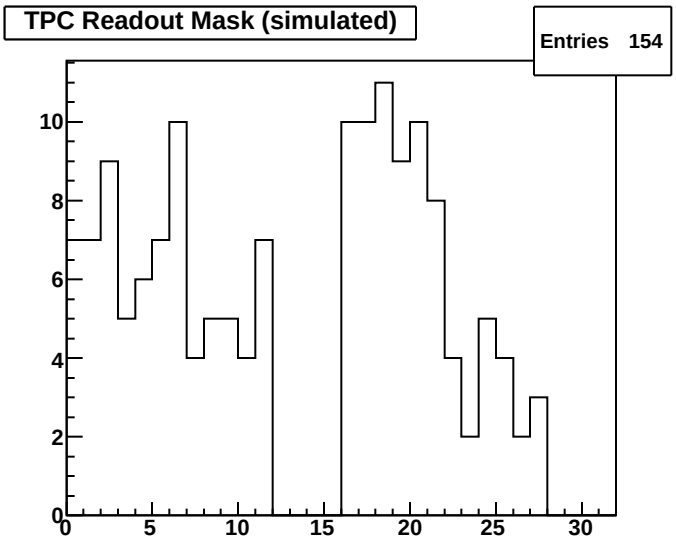

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

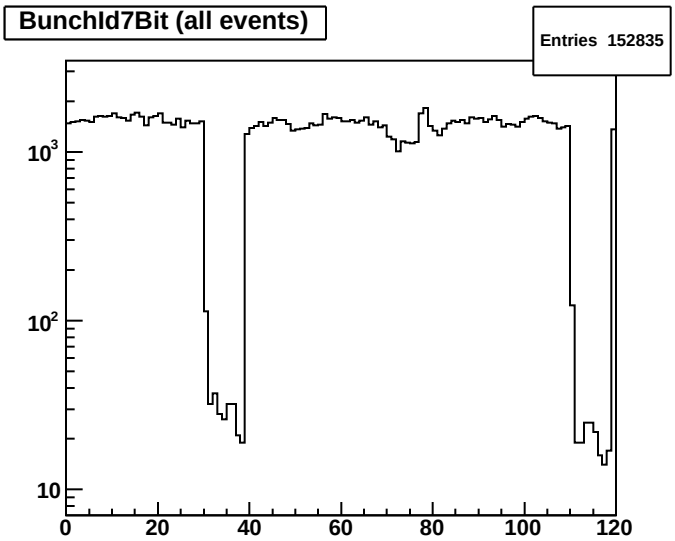

EMC L2 Input - HT23 bits
Entries 1222680
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

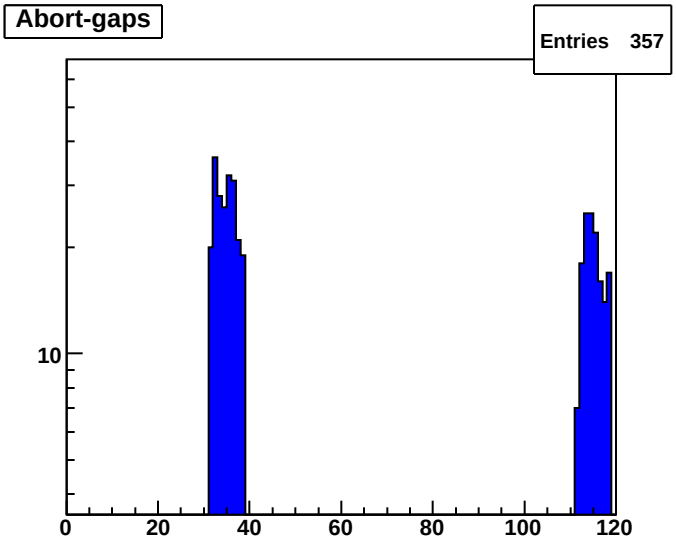

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

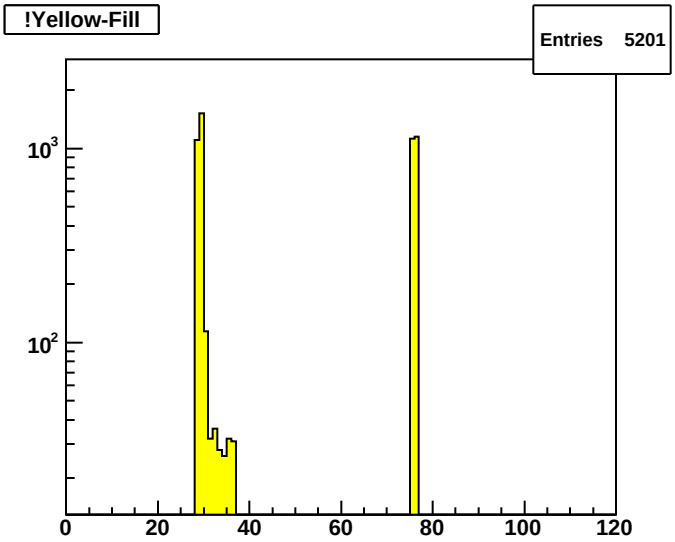

DAQ10k HT bits
Entries 83

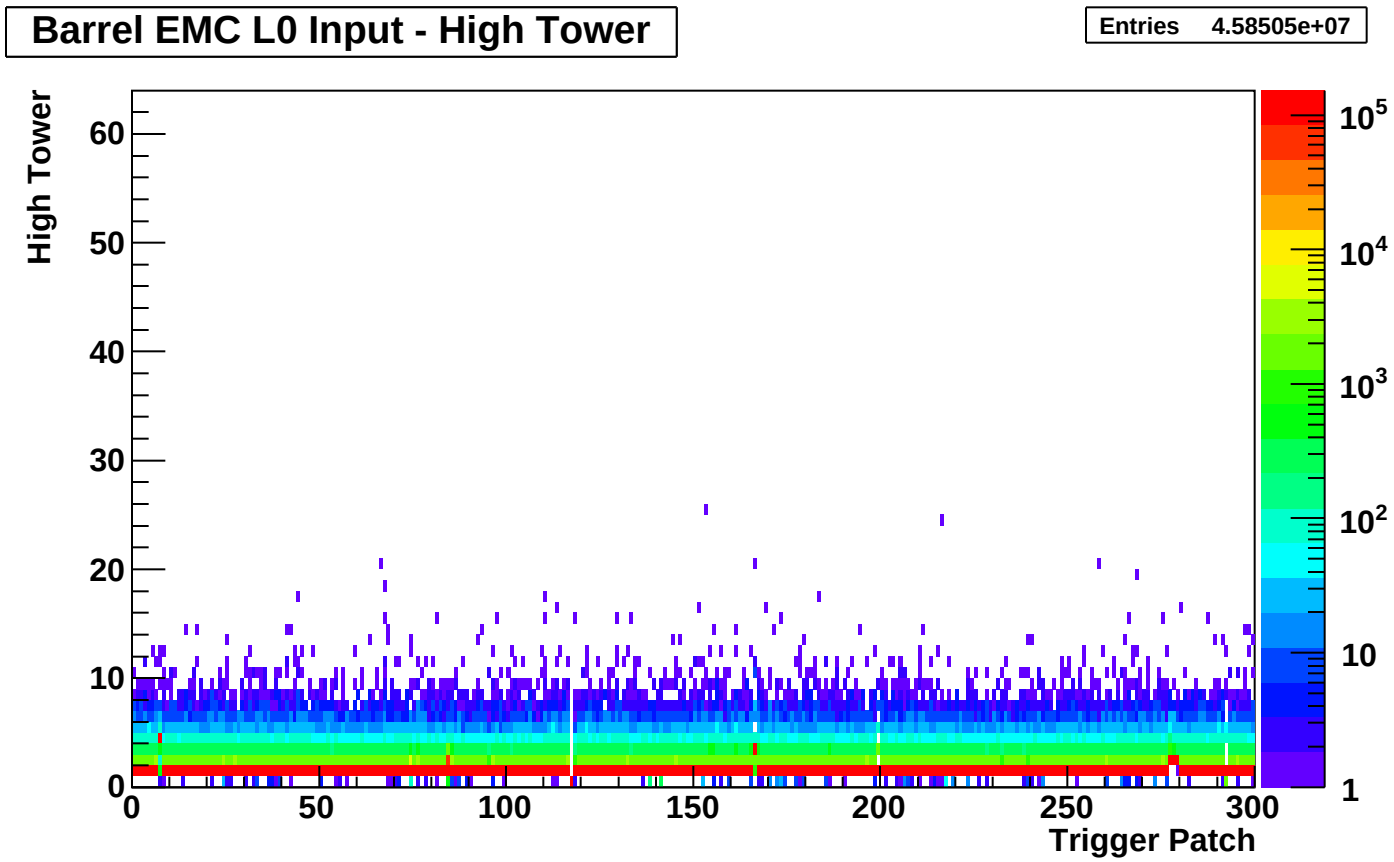

DAQ10k HT bits errors
Entries 0

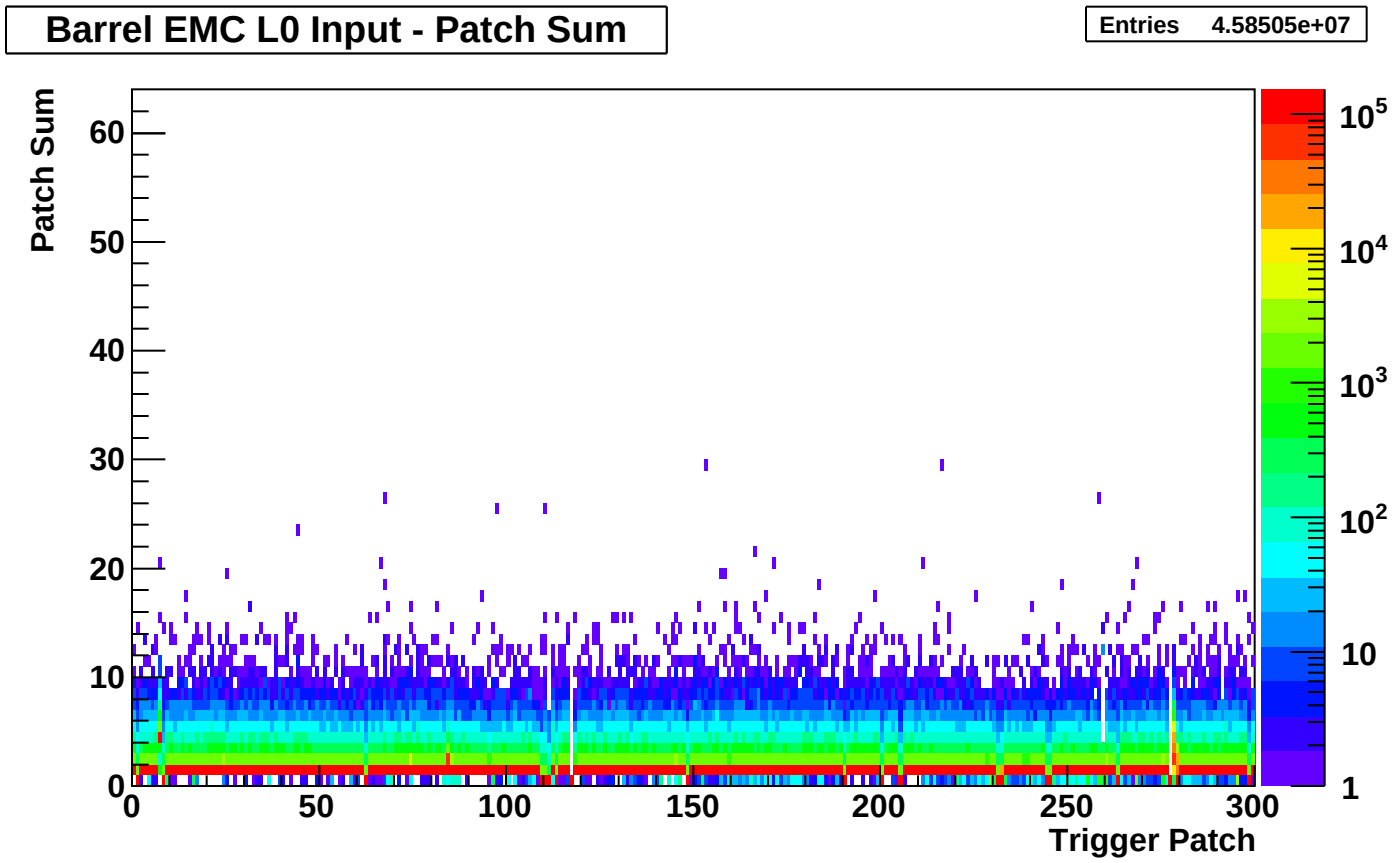

TPC Readout Mask (simulated)
Entries 154

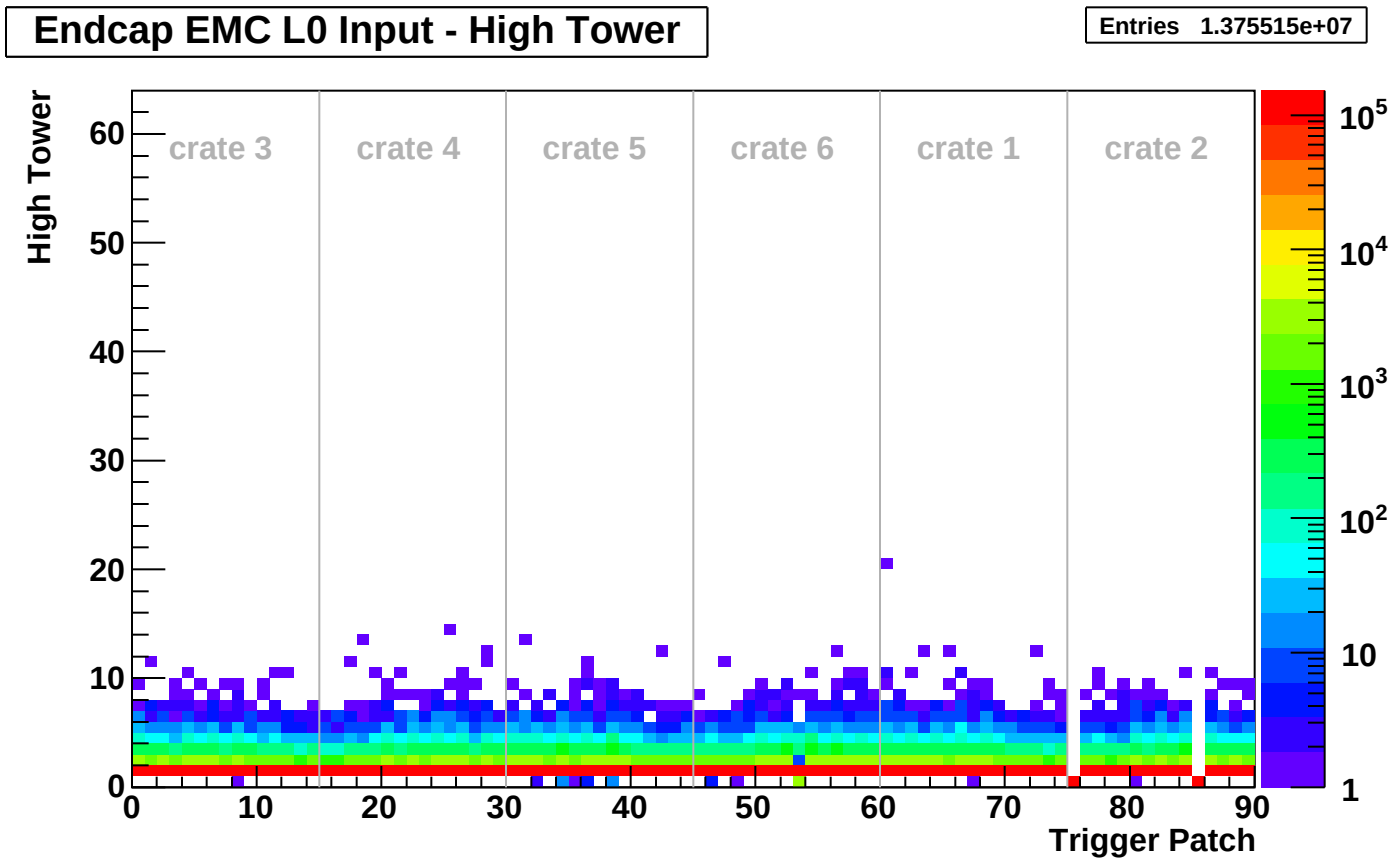

BunchId7Bit (all events)
Entries 152835

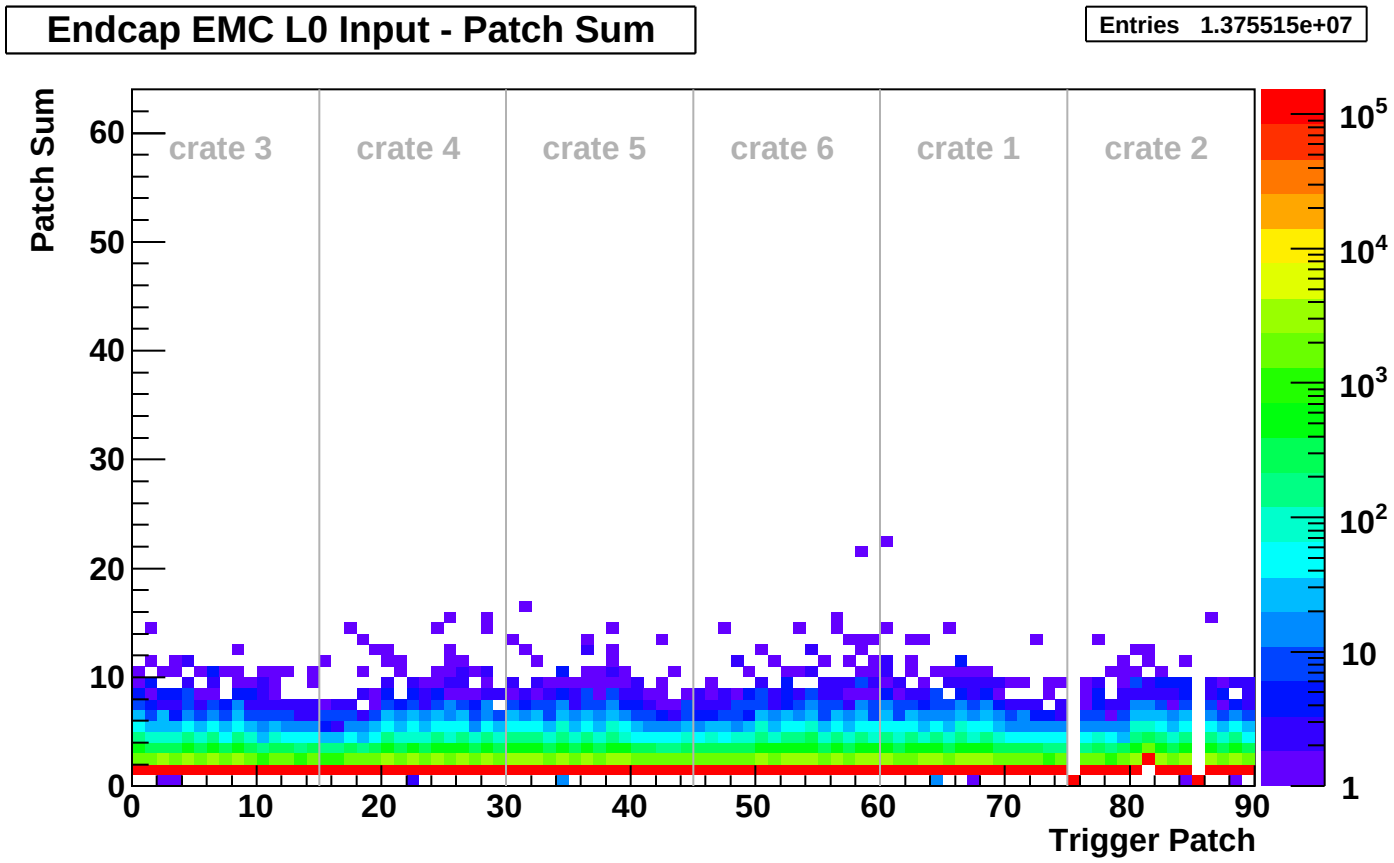

Abort-gaps
Entries 357

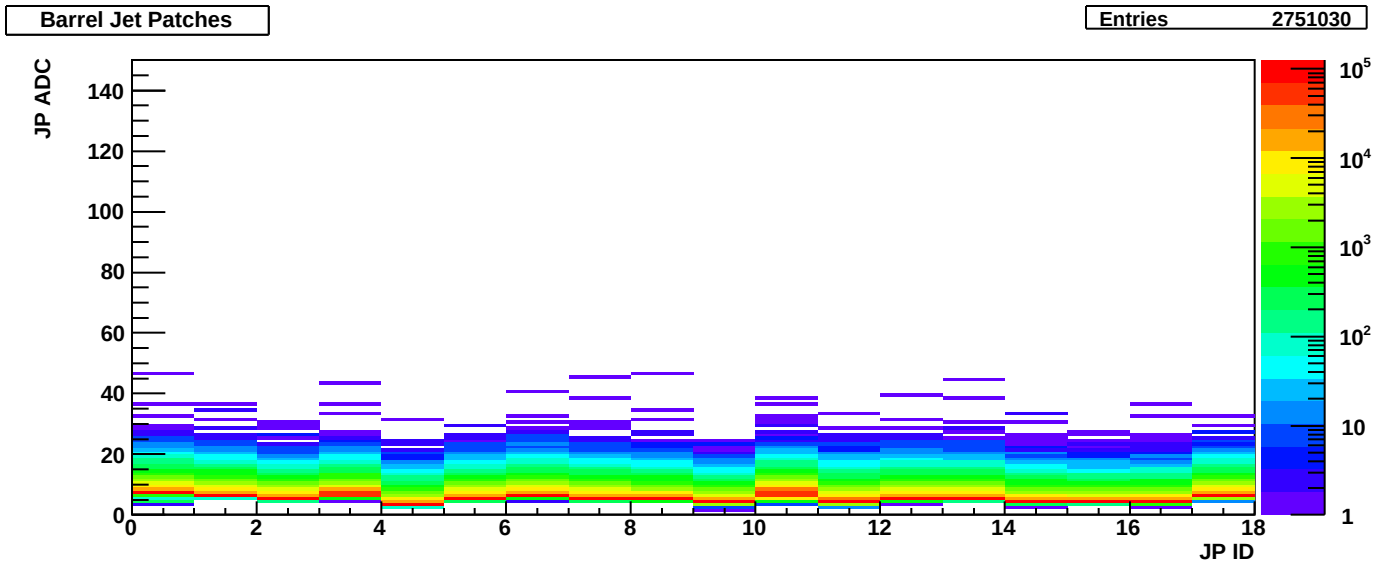

!Yellow-Fill
Entries 5201

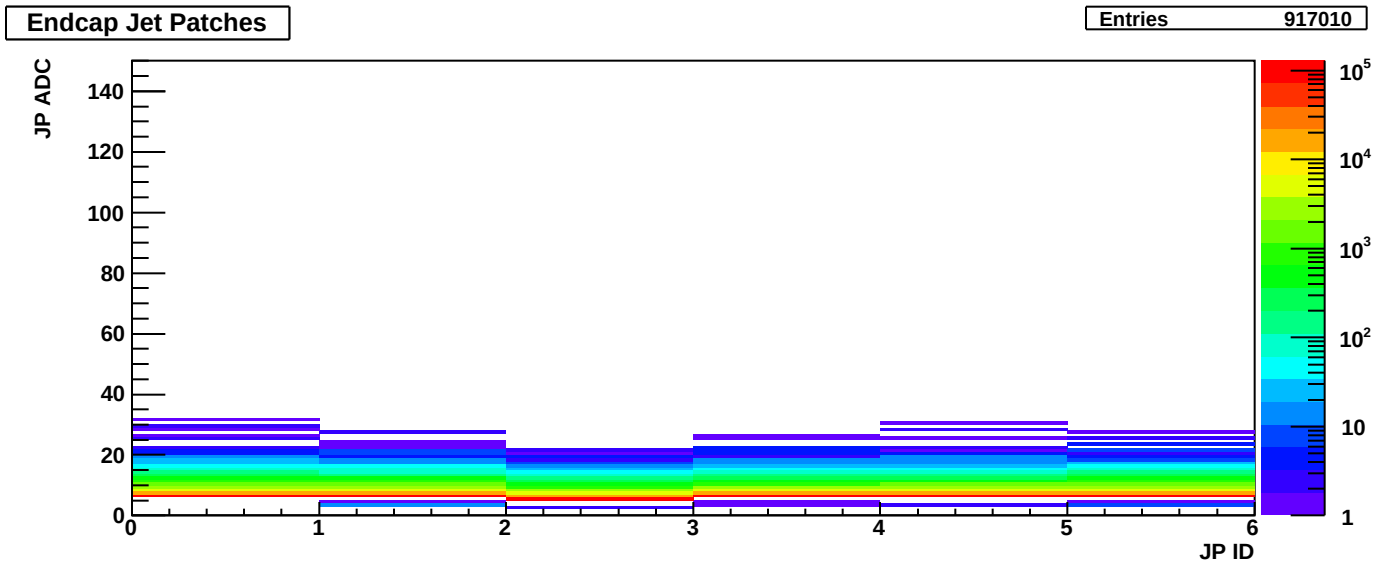

Barrel EMC L0 Input - High Tower
Entries 4.58505e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

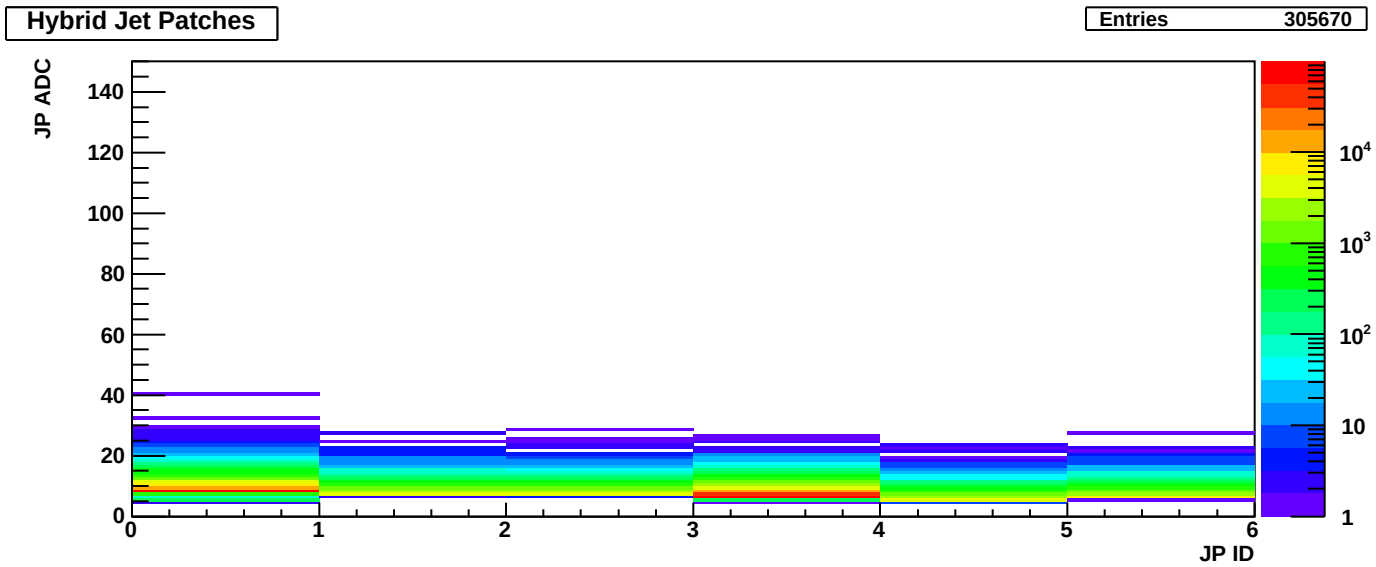

Barrel EMC L0 Input - Patch Sum
Entries 4.58505e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

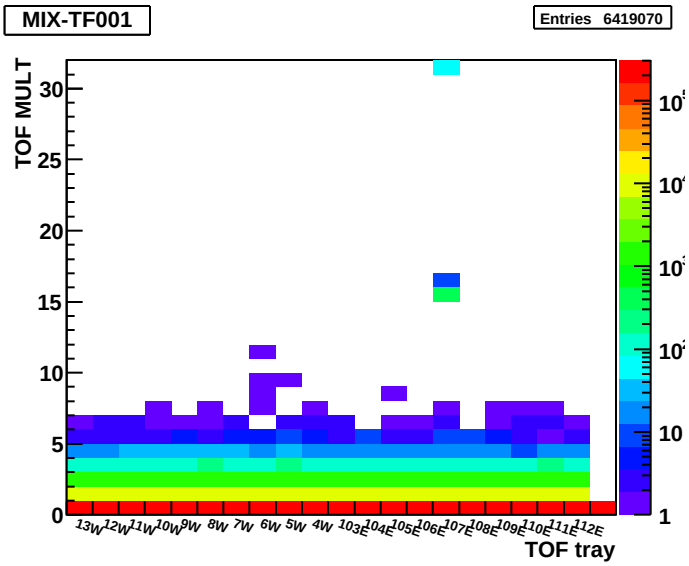

Endcap EMC L0 Input - High Tower
Entries 1.375515e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

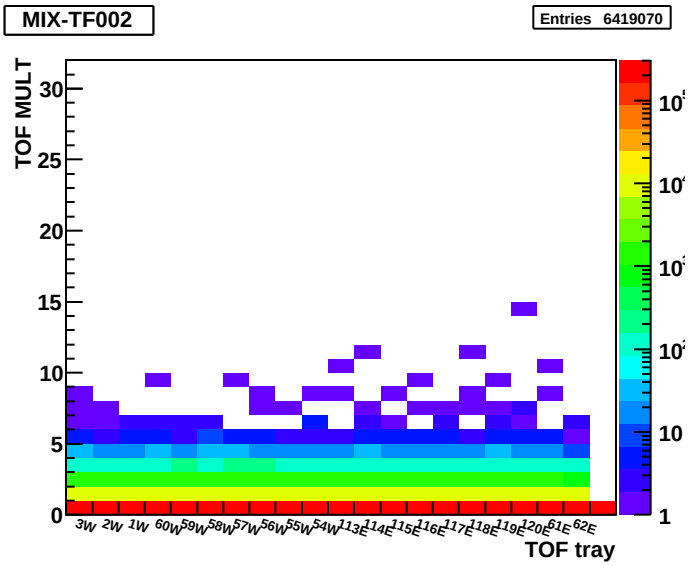

Endcap EMC L0 Input - Patch Sum
Entries 1.375515e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

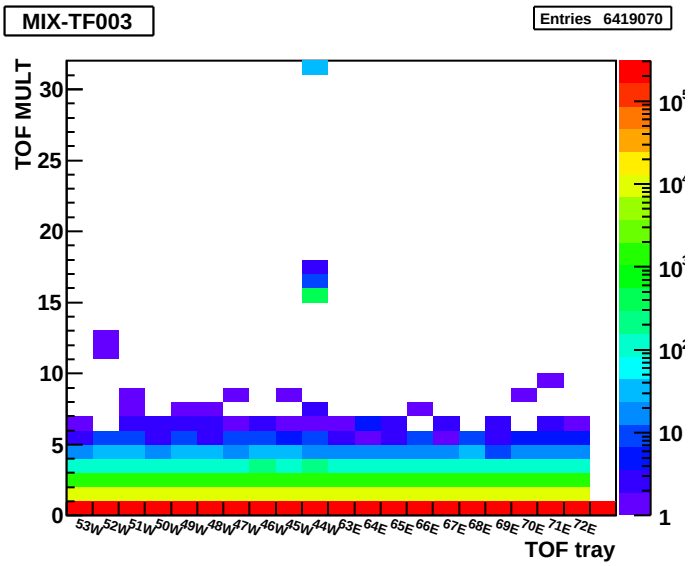

Barrel Jet Patches
Entries 2751030
JP ADC
JP ID

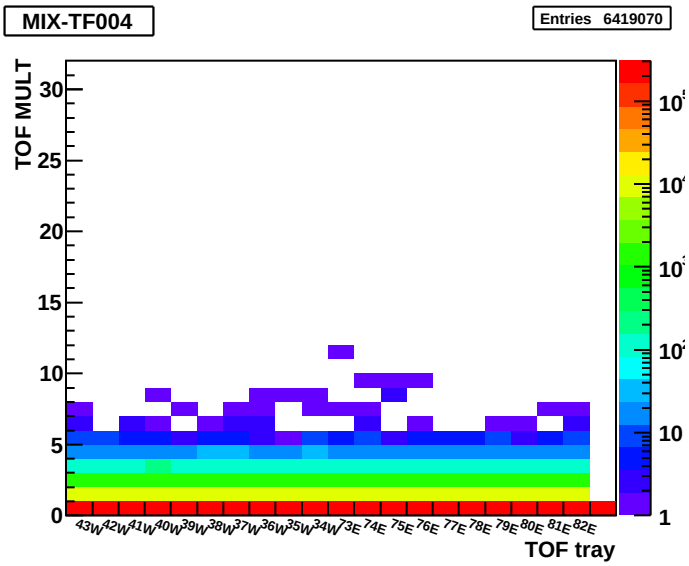

Endcap Jet Patches
Entries 917010
JP ADC
JP ID

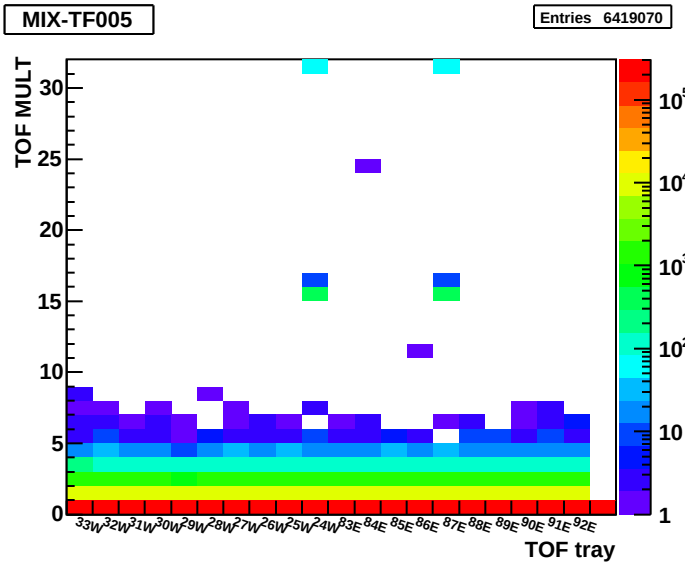

Hybrid Jet Patches
Entries 305670
JP ADC
JP ID

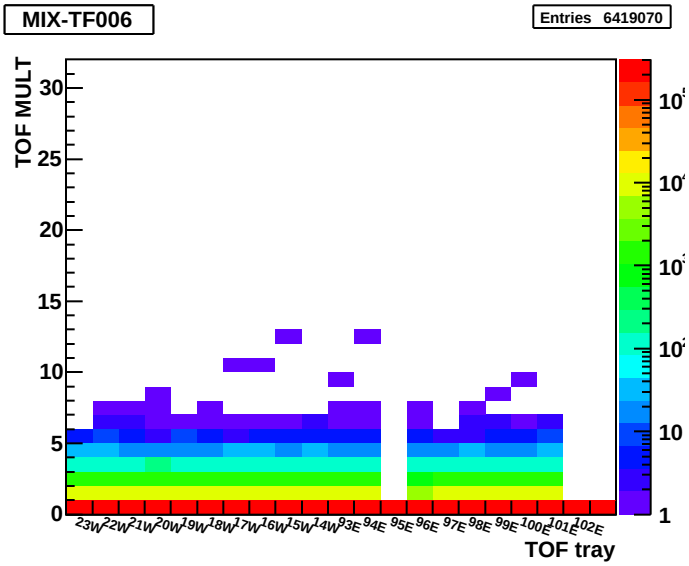

MIX-TF001
Entries 6419070
TOF MULT
TOF tray

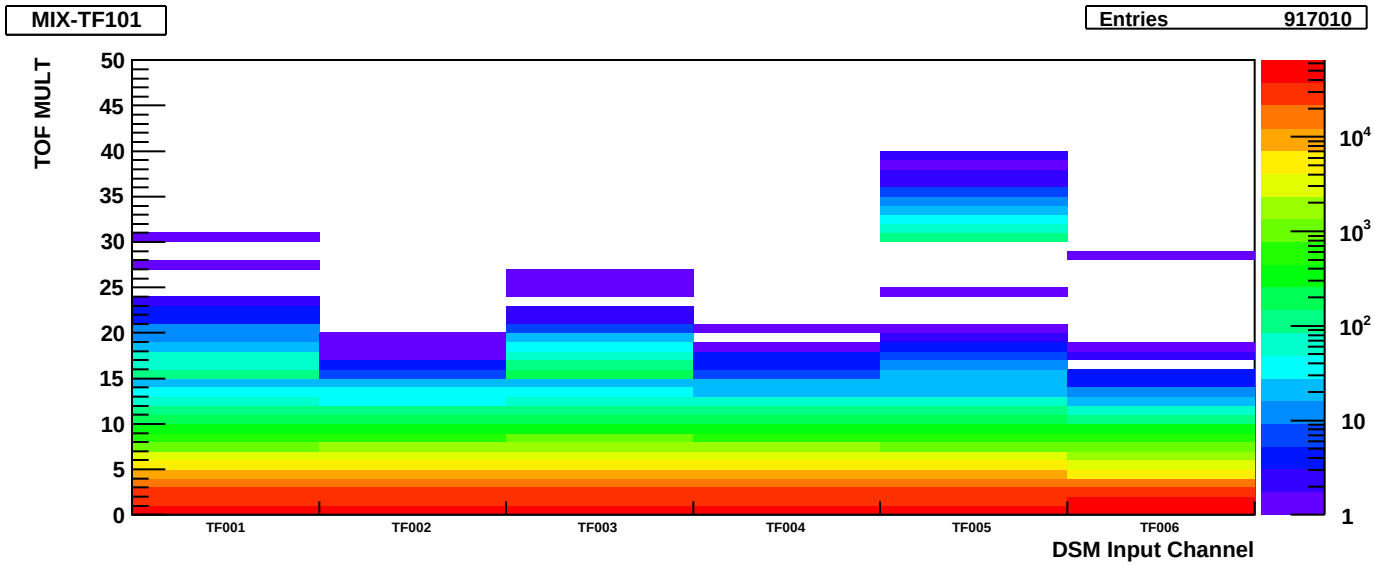

MIX-TF002
Entries 6419070
TOF MULT
TOF tray

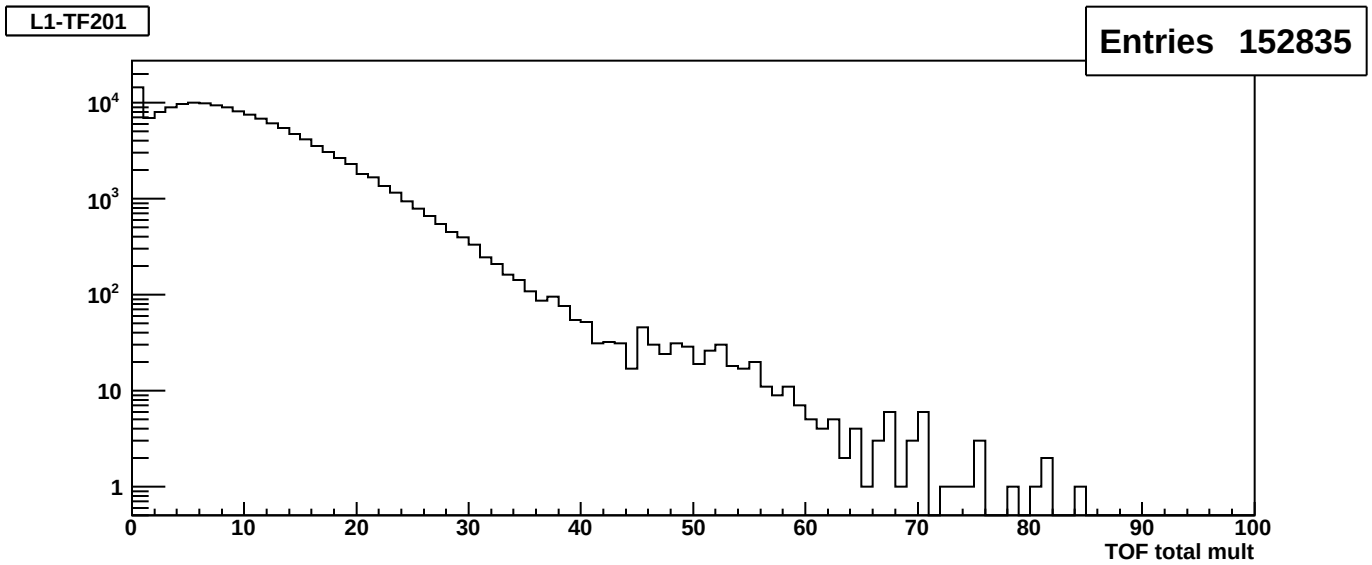

MIX-TF003
Entries 6419070
TOF MULT
TOF tray

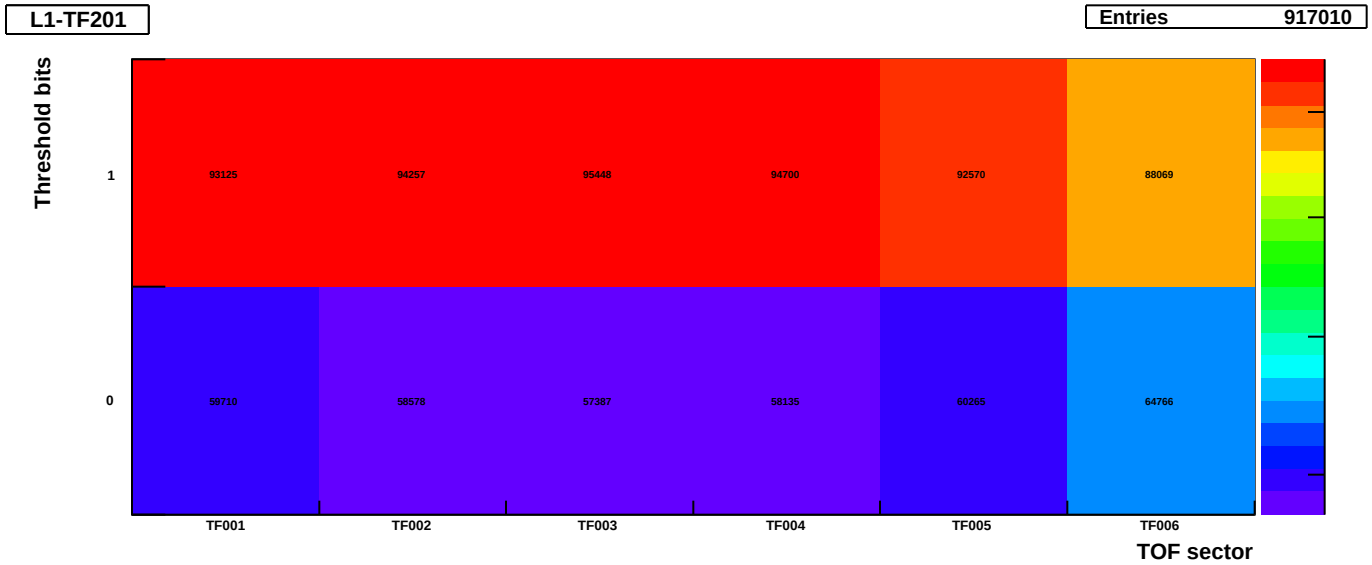

MIX-TF004
Entries 6419070
TOF MULT
TOF tray

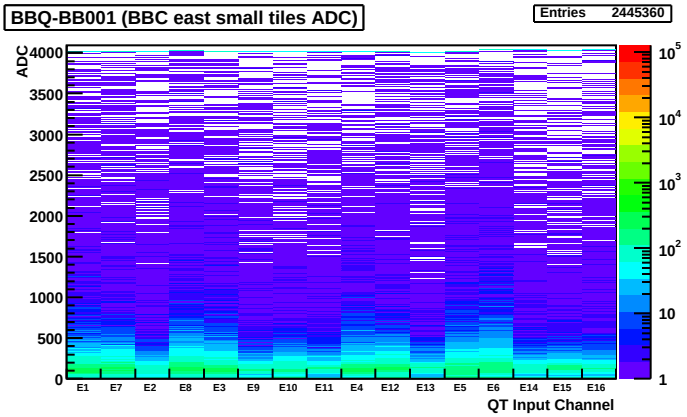

MIX-TF005
Entries 6419070
TOF MULT
TOF tray

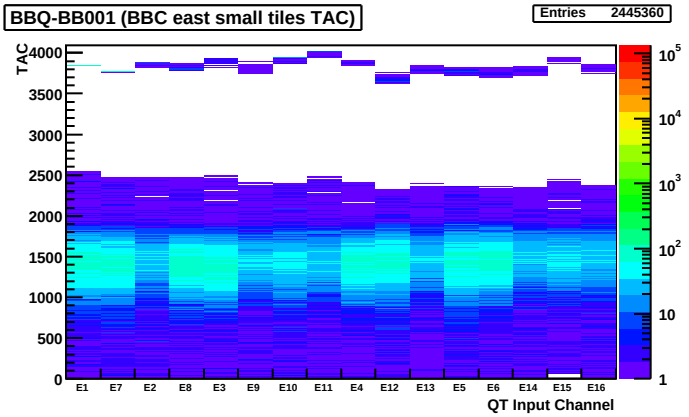

MIX-TF006
Entries 6419070
TOF MULT
TOF tray

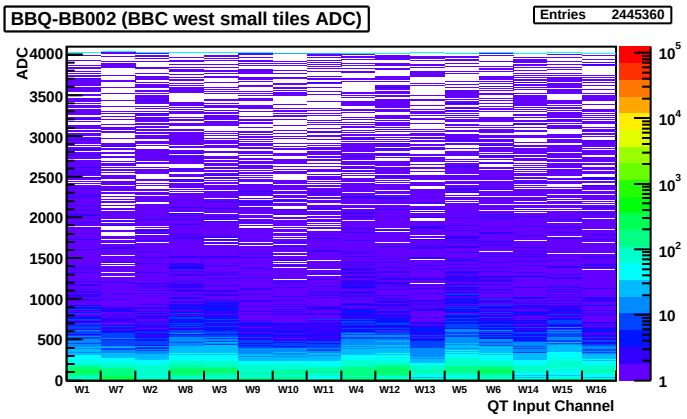

MIX-TF101
Entries 917010
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

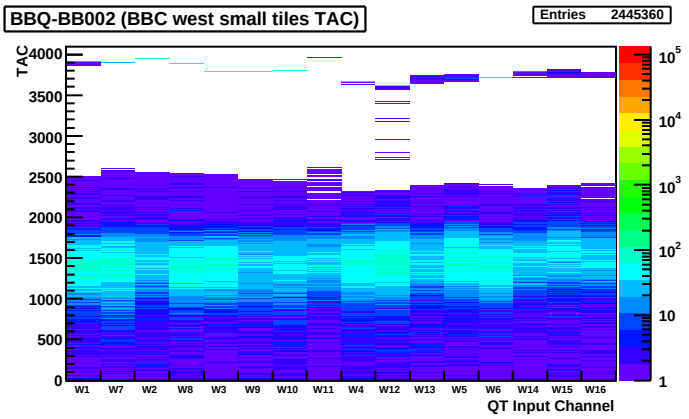

L1-TF201
Entries 152835
TOF total mult

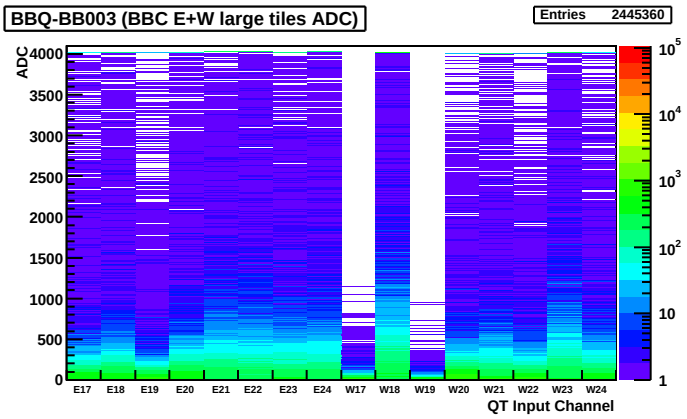

L1-TF201
Entries 917010
Threshold bits
93125
94257
95448
94700
92570
88069
59710
58578
57387
58135
60265
64766
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

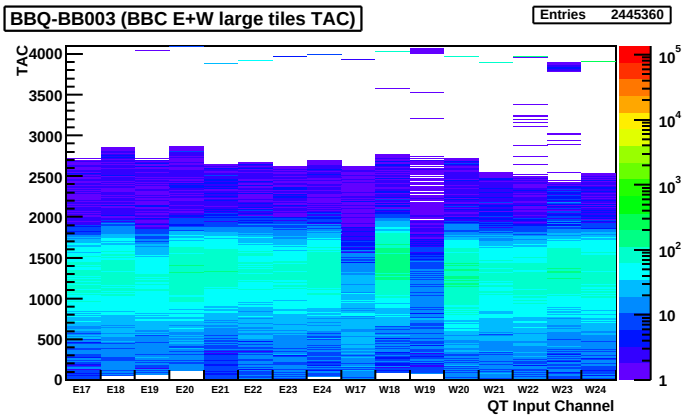

BBQ-BB001 (BBC east small tiles ADC)
Entries 2445360
ADC
QT Input Channel

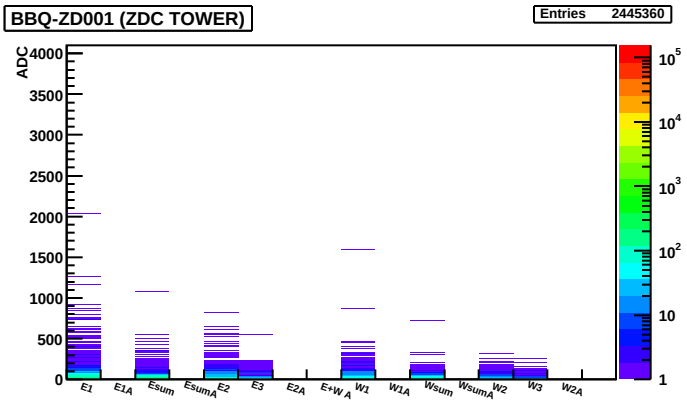

BBQ-BB001 (BBC east small tiles TAC)
Entries 2445360
TAC
QT Input Channel

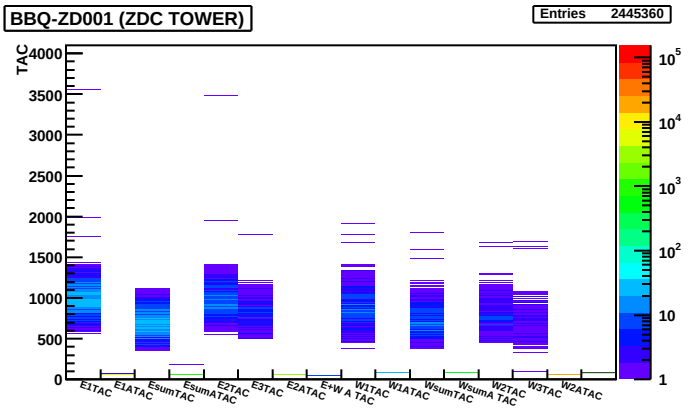

BBQ-BB002 (BBC west small tiles ADC)
Entries 2445360
ADC
QT Input Channel

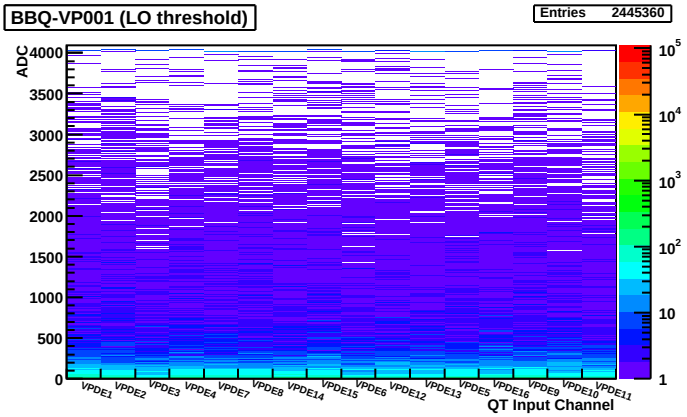

BBQ-BB002 (BBC west small tiles TAC)
Entries 2445360
TAC
QT Input Channel

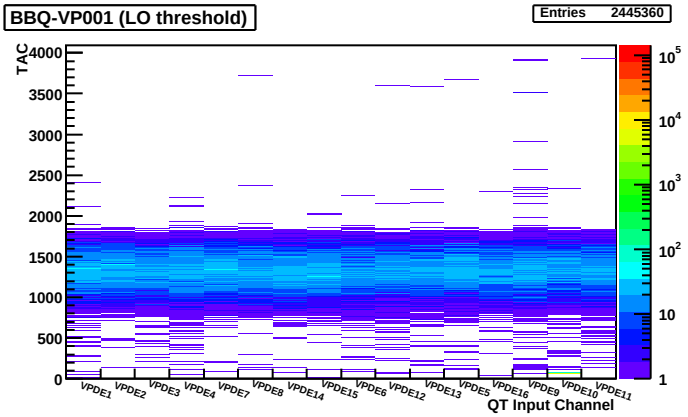

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2445360
ADC
QT Input Channel

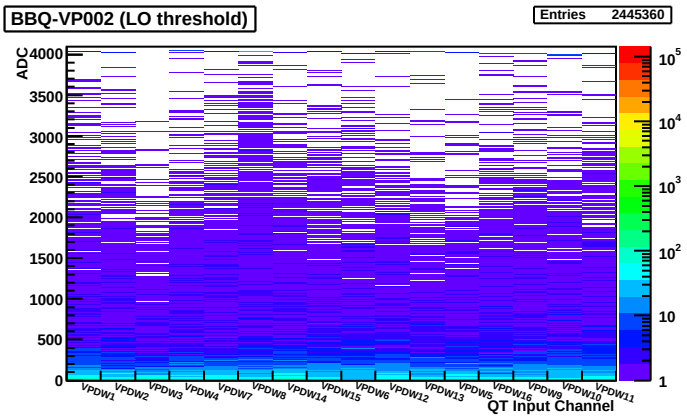

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2445360
TAC
QT Input Channel

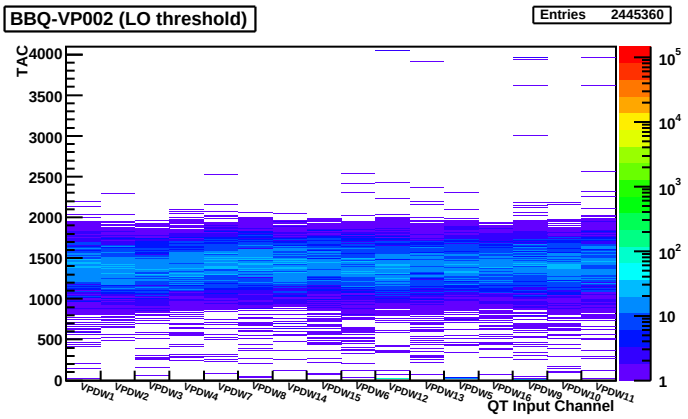

BBQ-ZD001 (ZDC TOWER)
Entries 2445360
ADC

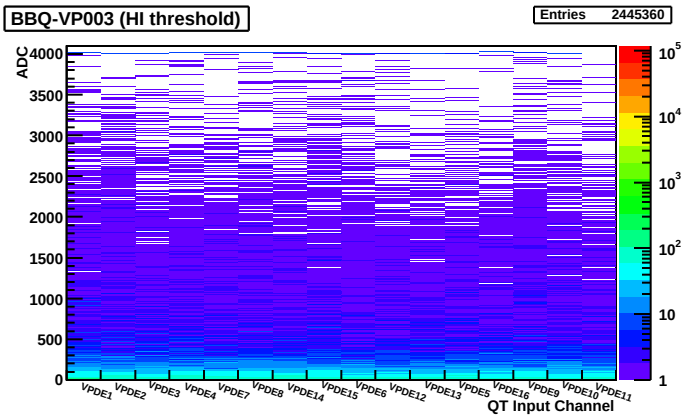

BBQ-ZD001 (ZDC TOWER)
Entries 2445360
TAC

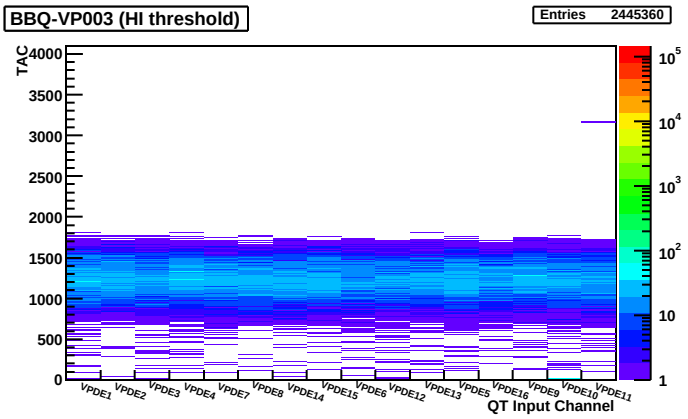

BBQ-VP001 (LO threshold)
Entries 2445360
ADC
QT Input Channel

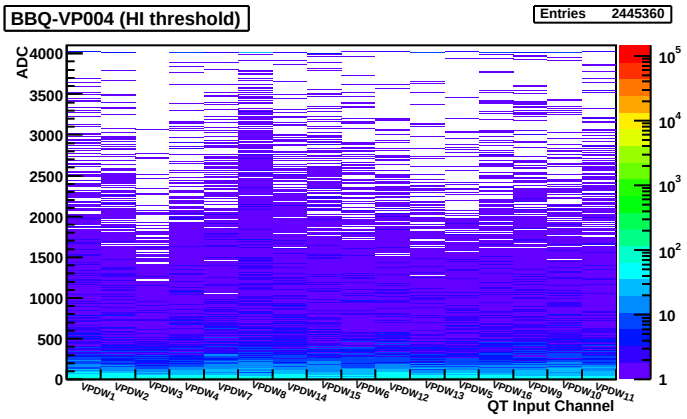

BBQ-VP001 (LO threshold)
Entries 2445360
TAC
QT Input Channel

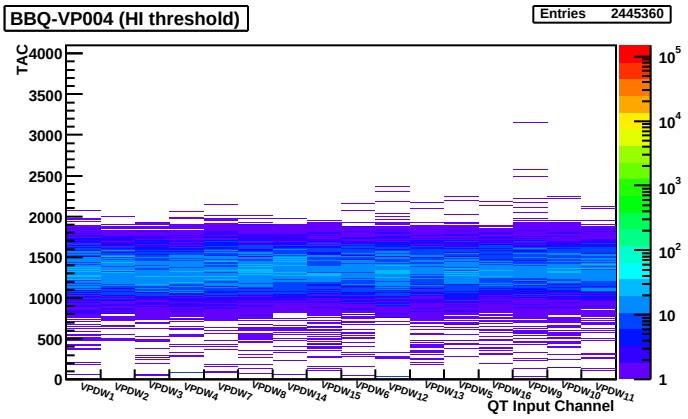

BBQ-VP002 (LO threshold)
Entries 2445360
ADC
QT Input Channel

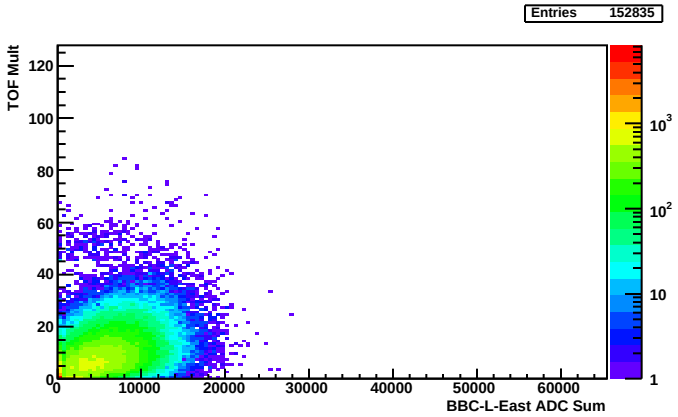

BBQ-VP002 (LO threshold)
Entries 2445360
TAC
QT Input Channel

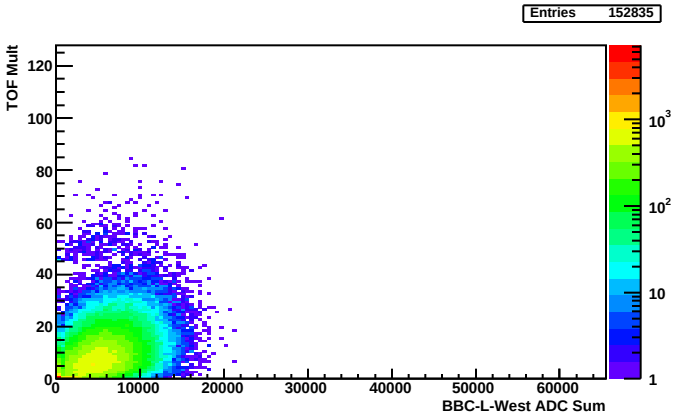

BBQ-VP003 (HI threshold)
Entries 2445360
ADC
QT Input Channel

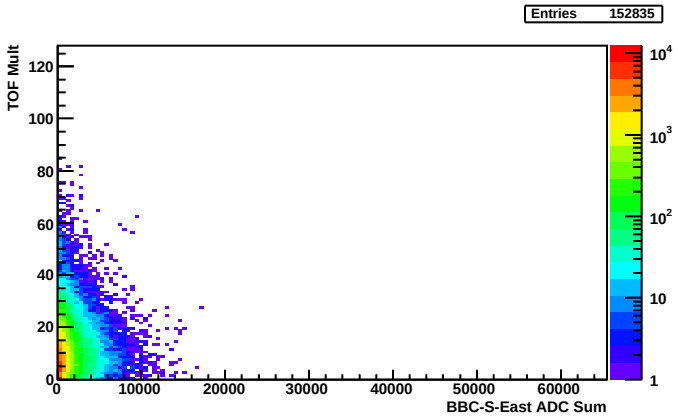

BBQ-VP003 (HI threshold)
Entries 2445360
TAC
QT Input Channel

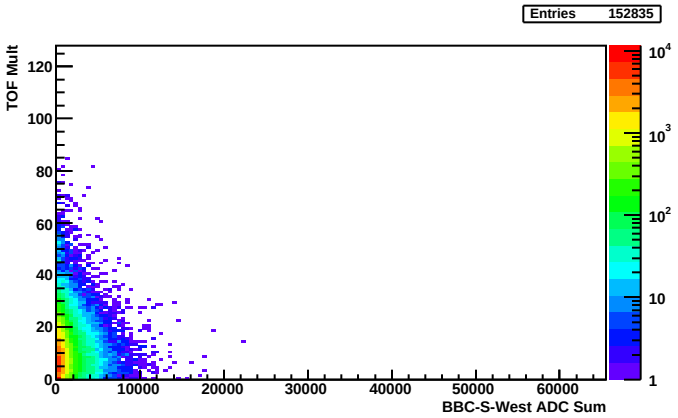

BBQ-VP004 (HI threshold)
Entries 2445360
ADC
QT Input Channel

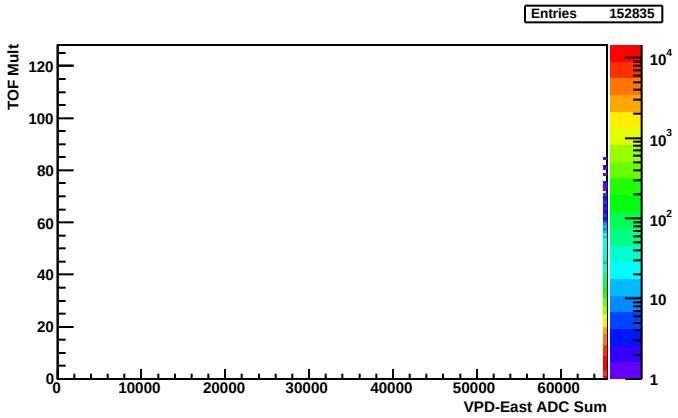

BBQ-VP004 (HI threshold)
Entries 2445360
TAC
QT Input Channel

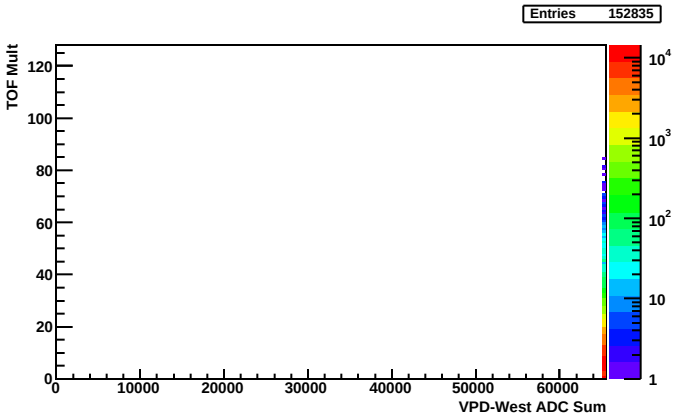

Entries 152835
TOF Mult
BBC-L-East ADC Sum

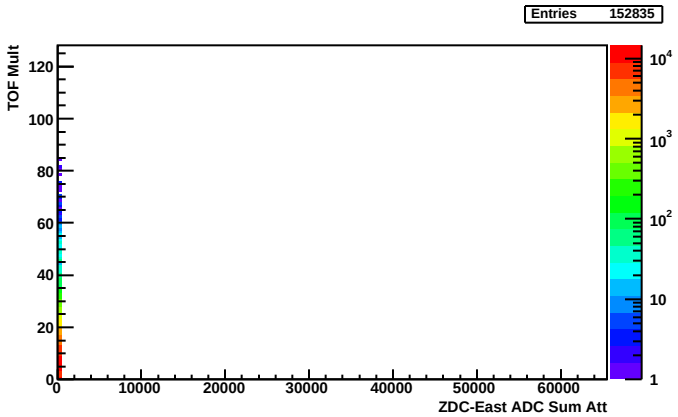

Entries 152835
TOF Mult
BBC-L-West ADC Sum

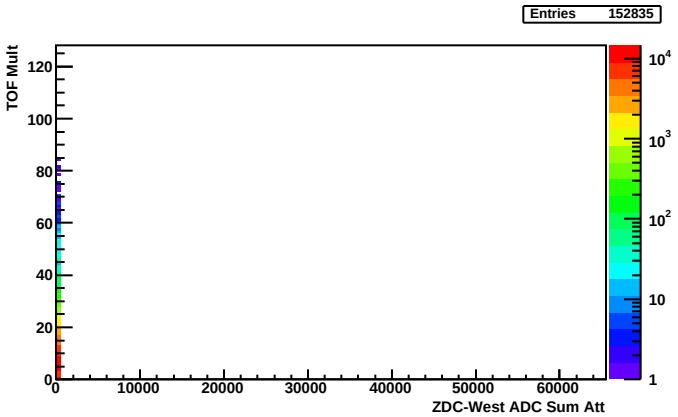

Entries 152835
TOF Mult
BBC-S-East ADC Sum

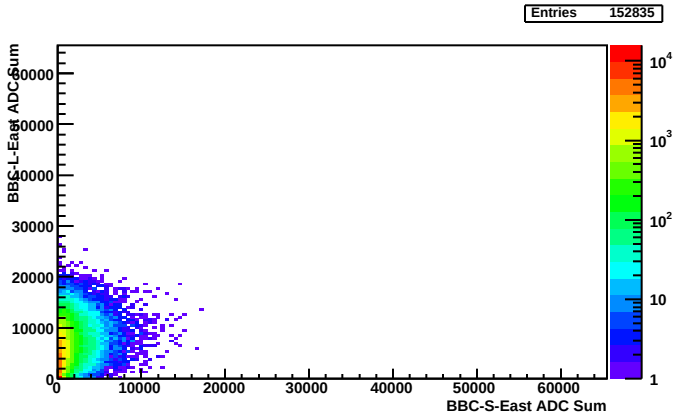

Entries 152835
TOF Mult
BBC-S-West ADC Sum

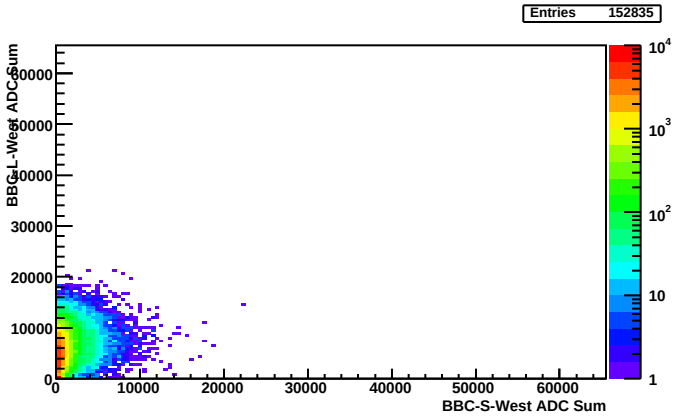

Entries 152835
TOF Mult
VPD-East ADC Sum

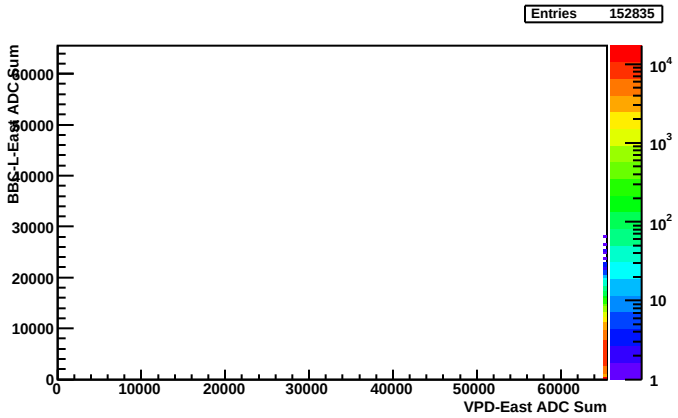

Entries 152835
TOF Mult
VPD-West ADC Sum

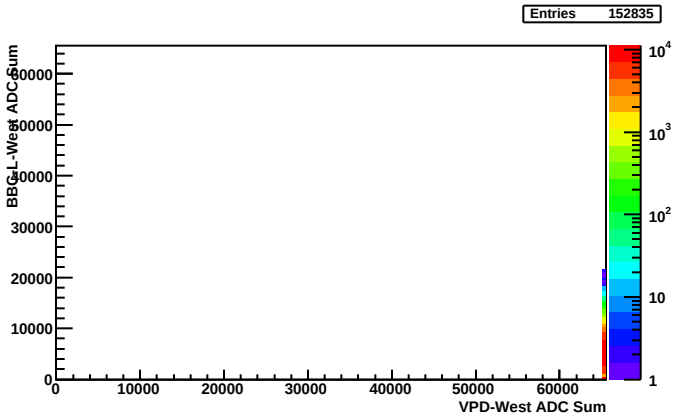

Entries 152835
TOF Mult
ZDC-East ADC Sum Att

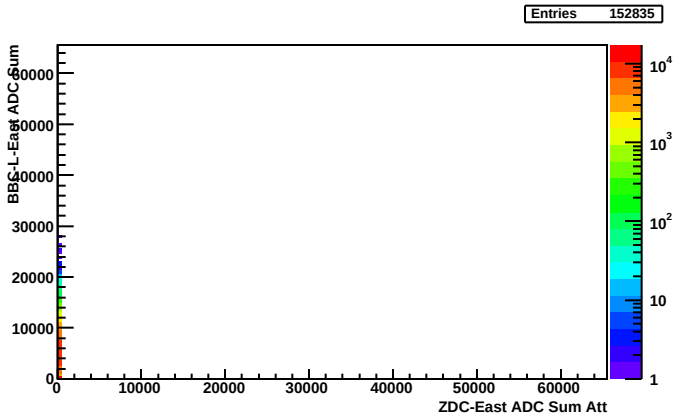

Entries 152835
TOF Mult
ZDC-West ADC Sum Att

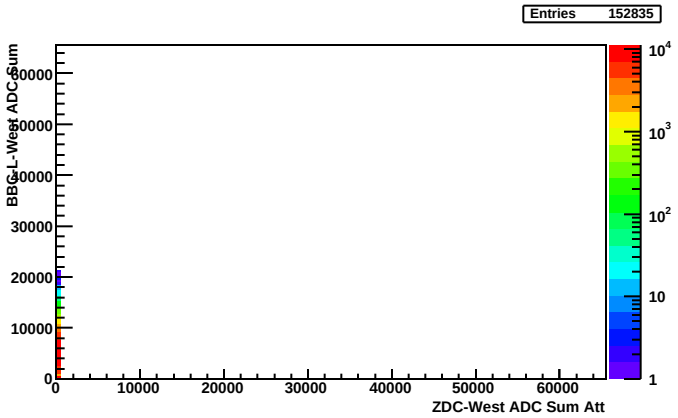

Entries 152835
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 152835
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 152835
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 152835
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 152835
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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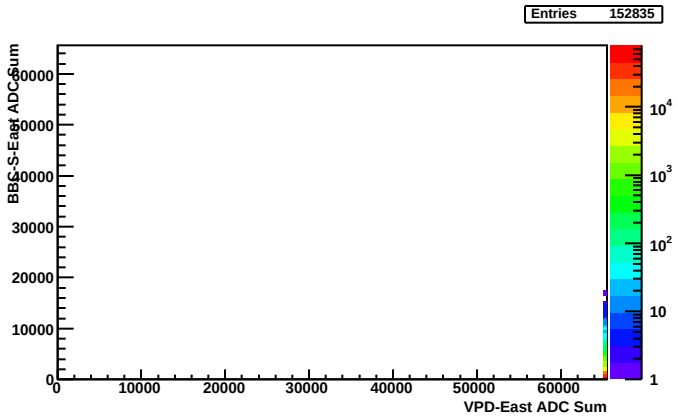
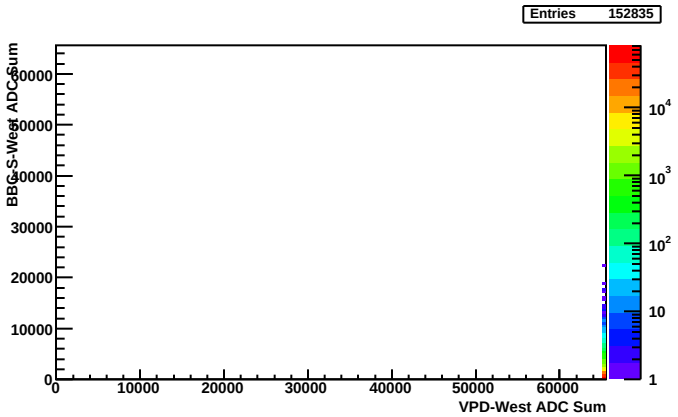
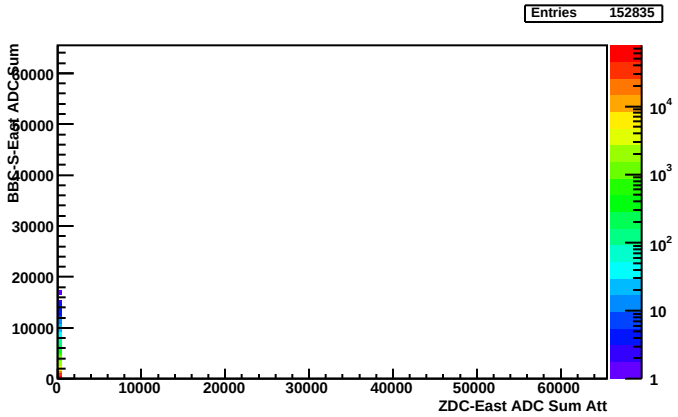
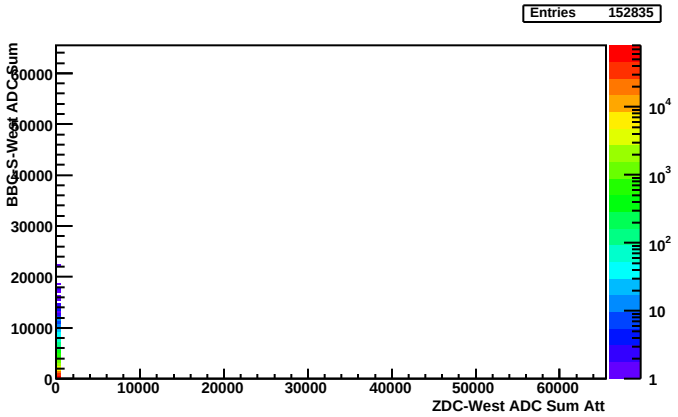
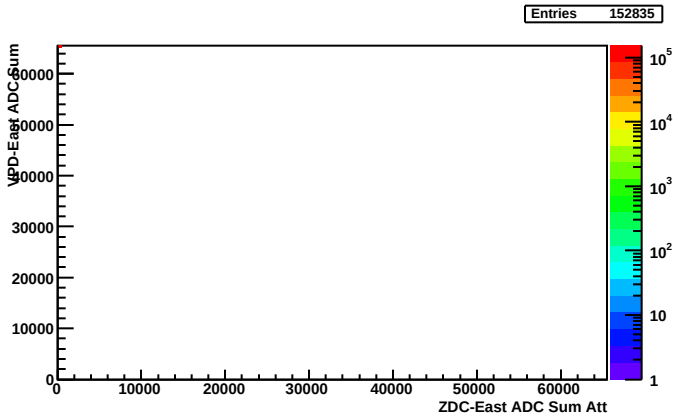
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

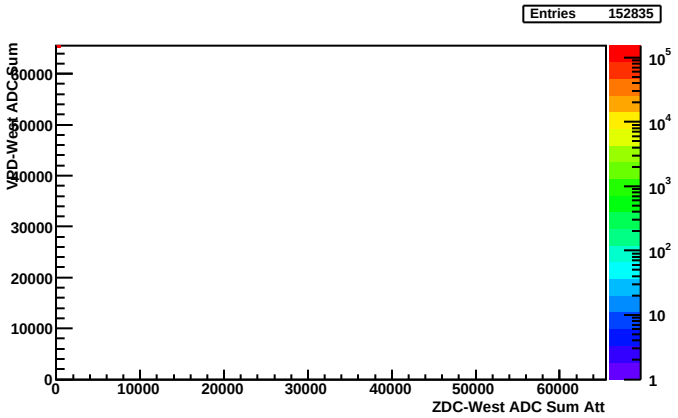

Entries 152835
BBC-S-East ADC Sum
VPD-East ADC Sum

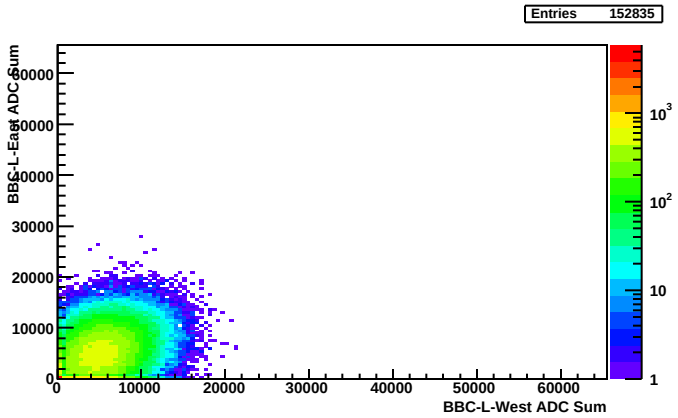

Entries 152835
BBC-S-West ADC Sum
VPD-West ADC Sum

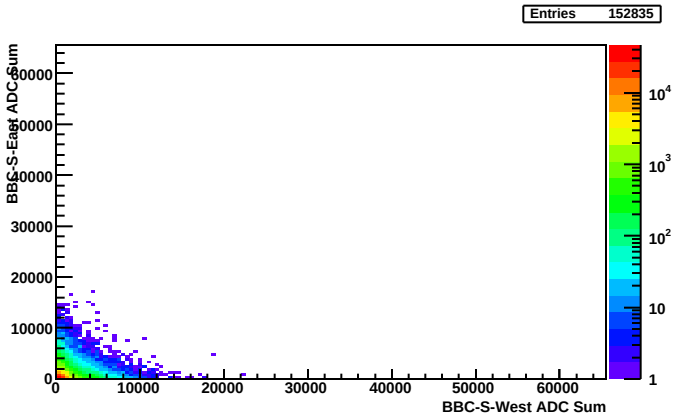

Entries 152835
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

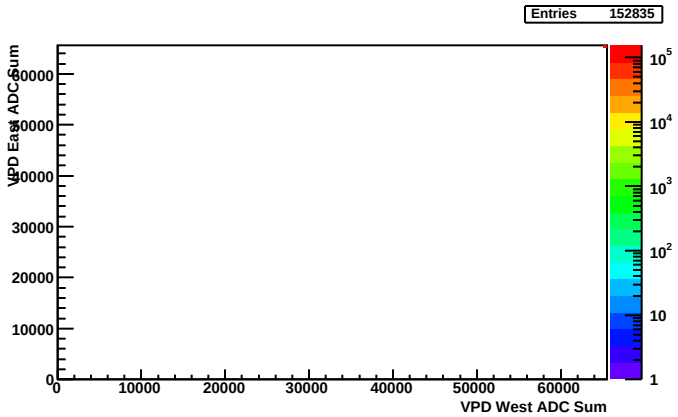

Entries 152835
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

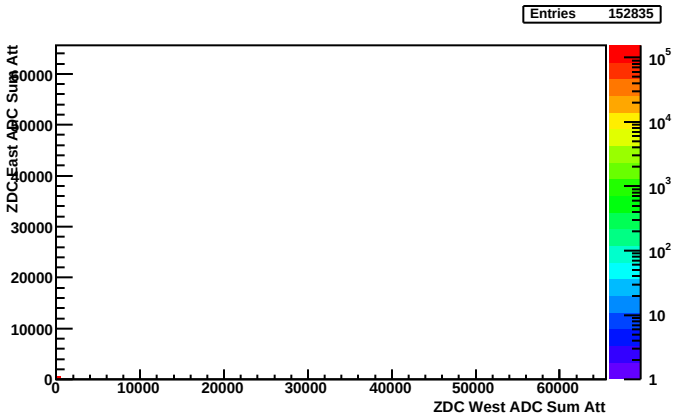

Entries 152835
VPD-East ADC Sum
ZDC-East ADC Sum Att

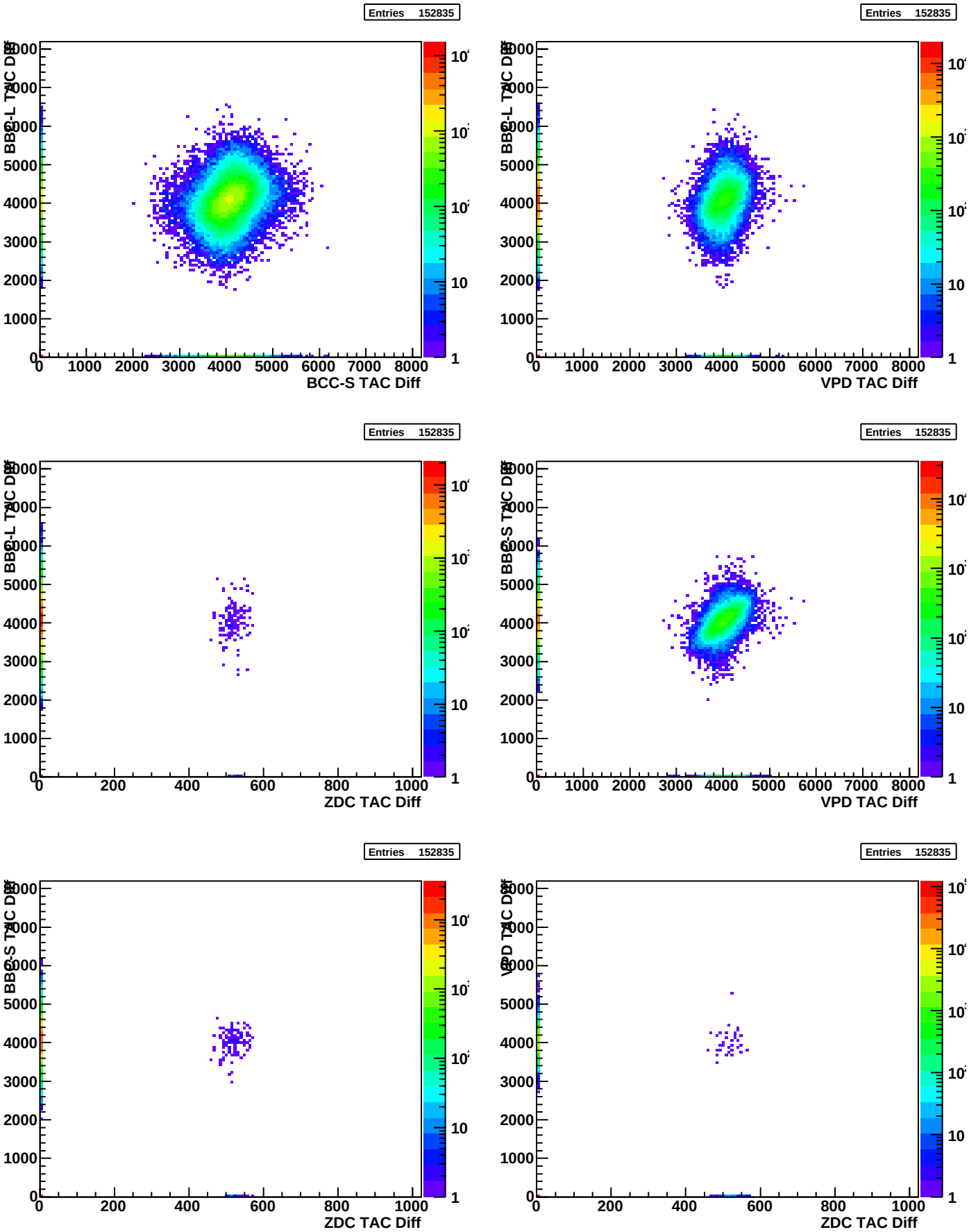

Entries 152835
VPD-West ADC Sum
ZDC-West ADC Sum Att

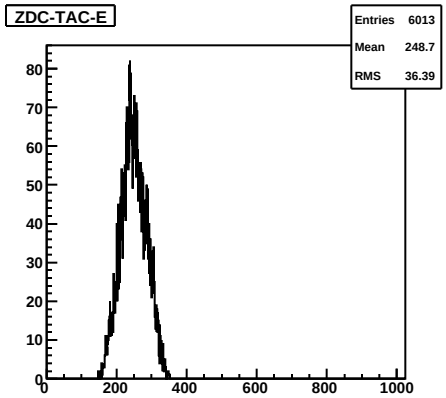

Entries 152835
BBC-L-East ADC Sum
BBC-L-West ADC Sum

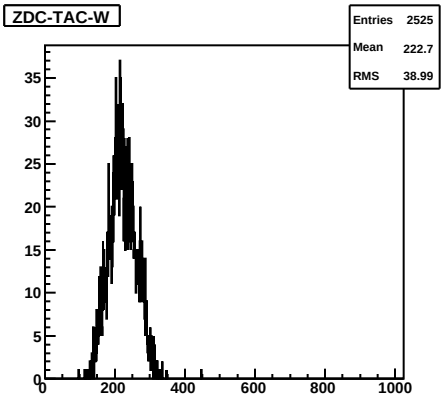

Entries 152835
BBC-S-East ADC Sum
BBC-S-West ADC Sum

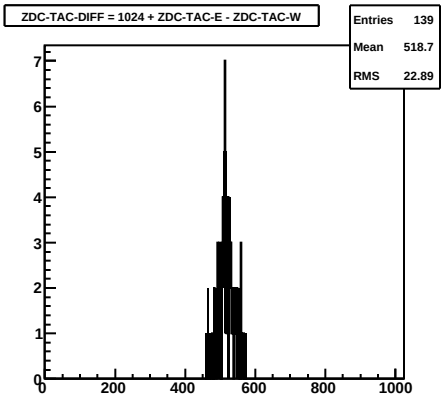

Entries 152835
VPD East ADC Sum
VPD West ADC Sum

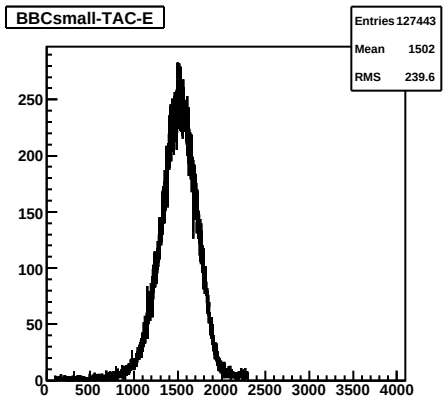

Entries 152835
ZDC East ADC Sum Att
ZDC West ADC Sum Att

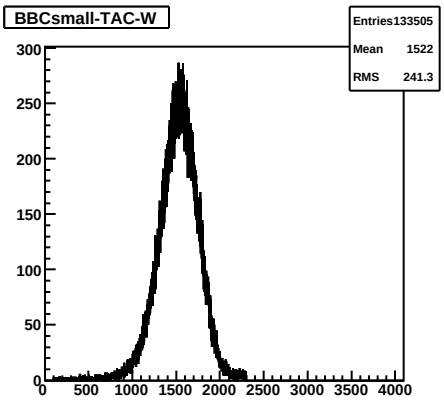

Entries 152835
BBC-L TAC Diff
BCC-S TAC Diff
Entries 152835
BBC-L TAC Diff
VPD TAC Diff
Entries 152835
BBC-L TAC Diff
ZDC TAC Diff
Entries 152835
BBC-S TAC Diff
VPD TAC Diff
Entries 152835
BBC-S TAC Diff
ZDC TAC Diff
Entries 152835
VPD TAC Diff
ZDC TAC Diff

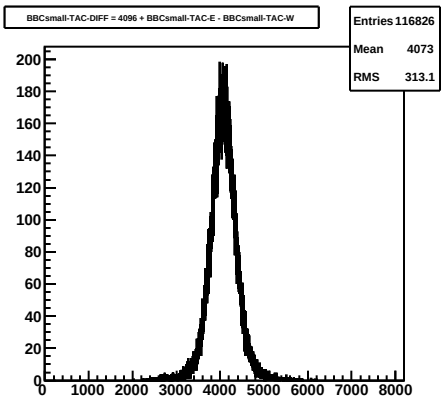

ZDC-TAC-E
Entries 6013
Mean 248.7
RMS 36.39

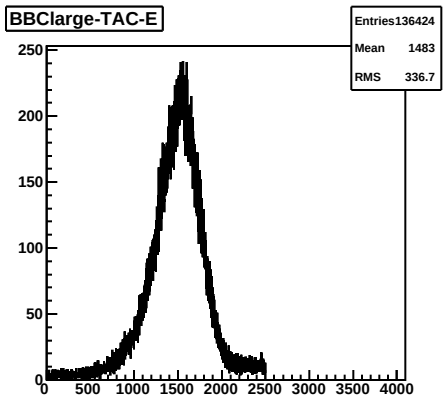

ZDC-TAC-W
Entries 2525
Mean 222.7
RMS 38.99

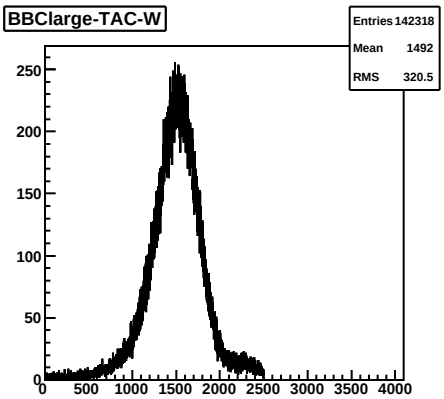

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 139
Mean 518.7
RMS 22.89

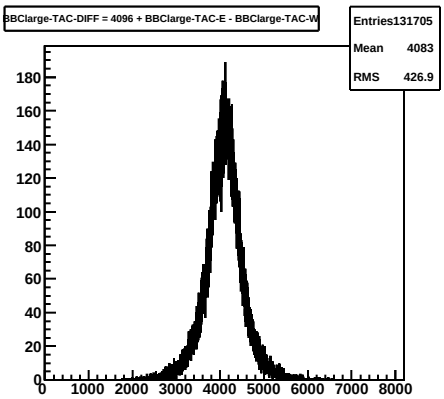

BBCsmall-TAC-E
Entries 127443
Mean 1502
RMS 239.6

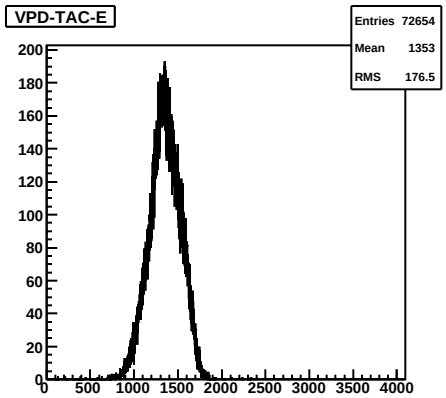

BBCsmall-TAC-W
Entries 133505
Mean 1522
RMS 241.3

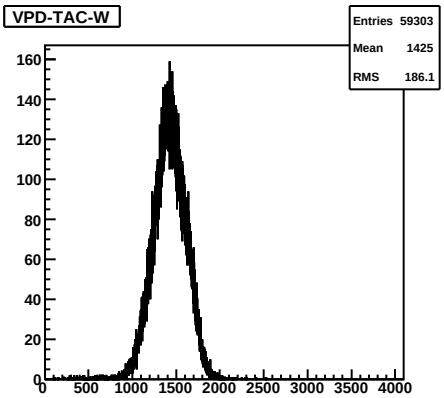

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 116826
Mean 4073
RMS 313.1

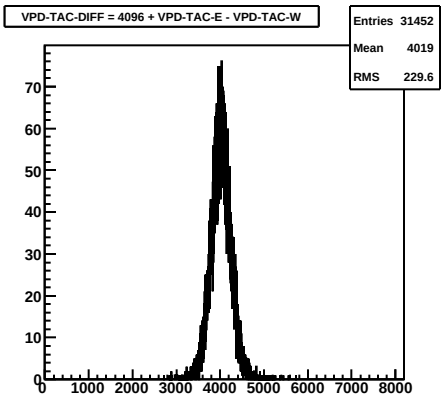

BBClarge-TAC-E
Entries 136424
Mean 1483
RMS 336.7

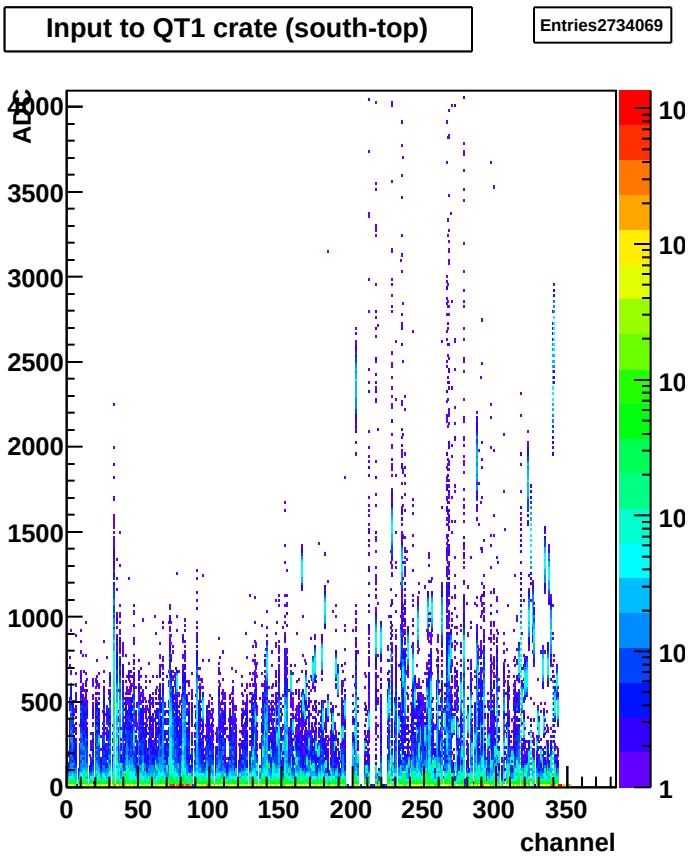

BBClarge-TAC-W
Entries 142318
Mean 1492
RMS 320.5

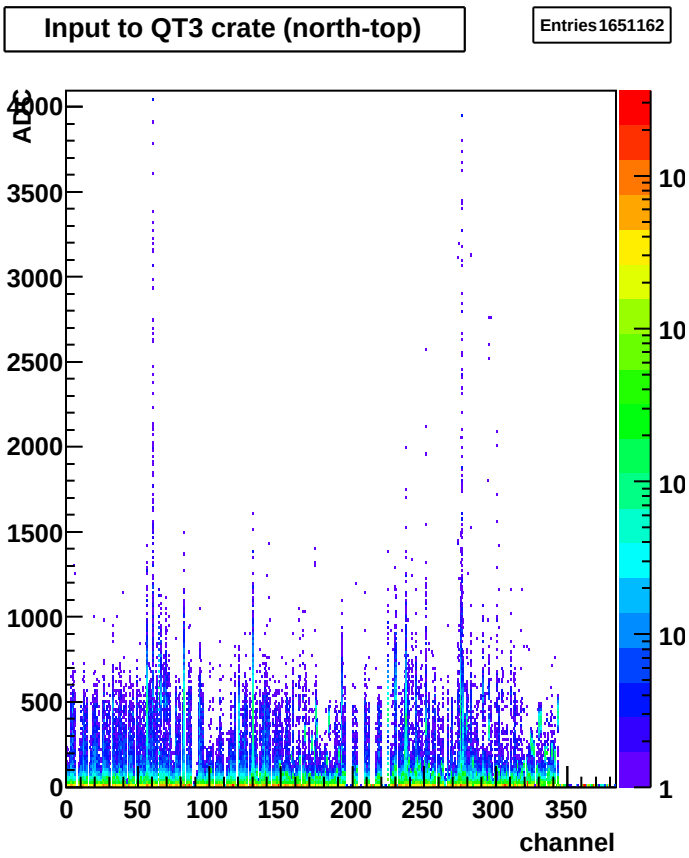

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 131705
Mean 4083
RMS 426.9

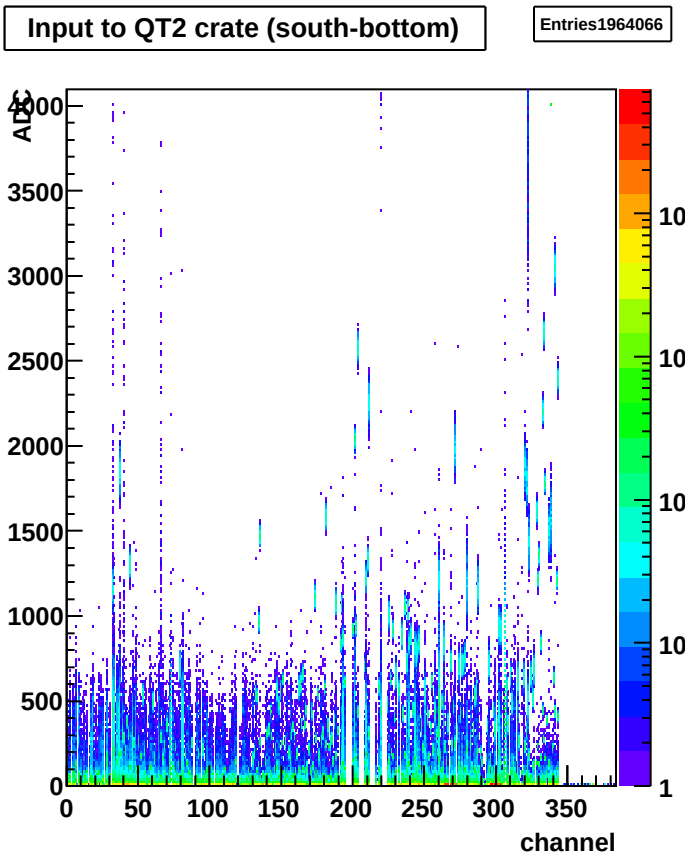

VPD-TAC-E
Entries 72654
Mean 1353
RMS 176.5

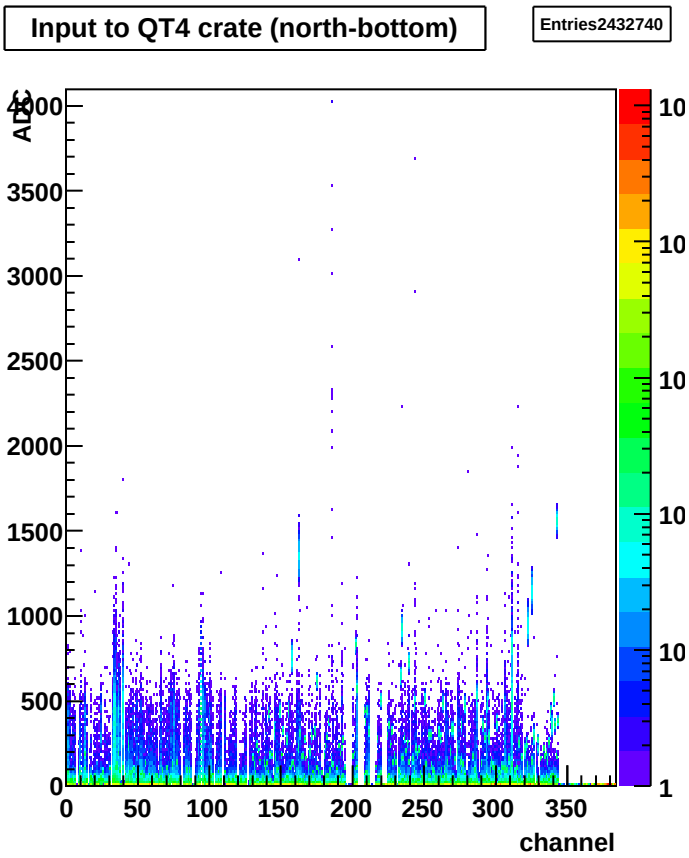

VPD-TAC-W
Entries 59303
Mean 1425
RMS 186.1

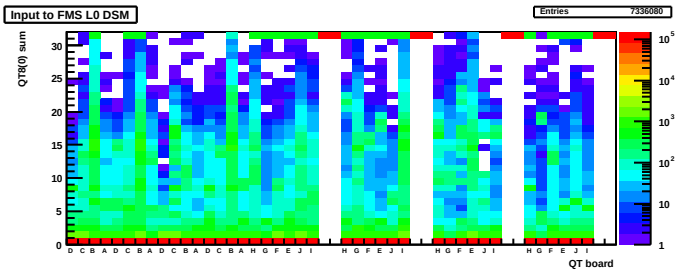

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 31452
Mean 4019
RMS 229.6

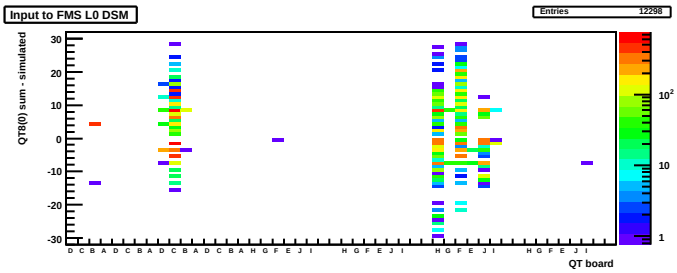

Input to QT1 crate (south-top)
Entries2734069
ADC
channel

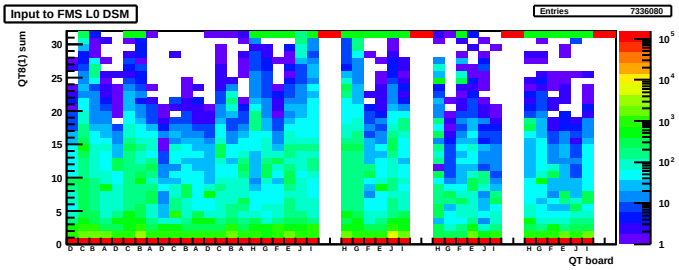

Input to QT3 crate (north-top)
Entries1651162
ADC
channel

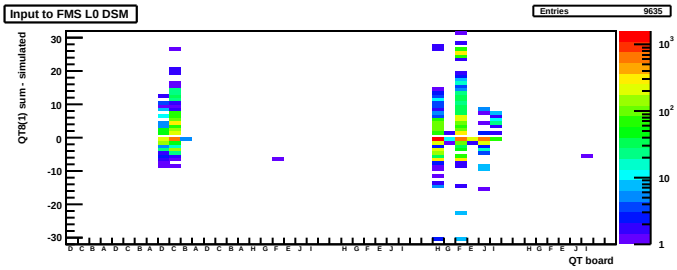

Input to QT2 crate (south-bottom)
Entries1964066
ADC
channel

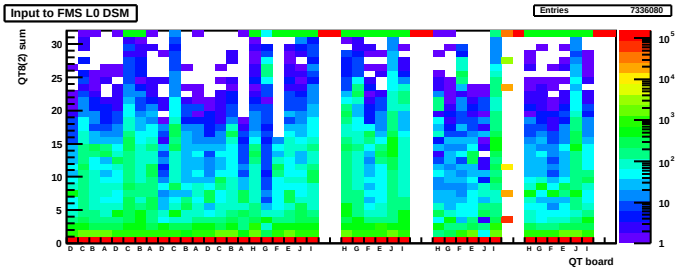

Input to QT4 crate (north-bottom)
Entries2432740
ADC
channel

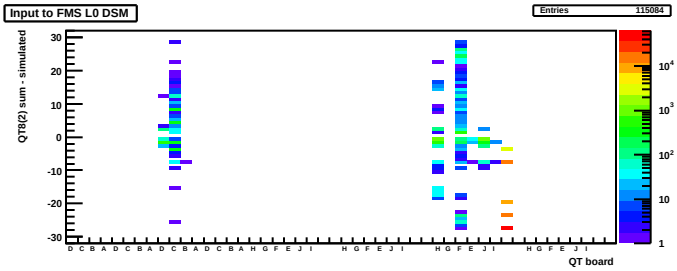

Input to FMS L0 DSM
Entries 7336080
QT8(0) sum
QT board

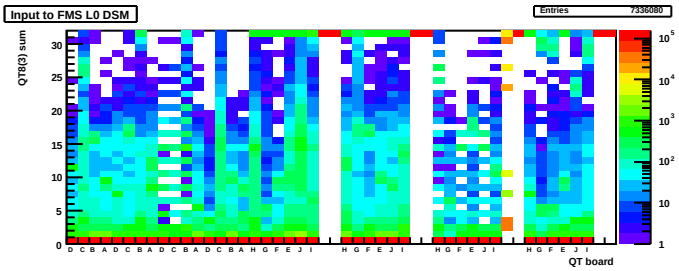

Input to FMS L0 DSM
Entries 12298
QT8(0) sum - simulated
QT board

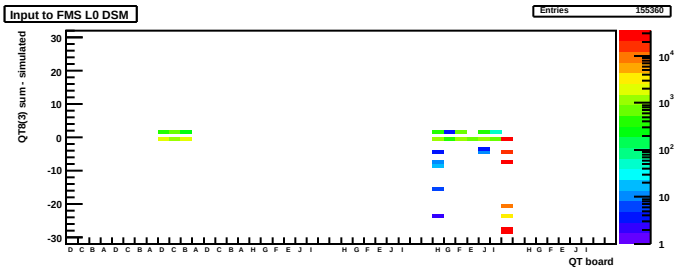

Input to FMS L0 DSM
Entries 7336080
QT8(1) sum
QT board

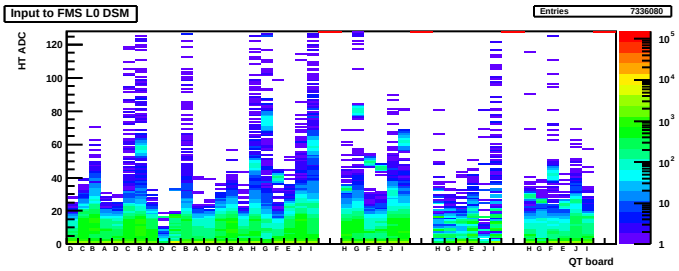

Input to FMS L0 DSM
Entries 9635
QT8(1) sum - simulated
QT board

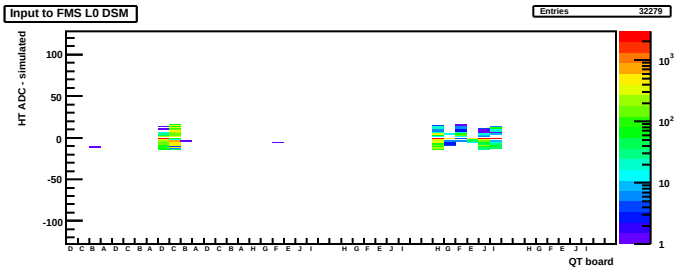

Input to FMS L0 DSM
Entries 7336080
QT8(2) sum
QT board

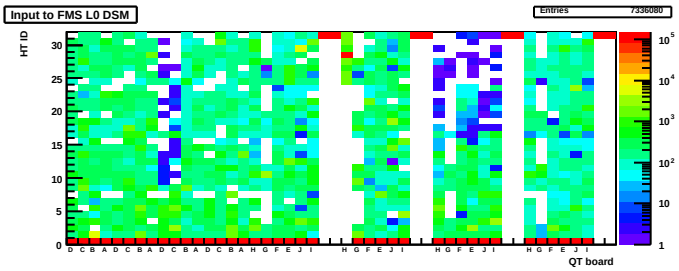

Input to FMS L0 DSM
Entries 115084
QT8(2) sum - simulated
QT board

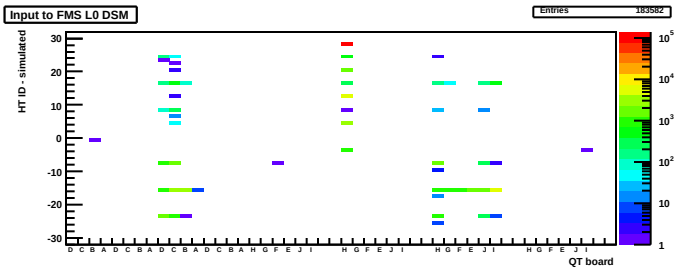

Input to FMS L0 DSM
Entries 7336080
QT8(3) sum
QT board

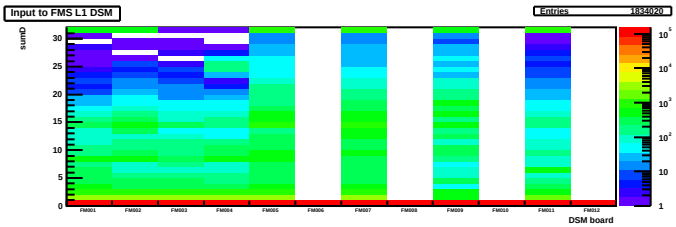

Input to FMS L0 DSM
Entries 155360
QT8(3) sum - simulated
QT board

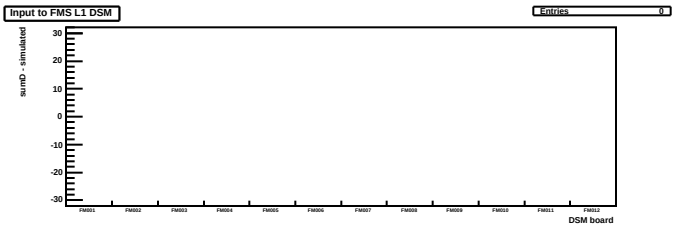

Input to FMS L0 DSM
Entries 7336080
HT ADC
QT board

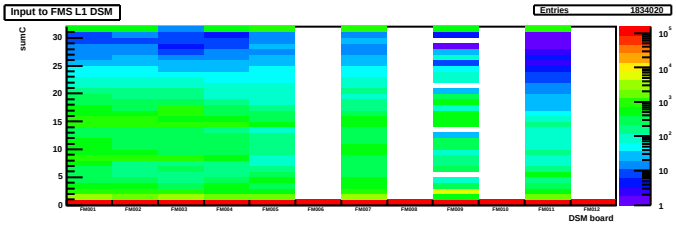

Input to FMS L0 DSM
Entries 32279
HT ADC - simulated
QT board

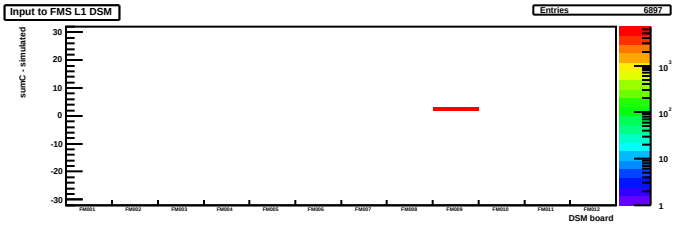

Input to FMS L0 DSM
Entries 7336080
HT ID
QT board

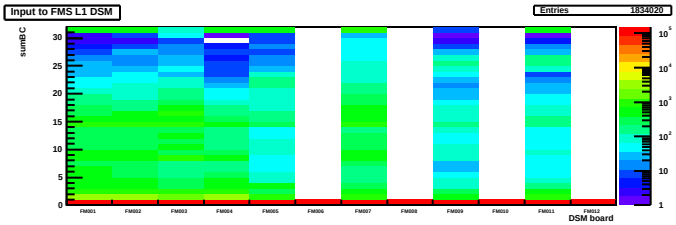

Input to FMS L0 DSM
Entries 183582
HT ID - simulated
QT board

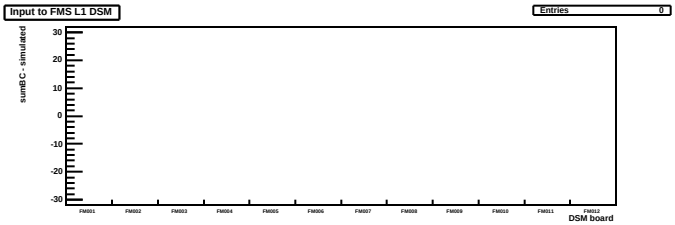

Input to FMS L1 DSM
Entries 1834020
sumD
DSM board

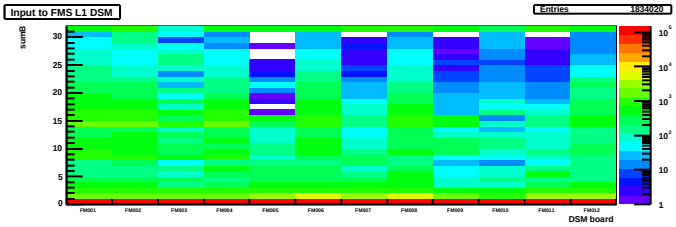

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

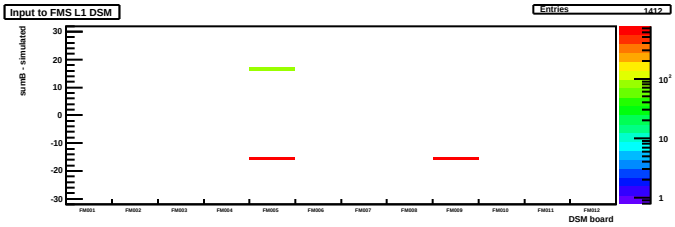

Input to FMS L1 DSM
Entries 1834020
sumC
DSM board

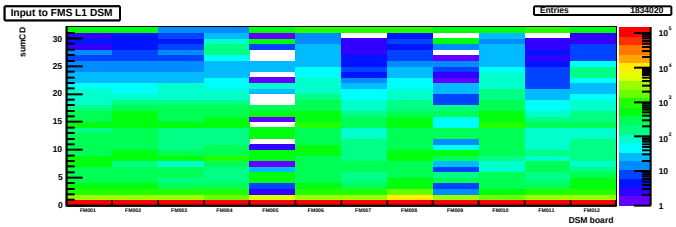

Input to FMS L1 DSM
Entries 6897
sumC - simulated
DSM board

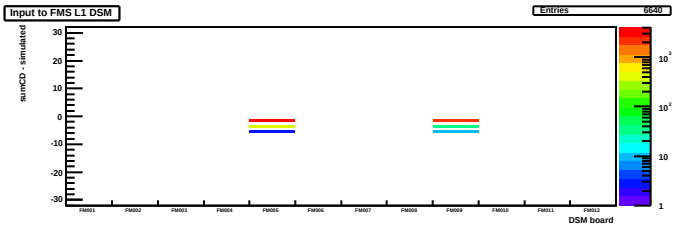

Input to FMS L1 DSM
Entries 1834020
sumBC
DSM board

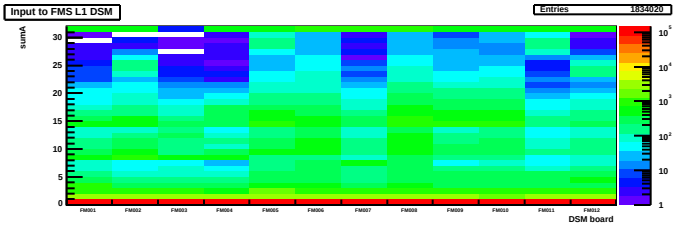

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

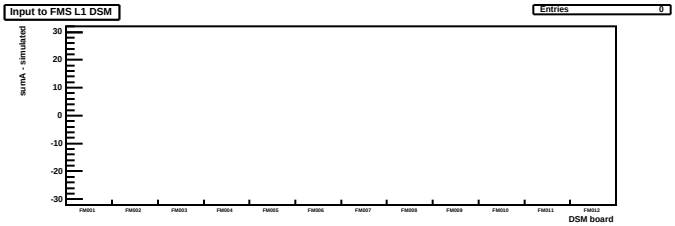

Input to FMS L1 DSM
Entries 1834020
sumB
DSM board

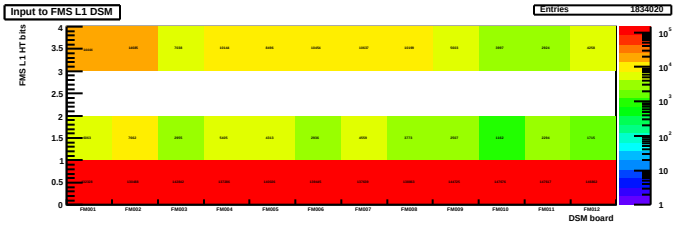

Input to FMS L1 DSM
Entries 1412
sumB - simulated
DSM board

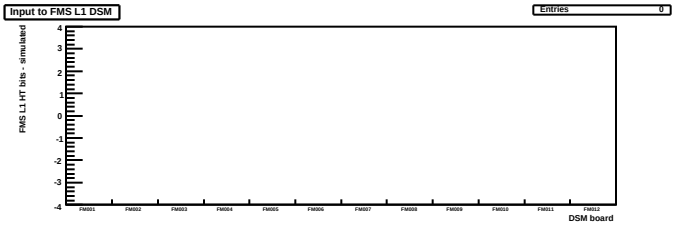

Input to FMS L1 DSM
Entries 1834020
sumCD
DSM board

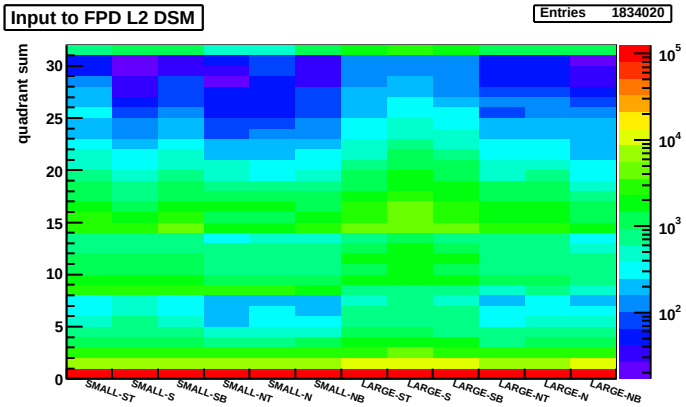

Input to FMS L1 DSM
Entries 6640
sumCD - simulated
DSM board

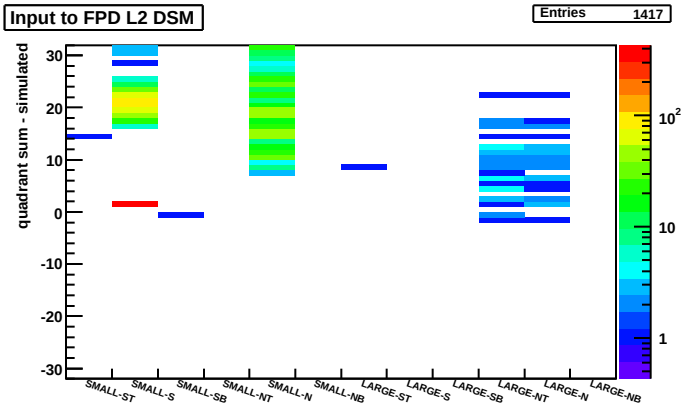

Input to FMS L1 DSM
Entries 1834020
sumA
DSM board

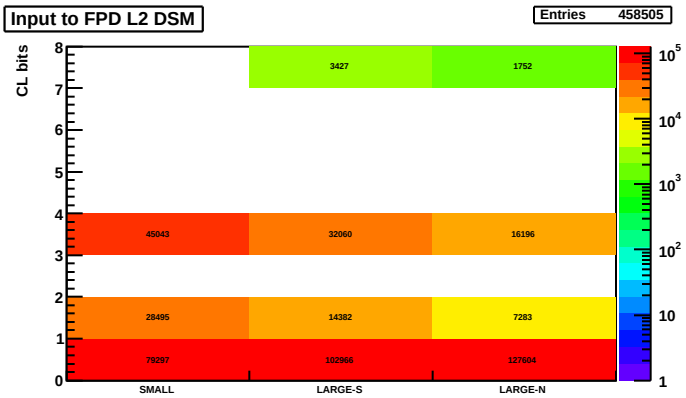

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

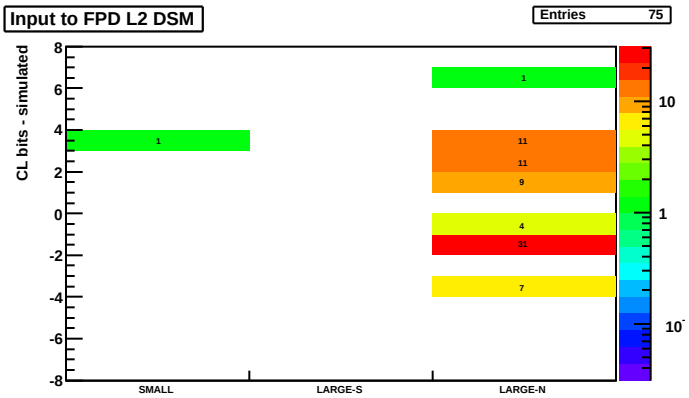

Input to FMS L1 DSM
Entries 1834020
FMS L1 HT bits
DSM board

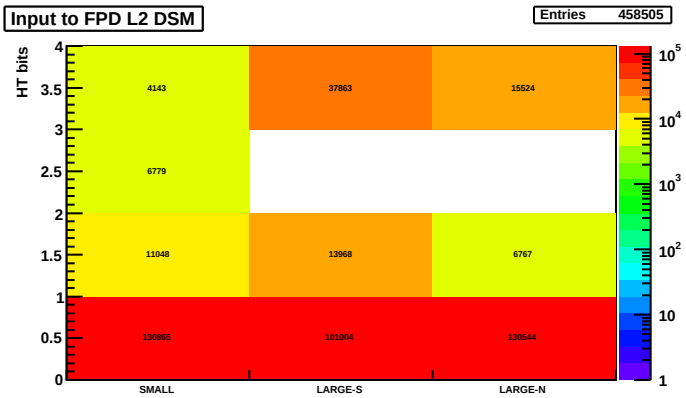

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

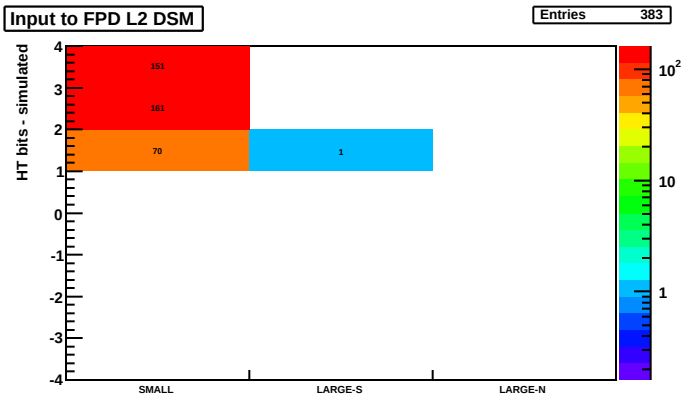

Input to FPD L2 DSM
Entries 1834020
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

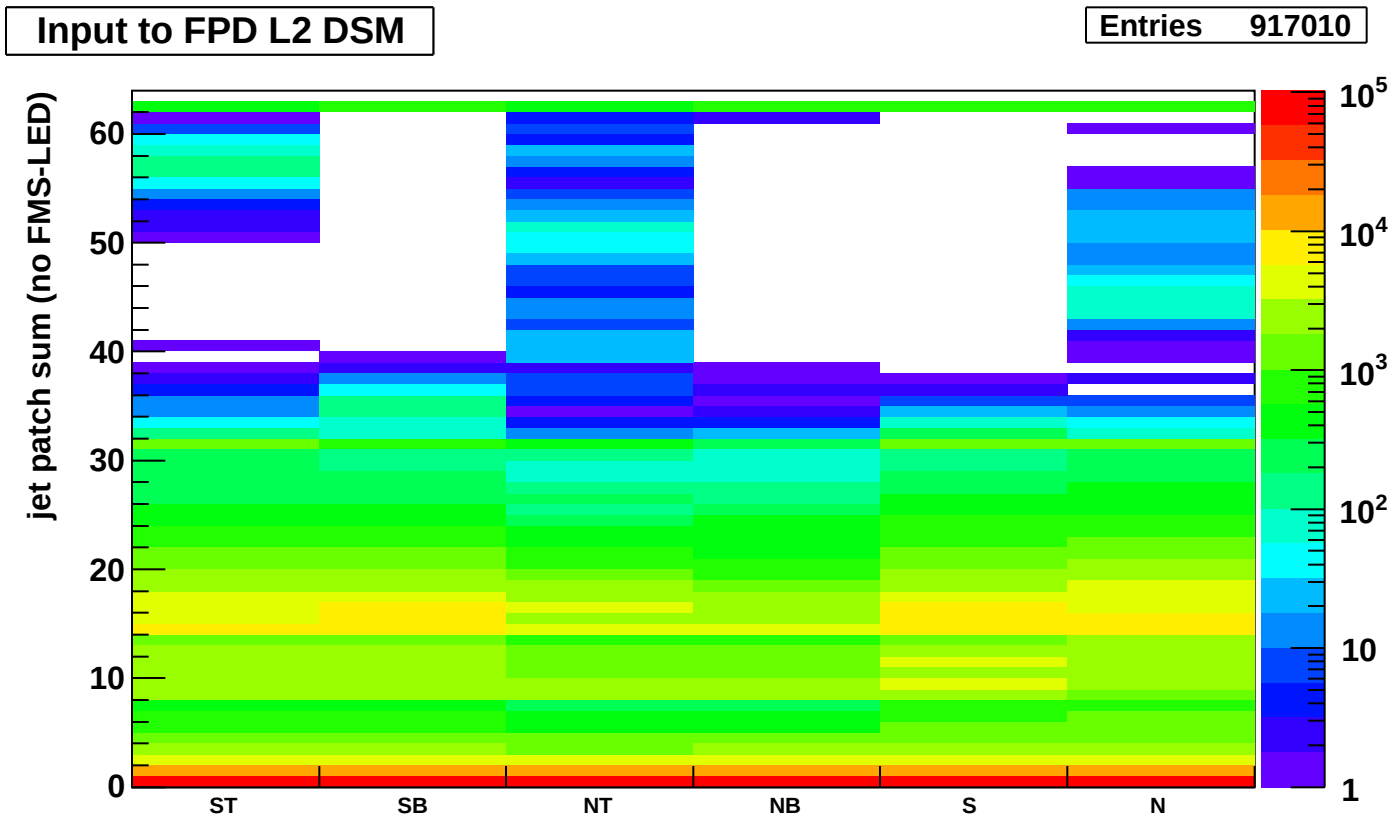

Input to FPD L2 DSM
Entries 1417
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

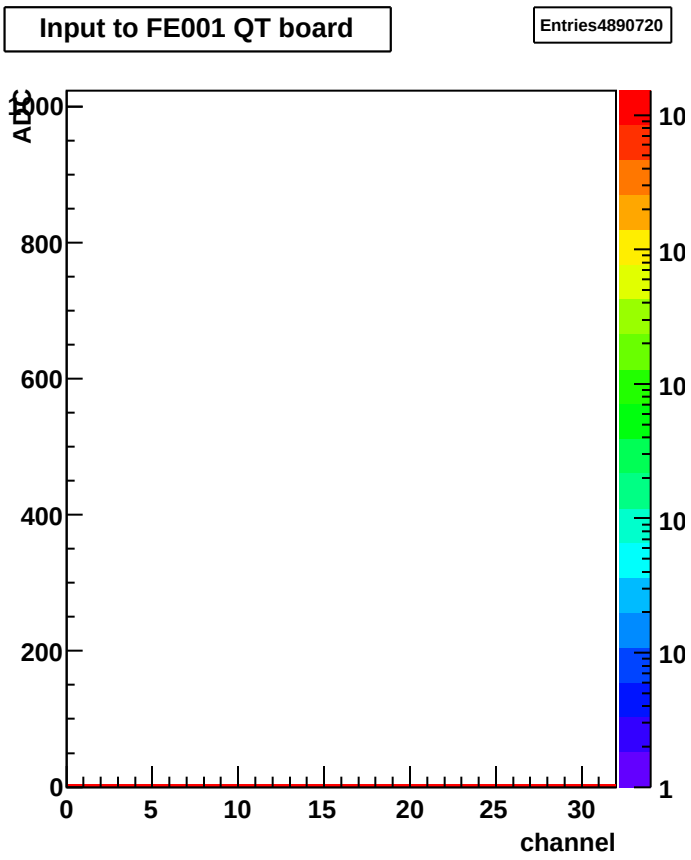

Input to FPD L2 DSM
Entries 458505
CL bits
3427
1752
45043
32060
16196
28495
14382
7283
79297
102966
127604
SMALL
LARGE-S
LARGE-N

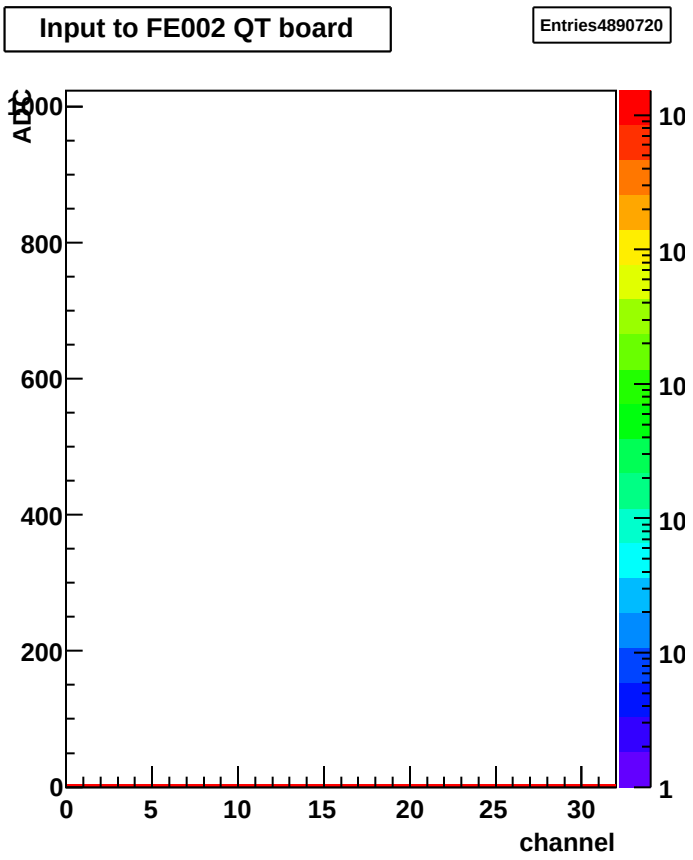

Input to FPD L2 DSM
Entries 75
CL bits - simulated
1
1
11
11
9
4
31
7
SMALL
LARGE-S
LARGE-N

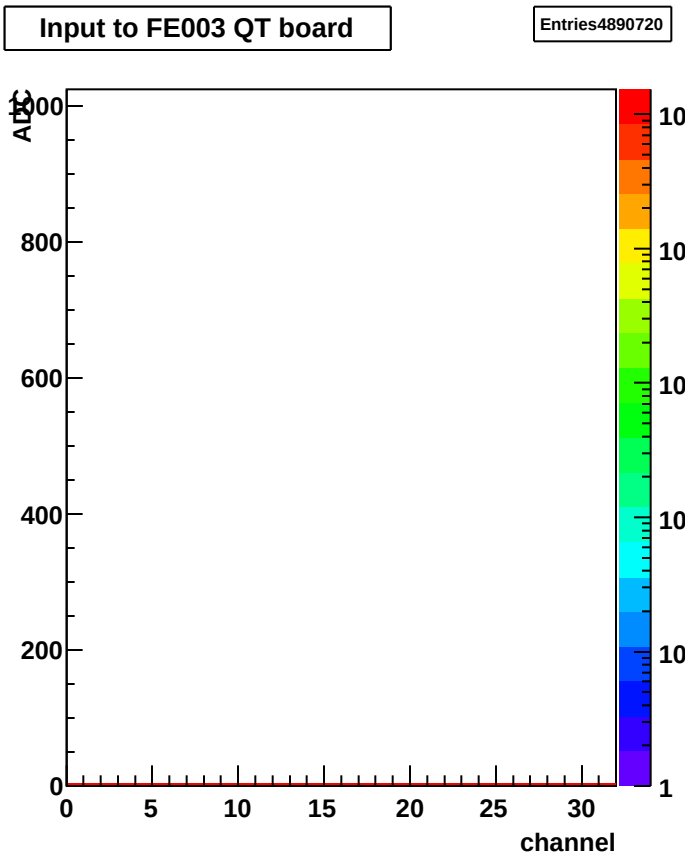

Input to FPD L2 DSM
Entries 458505
HT bits
4143
37863
15524
6779
11048
13968
6767
130865
101004
130544
SMALL
LARGE-S
LARGE-N

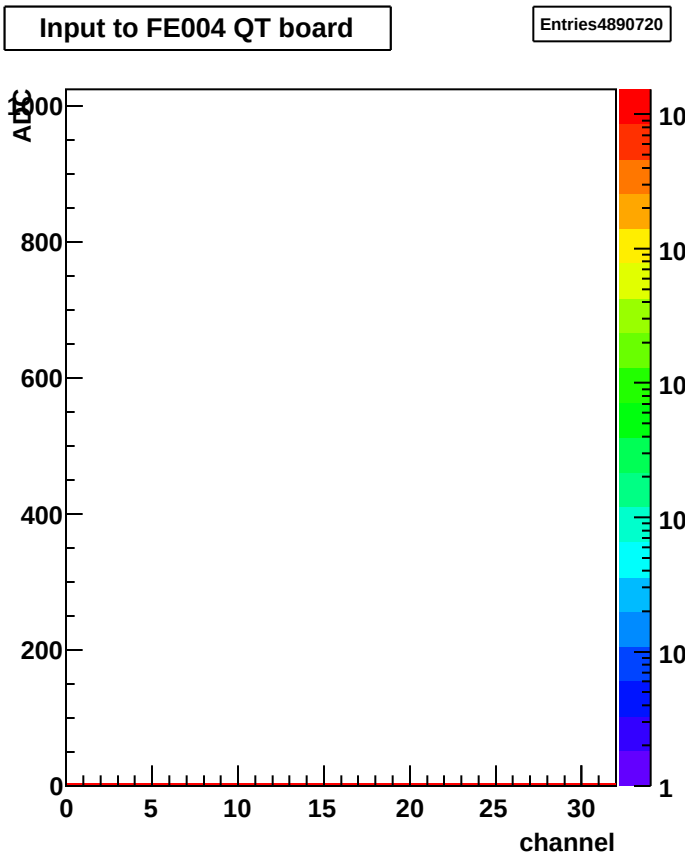

Input to FPD L2 DSM
Entries 383
HT bits - simulated
151
161
70
1
SMALL
LARGE-S
LARGE-N

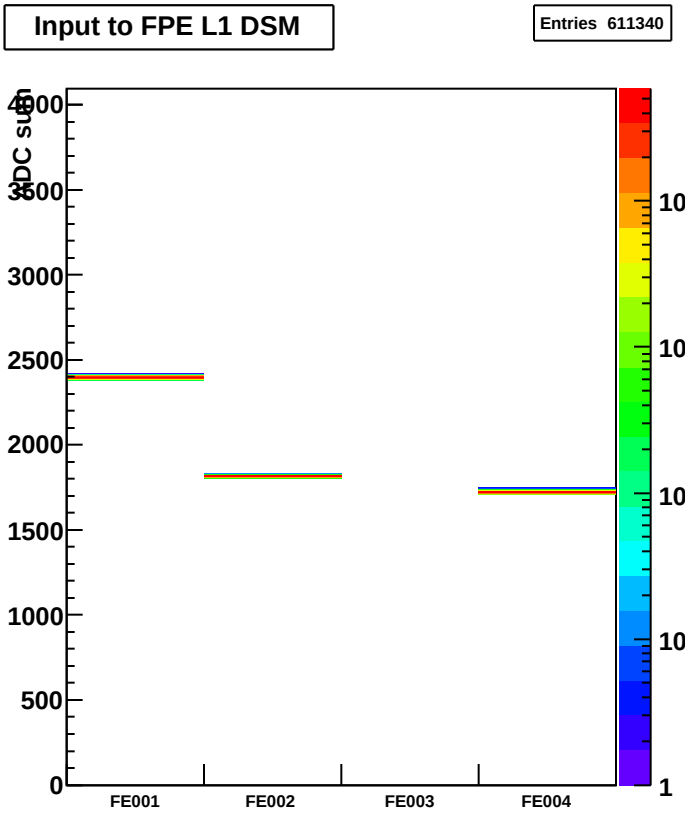

Input to FPD L2 DSM
Entries 917010
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

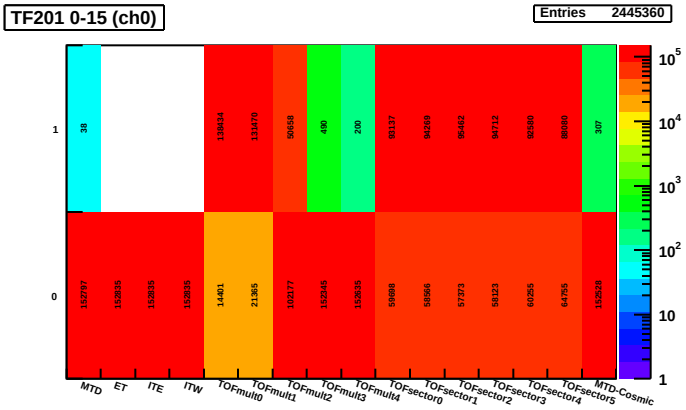

Input to FE001 QT board
Entries4890720
ADC
channel

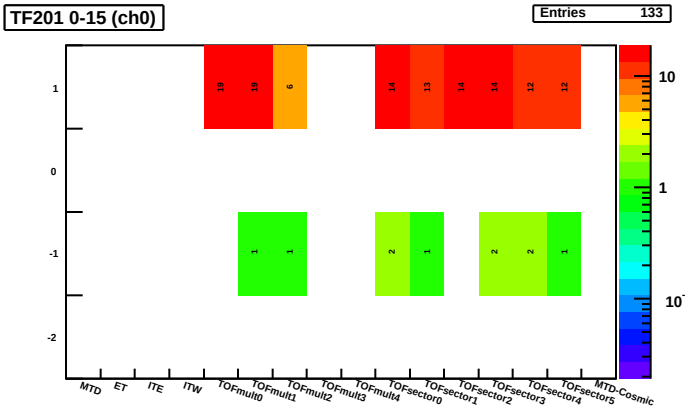

Input to FE002 QT board
Entries4890720
ADC
channel

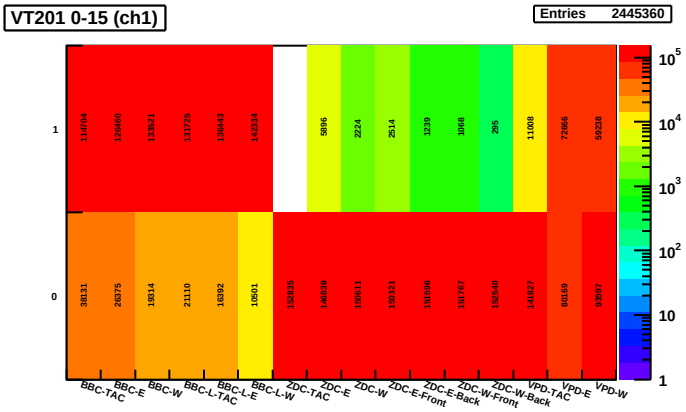

Input to FE003 QT board
Entries4890720
ADC
channel

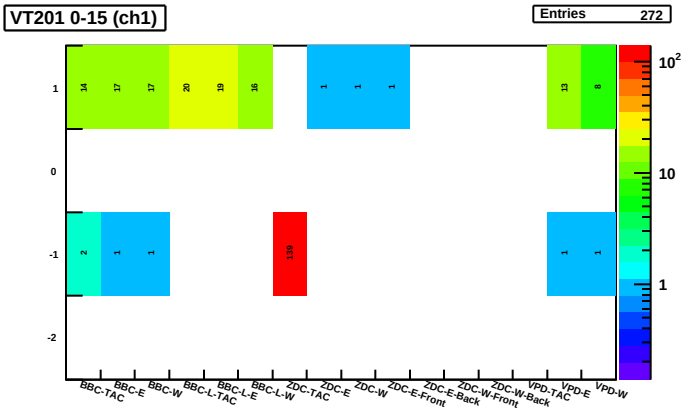

Input to FE004 QT board
Entries4890720
ADC
channel

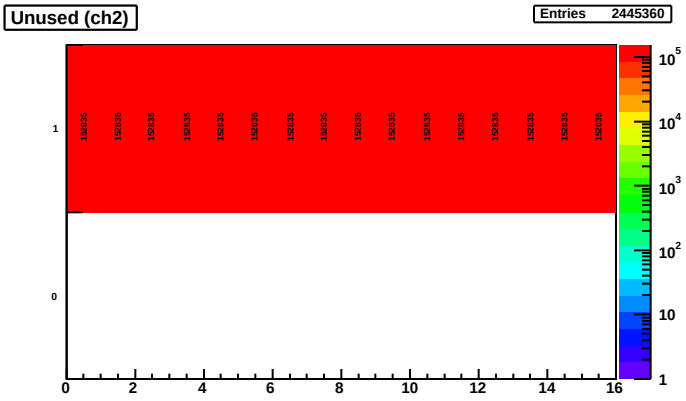

Input to FPE L1 DSM
Entries 611340
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

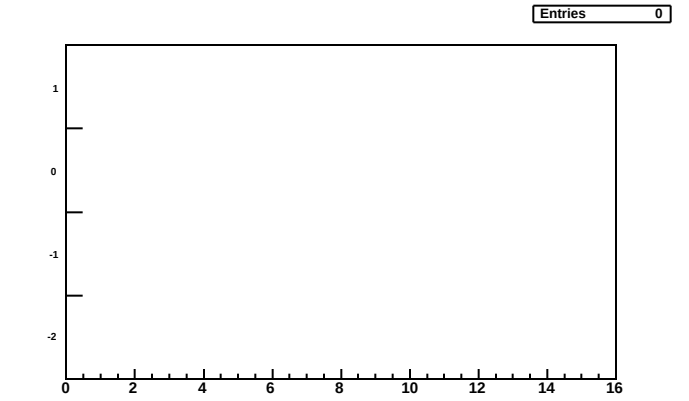

TF201 0-15 (ch0)
Entries 2445360
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 133
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 2445360
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 272
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2445360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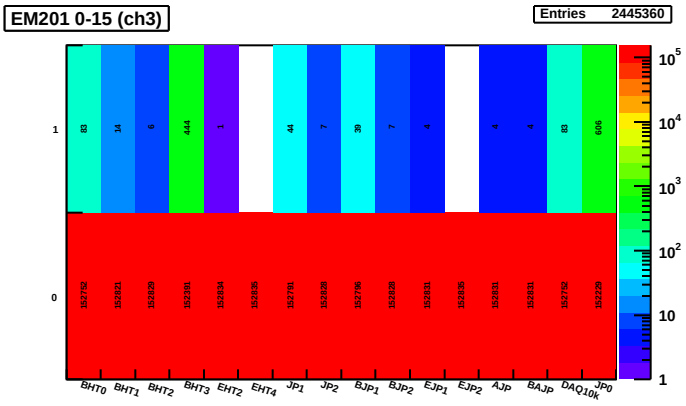
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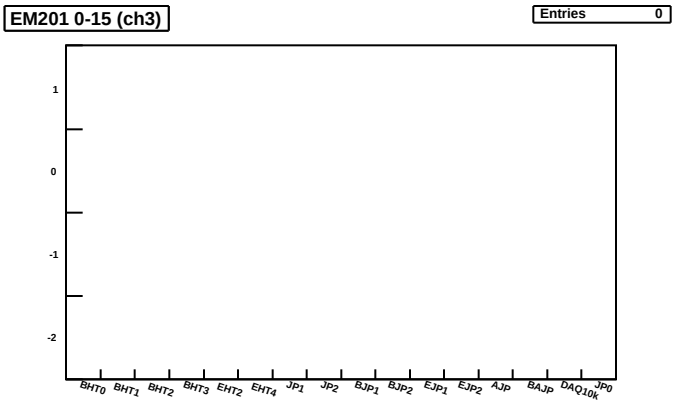
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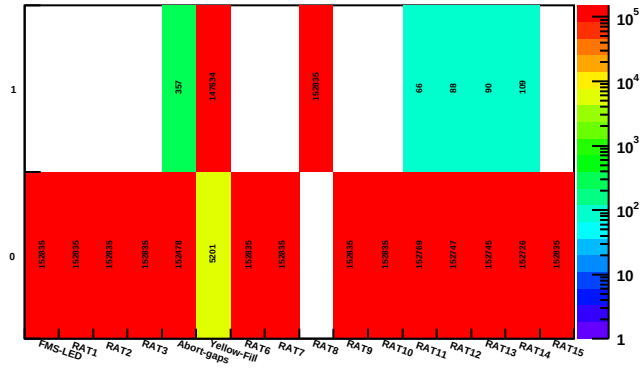
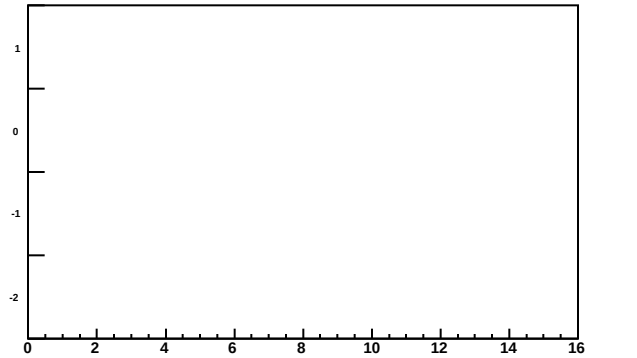
RAT board (ch4)





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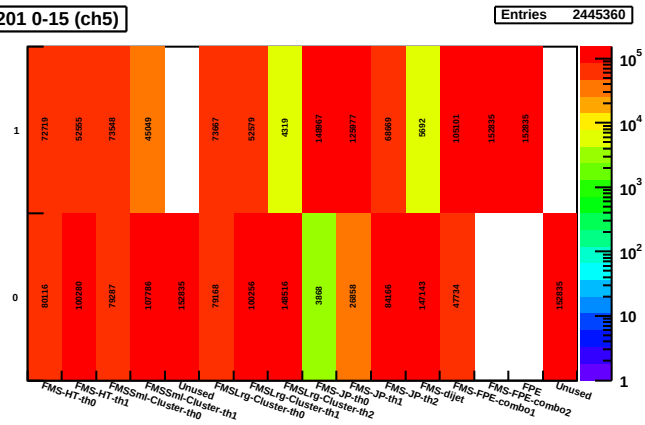

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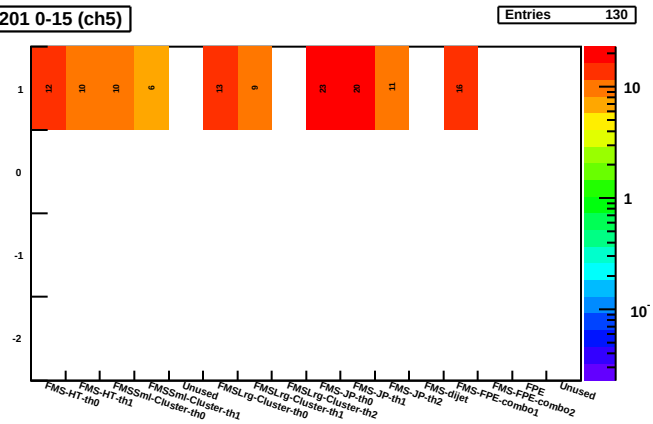

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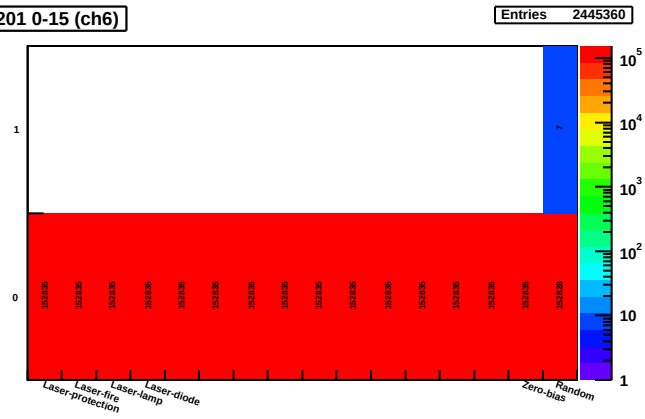

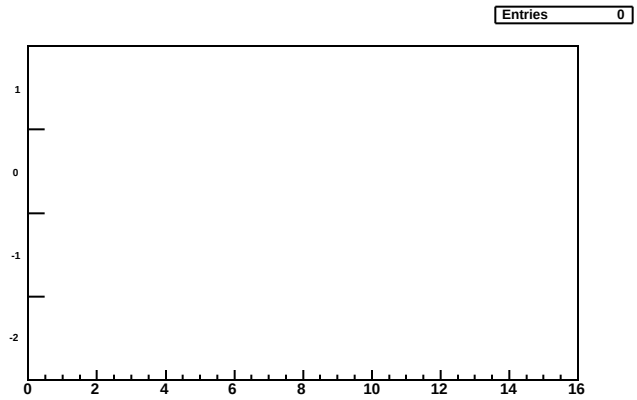
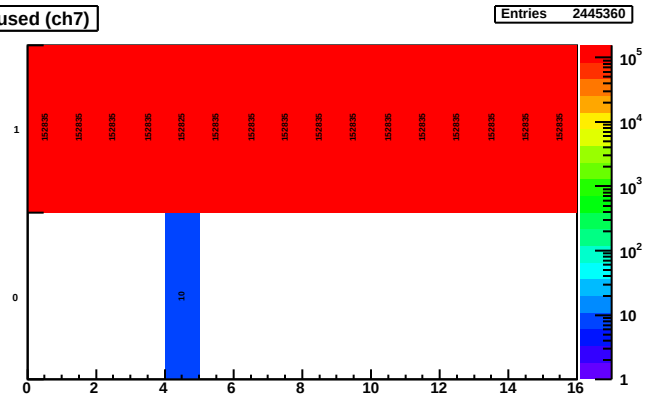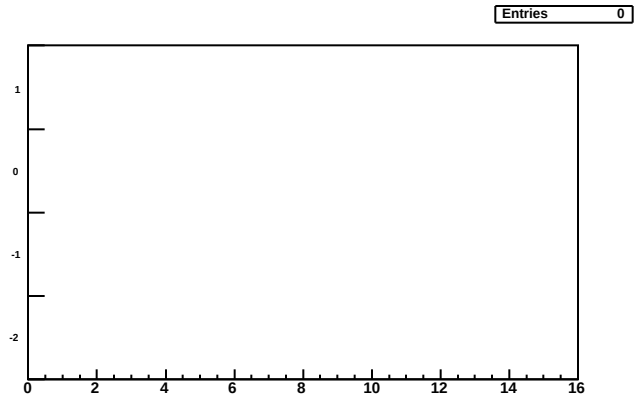
Unused (ch7)