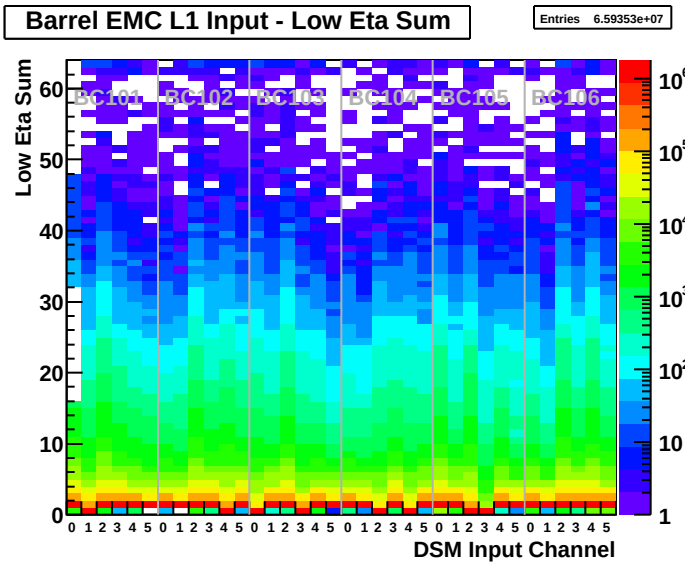
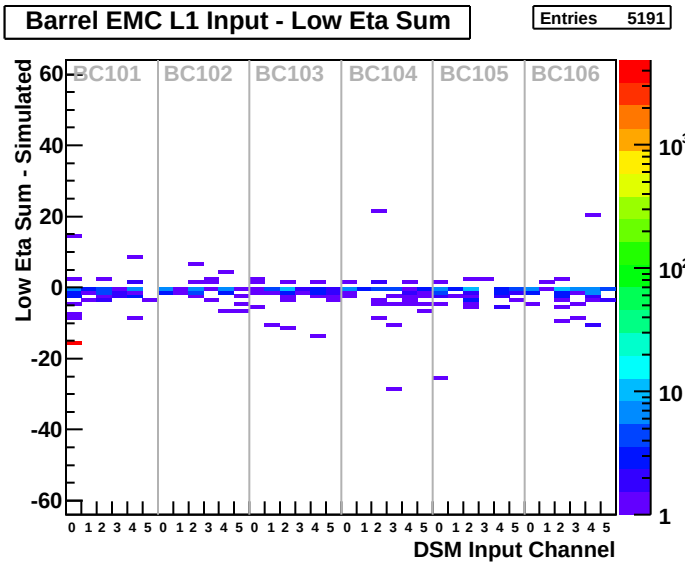
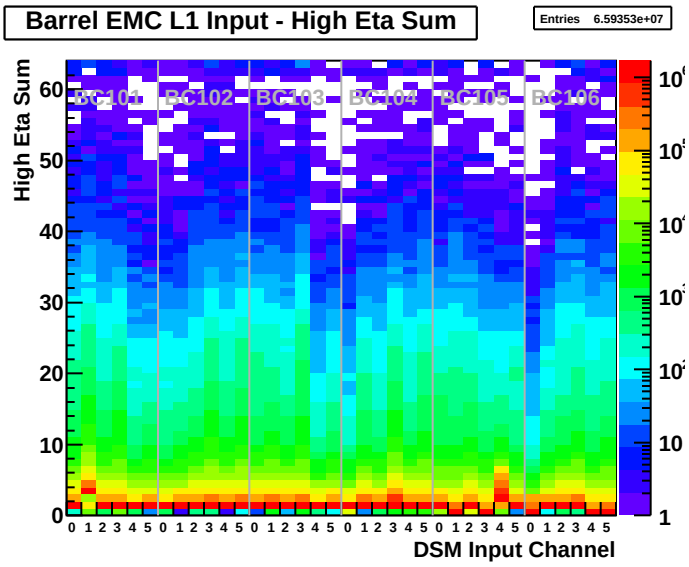
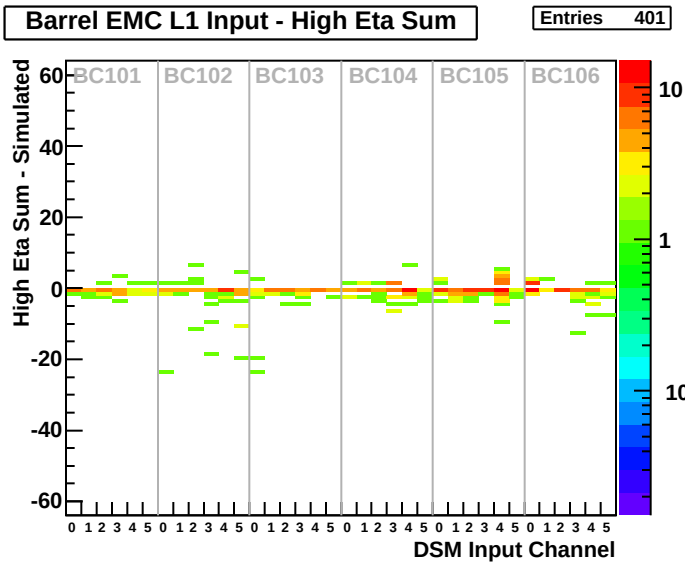
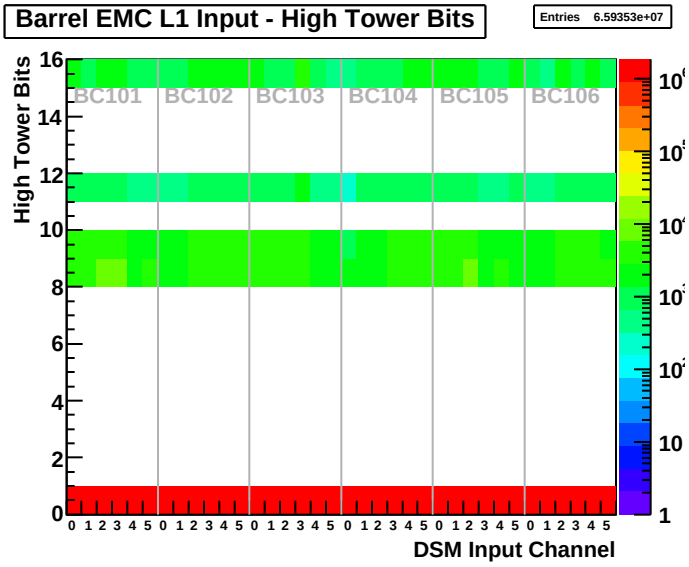
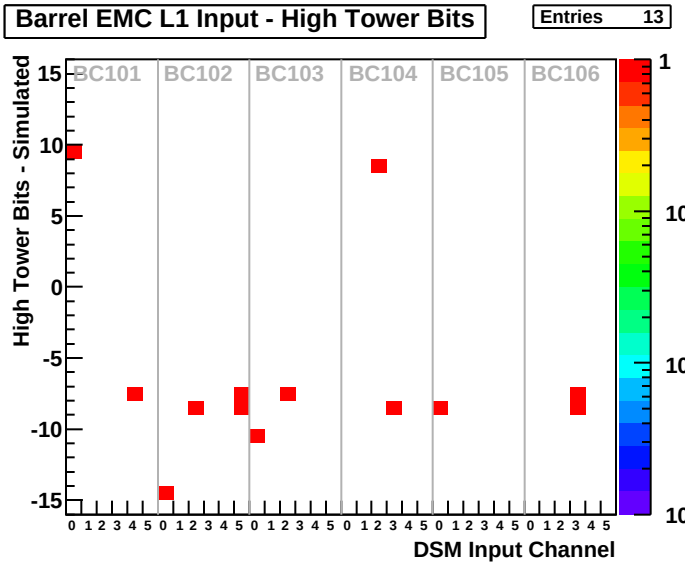

Barrel EMC L1 Input - Low Eta Sum
Entries 6.59353e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

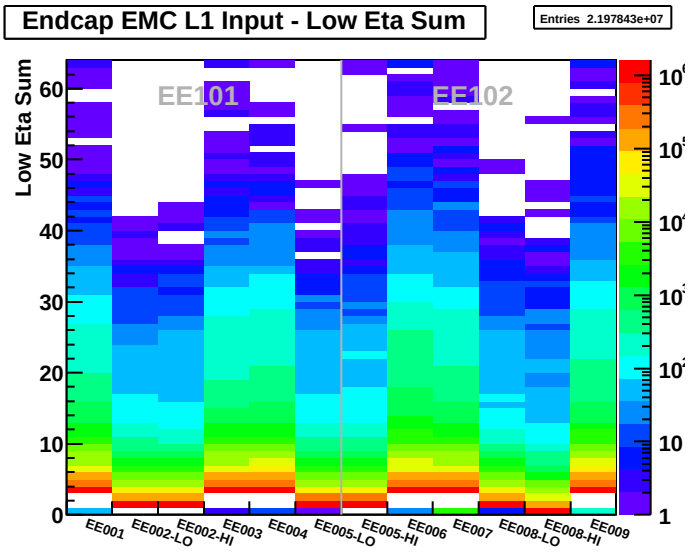

Barrel EMC L1 Input - Low Eta Sum
Entries 5191
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

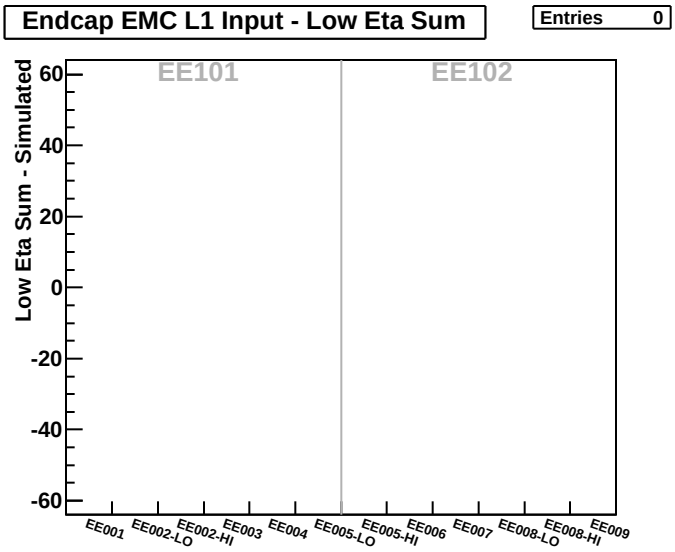

Barrel EMC L1 Input - High Eta Sum
Entries 6.59353e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

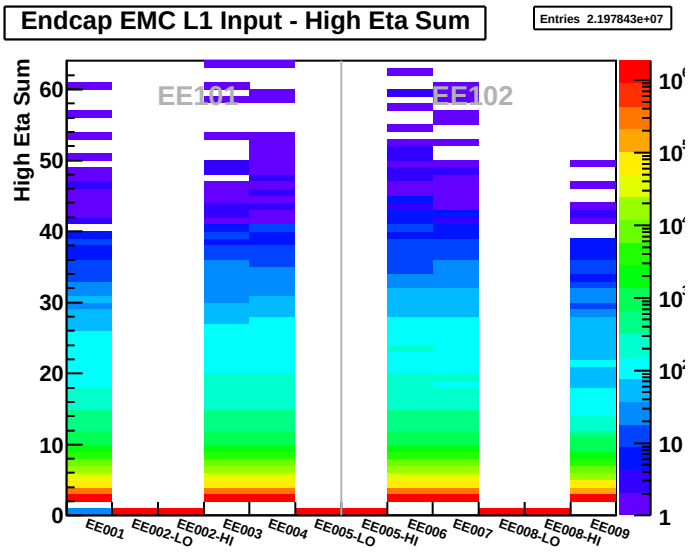

Barrel EMC L1 Input - High Eta Sum
Entries 401
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

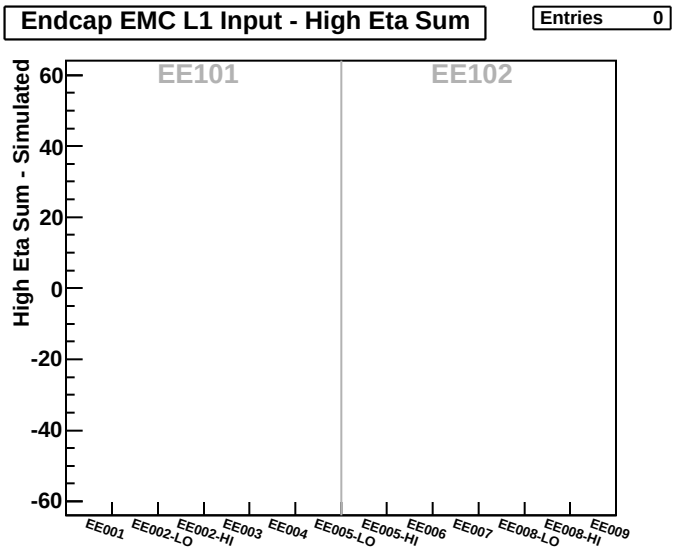

Barrel EMC L1 Input - High Tower Bits
Entries 6.59353e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

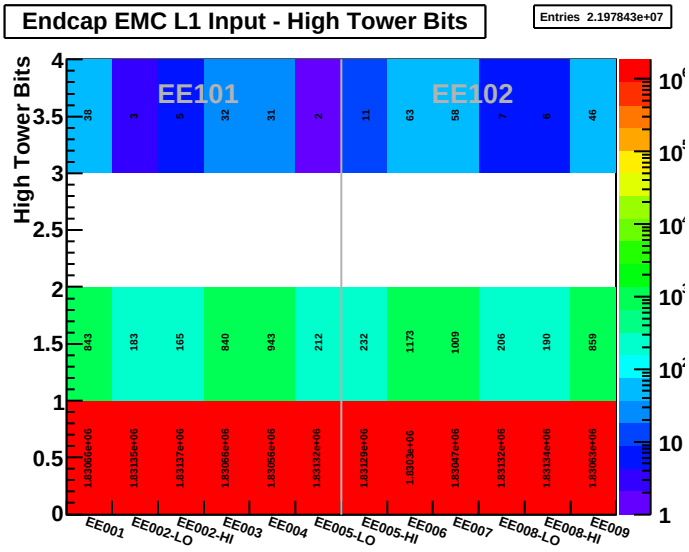

Barrel EMC L1 Input - High Tower Bits
Entries 13
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

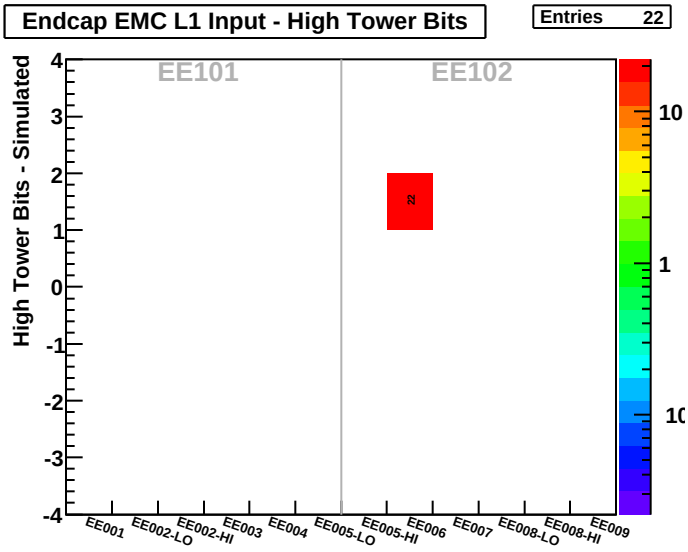

Endcap EMC L1 Input - Low Eta Sum
Entries 2.197843e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

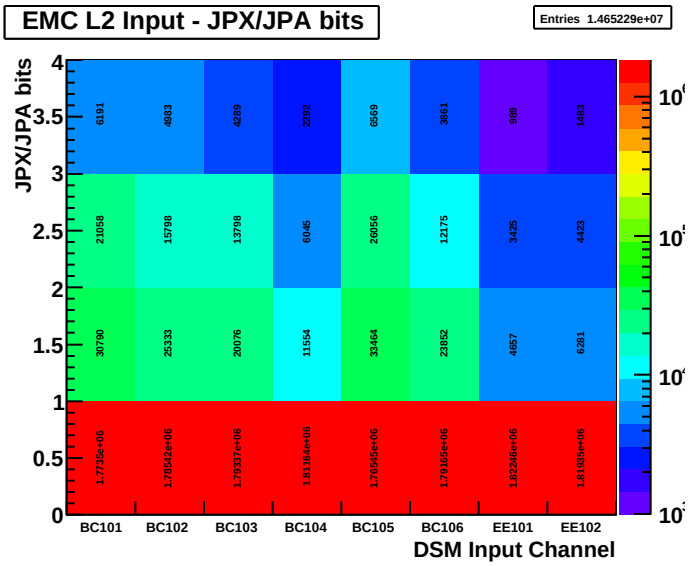

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

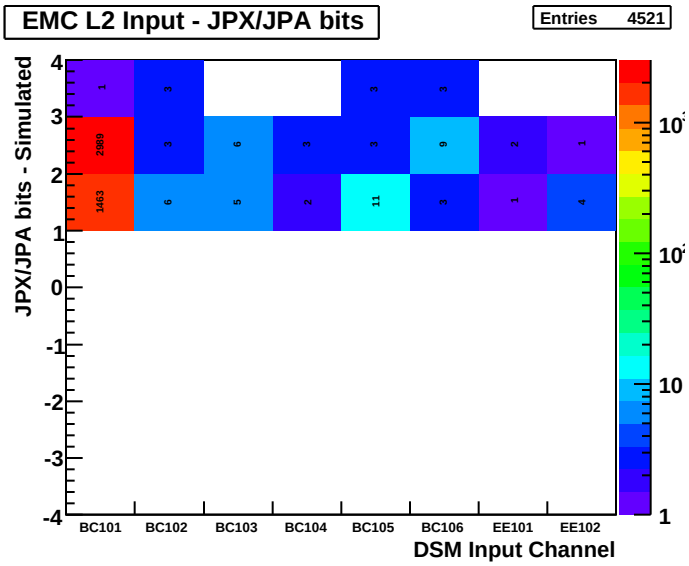

Endcap EMC L1 Input - High Eta Sum
Entries 2.197843e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

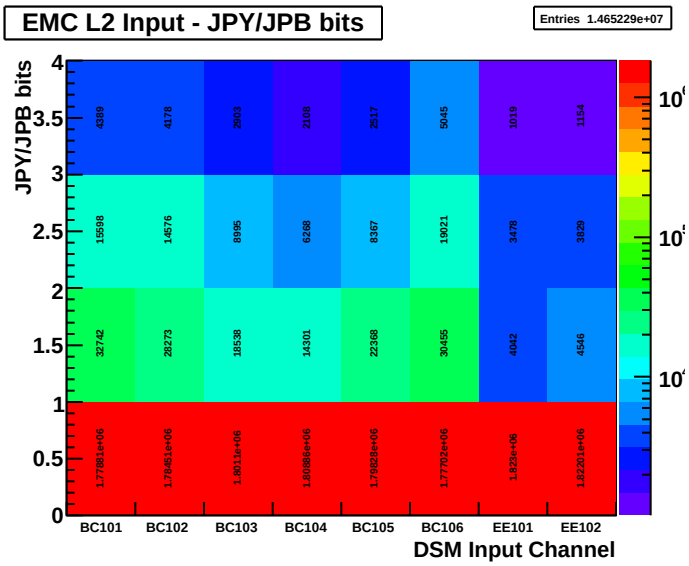

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

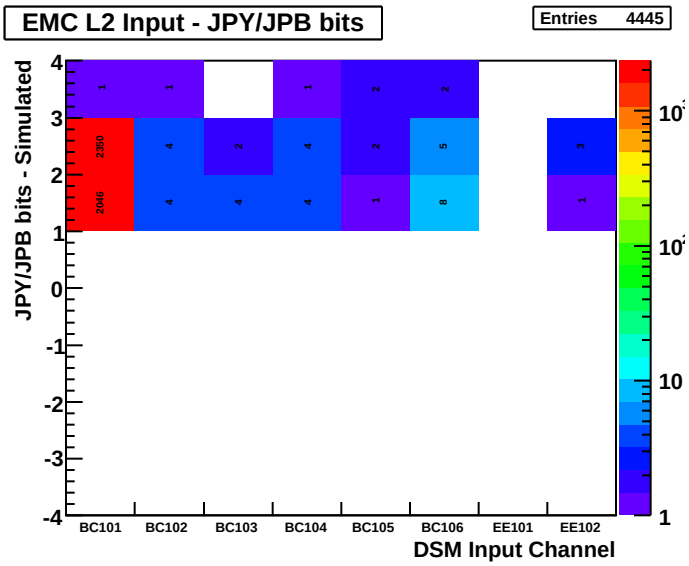

Endcap EMC L1 Input - High Tower Bits
Entries 2.197843e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

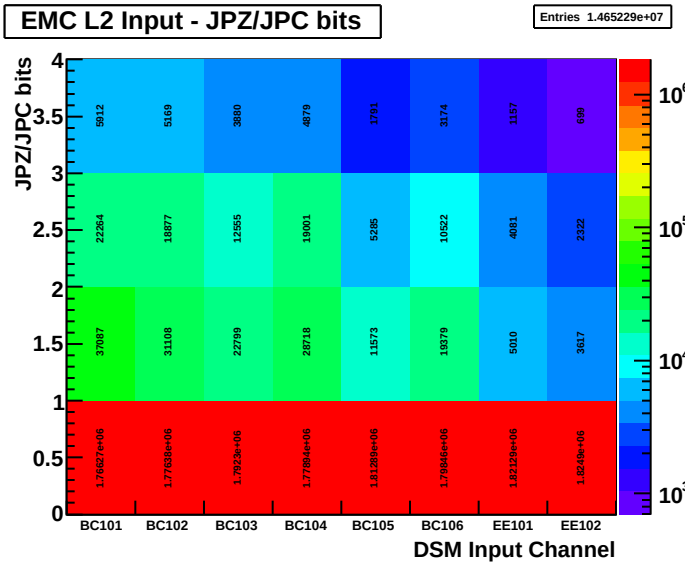

Endcap EMC L1 Input - High Tower Bits
Entries 22
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

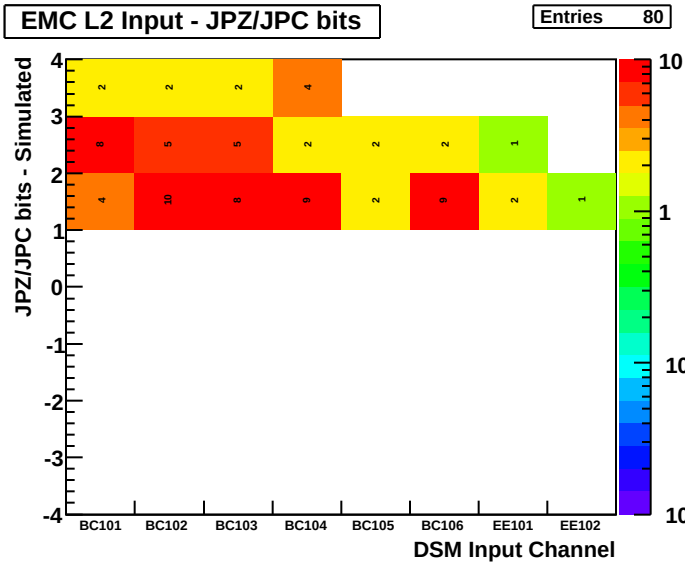

EMC L2 Input - JPX/JPA bits
Entries 1.465229e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

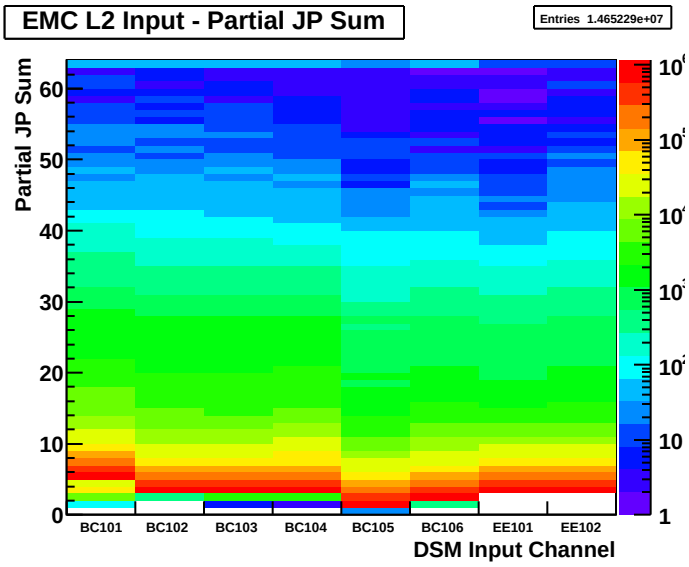

EMC L2 Input - JPX/JPA bits
Entries 4521
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

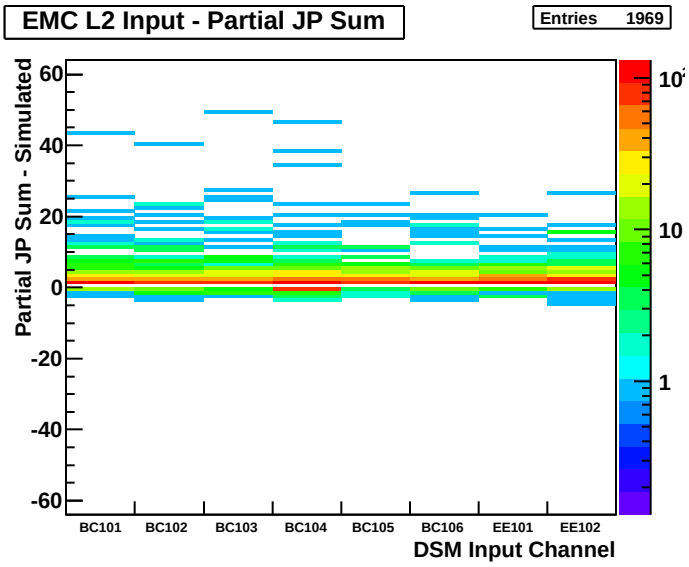

EMC L2 Input - JPY/JPB bits
Entries 1.465229e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

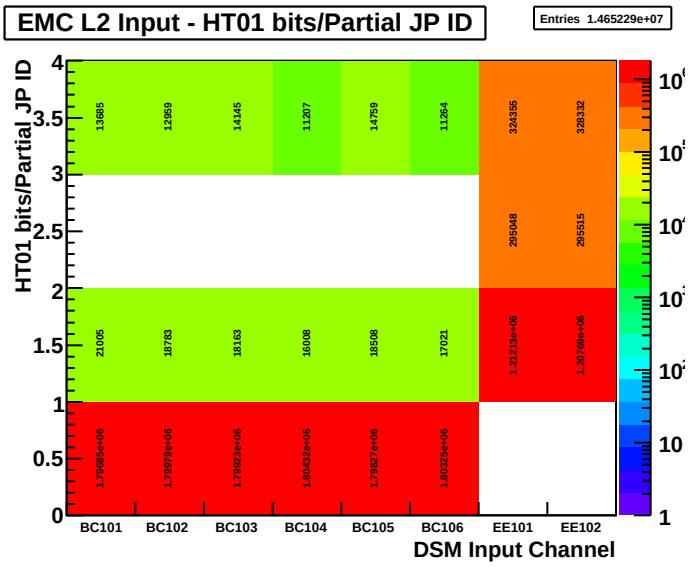

EMC L2 Input - JPY/JPB bits
Entries 4445
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

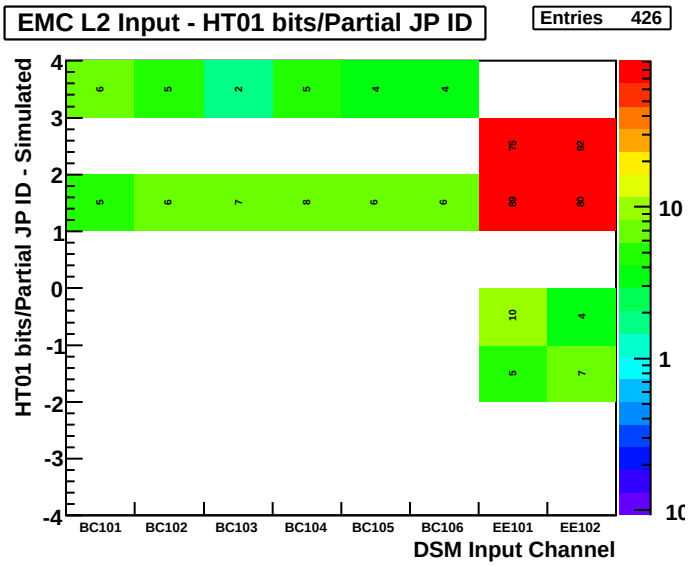

EMC L2 Input - JPZ/JPC bits
Entries 1.465229e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

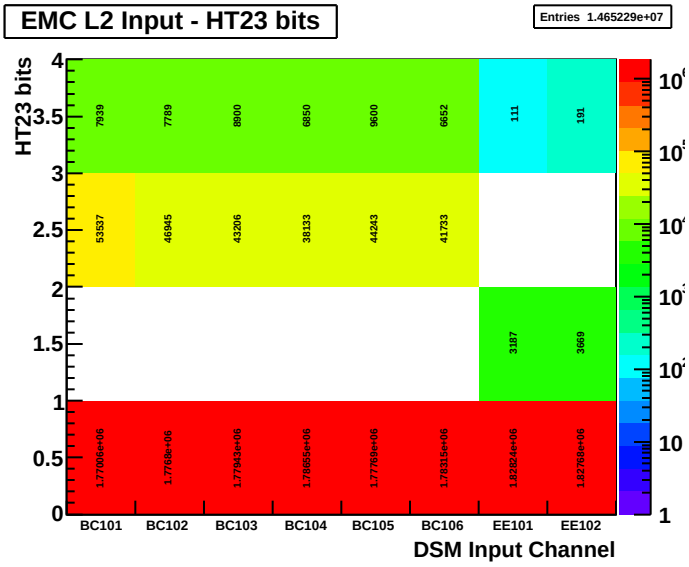

EMC L2 Input - JPZ/JPC bits
Entries 80
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

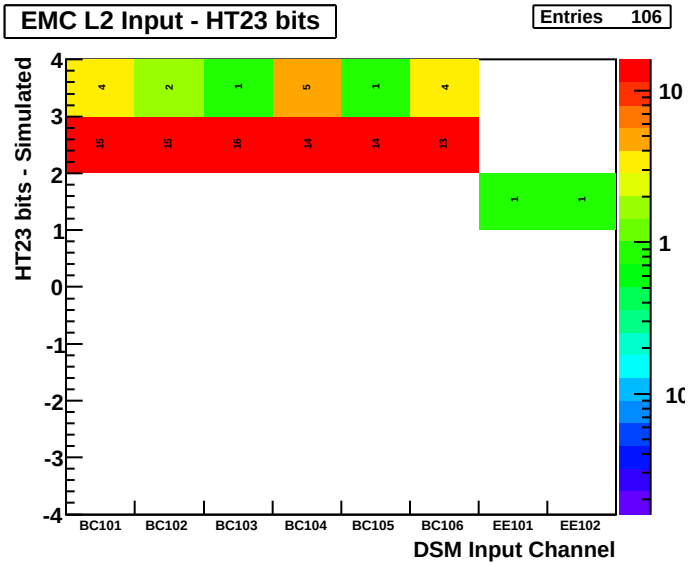

EMC L2 Input - Partial JP Sum
Entries 1.465229e+07
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

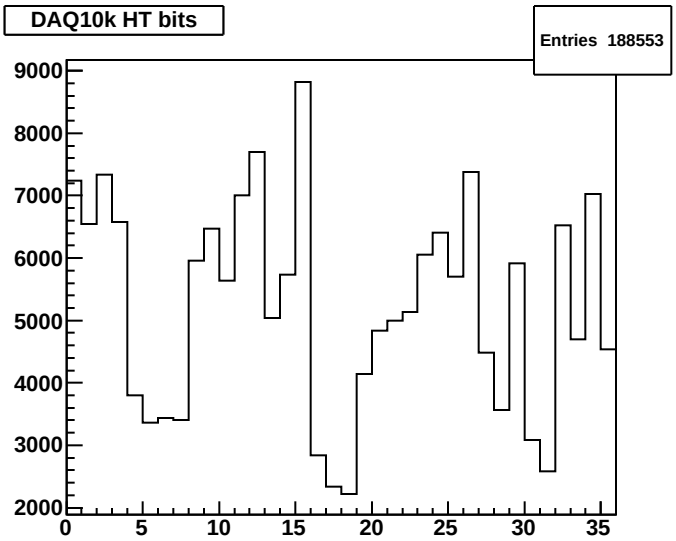

EMC L2 Input - Partial JP Sum
Entries 1969
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

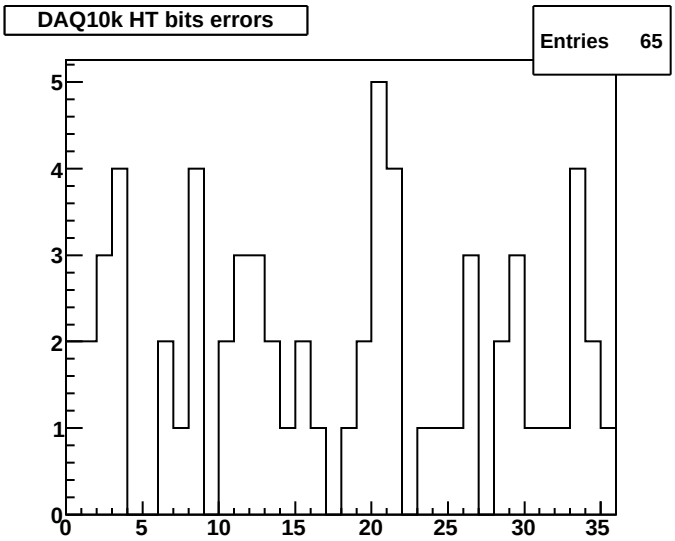

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.465229e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

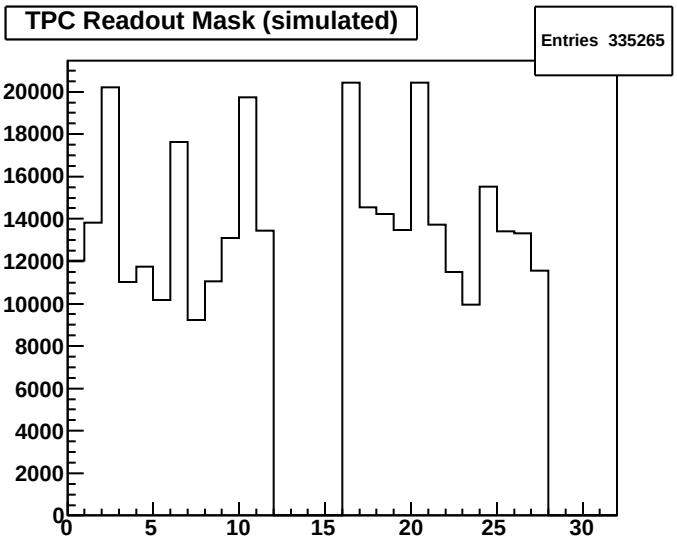

EMC L2 Input - HT01 bits/Partial JP ID
Entries 426
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

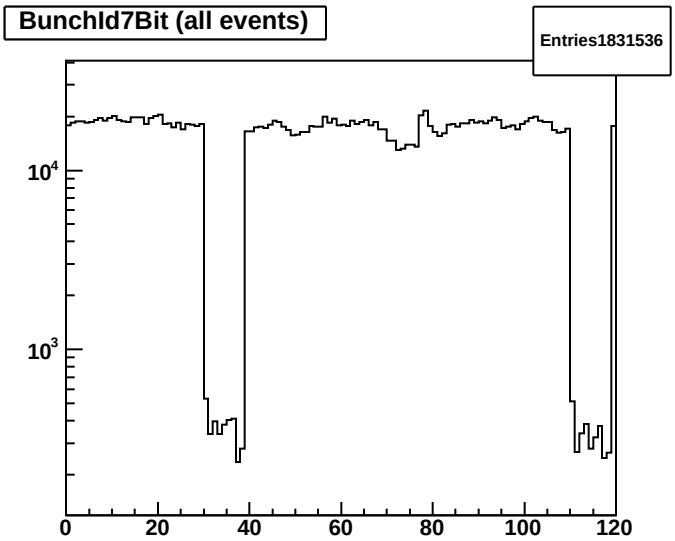

EMC L2 Input - HT23 bits
Entries 1.465229e+07
HT23 bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

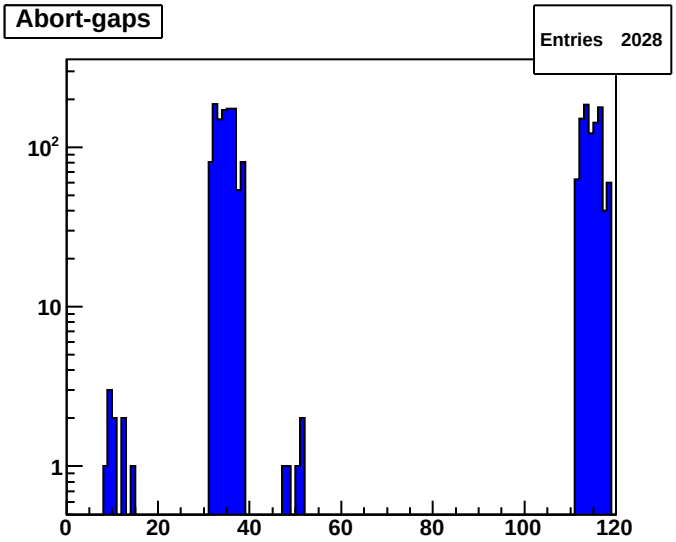

EMC L2 Input - HT23 bits
Entries 106
HT23 bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

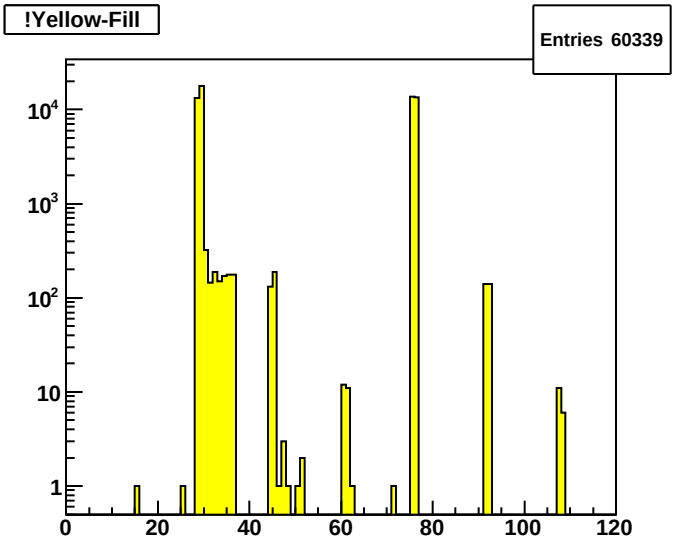

DAQ10k HT bits
Entries 188553

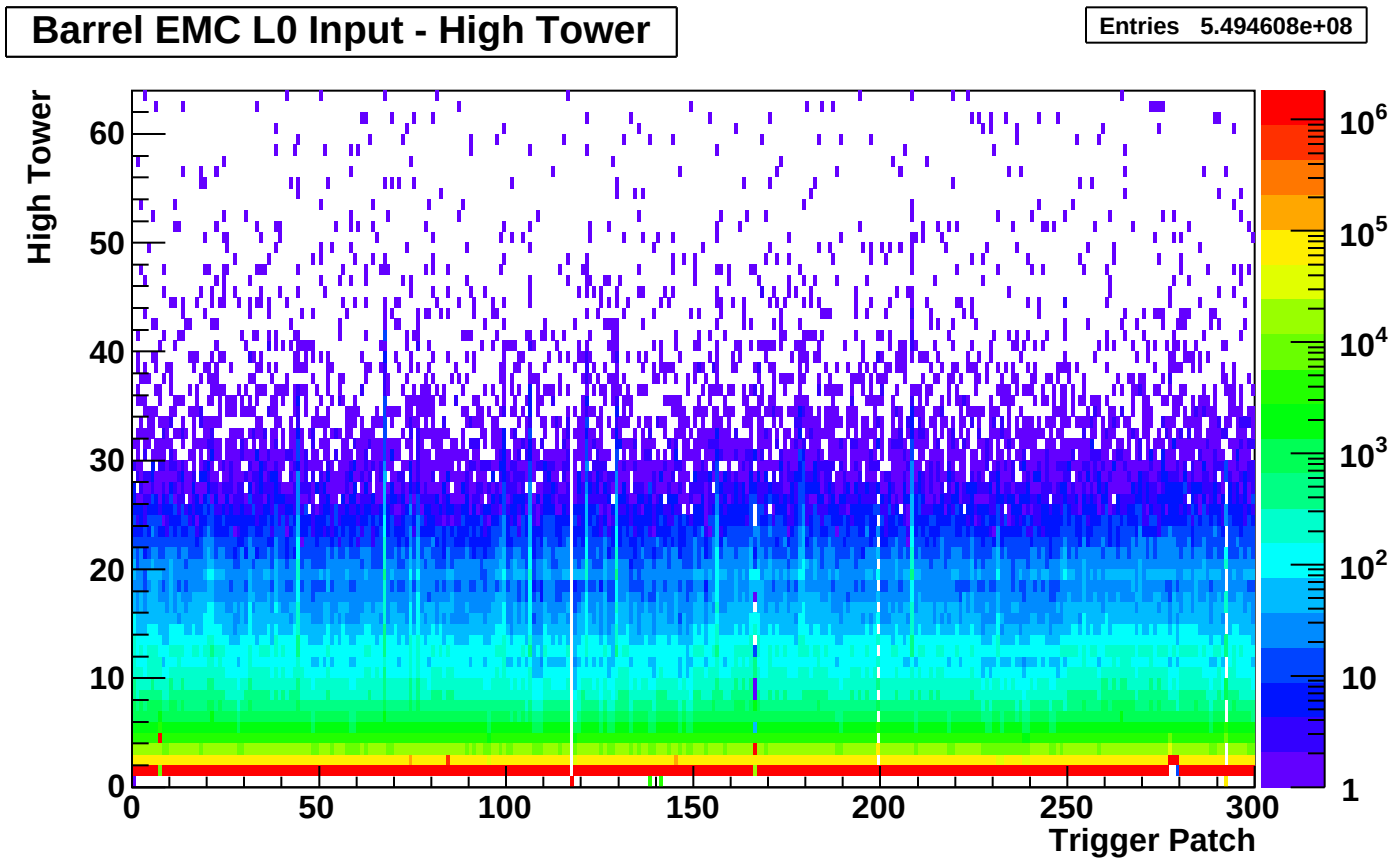

DAQ10k HT bits errors
Entries 65

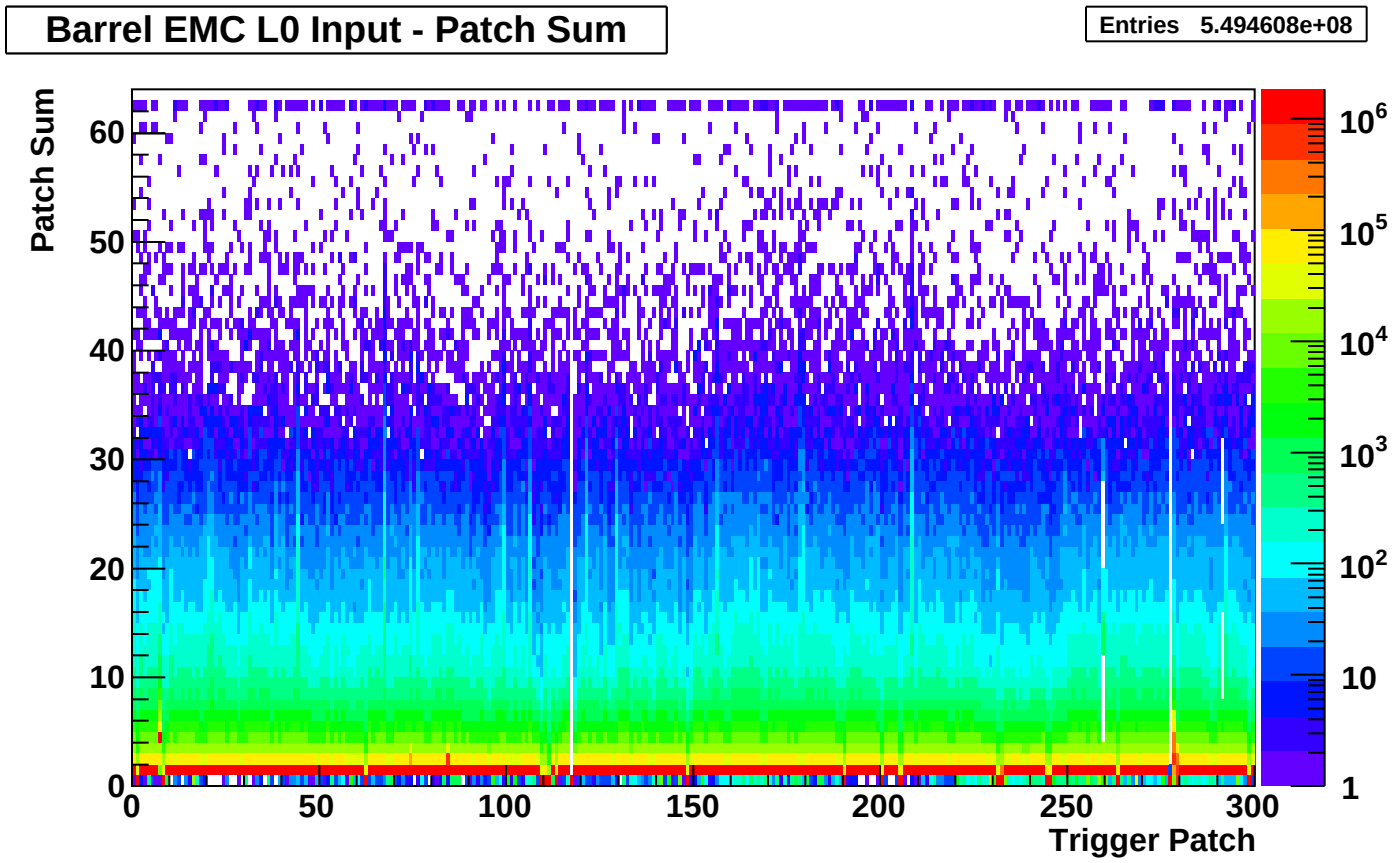

TPC Readout Mask (simulated)
Entries 335265

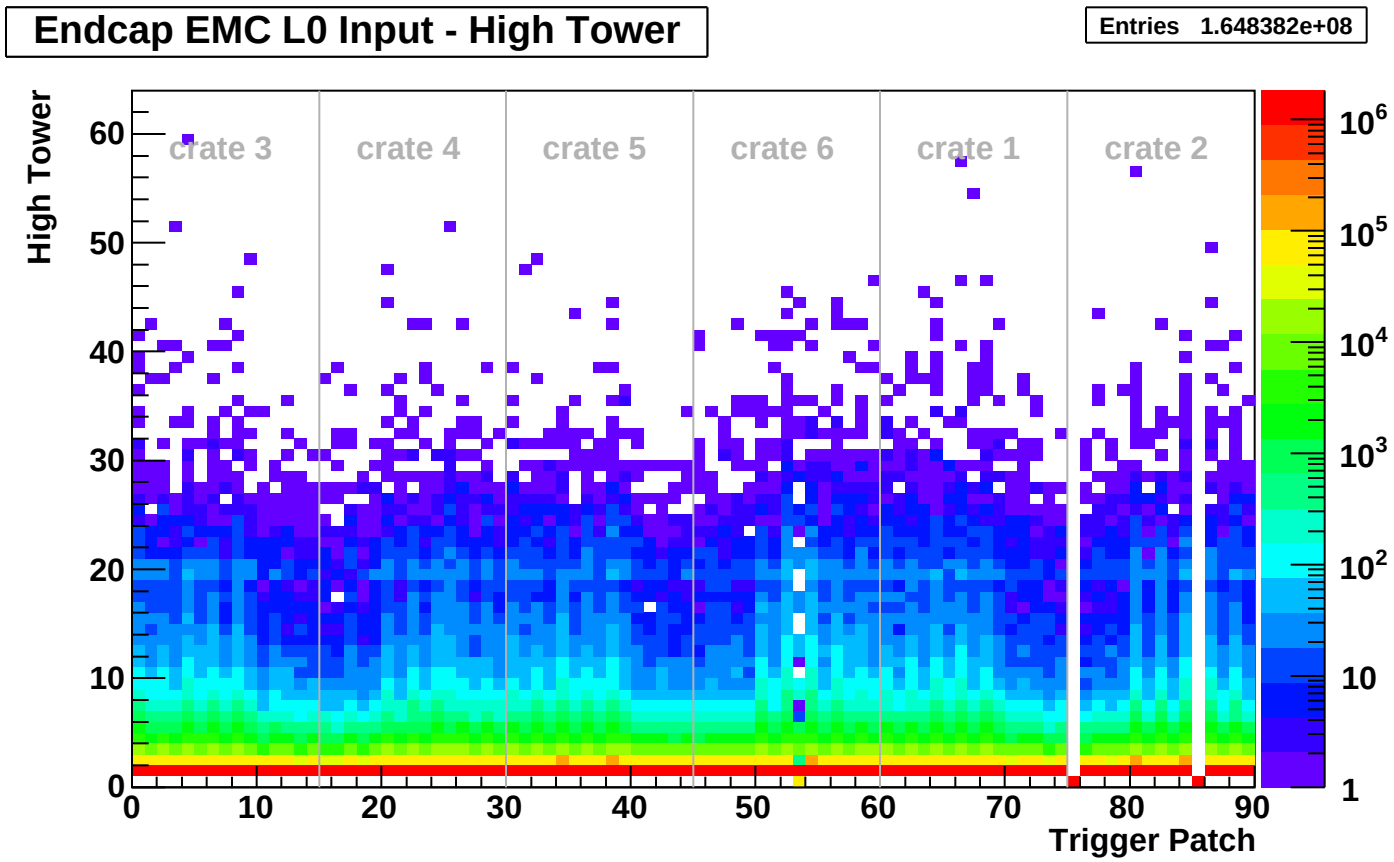

BunchId7Bit (all events)
Entries1831536

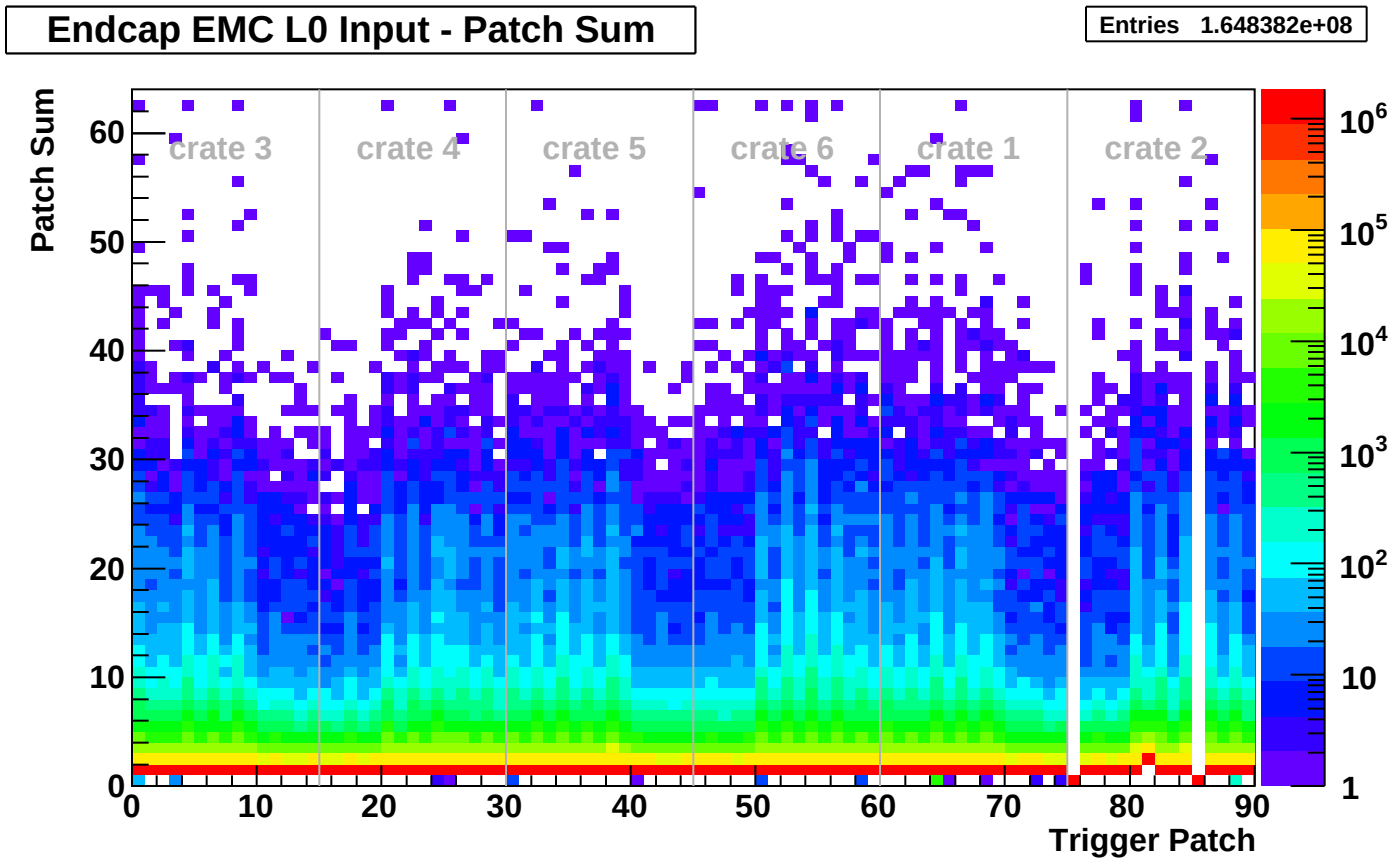

Abort-gaps
Entries 2028

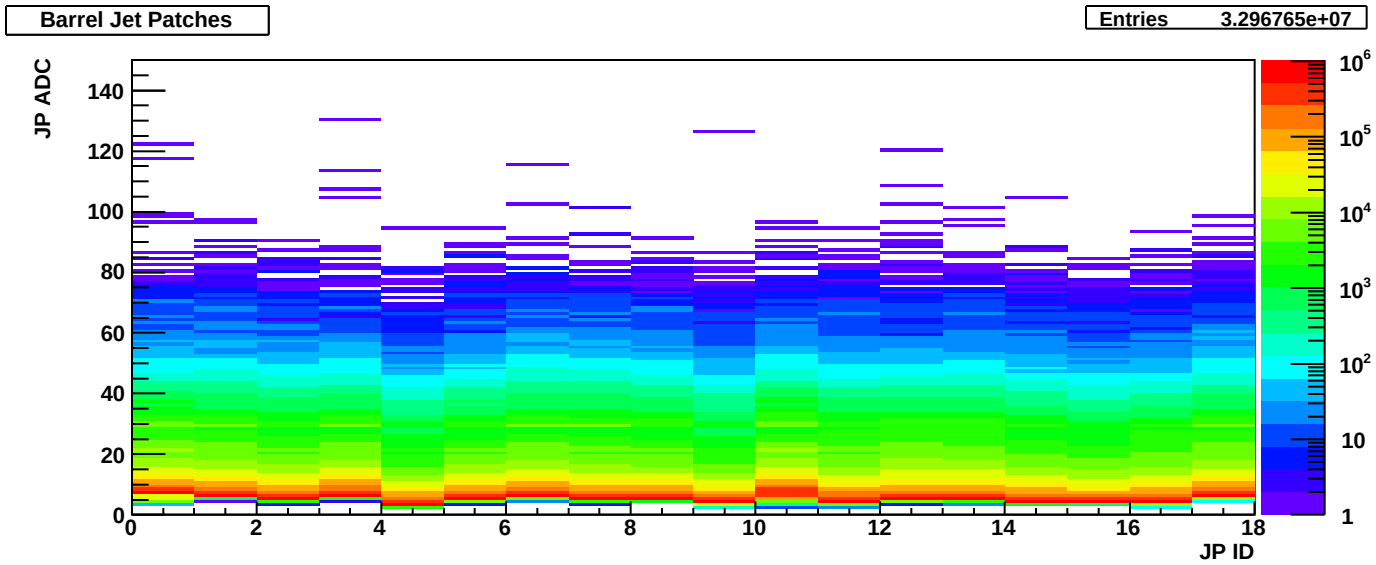

!Yellow-Fill
Entries 60339

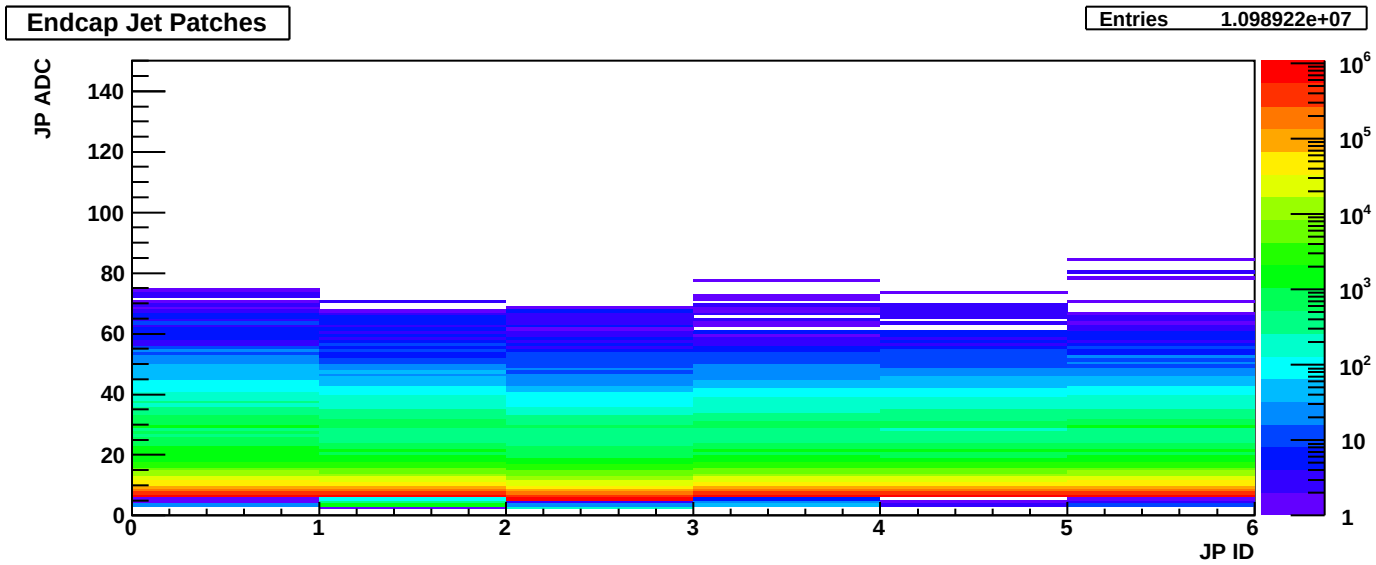

Barrel EMC L0 Input - High Tower
Entries 5.494608e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

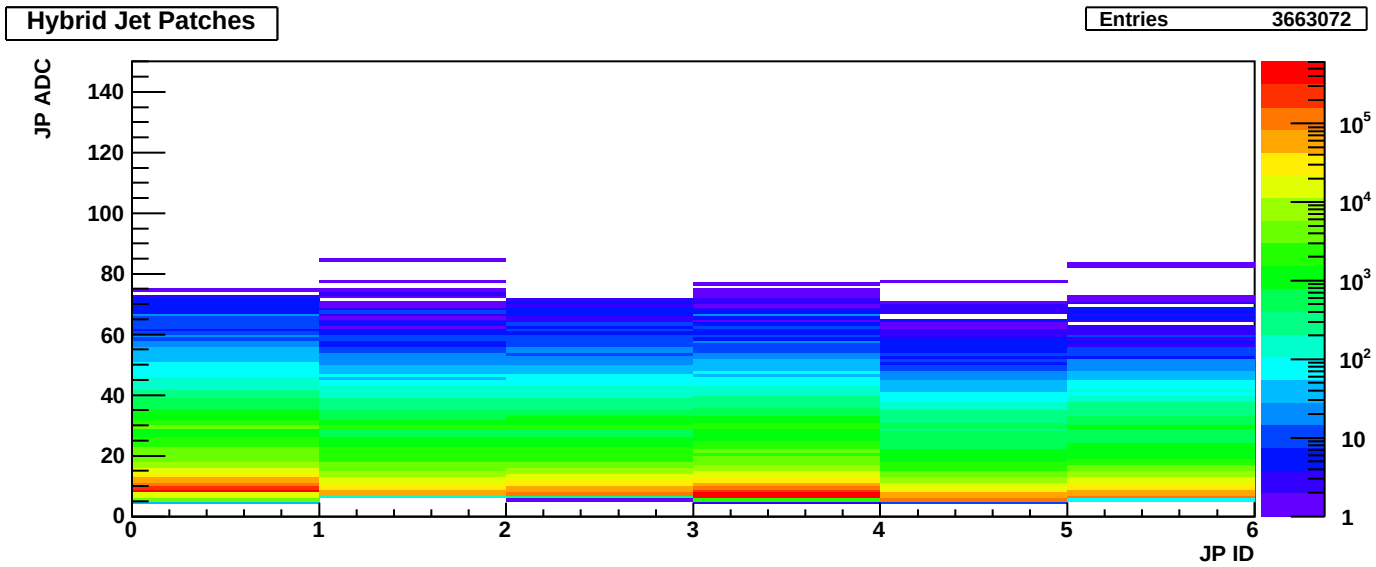

Barrel EMC L0 Input - Patch Sum
Entries 5.494608e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

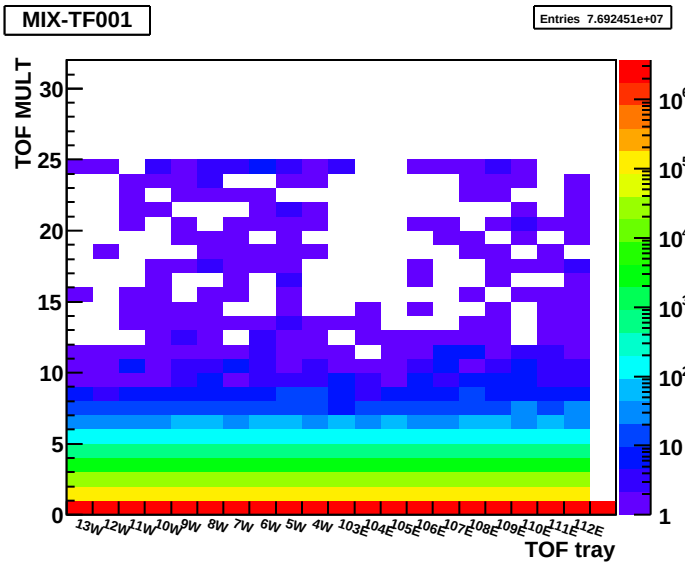

Endcap EMC L0 Input - High Tower
Entries 1.648382e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

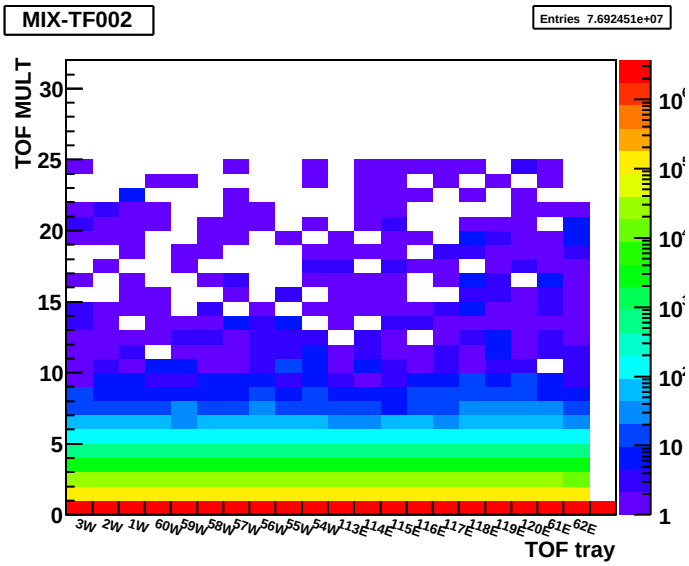

Endcap EMC L0 Input - Patch Sum
Entries 1.648382e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

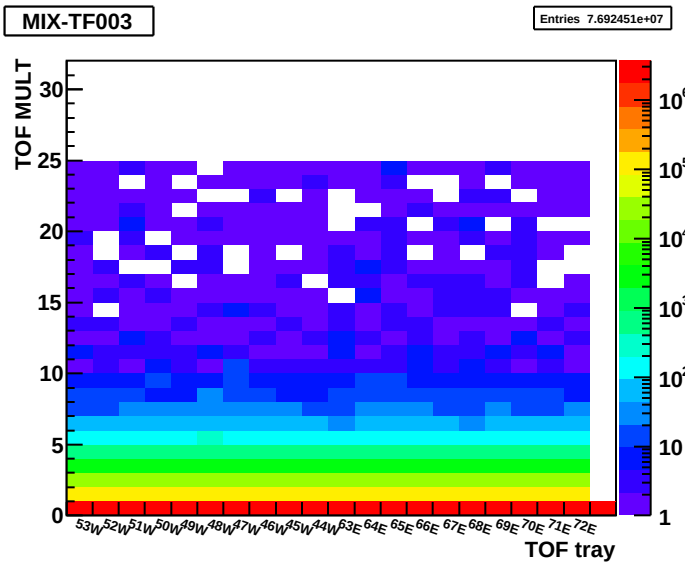

Barrel Jet Patches
Entries 3.296765e+07
JP ADC
JP ID

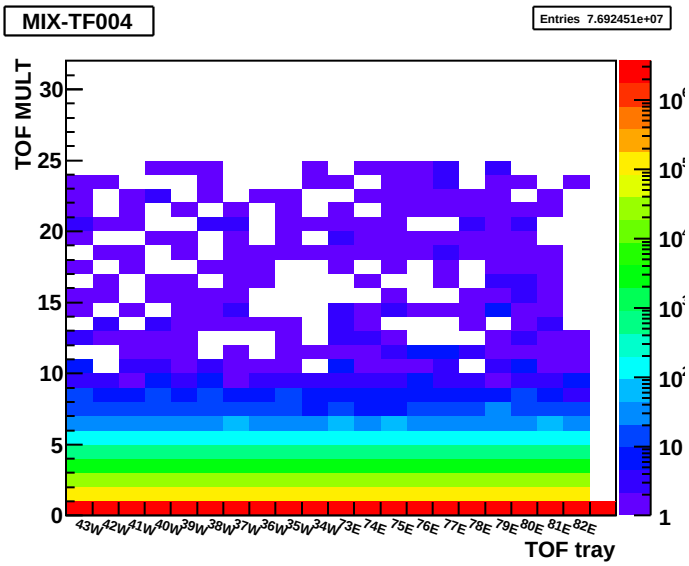

Endcap Jet Patches
Entries 1.098922e+07
JP ADC
JP ID

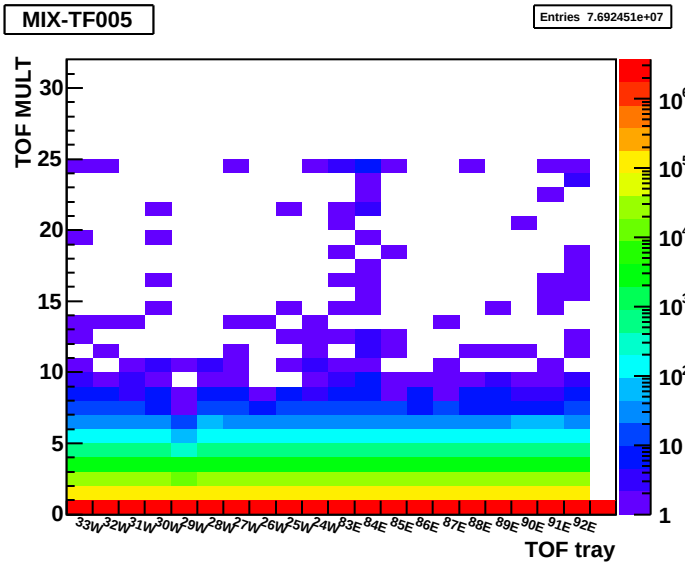

Hybrid Jet Patches
Entries 3663072
JP ADC
JP ID

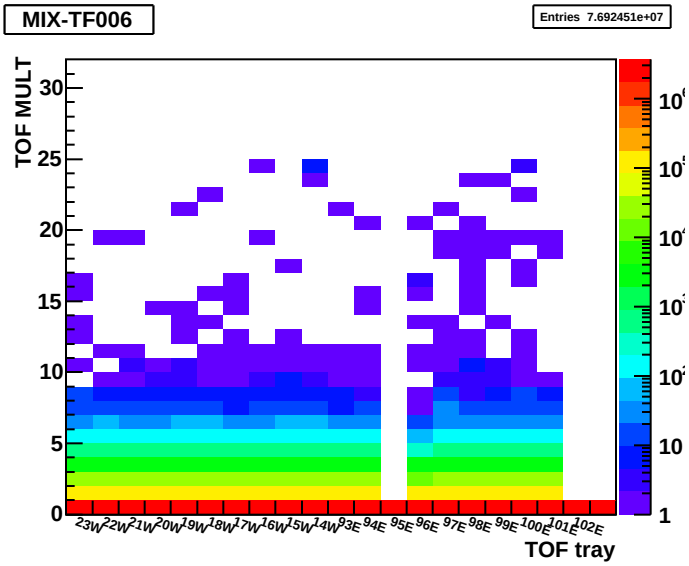

MIX-TF001
Entries 7.692451e+07
TOF MULT
TOF tray

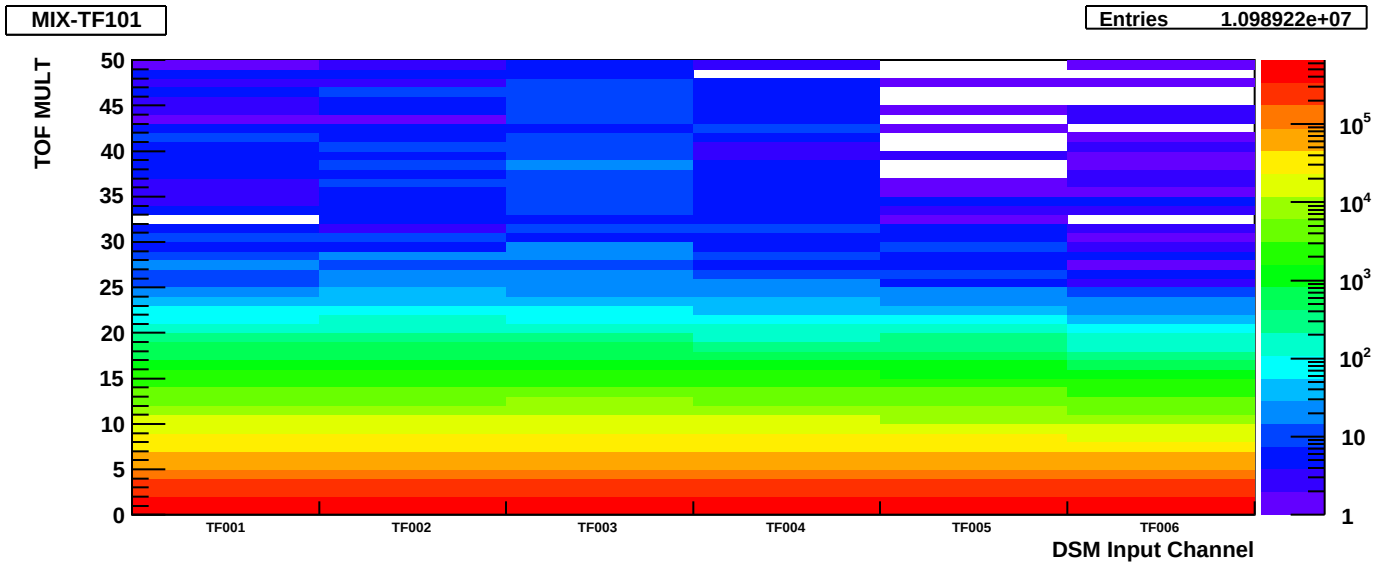

MIX-TF002
Entries 7.692451e+07
TOF MULT
TOF tray

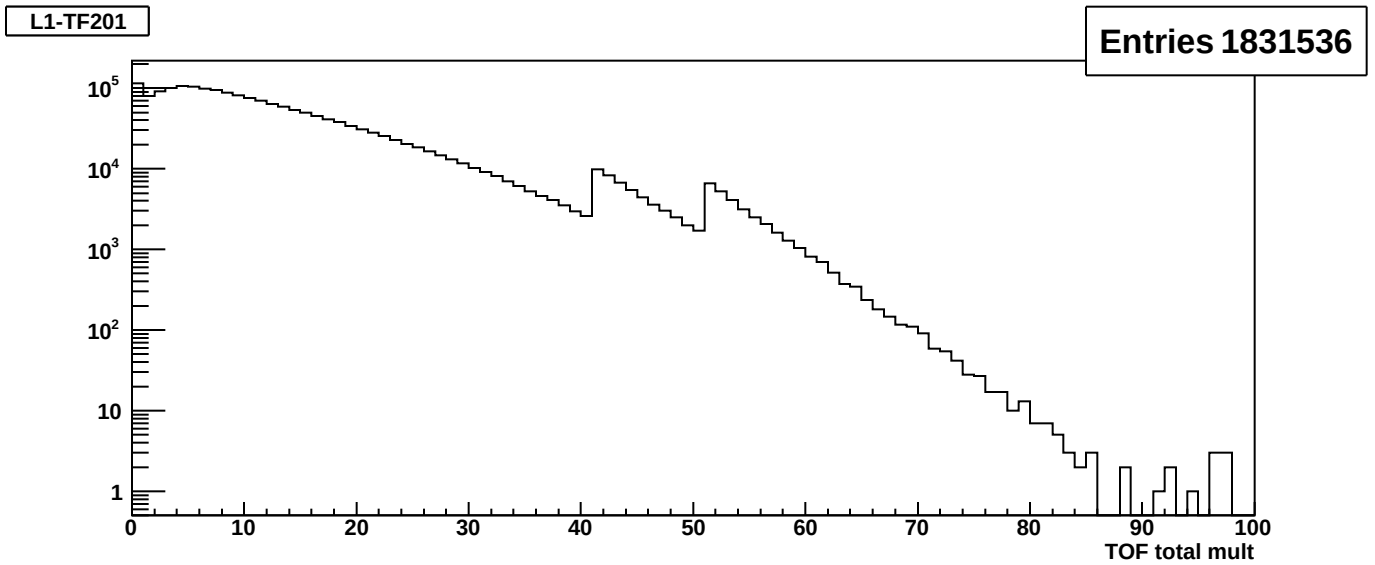

MIX-TF003
Entries 7.692451e+07
TOF MULT
TOF tray

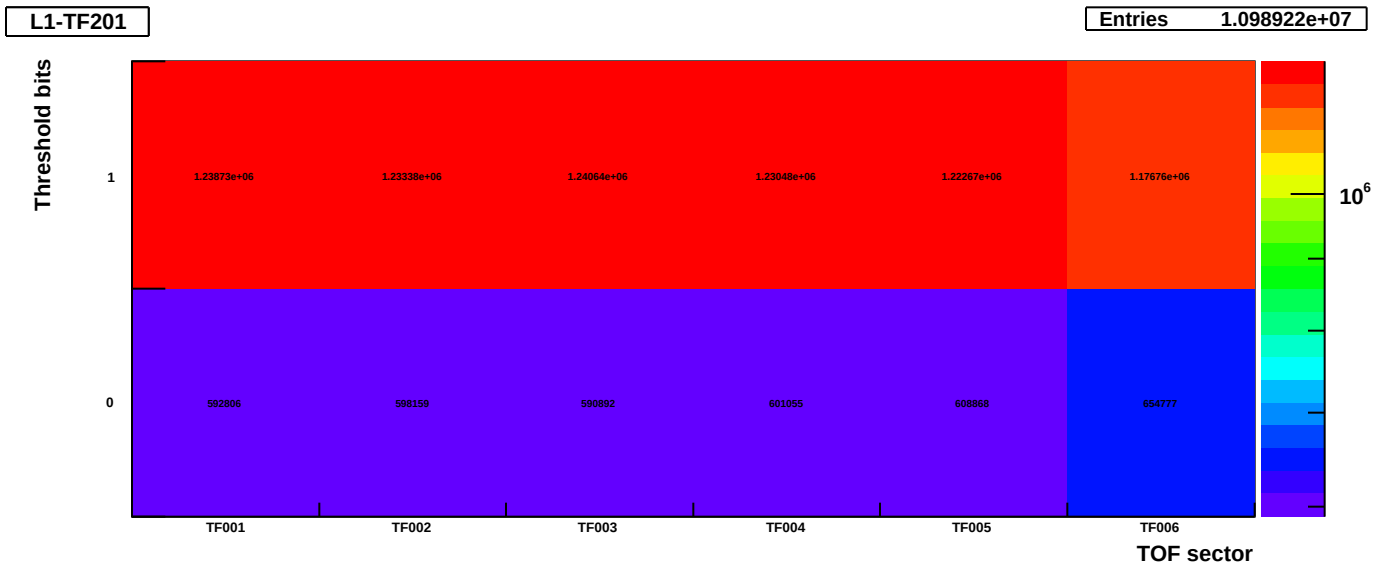

MIX-TF004
Entries 7.692451e+07
TOF MULT
TOF tray

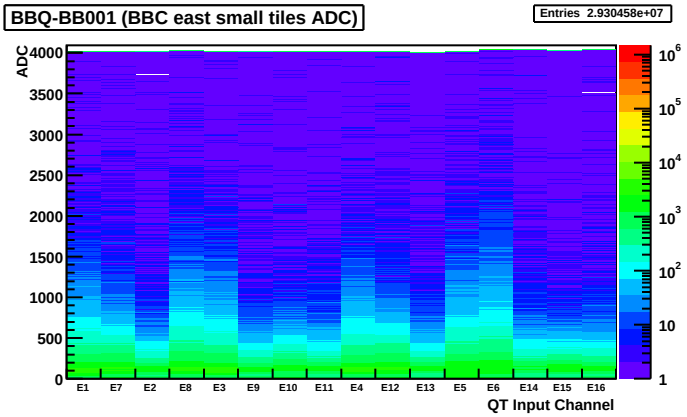

MIX-TF005
Entries 7.692451e+07
TOF MULT
TOF tray

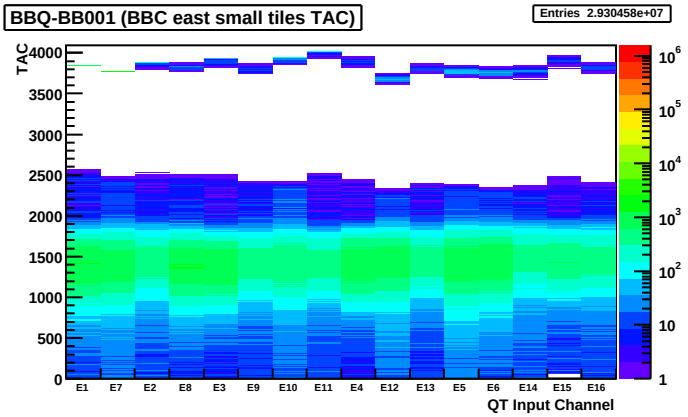

MIX-TF006
Entries 7.692451e+07
TOF MULT
TOF tray

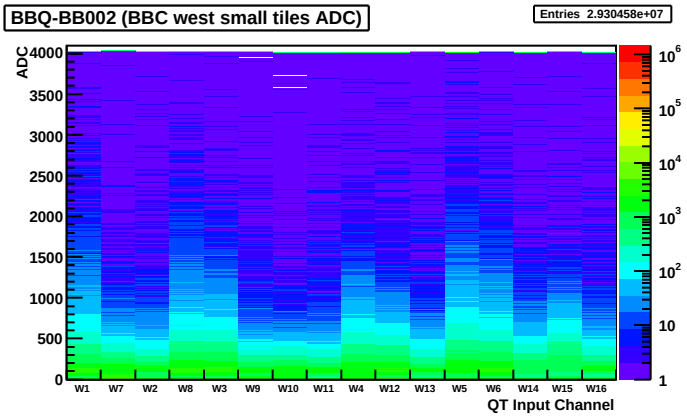

MIX-TF101
Entries 1.098922e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

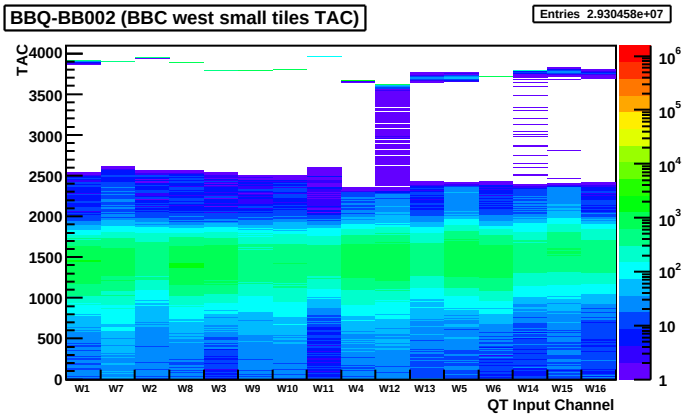

L1-TF201
Entries 1831536
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

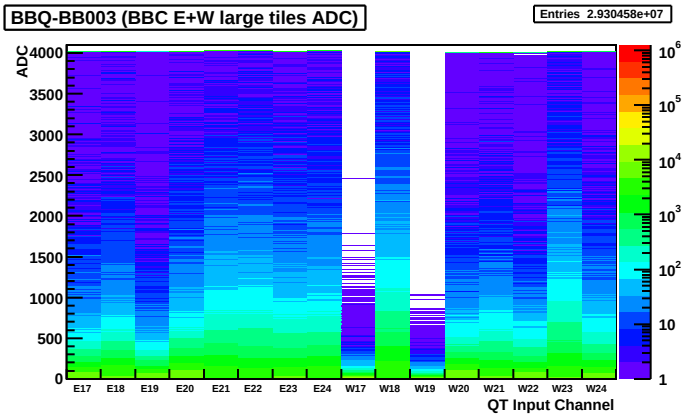

L1-TF201
Entries 1.098922e+07
Threshold bits
1
0
1.23873e+06
1.23338e+06
1.24064e+06
1.23048e+06
1.22267e+06
1.17676e+06
592806
598159
590892
601055
608868
654777
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

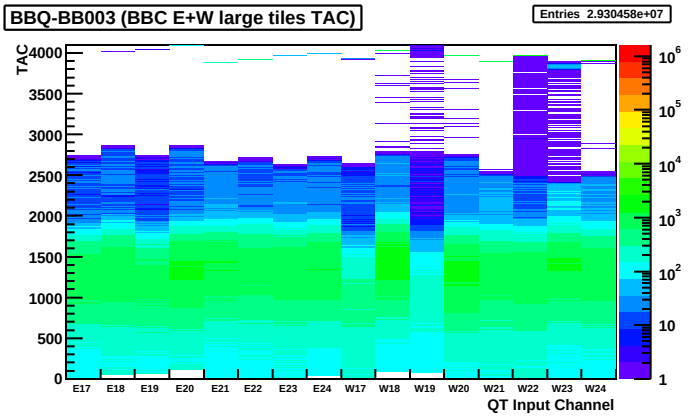

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.930458e+07
ADC
QT Input Channel

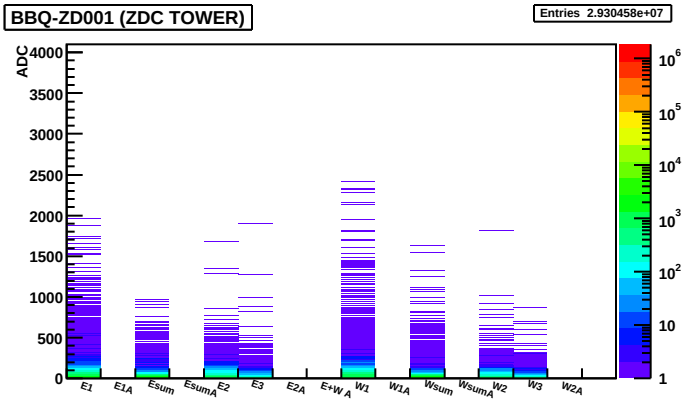

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.930458e+07
TAC
QT Input Channel

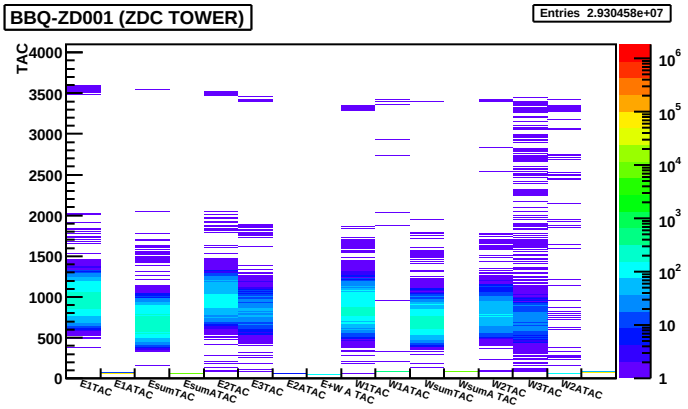

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.930458e+07
ADC
QT Input Channel

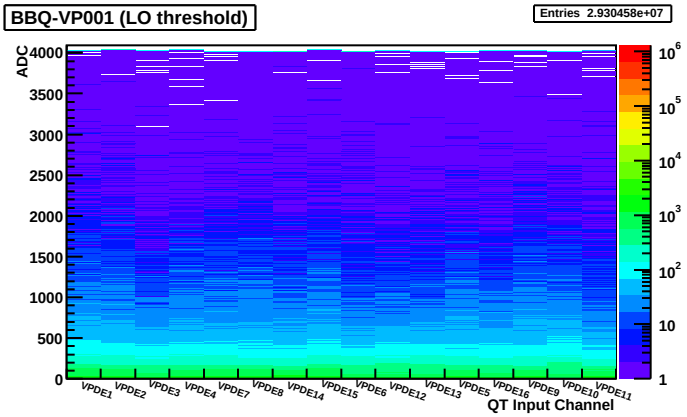

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.930458e+07
TAC
QT Input Channel

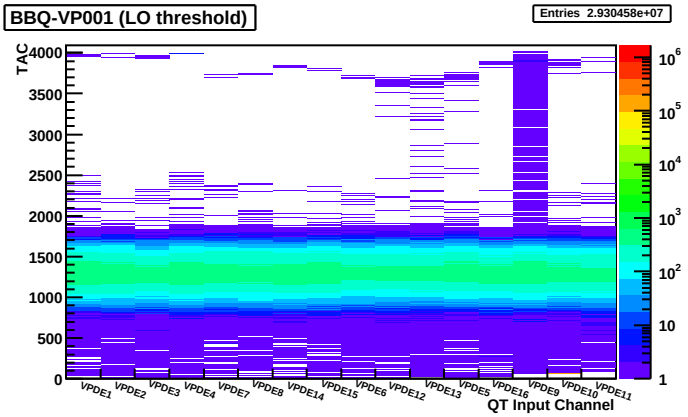

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.930458e+07
ADC
QT Input Channel

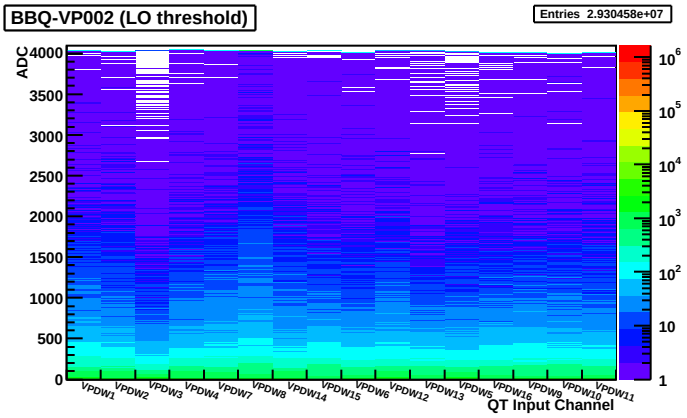

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.930458e+07
TAC
QT Input Channel

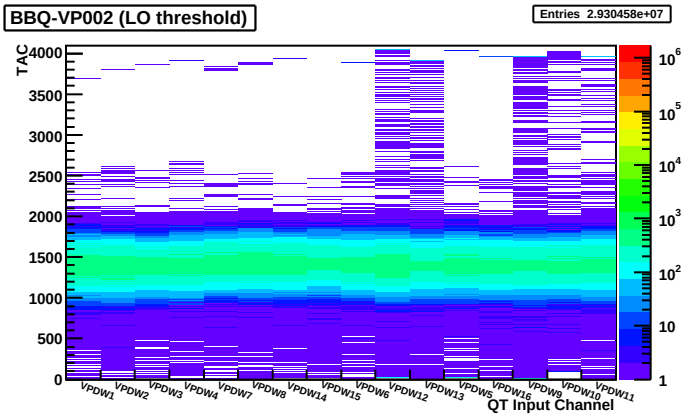

BBQ-ZD001 (ZDC TOWER)
Entries 2.930458e+07
ADC

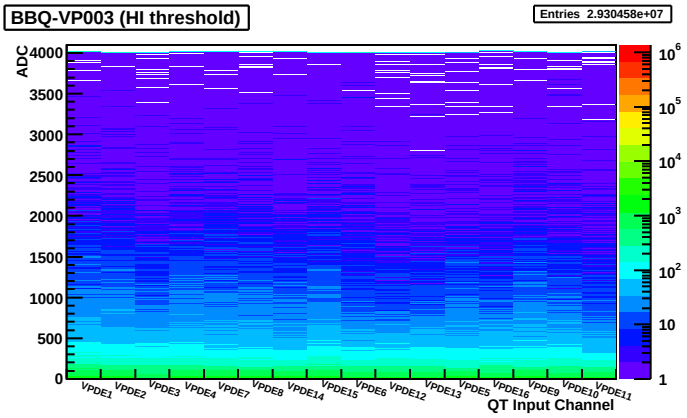

BBQ-ZD001 (ZDC TOWER)
Entries 2.930458e+07
TAC

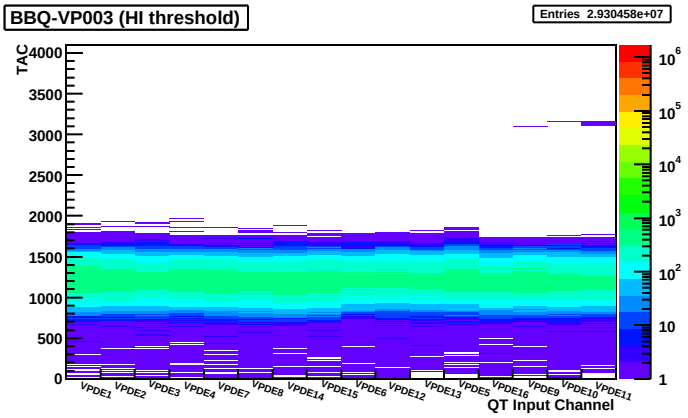

BBQ-VP001 (LO threshold)
Entries 2.930458e+07
ADC
QT Input Channel

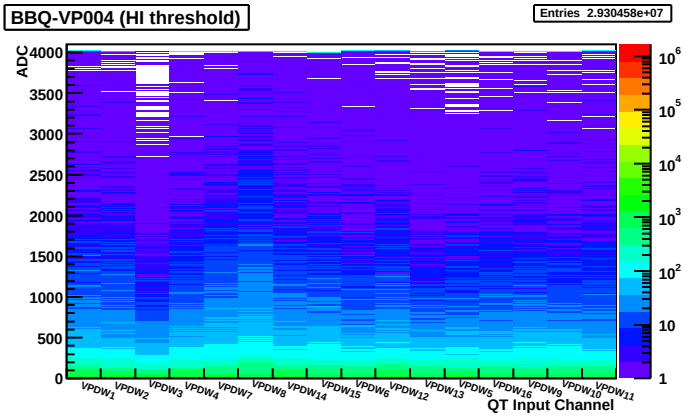

BBQ-VP001 (LO threshold)
Entries 2.930458e+07
TAC
QT Input Channel

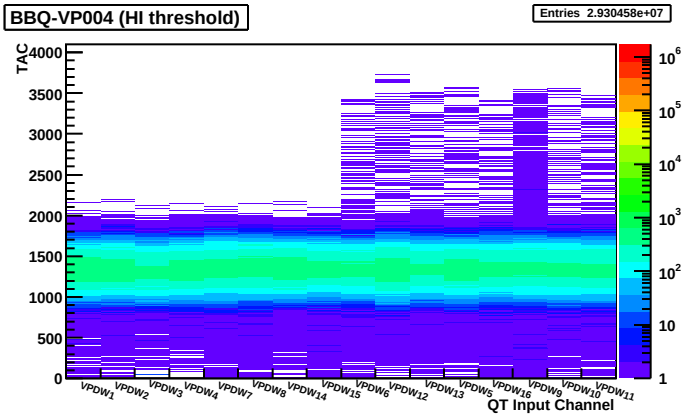

BBQ-VP002 (LO threshold)
Entries 2.930458e+07
ADC
QT Input Channel

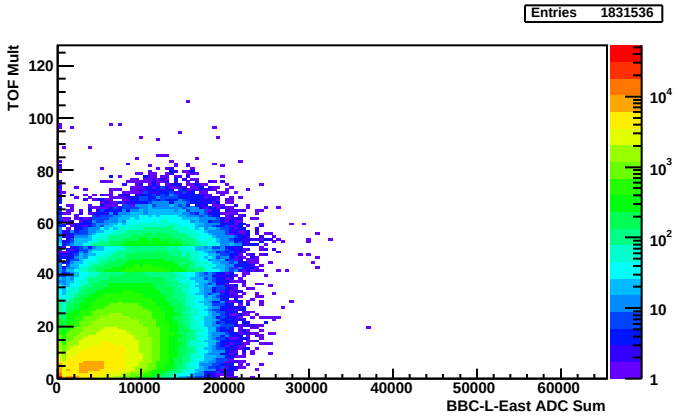

BBQ-VP002 (LO threshold)
Entries 2.930458e+07
TAC
QT Input Channel

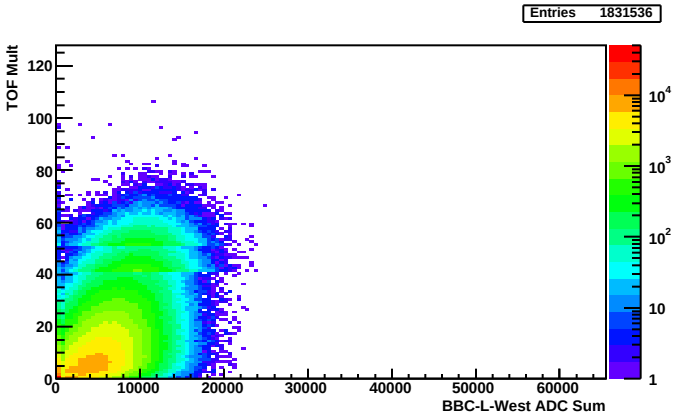

BBQ-VP003 (HI threshold)
Entries 2.930458e+07
ADC
QT Input Channel

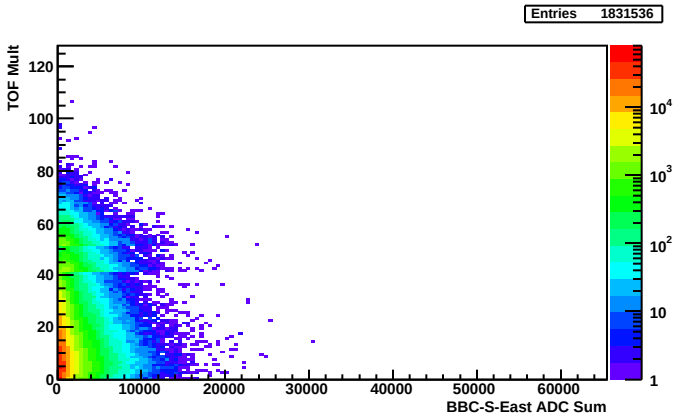

BBQ-VP003 (HI threshold)
Entries 2.930458e+07
TAC
QT Input Channel

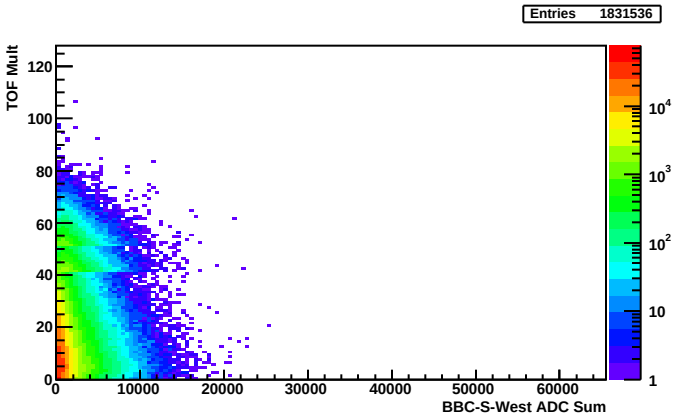

BBQ-VP004 (HI threshold)
Entries 2.930458e+07
ADC
QT Input Channel

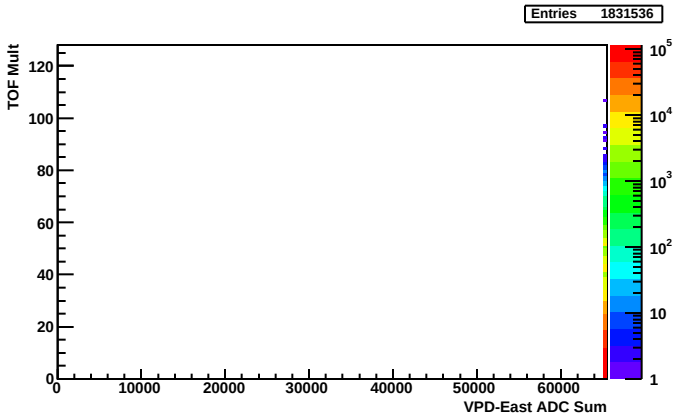

BBQ-VP004 (HI threshold)
Entries 2.930458e+07
TAC
QT Input Channel

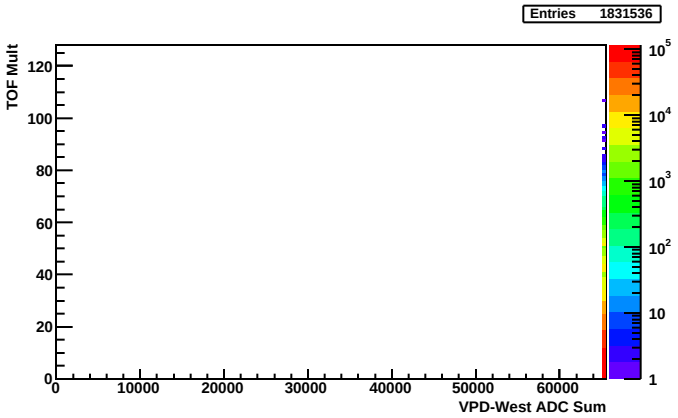

Entries 1831536
TOF Mult
BBC-L-East ADC Sum

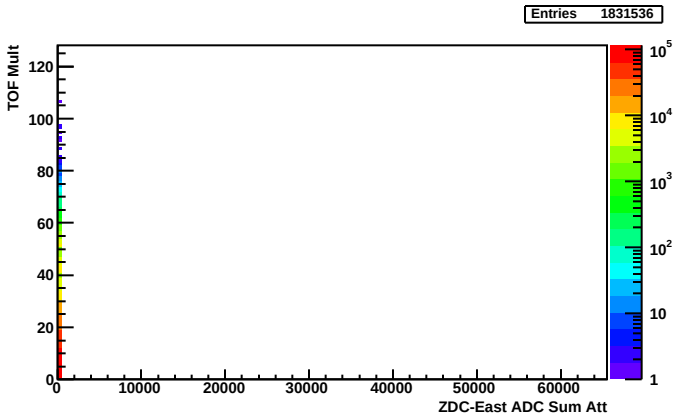

Entries 1831536
TOF Mult
BBC-L-West ADC Sum

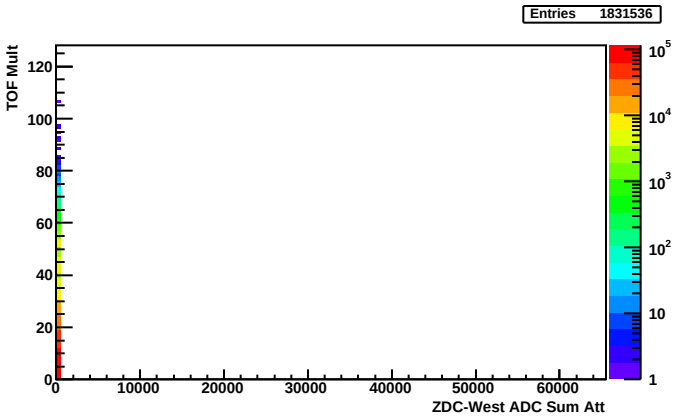

Entries 1831536
TOF Mult
BBC-S-East ADC Sum

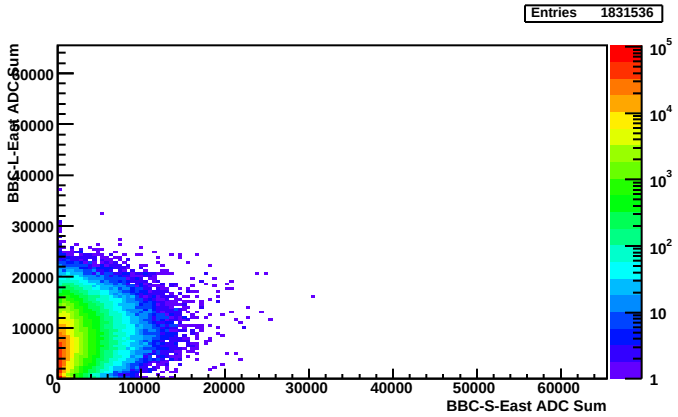

Entries 1831536
TOF Mult
BBC-S-West ADC Sum

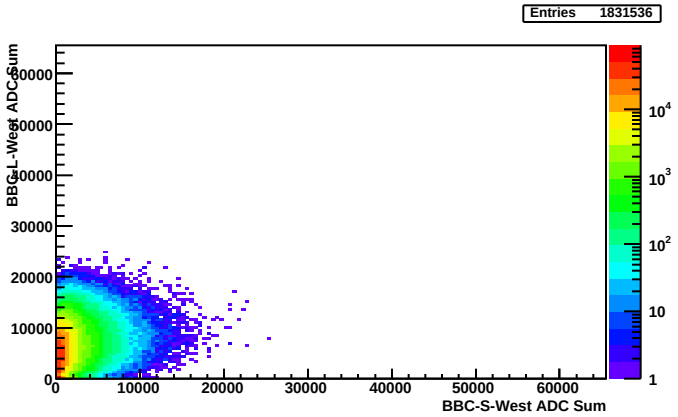

Entries 1831536
TOF Mult
VPD-East ADC Sum

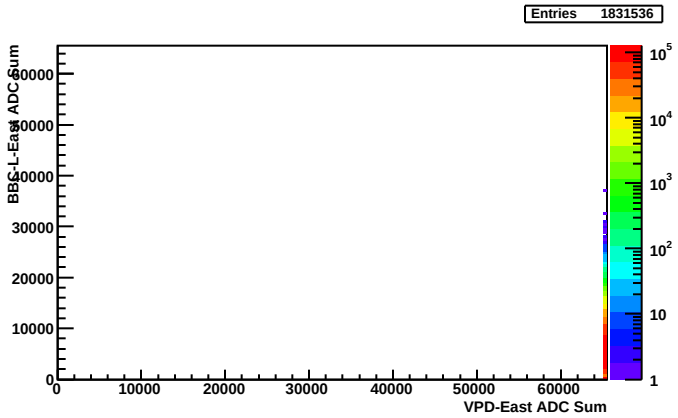

Entries 1831536
TOF Mult
VPD-West ADC Sum

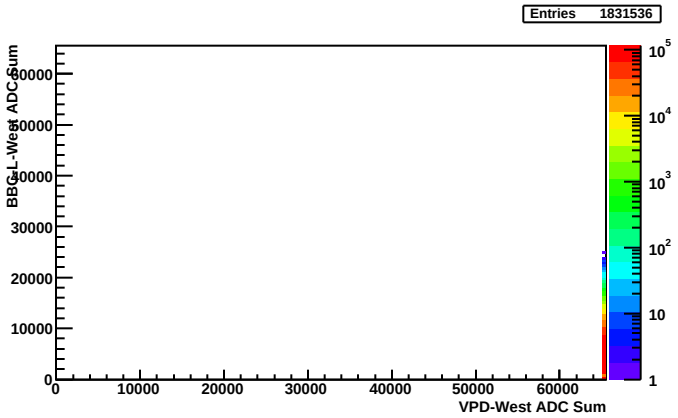

Entries 1831536
TOF Mult
ZDC-East ADC Sum Att

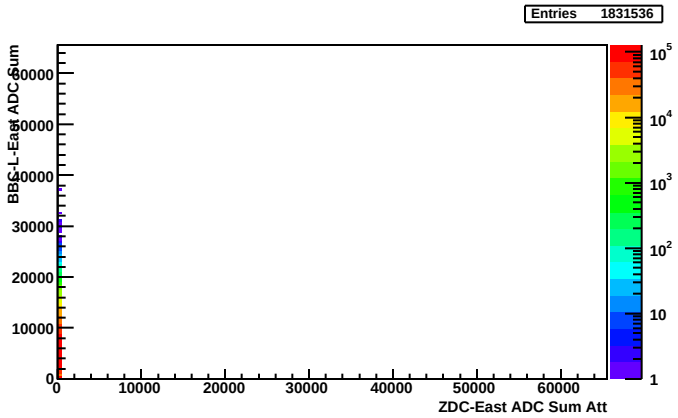

Entries 1831536
TOF Mult
ZDC-West ADC Sum Att

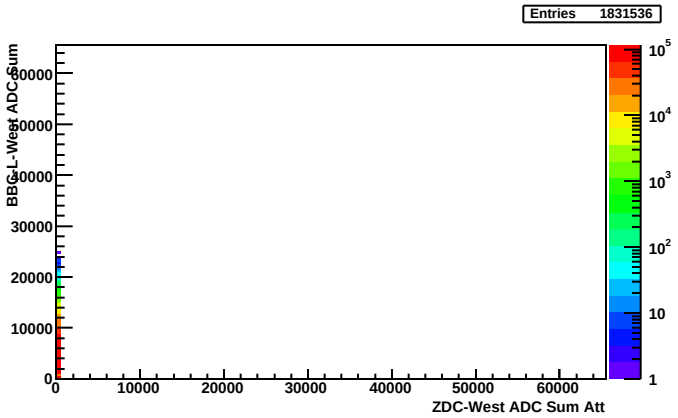

Entries 1831536
BBC-L-East ADC Sum
BBC-S-East ADC Sum

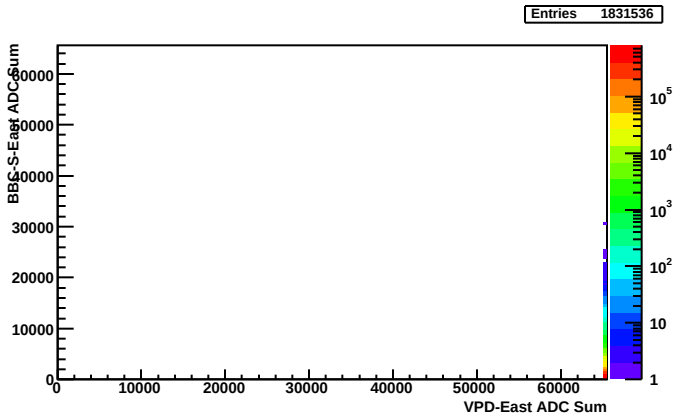

Entries 1831536
BBC-L-West ADC Sum
BBC-S-West ADC Sum

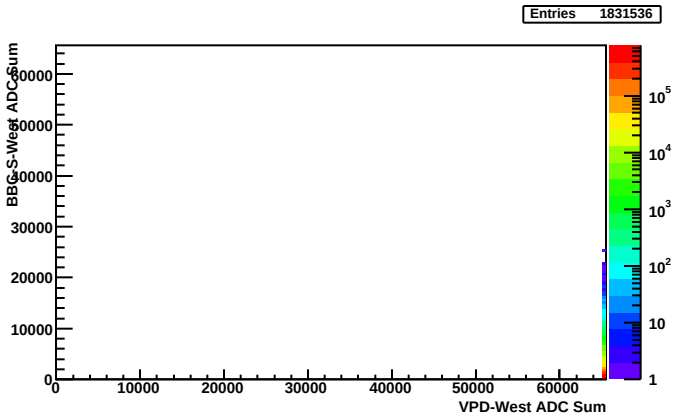

Entries 1831536
BBC-L-East ADC Sum
VPD-East ADC Sum

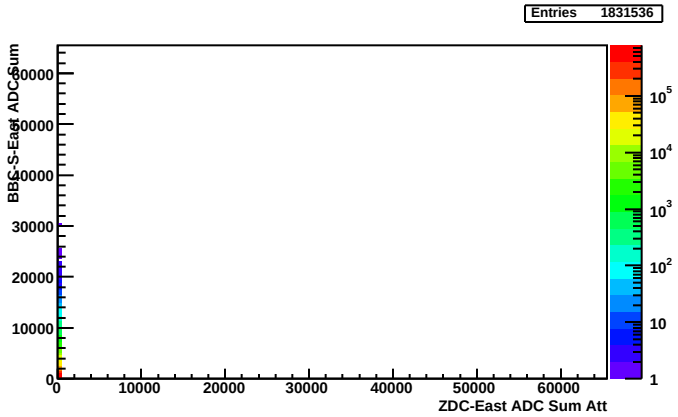

Entries 1831536
BBC-L-West ADC Sum
VPD-West ADC Sum

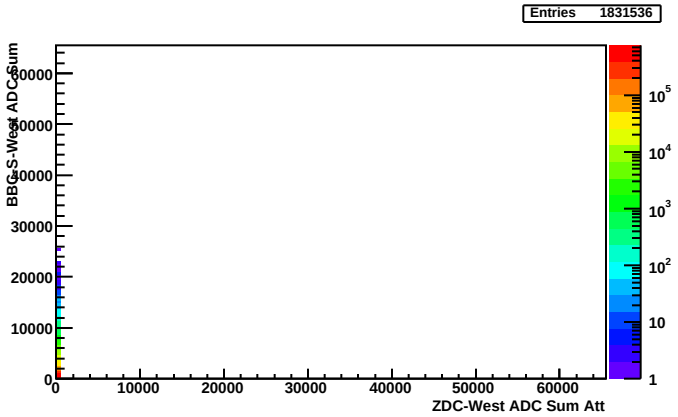

Entries 1831536
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

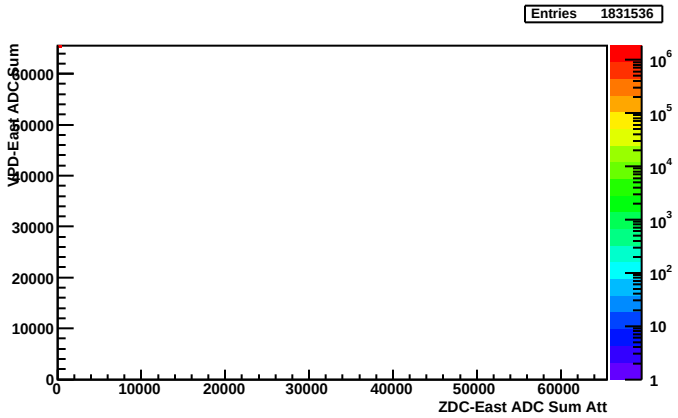

Entries 1831536
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

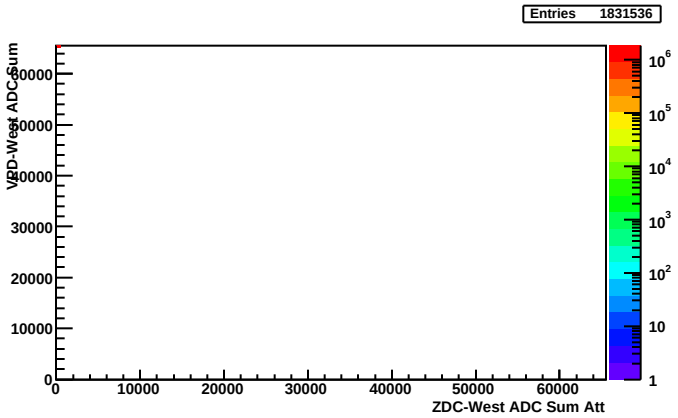

Entries 1831536
BBC-S-East ADC Sum
VPD-East ADC Sum

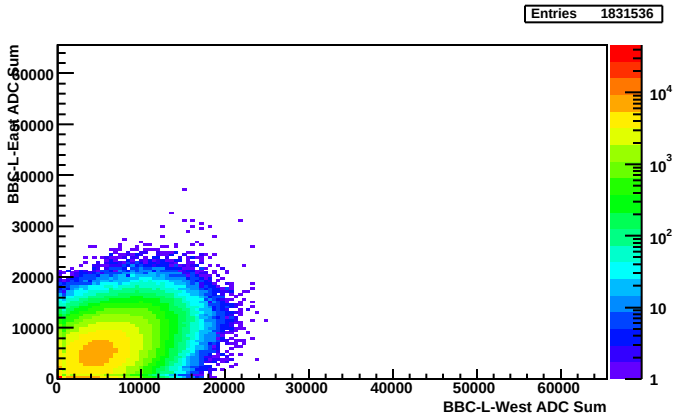

Entries 1831536
BBC-S-West ADC Sum
VPD-West ADC Sum

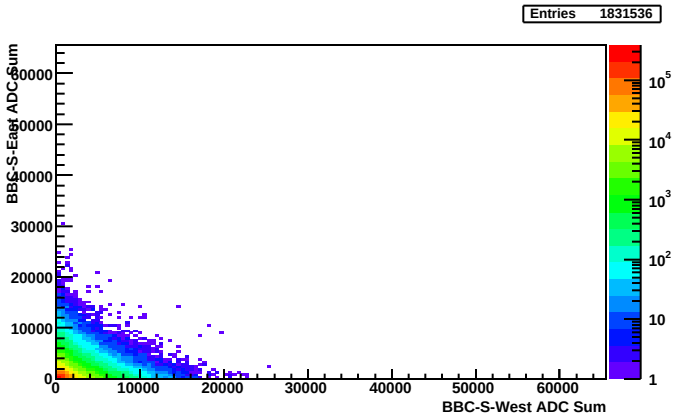

Entries 1831536
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

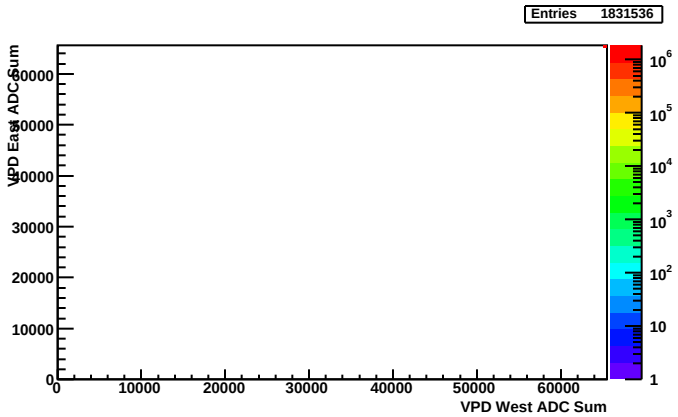

Entries 1831536
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

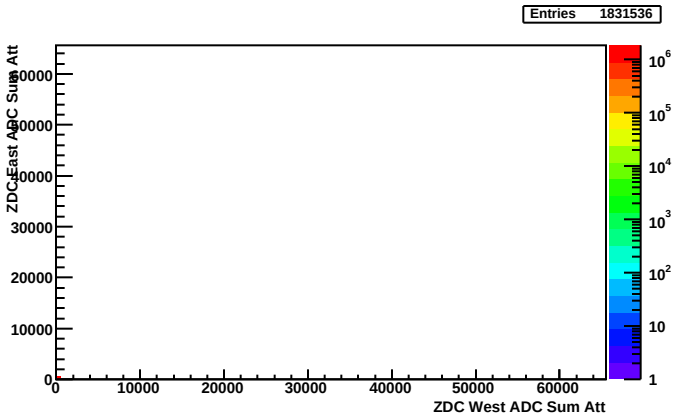

Entries 1831536
VPD-East ADC Sum
ZDC-East ADC Sum Att

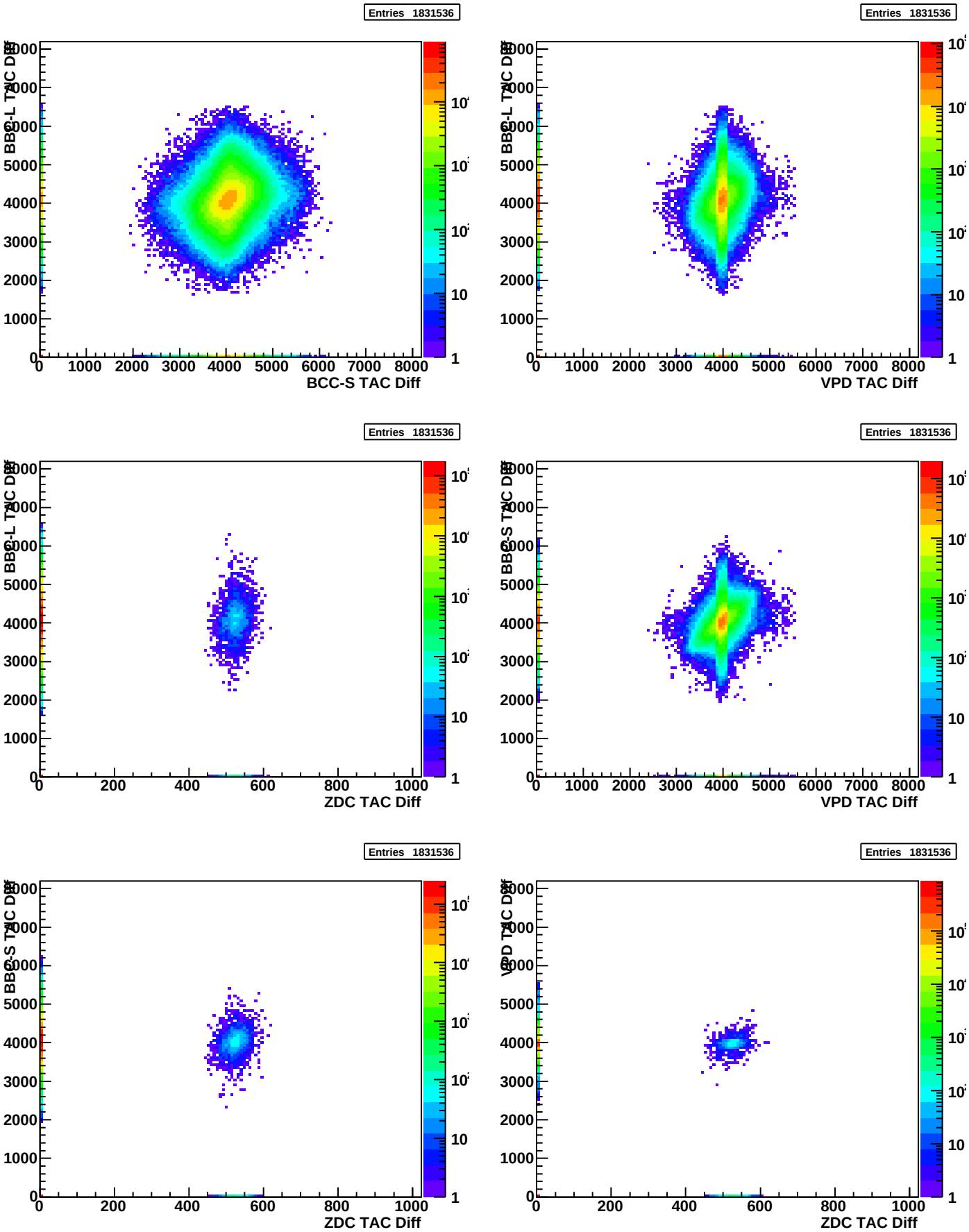

Entries 1831536
VPD-West ADC Sum
ZDC-West ADC Sum Att

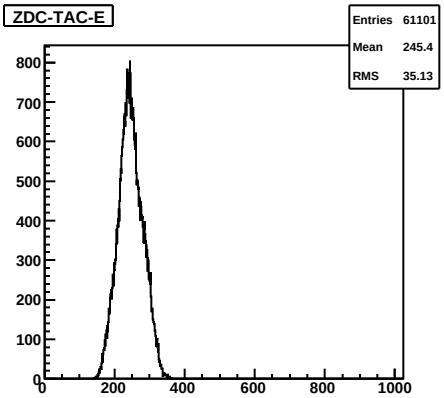

Entries 1831536
BBC-L-East ADC Sum
BBC-L-West ADC Sum

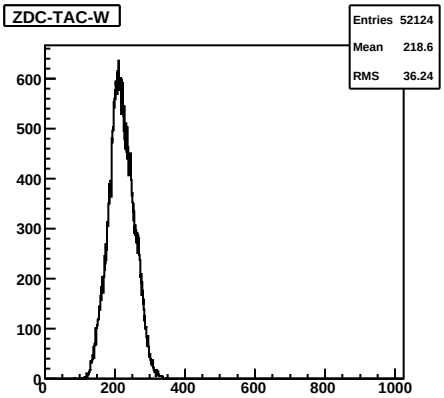

Entries 1831536
BBC-S-East ADC Sum
BBC-S-West ADC Sum

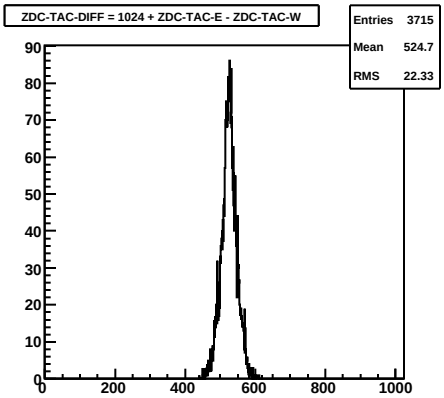

Entries 1831536
VPD East ADC Sum
VPD West ADC Sum

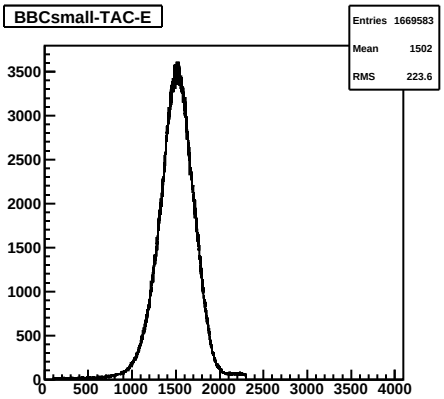

Entries 1831536
ZDC East ADC Sum Att
ZDC West ADC Sum Att

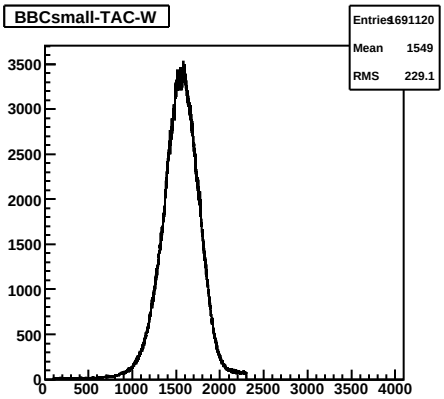

Entries 1831536
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1831536
BBC-L TAC Diff
VPD TAC Diff
Entries 1831536
BBC-L TAC Diff
ZDC TAC Diff
Entries 1831536
BBC-S TAC Diff
VPD TAC Diff
Entries 1831536
BBC-S TAC Diff
ZDC TAC Diff
Entries 1831536
VPD TAC Diff
ZDC TAC Diff

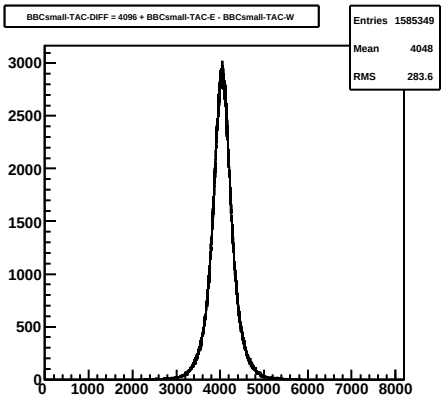

ZDC-TAC-E
Entries 61101
Mean 245.4
RMS 35.13

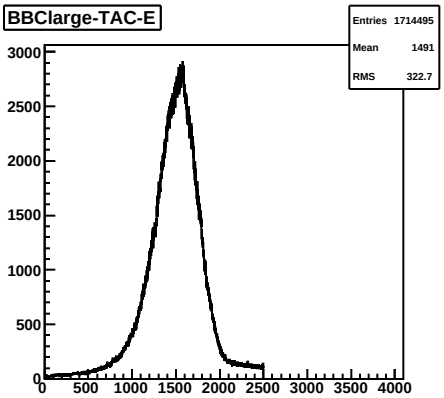

ZDC-TAC-W
Entries 52124
Mean 218.6
RMS 36.24

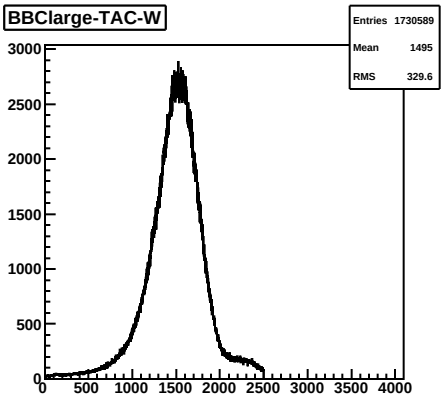

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3715
Mean 524.7
RMS 22.33

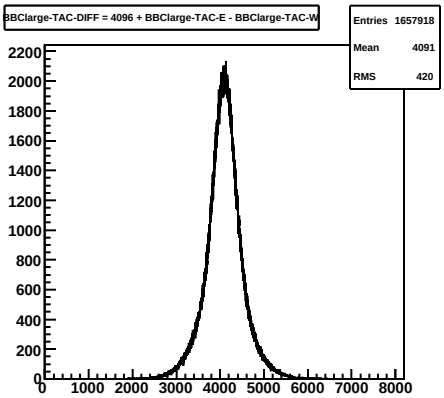

BBCsmall-TAC-E
Entries 1669583
Mean 1502
RMS 223.6

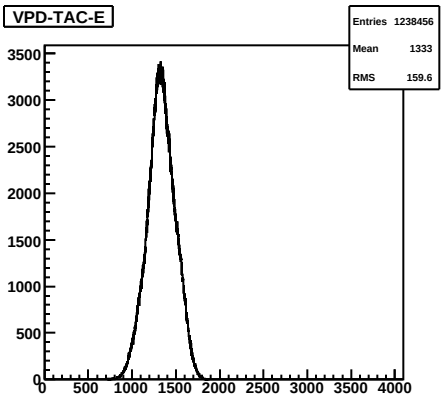

BBCsmall-TAC-W
Entries 1691120
Mean 1549
RMS 229.1

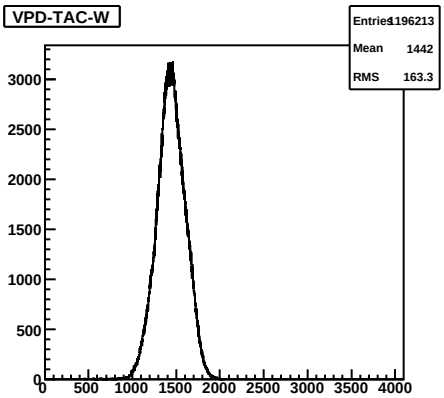

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1585349
Mean 4048
RMS 283.6

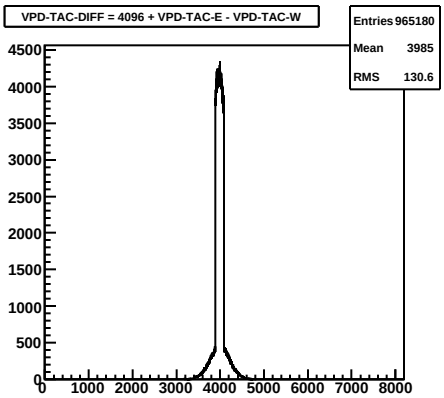

BBClarge-TAC-E
Entries 1714495
Mean 1491
RMS 322.7

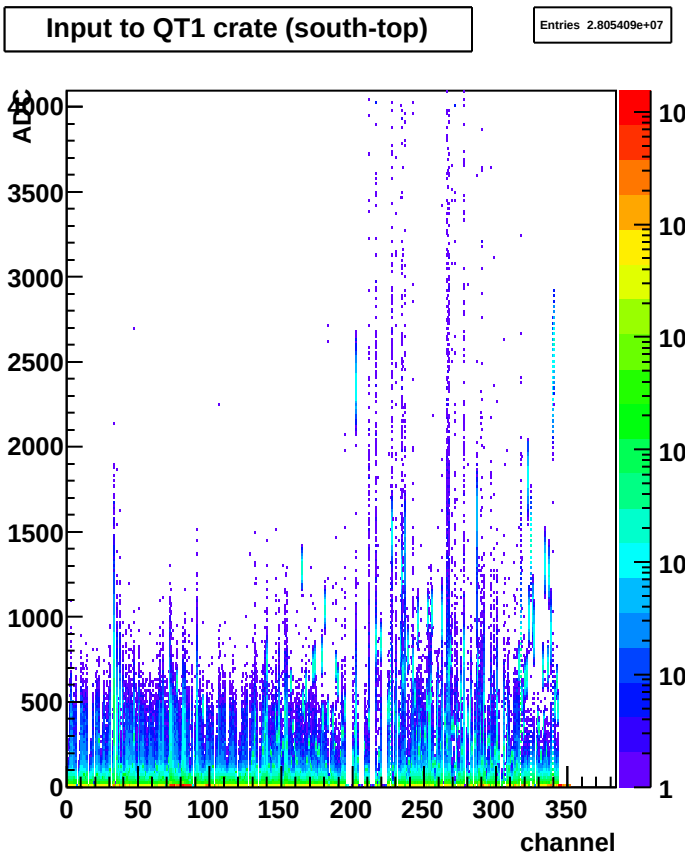

BBClarge-TAC-W
Entries 1730589
Mean 1495
RMS 329.6

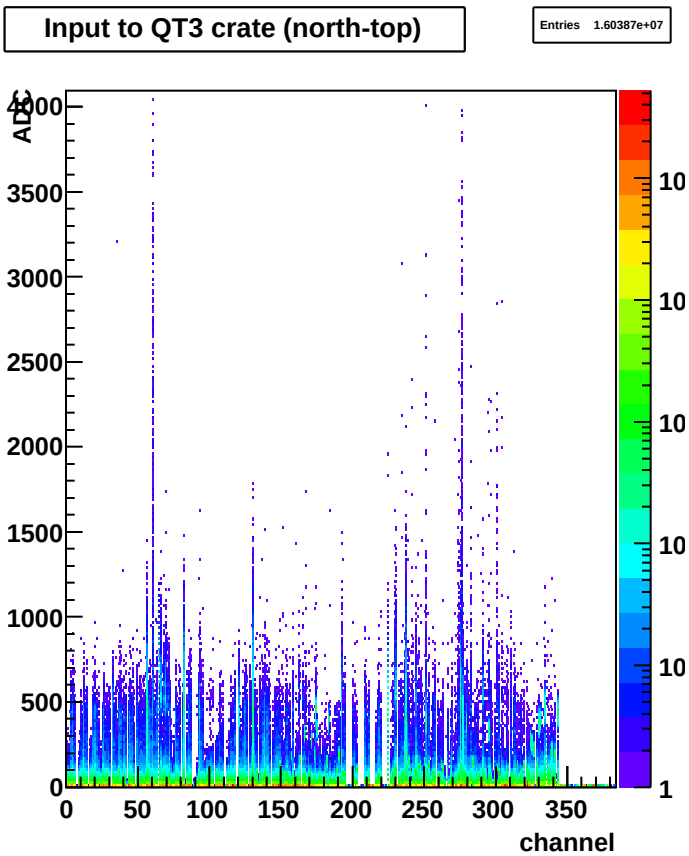

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1657918
Mean 4091
RMS 420

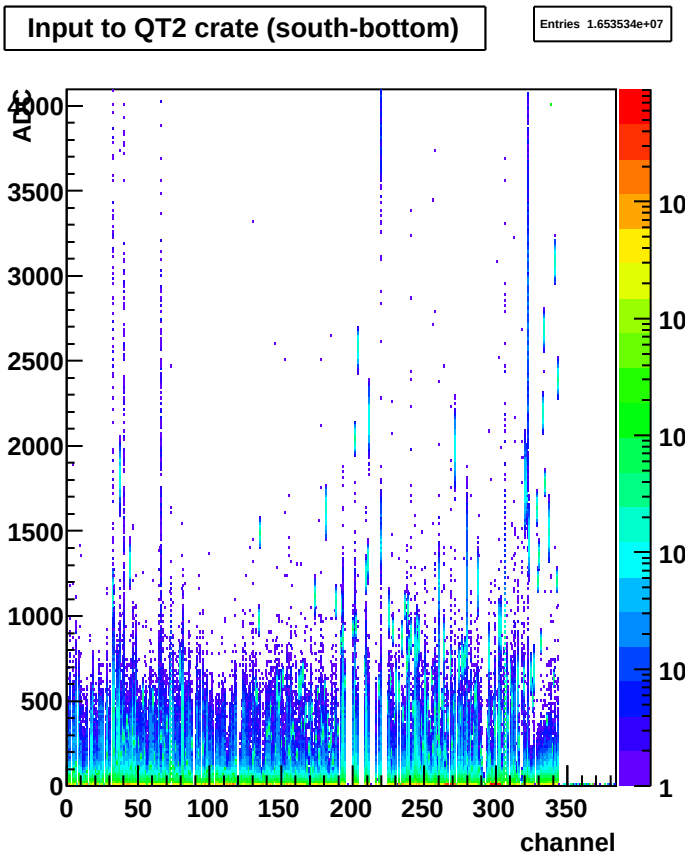

VPD-TAC-E
Entries 1238456
Mean 1333
RMS 159.6

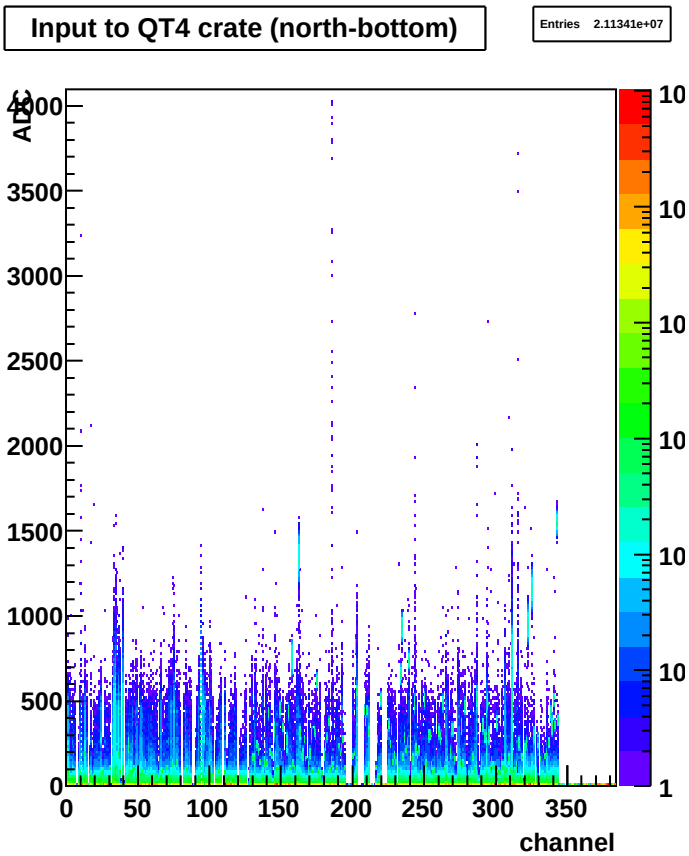

VPD-TAC-W
Entries 1196213
Mean 1442
RMS 163.3

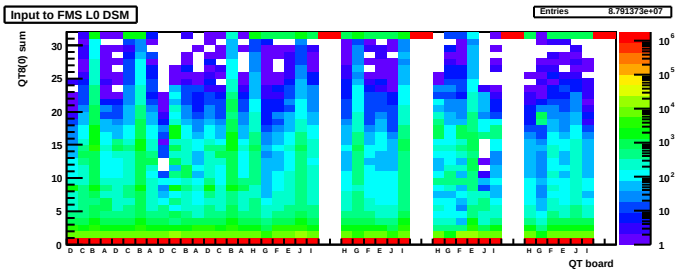

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 965180
Mean 3985
RMS 130.6

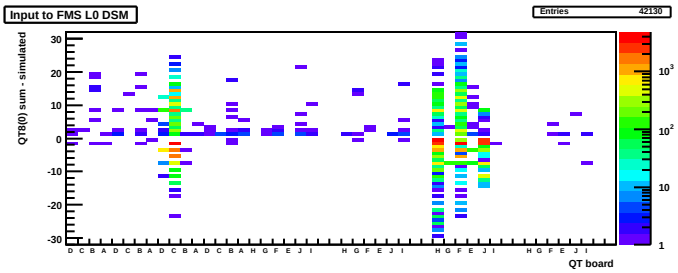

Input to QT1 crate (south-top)
Entries 2.805409e+07
ADC
channel

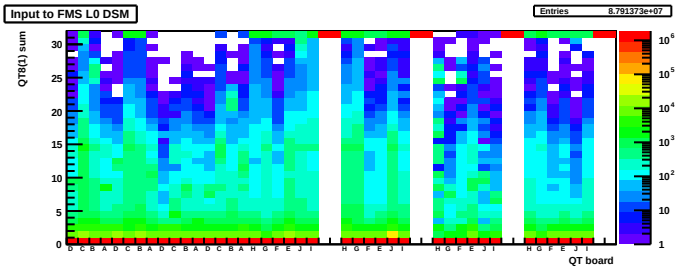

Input to QT3 crate (north-top)
Entries 1.60387e+07
ADC
channel

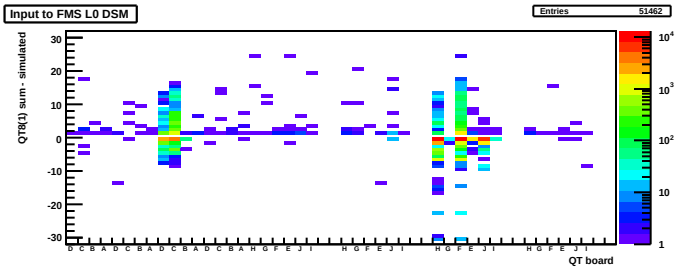

Input to QT2 crate (south-bottom)
Entries 1.653534e+07
ADC
channel

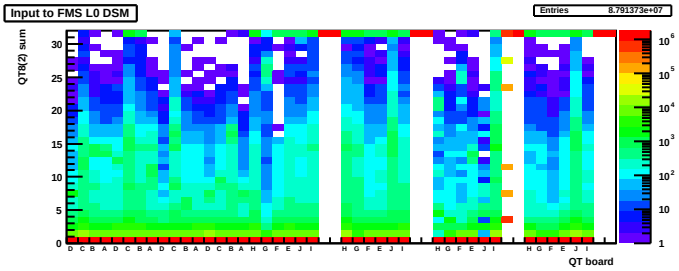

Input to QT4 crate (north-bottom)
Entries 2.11341e+07
ADC
channel

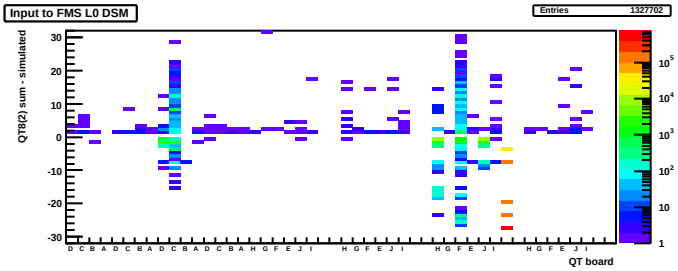

Input to FMS L0 DSM
Entries 8.791373e+07
QT8(0) sum
QT board

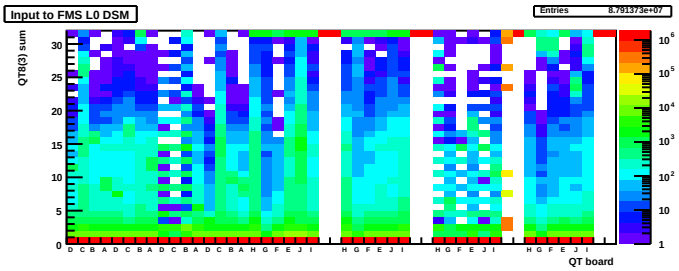

Input to FMS L0 DSM
Entries 42130
QT8(0) sum - simulated
QT board

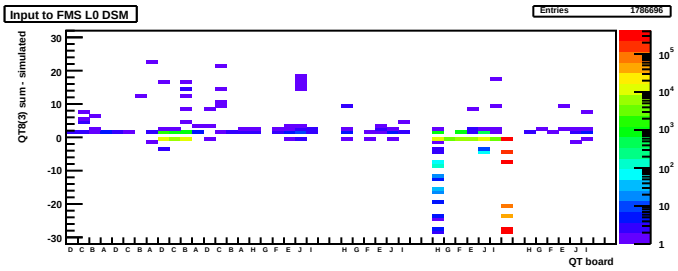

Input to FMS L0 DSM
Entries 8.791373e+07
QT8(1) sum
QT board

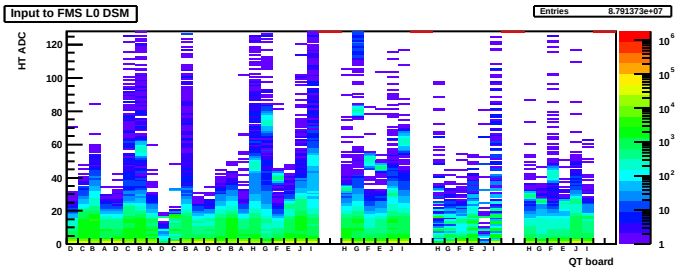

Input to FMS L0 DSM
Entries 51462
QT8(1) sum - simulated
QT board

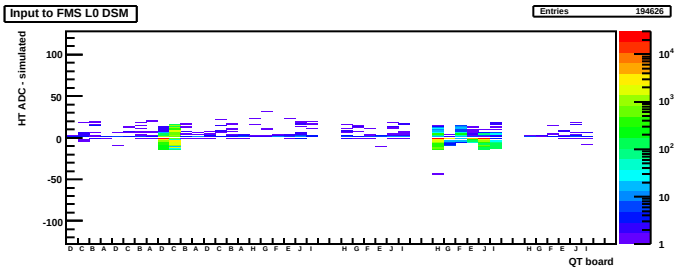

Input to FMS L0 DSM
Entries 8.791373e+07
QT8(2) sum
QT board

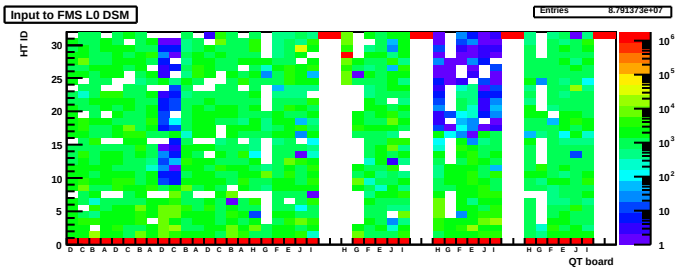

Input to FMS L0 DSM
Entries 1327702
QT8(2) sum - simulated
QT board

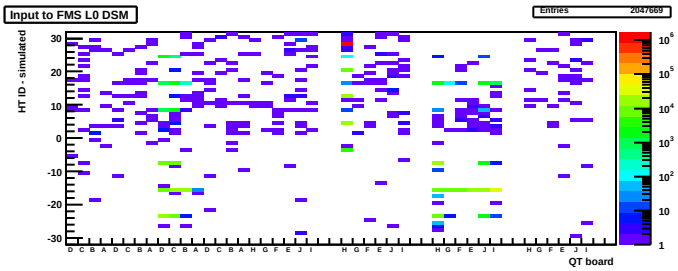

Input to FMS L0 DSM
Entries 8.791373e+07
QT8(3) sum
QT board

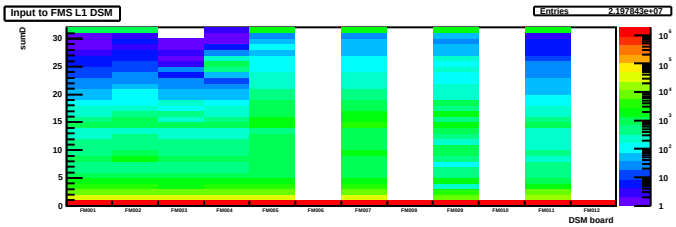

Input to FMS L0 DSM
Entries 1786696
QT8(3) sum - simulated
QT board

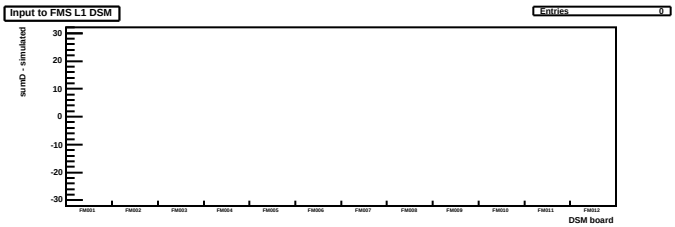

Input to FMS L0 DSM
Entries 8.791373e+07
HT ADC
QT board

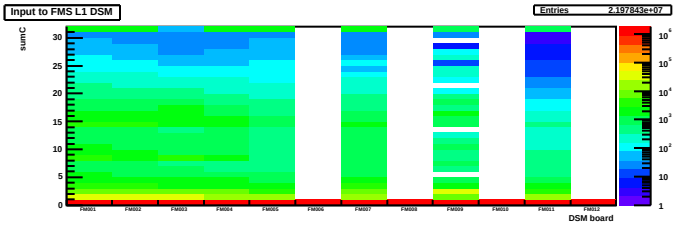

Input to FMS L0 DSM
Entries 194626
HT ADC - simulated
QT board

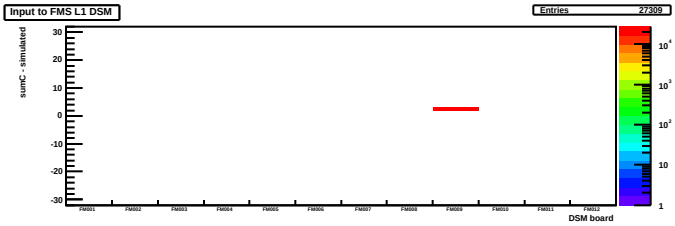

Input to FMS L0 DSM
Entries 8.791373e+07
HT ID
QT board

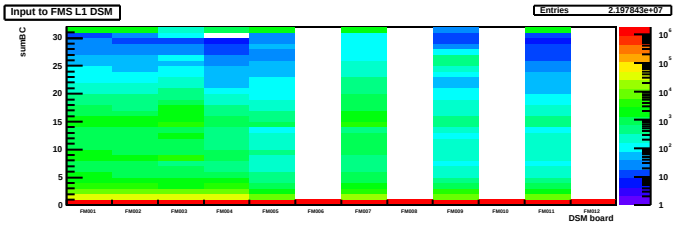

Input to FMS L0 DSM
Entries 2047669
HT ID - simulated
QT board

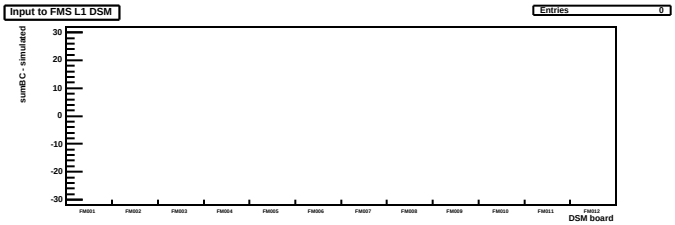

Input to FMS L1 DSM
Entries 2.197843e+07
sumD
DSM board

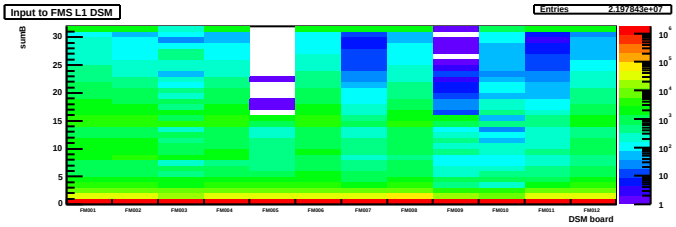

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

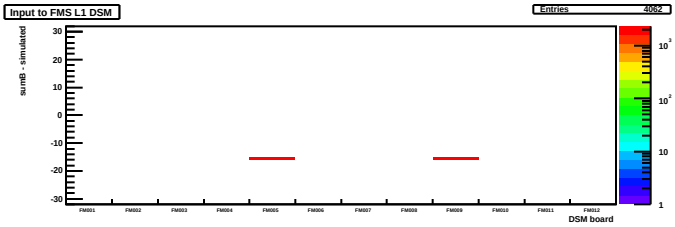

Input to FMS L1 DSM
Entries 2.197843e+07
sumC
DSM board

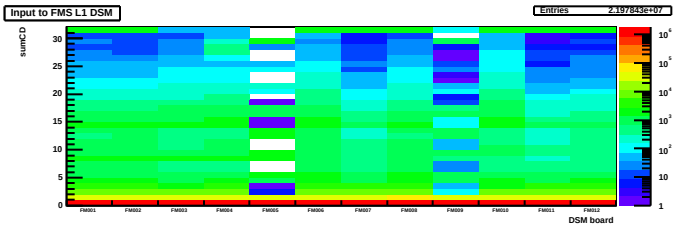

Input to FMS L1 DSM
Entries 27309
sumC - simulated
DSM board

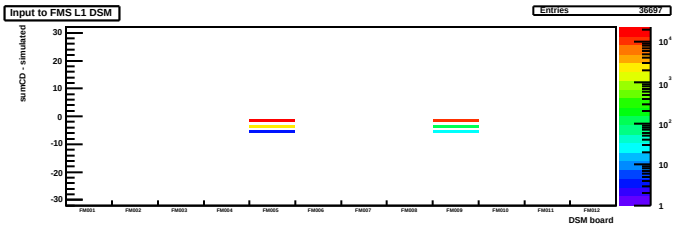

Input to FMS L1 DSM
Entries 2.197843e+07
sumBC
DSM board

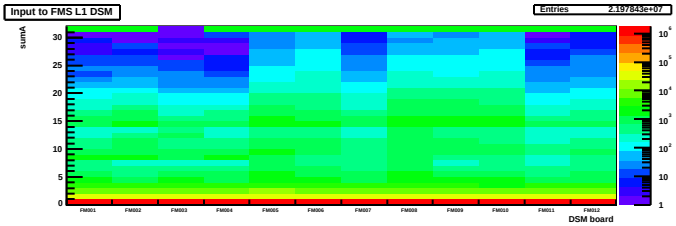

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

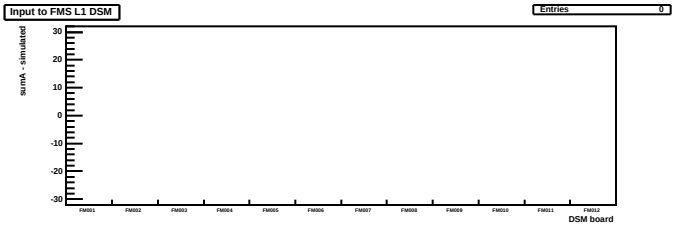

Input to FMS L1 DSM
Entries 2.197843e+07
sumB
DSM board

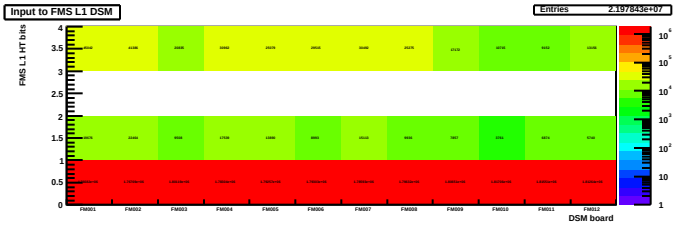

Input to FMS L1 DSM
Entries 4082
sumB - simulated
DSM board

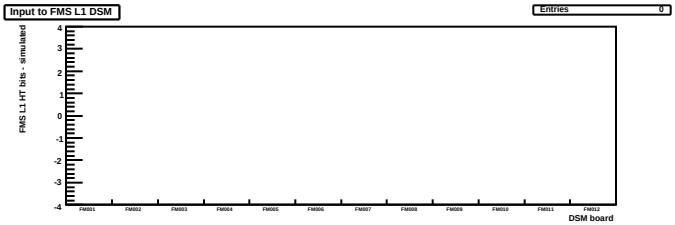

Input to FMS L1 DSM
Entries 2.197843e+07
sumCD
DSM board

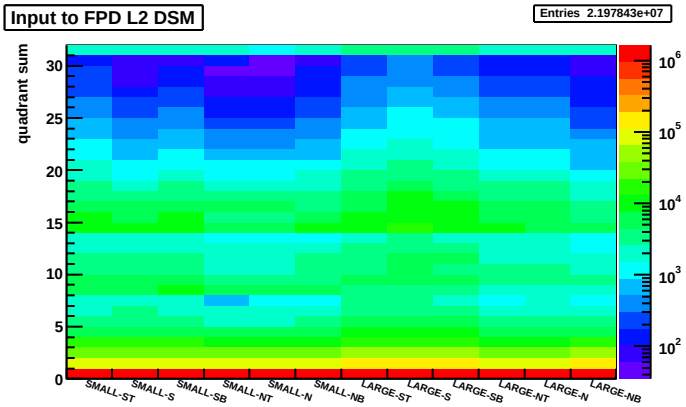

Input to FMS L1 DSM
Entries 36697
sumCD - simulated
DSM board

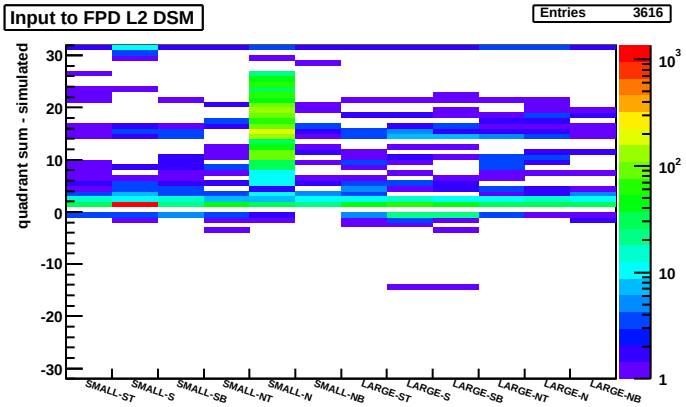

Input to FMS L1 DSM
Entries 2.197843e+07
sumA
DSM board

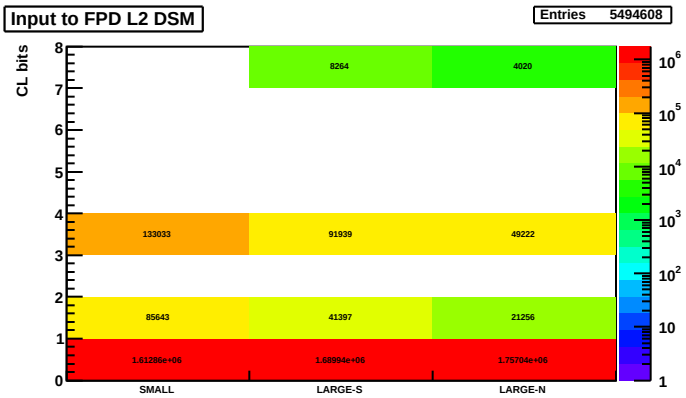

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

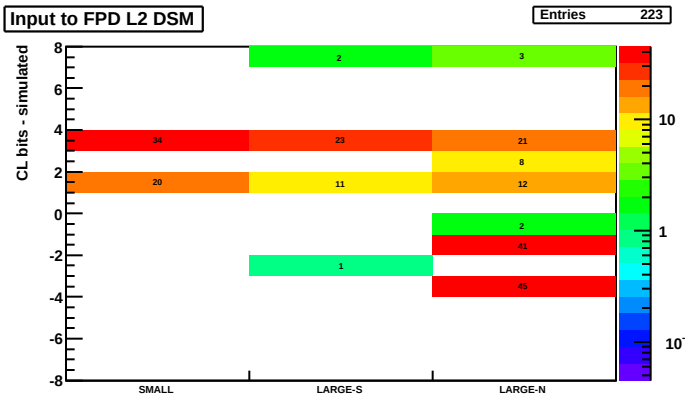

Input to FMS L1 DSM
Entries 2.197843e+07
FMS L1 HT bits
DSM board

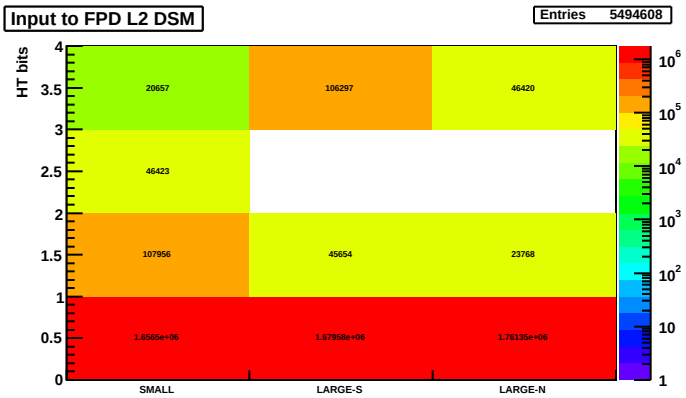

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

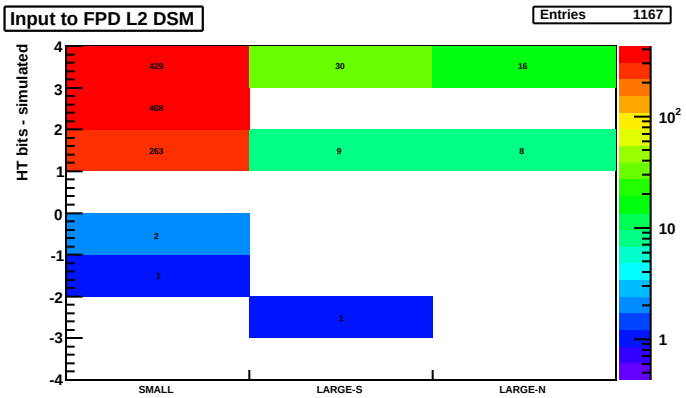

Input to FPD L2 DSM
Entries 2.197843e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

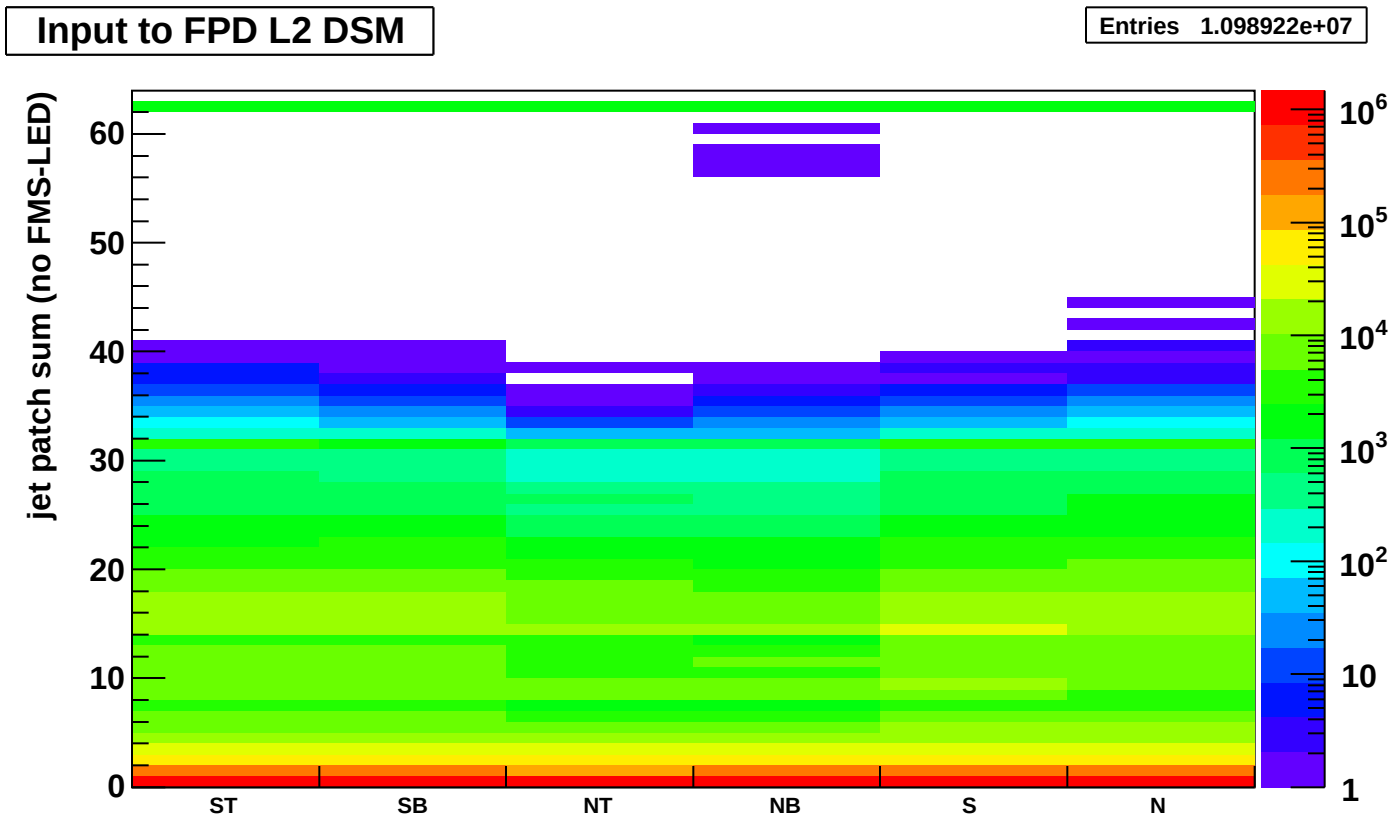

Input to FPD L2 DSM
Entries 3616
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

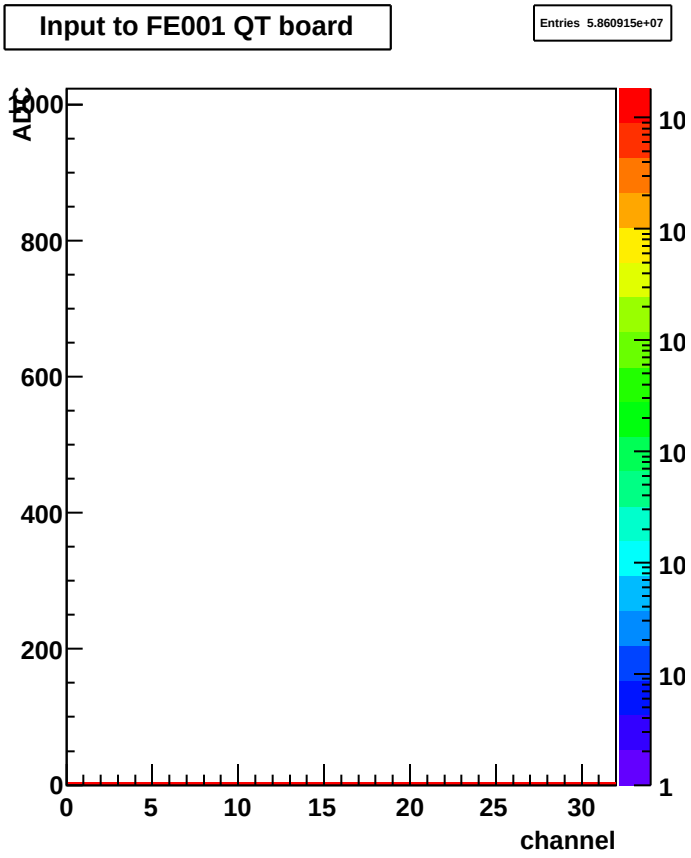

Input to FPD L2 DSM
Entries 5494608
CL bits
8264
4020
133033
91939
49222
85643
41397
21256
1.61286e+06
1.68994e+06
1.75704e+06
SMALL
LARGE-S
LARGE-N

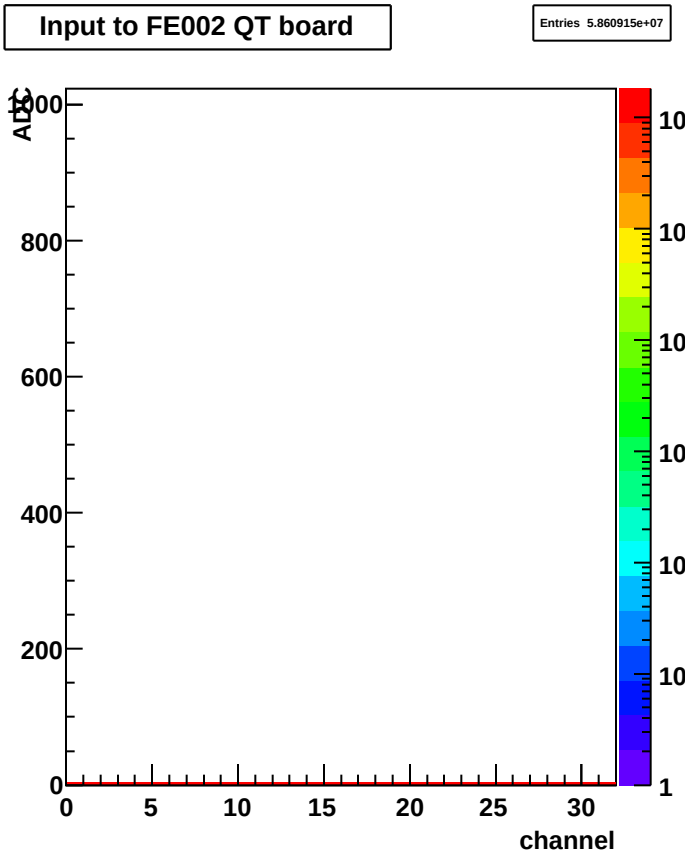

Input to FPD L2 DSM
Entries 223
CL bits - simulated
2
3
34
23
21
8
20
11
12
2
41
1
45
SMALL
LARGE-S
LARGE-N

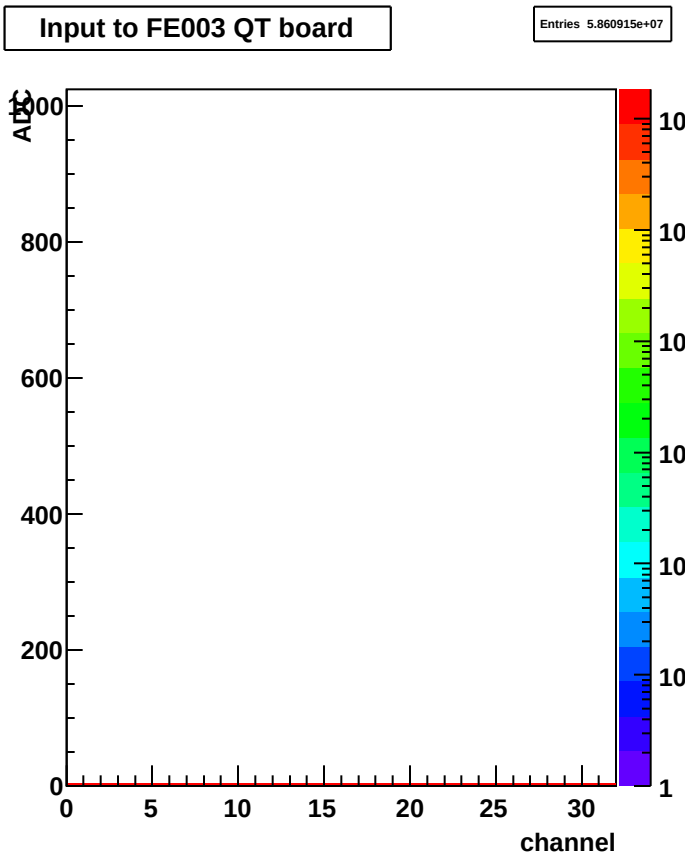

Input to FPD L2 DSM
Entries 5494608
HT bits
20657
106297
46420
46423
107956
45654
23768
1.6565e+06
1.67958e+06
1.76135e+06
SMALL
LARGE-S
LARGE-N

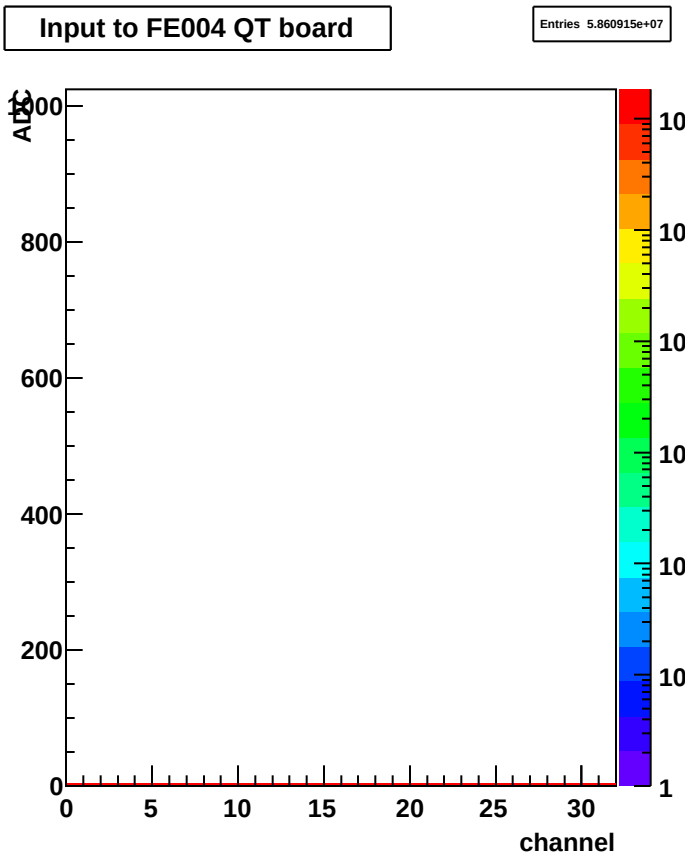

Input to FPD L2 DSM
Entries 1167
HT bits - simulated
429
30
16
408
263
9
8
2
1
1
SMALL
LARGE-S
LARGE-N

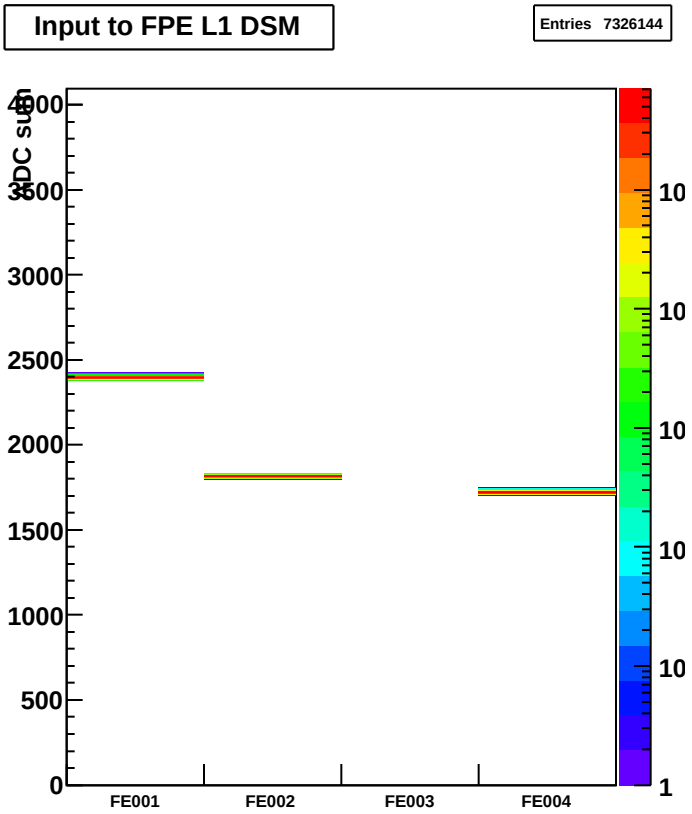

Input to FPD L2 DSM
Entries 1.098922e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

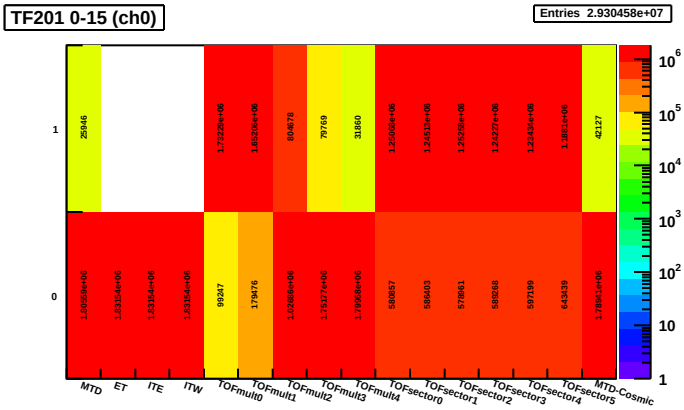

Input to FE001 QT board
Entries 5.860915e+07
ADC
channel

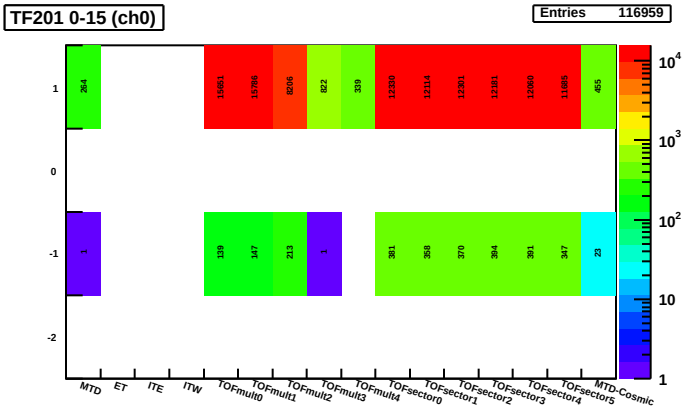

Input to FE002 QT board
Entries 5.860915e+07
ADC
channel

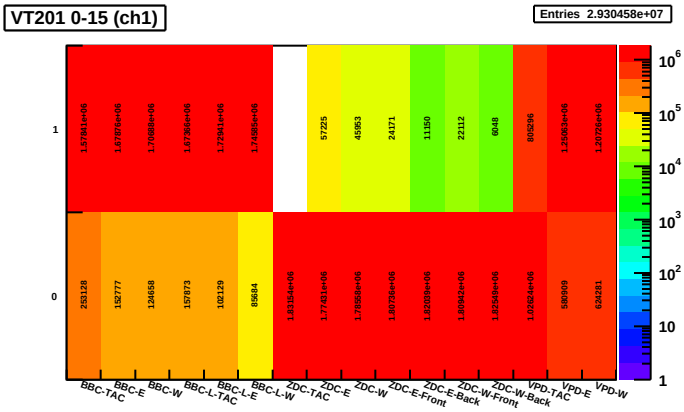

Input to FE003 QT board
Entries 5.860915e+07
ADC
channel


Input to FE004 QT board
Entries 5.860915e+07
ADC
channel


Input to FPE L1 DSM
Entries 7326144
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.930458e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 116959
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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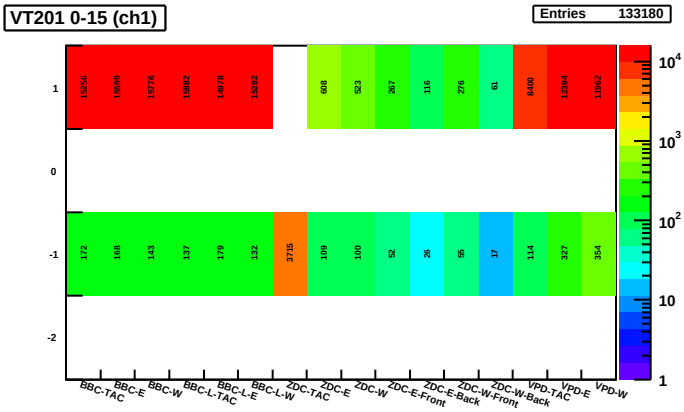
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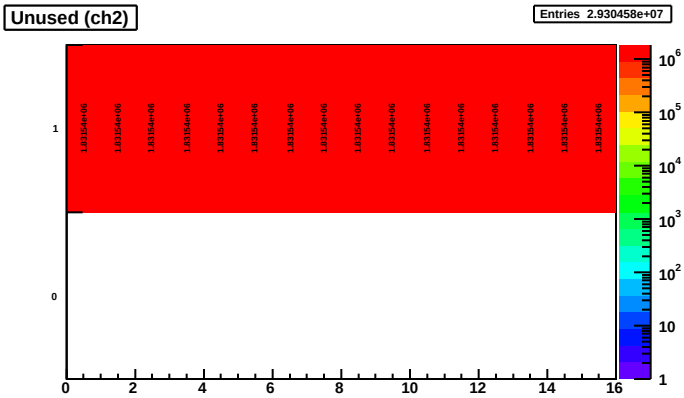
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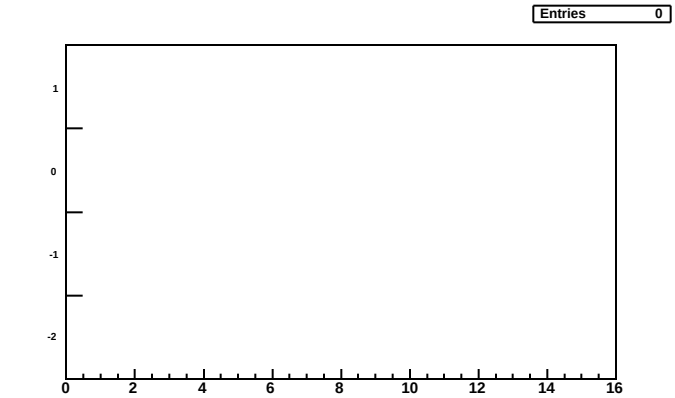
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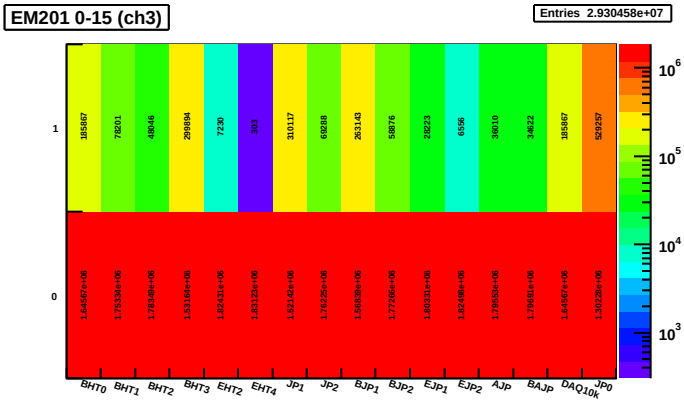
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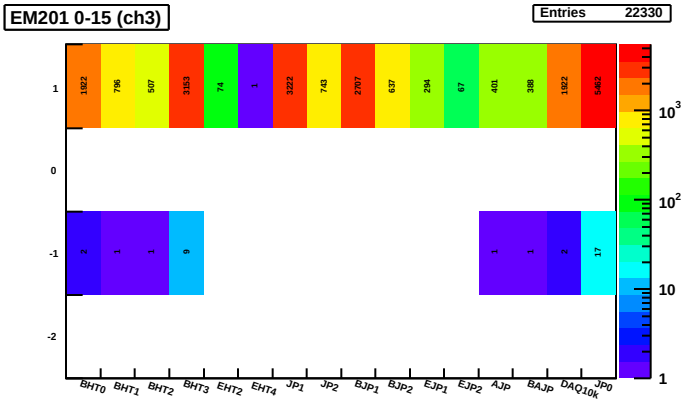
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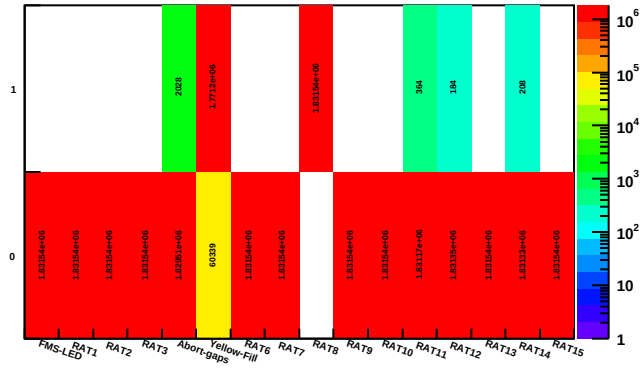
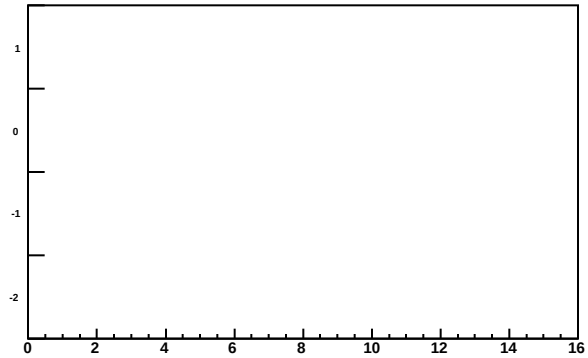
RAT board (ch4)





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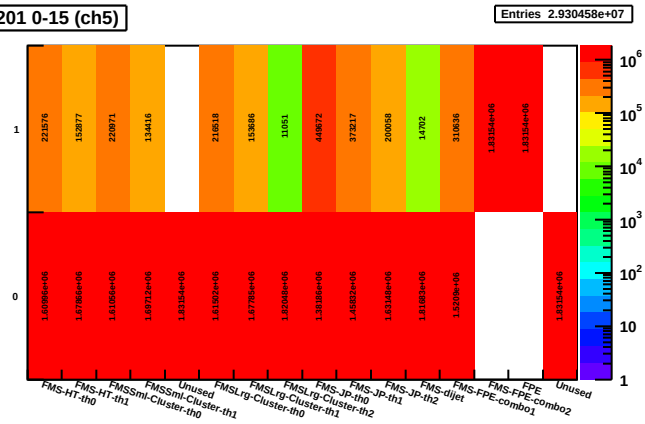

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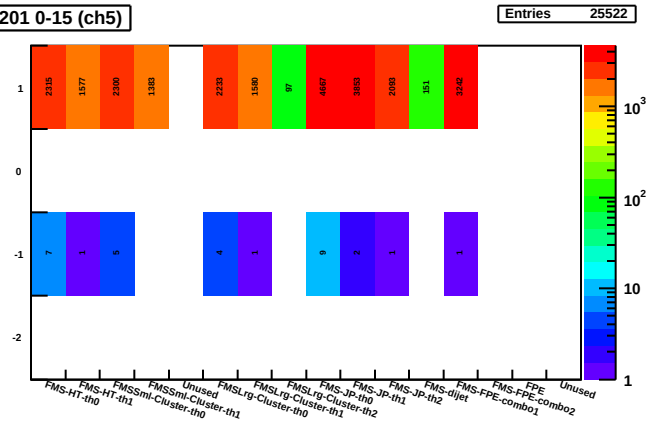
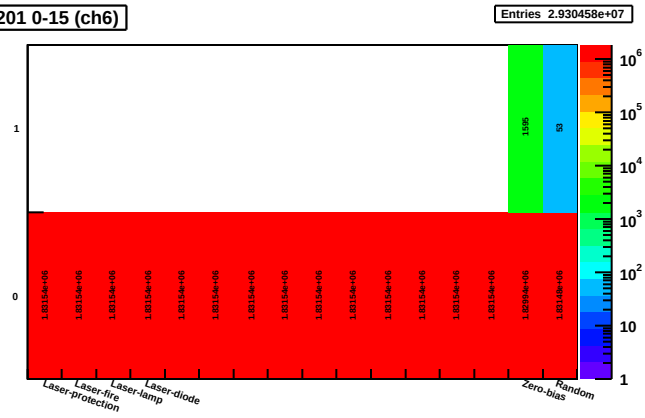
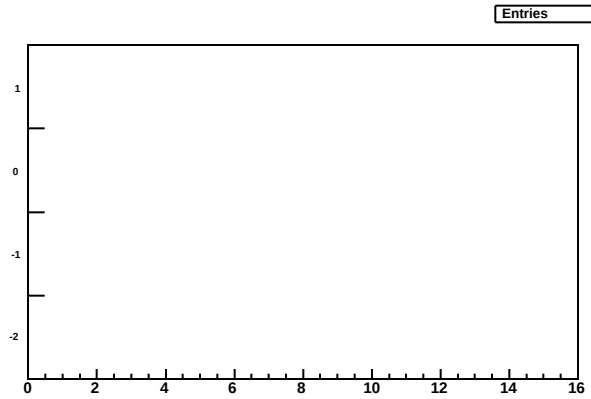
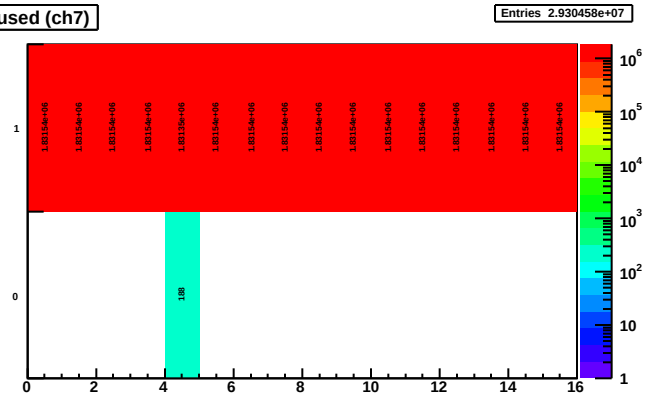
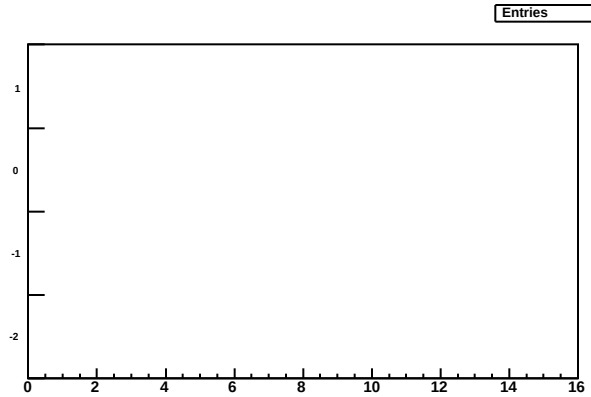


ST201 0-15 (ch6)





Unused (ch7)