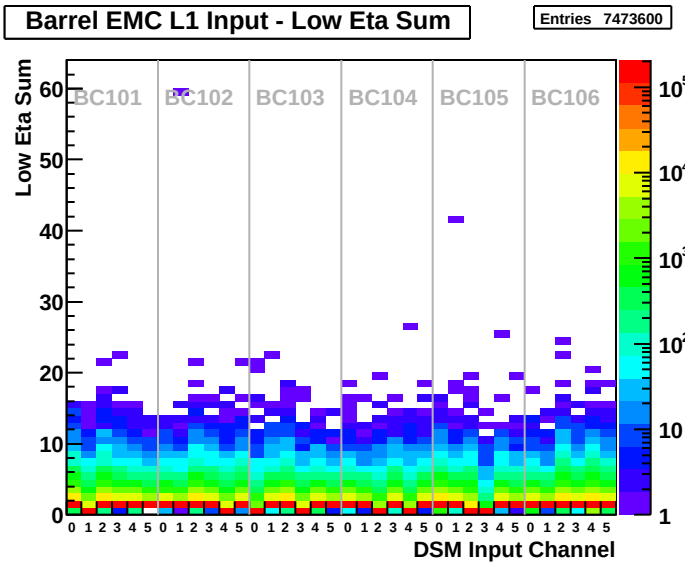
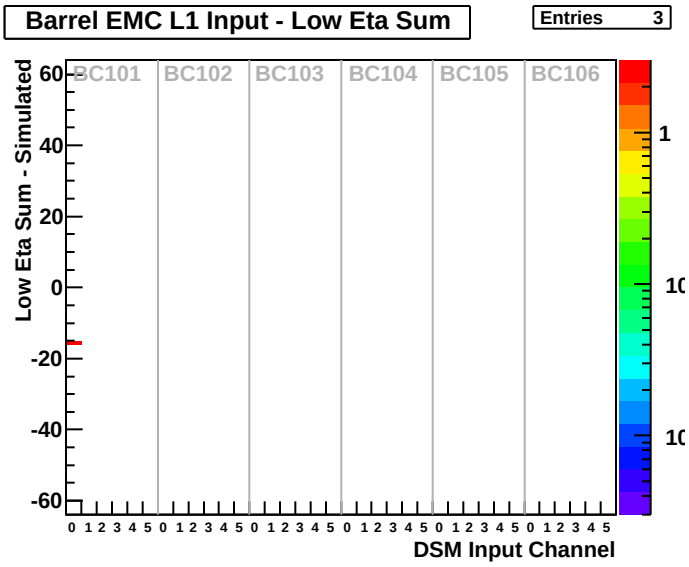
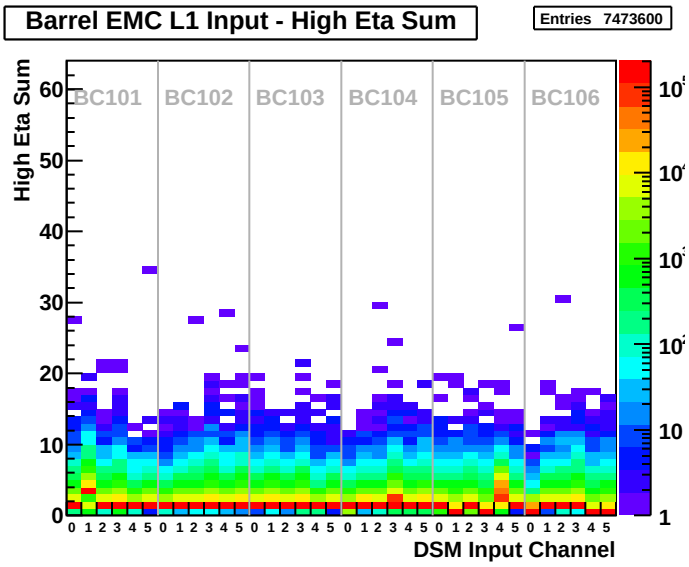
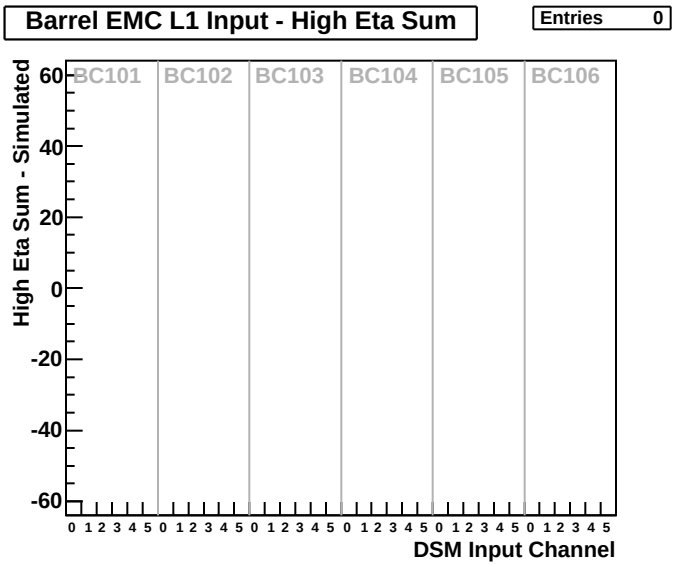
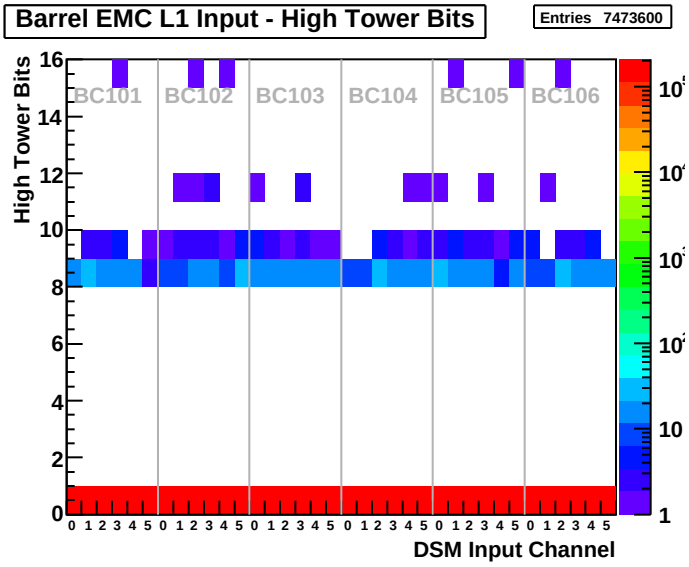
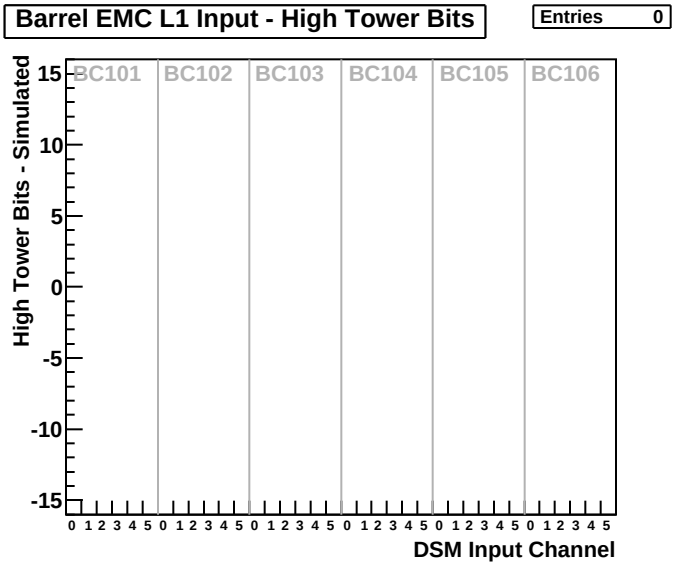

Barrel EMC L1 Input - Low Eta Sum
Entries 7473600
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

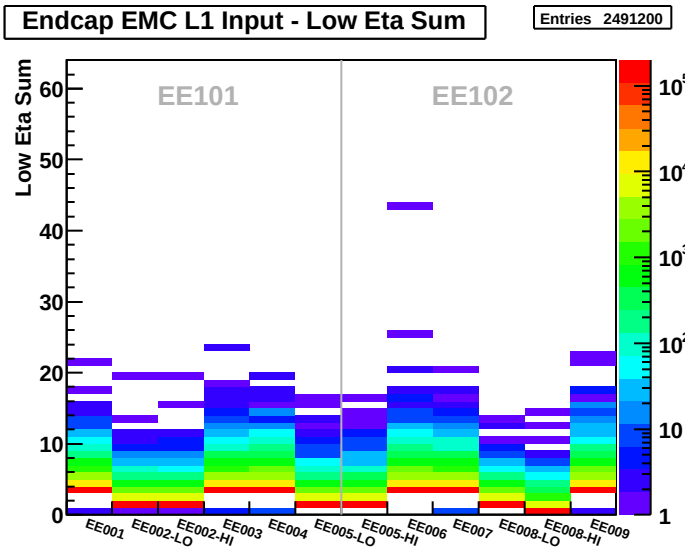

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

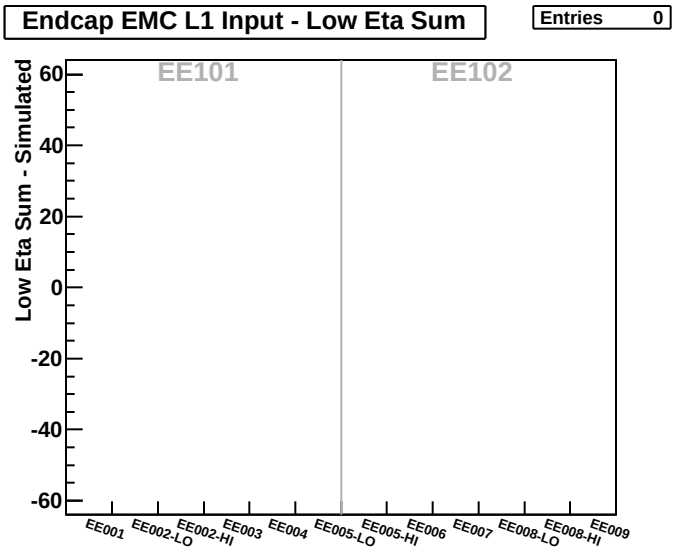

Barrel EMC L1 Input - High Eta Sum
Entries 7473600
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

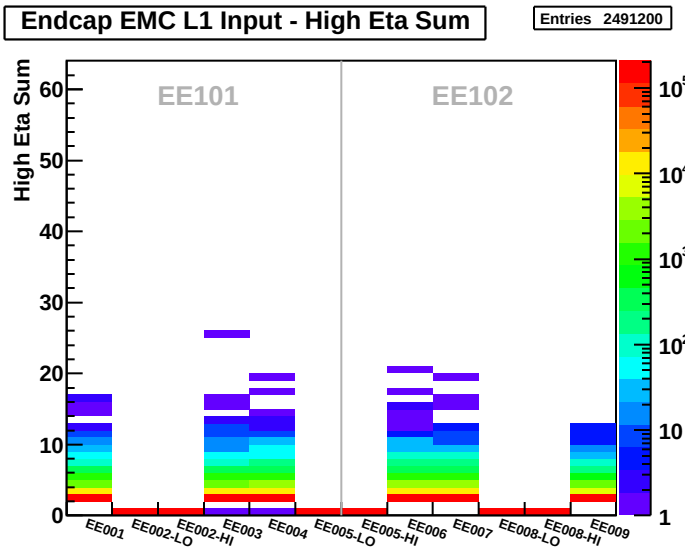

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

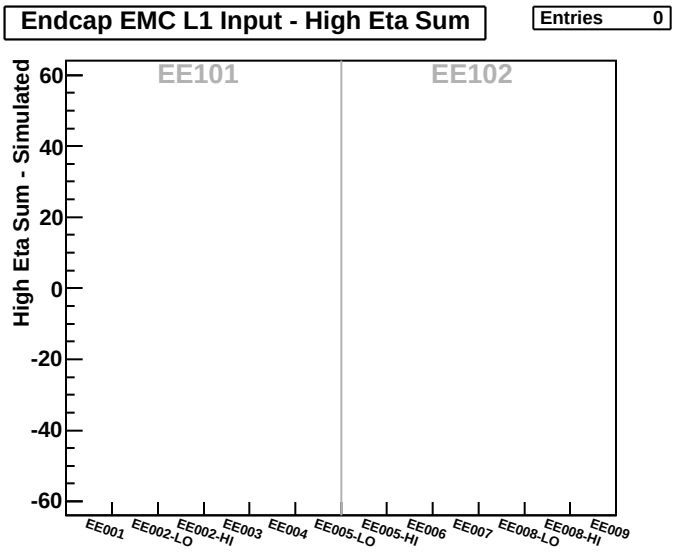

Barrel EMC L1 Input - High Tower Bits
Entries 7473600
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

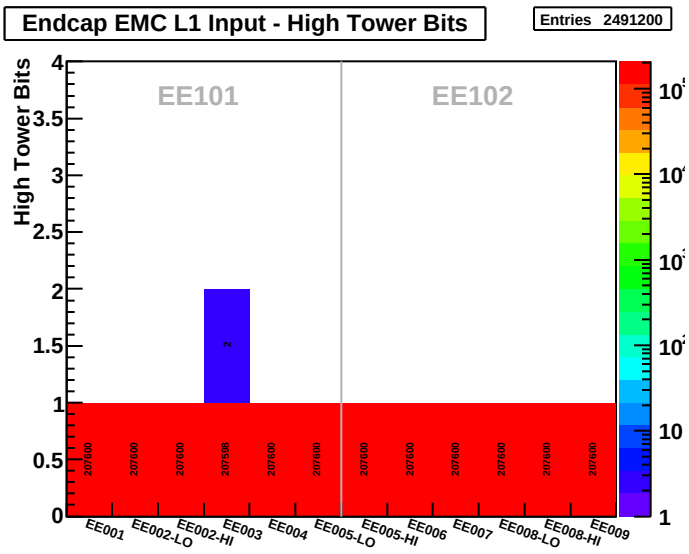

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

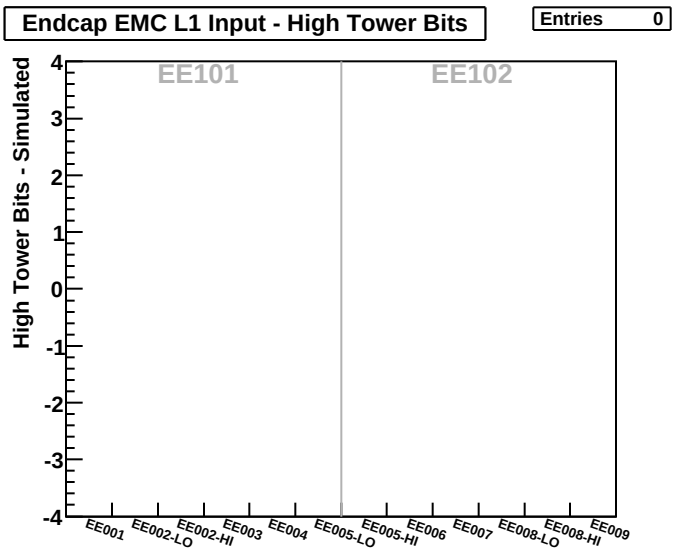

Endcap EMC L1 Input - Low Eta Sum
Entries 2491200
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

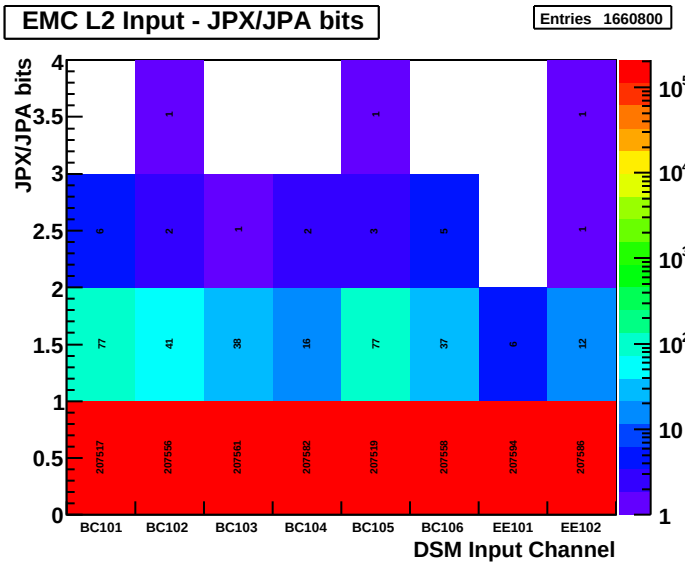

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

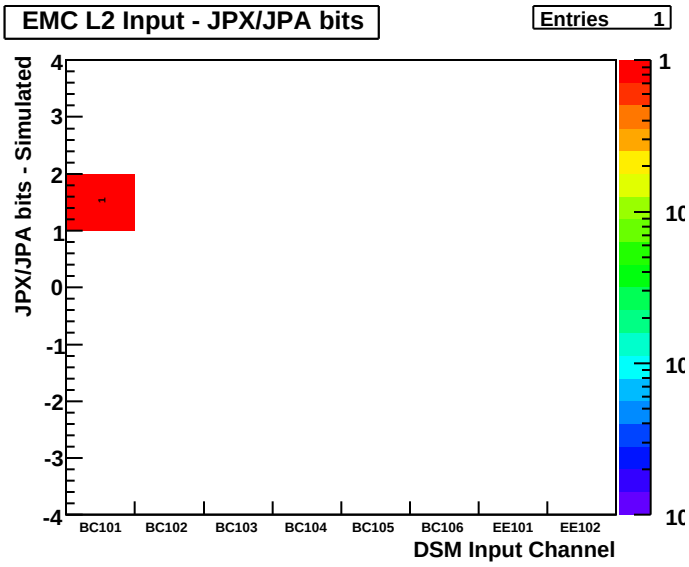

Endcap EMC L1 Input - High Eta Sum
Entries 2491200
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

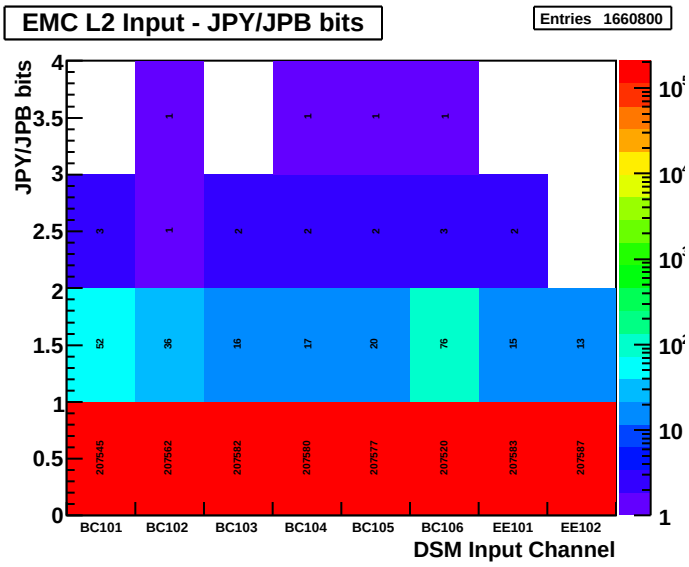

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

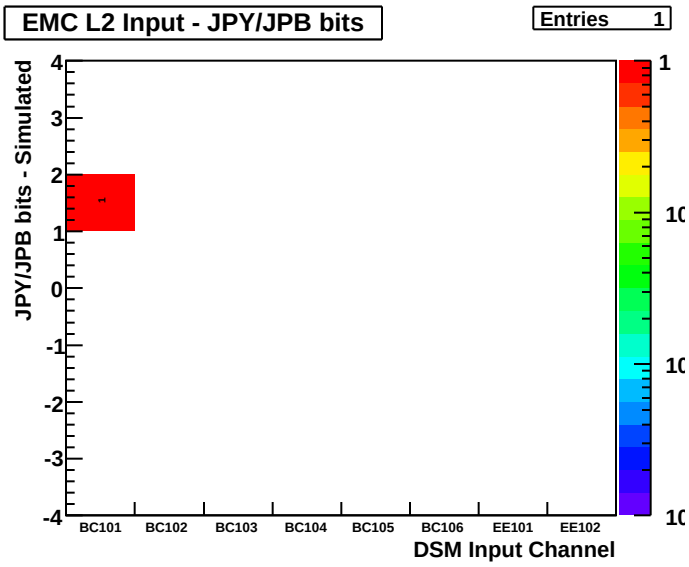

Endcap EMC L1 Input - High Tower Bits
Entries 2491200
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

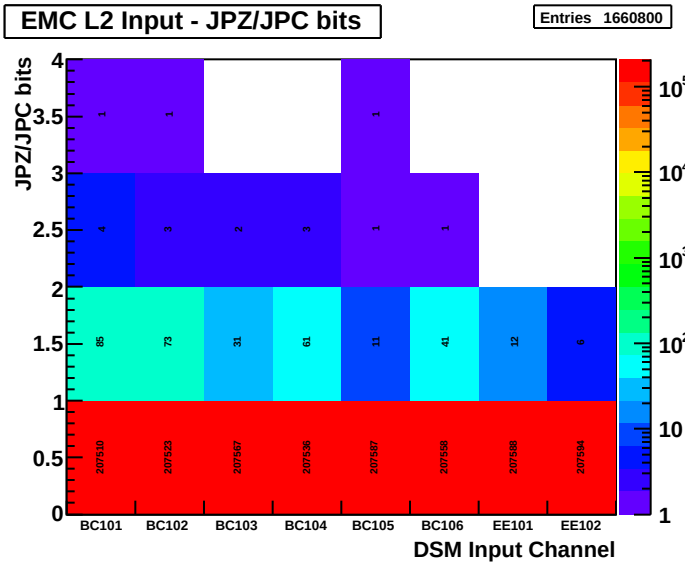

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

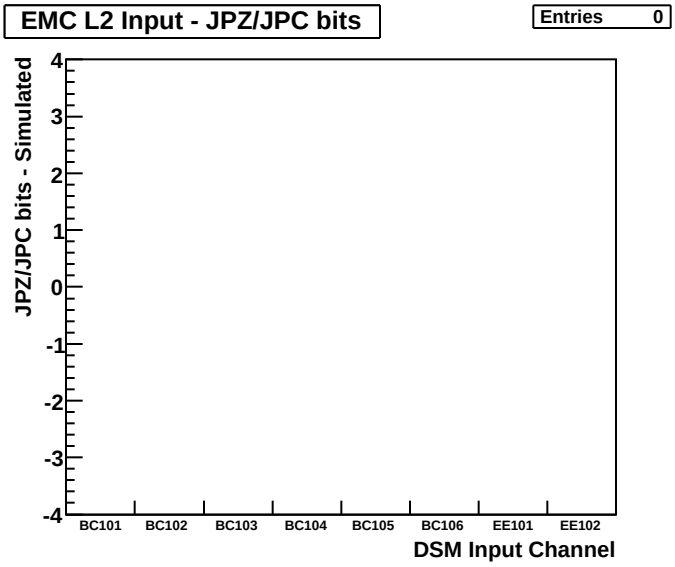

EMC L2 Input - JPX/JPA bits
Entries 1660800
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

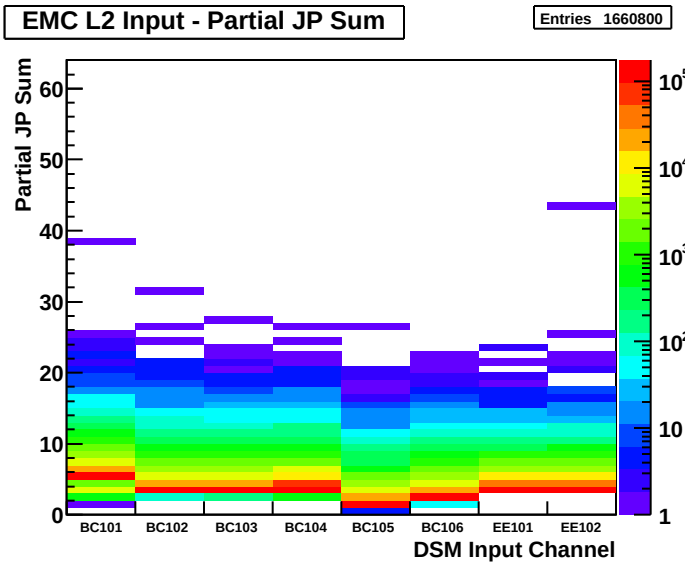

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

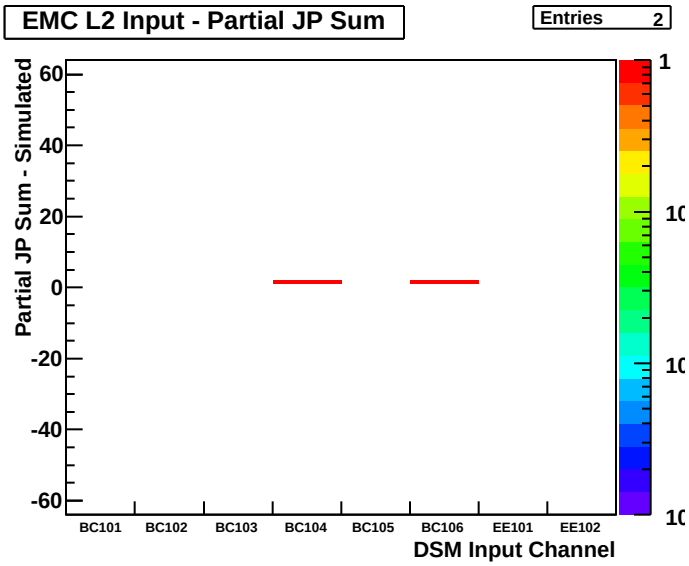

EMC L2 Input - JPY/JPB bits
Entries 1660800
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

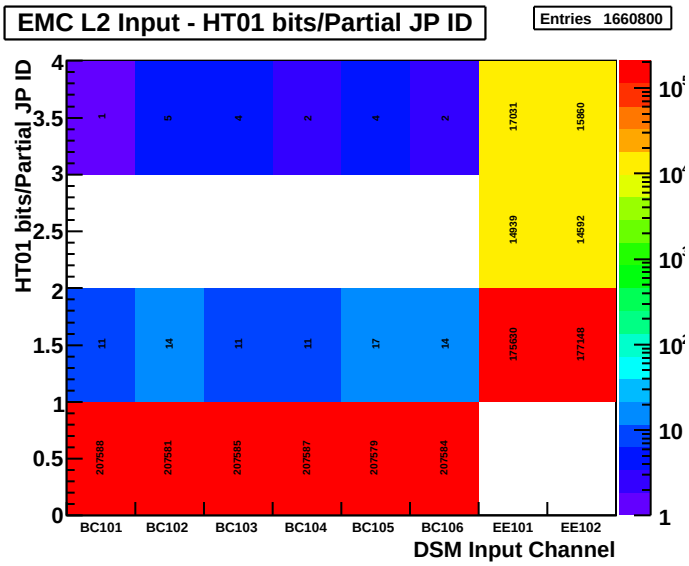

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

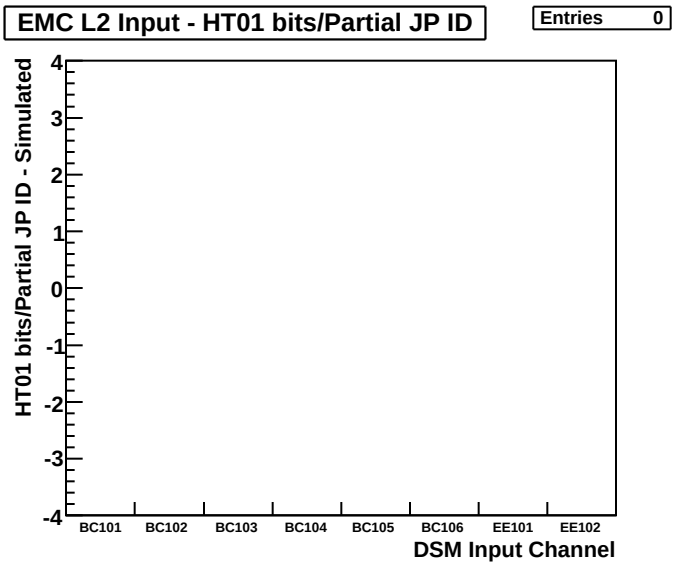

EMC L2 Input - JPZ/JPC bits
Entries 1660800
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

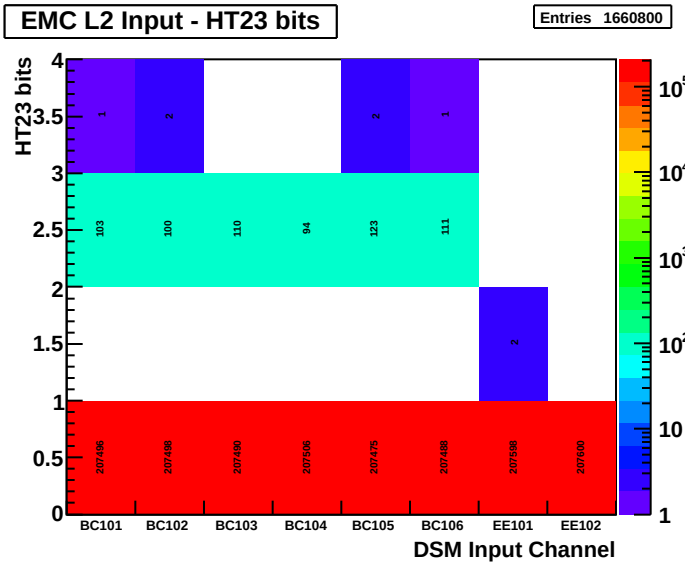

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

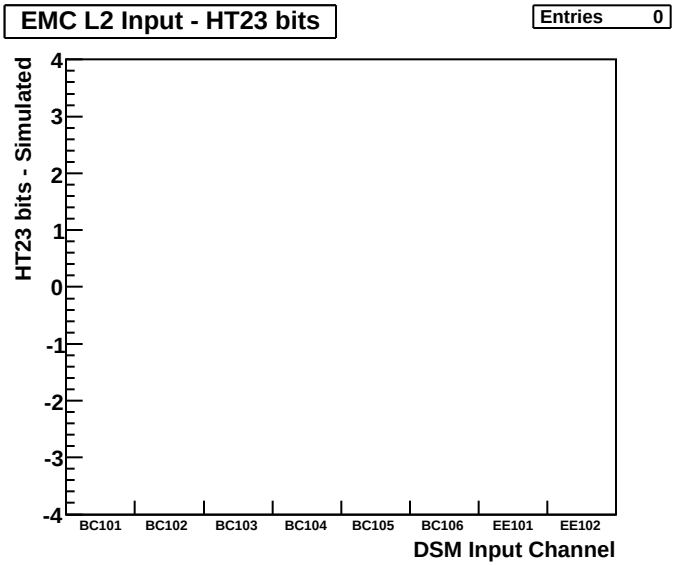

EMC L2 Input - Partial JP Sum
Entries 1660800
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

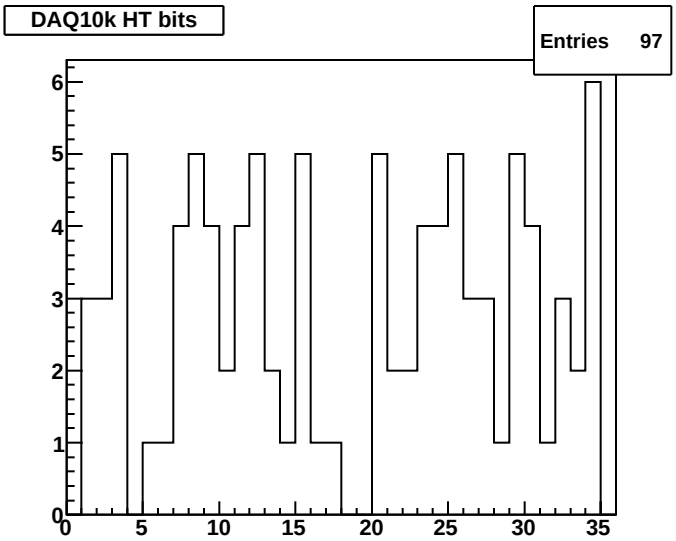

EMC L2 Input - Partial JP Sum
Entries 2
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

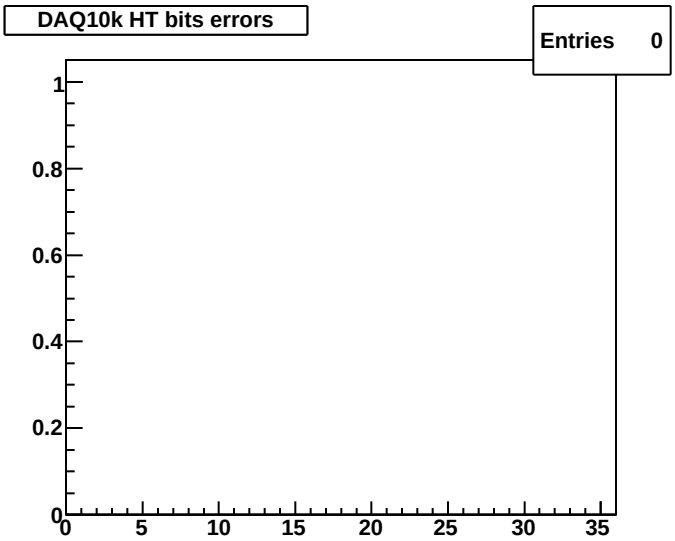

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1660800
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

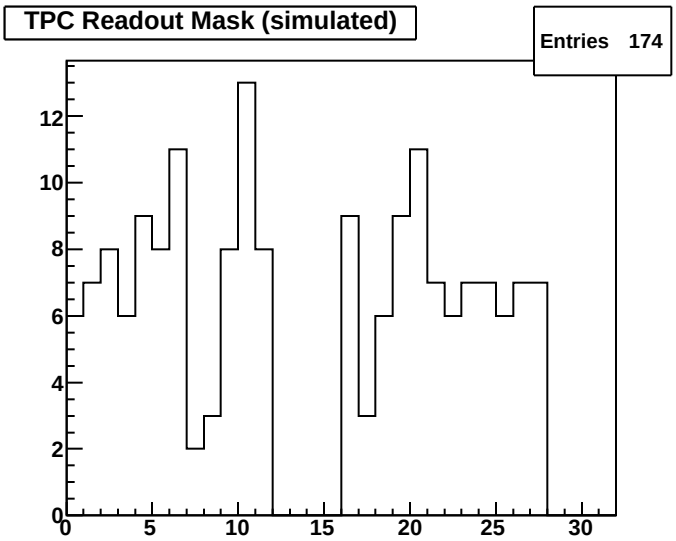

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

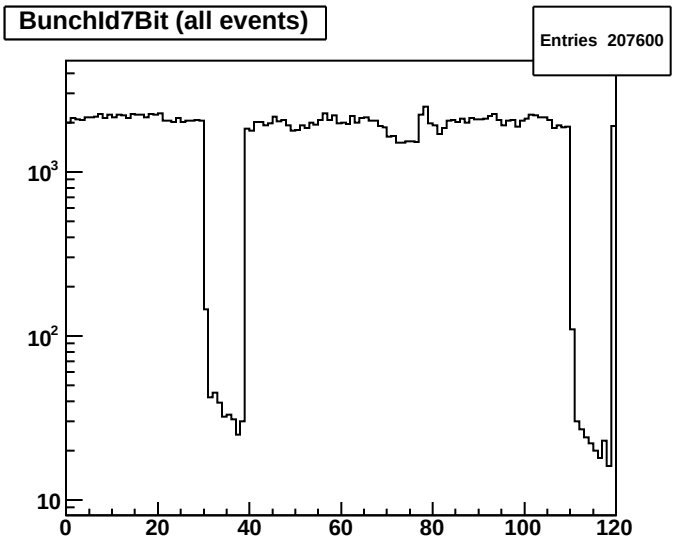

EMC L2 Input - HT23 bits
Entries 1660800
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

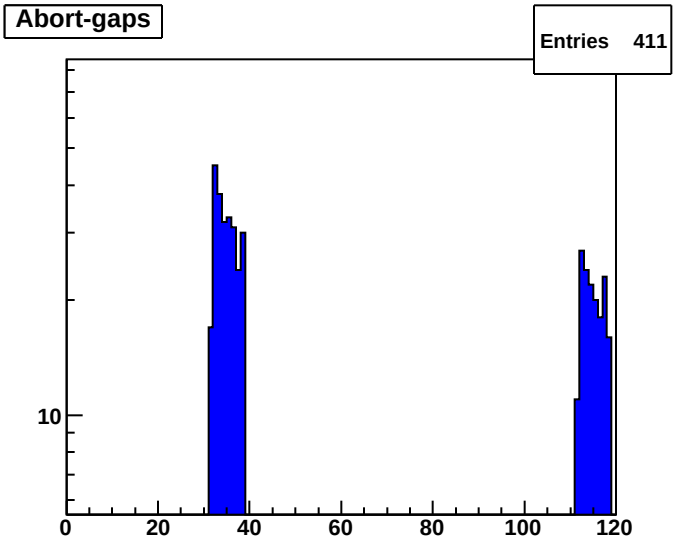

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

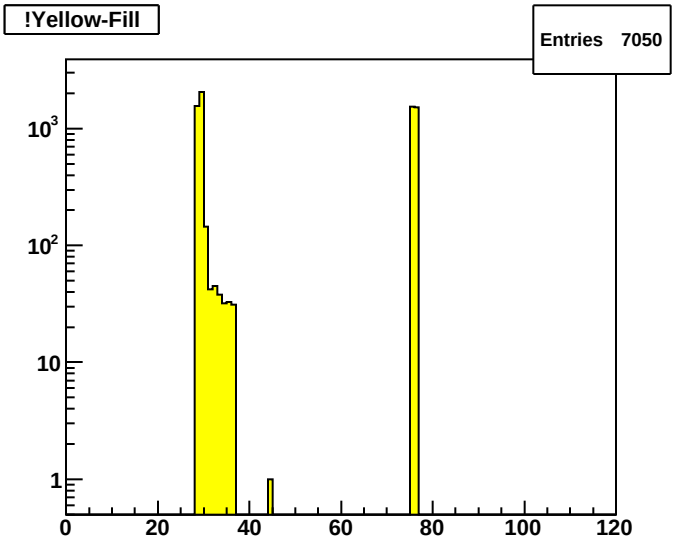

DAQ10k HT bits
Entries 97

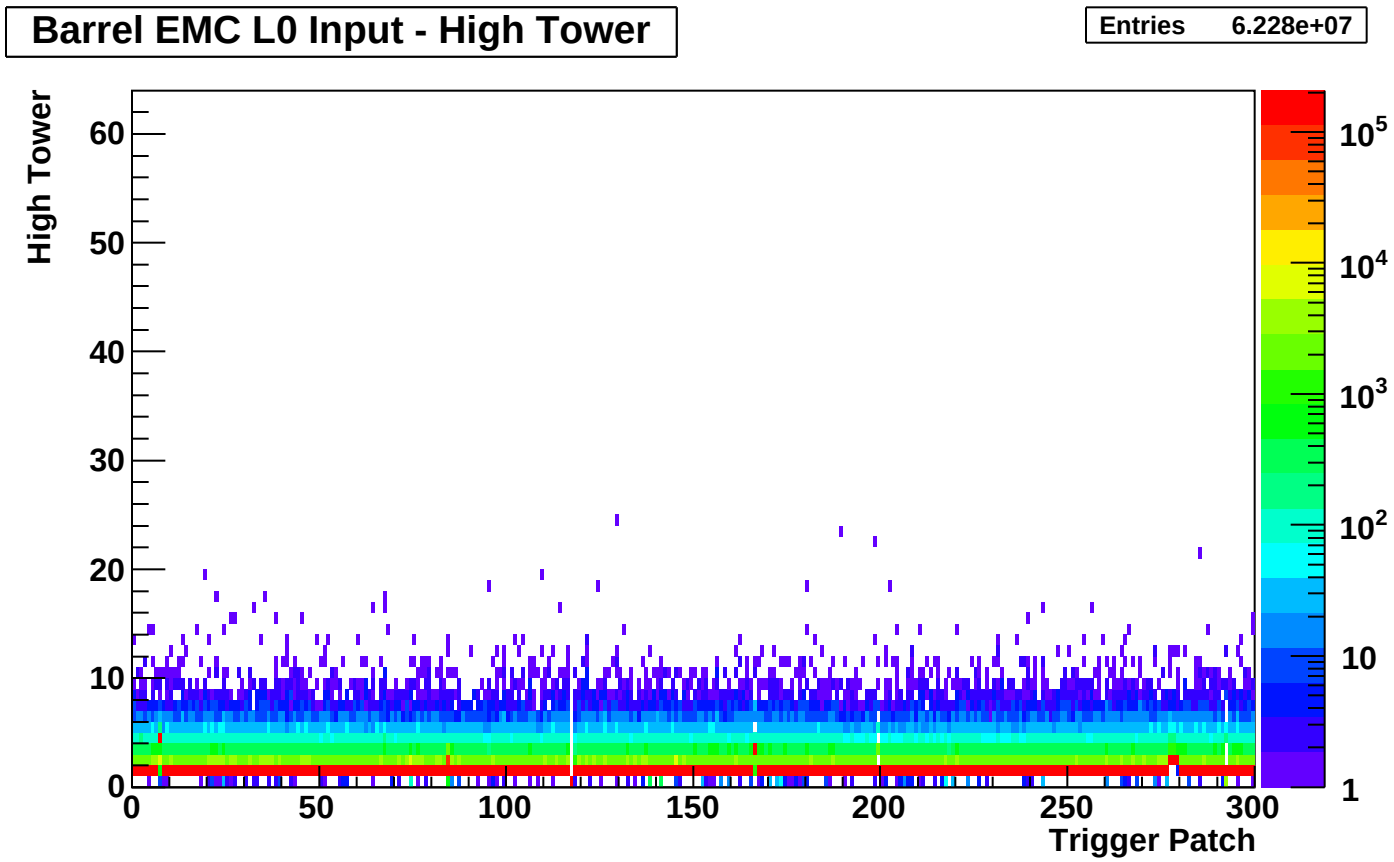

DAQ10k HT bits errors
Entries 0

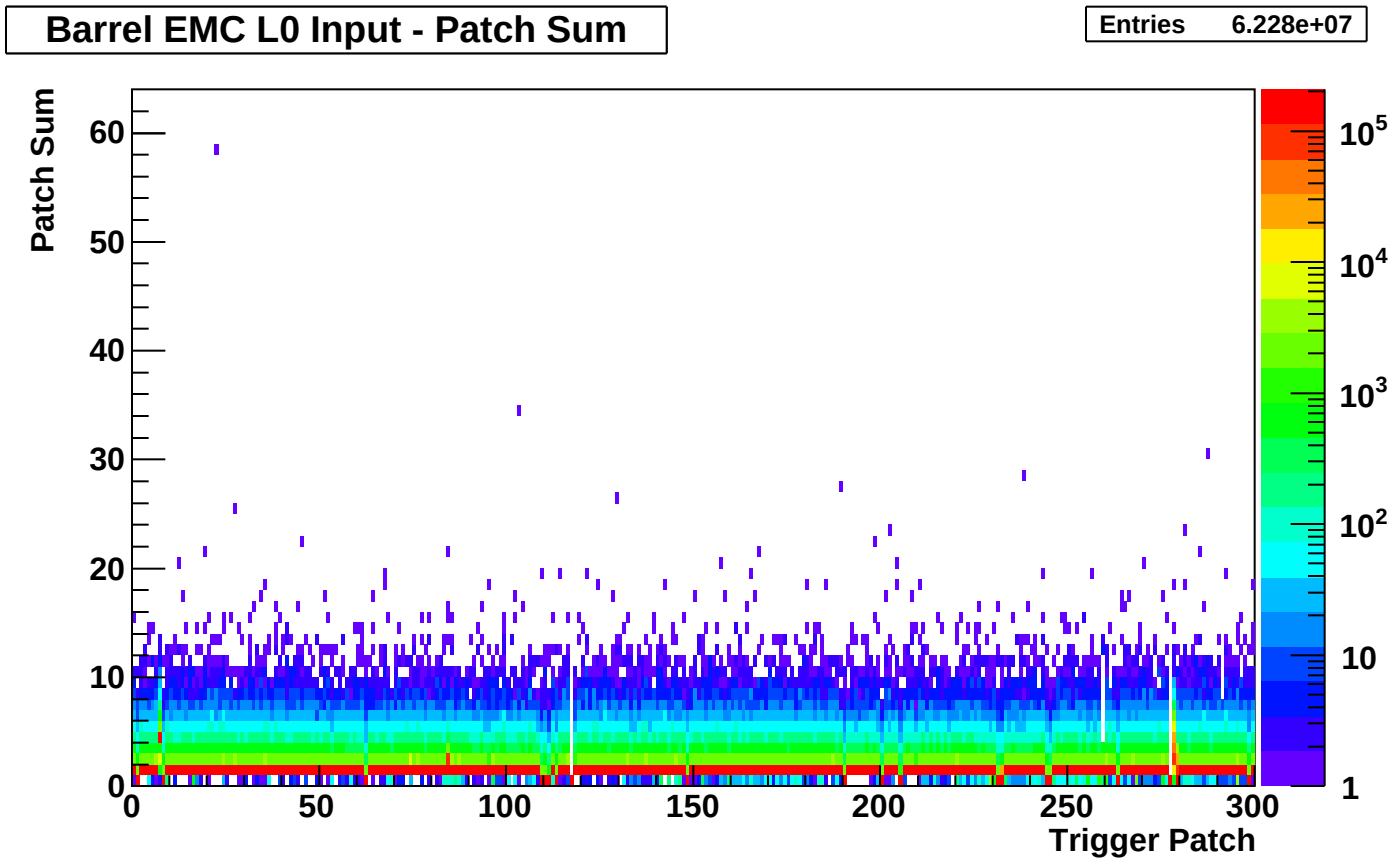

TPC Readout Mask (simulated)
Entries 174

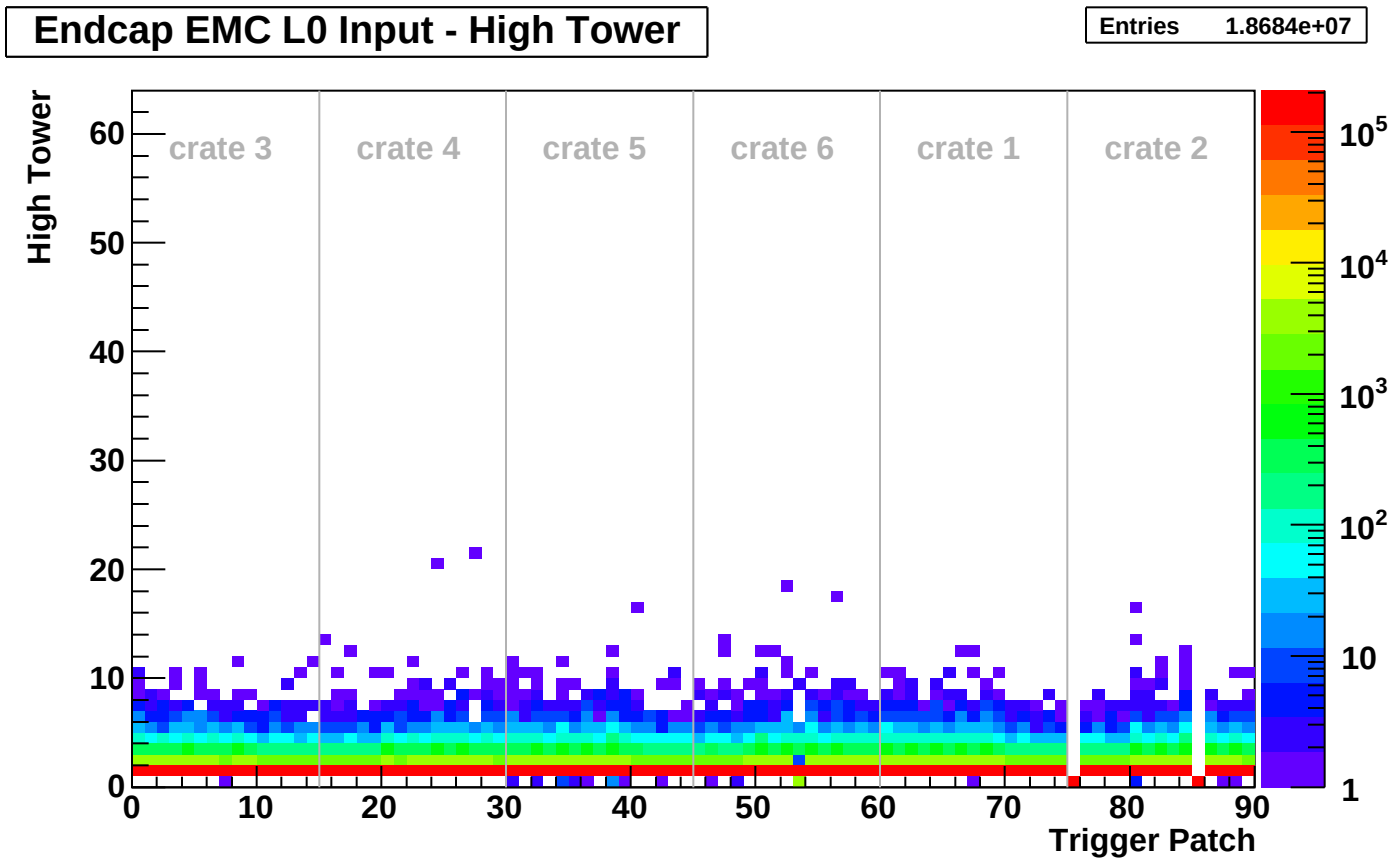

BunchId7Bit (all events)
Entries 207600

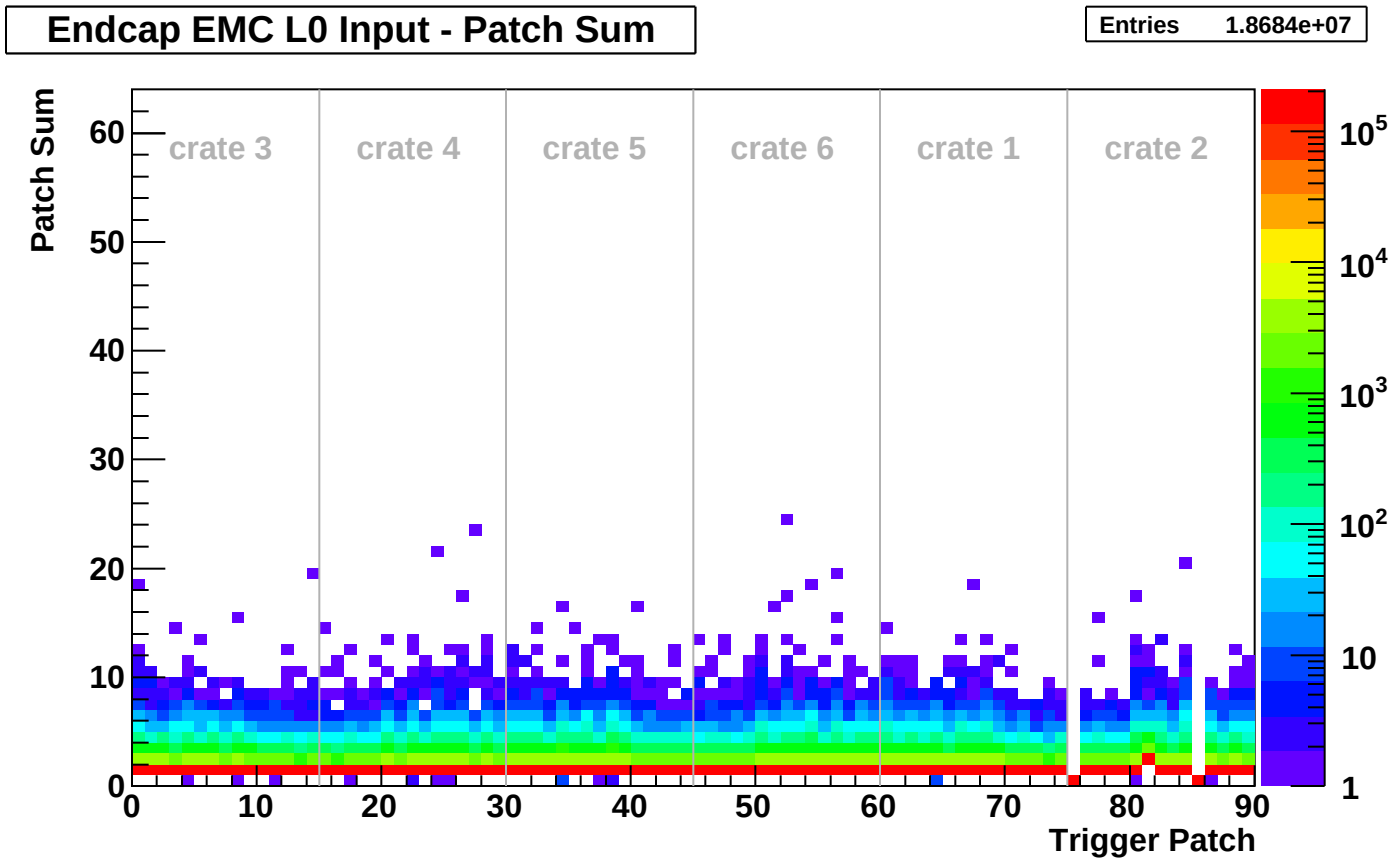

Abort-gaps
Entries 411

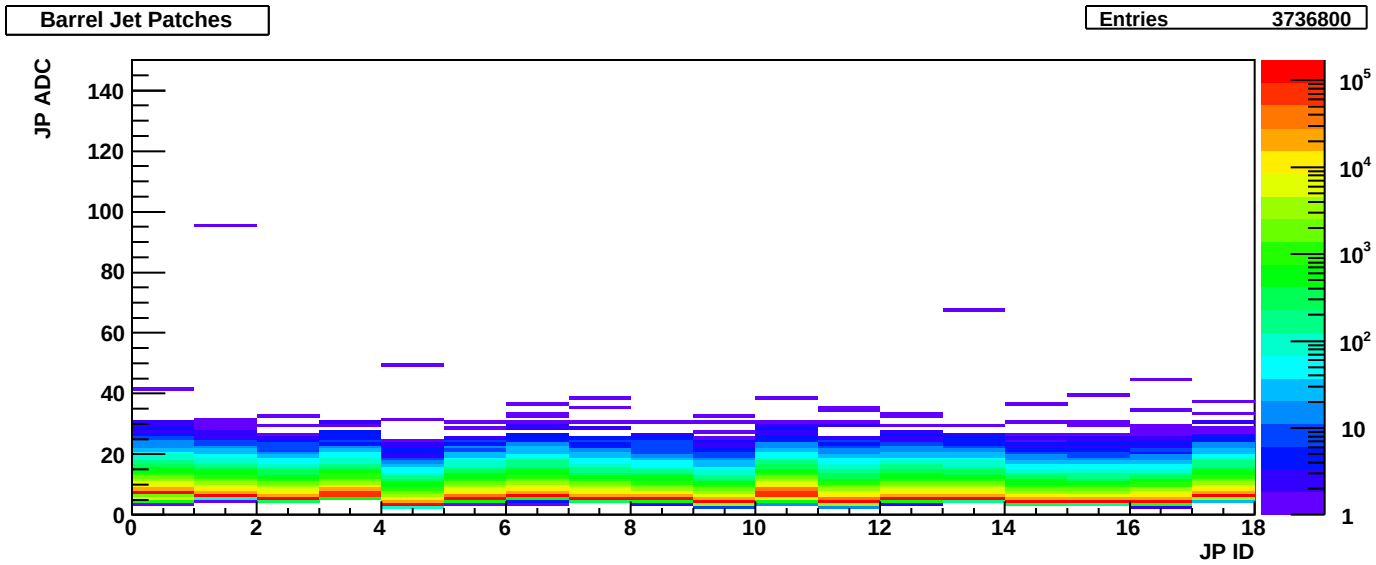

!Yellow-Fill
Entries 7050

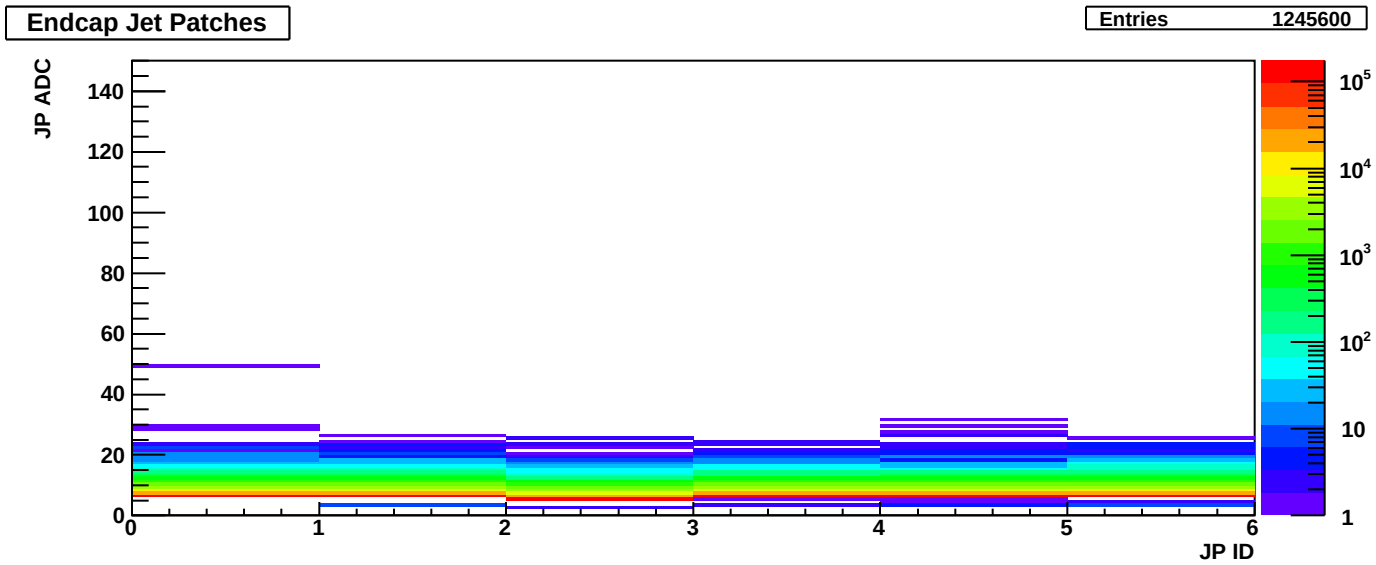

Barrel EMC L0 Input - High Tower
Entries 6.228e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

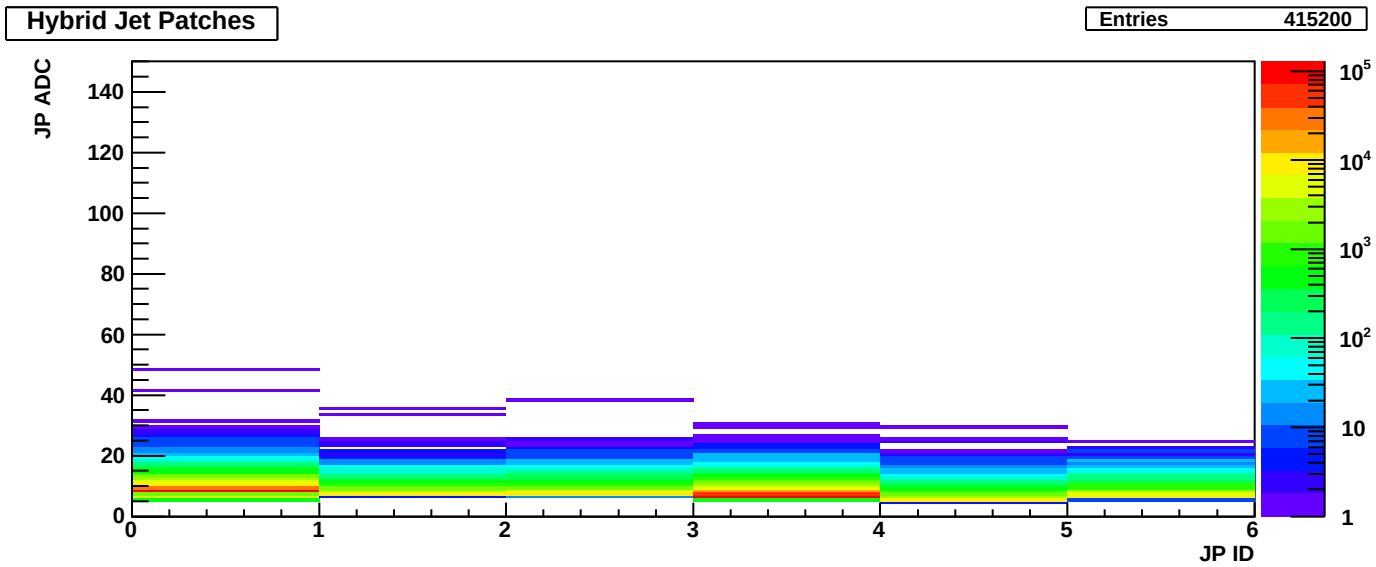

Barrel EMC L0 Input - Patch Sum
Entries 6.228e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

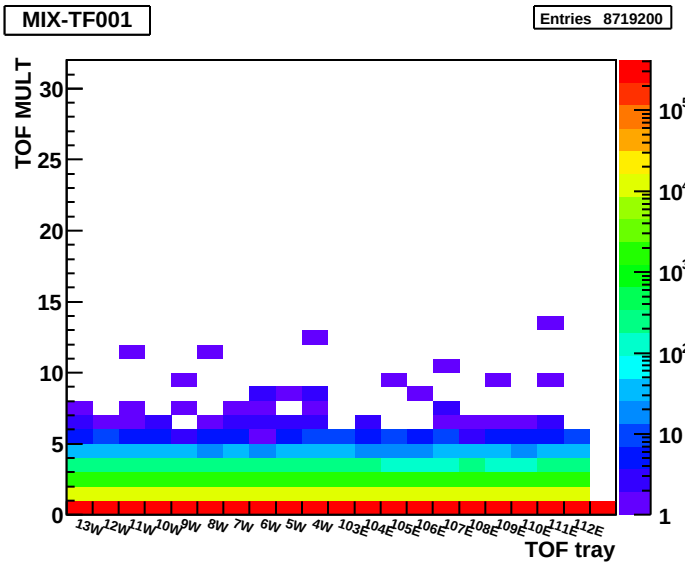

Endcap EMC L0 Input - High Tower
Entries 1.8684e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

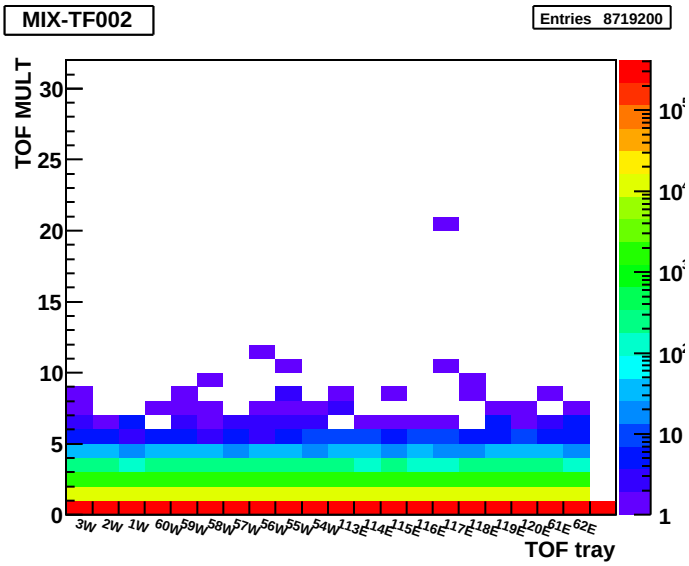

Endcap EMC L0 Input - Patch Sum
Entries 1.8684e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

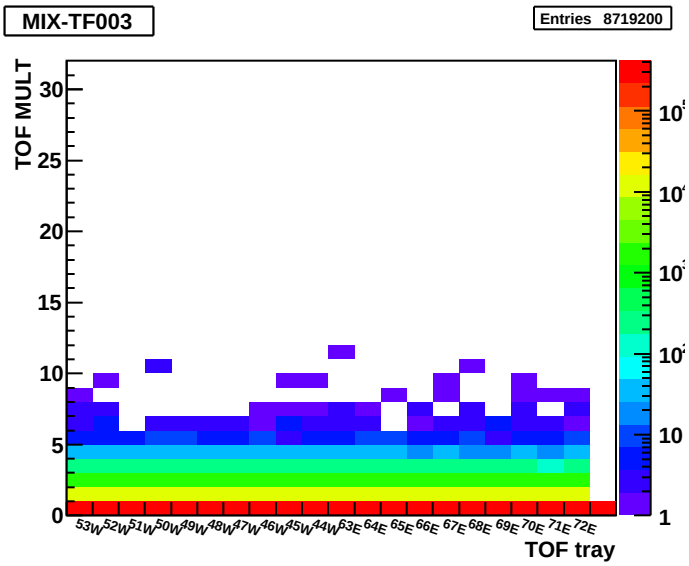

Barrel Jet Patches
Entries 3736800
JP ADC
JP ID

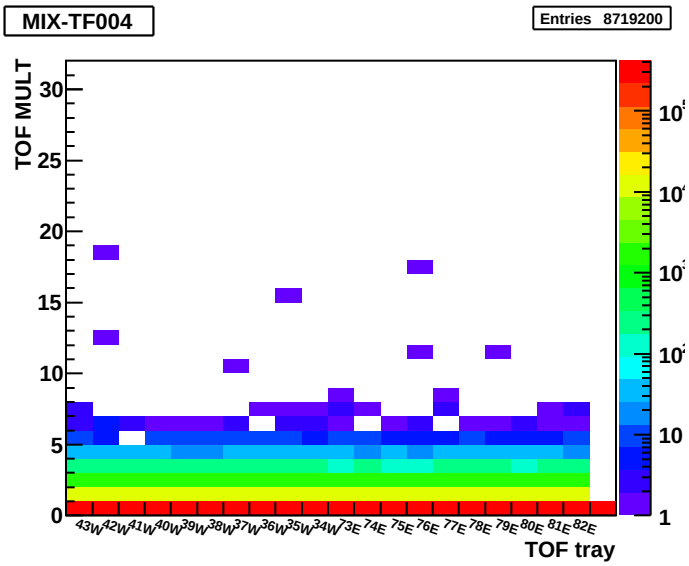

Endcap Jet Patches
Entries 1245600
JP ADC
JP ID

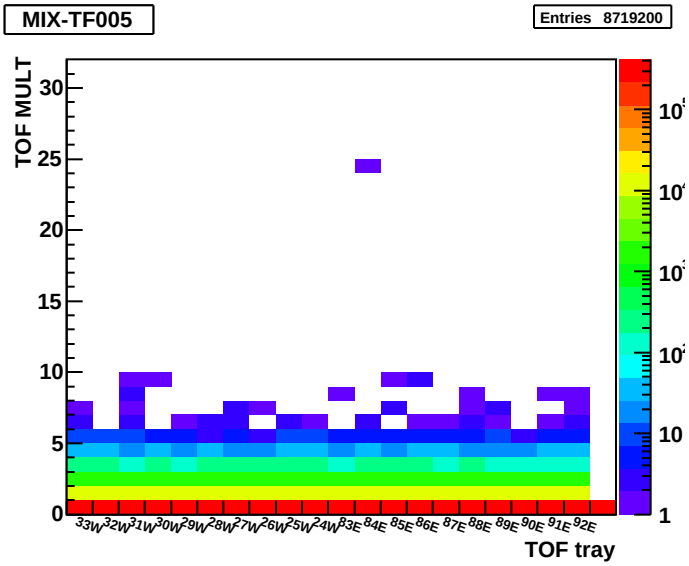

Hybrid Jet Patches
Entries 415200
JP ADC
JP ID

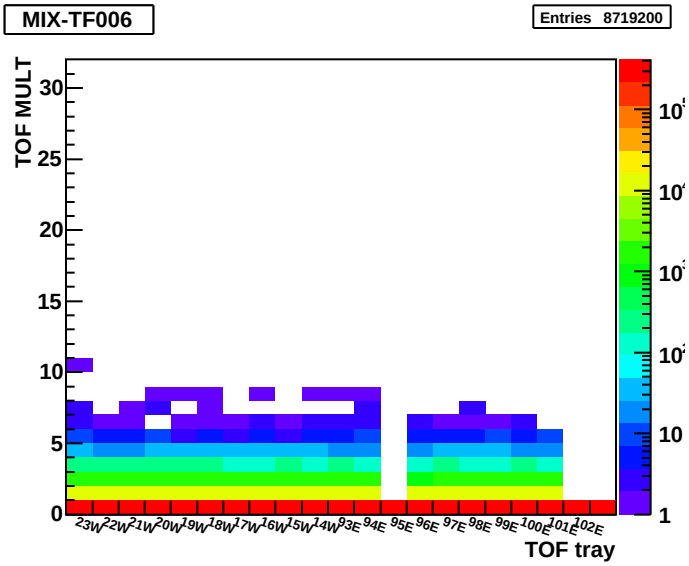

MIX-TF001
Entries 8719200
TOF MULT
TOF tray

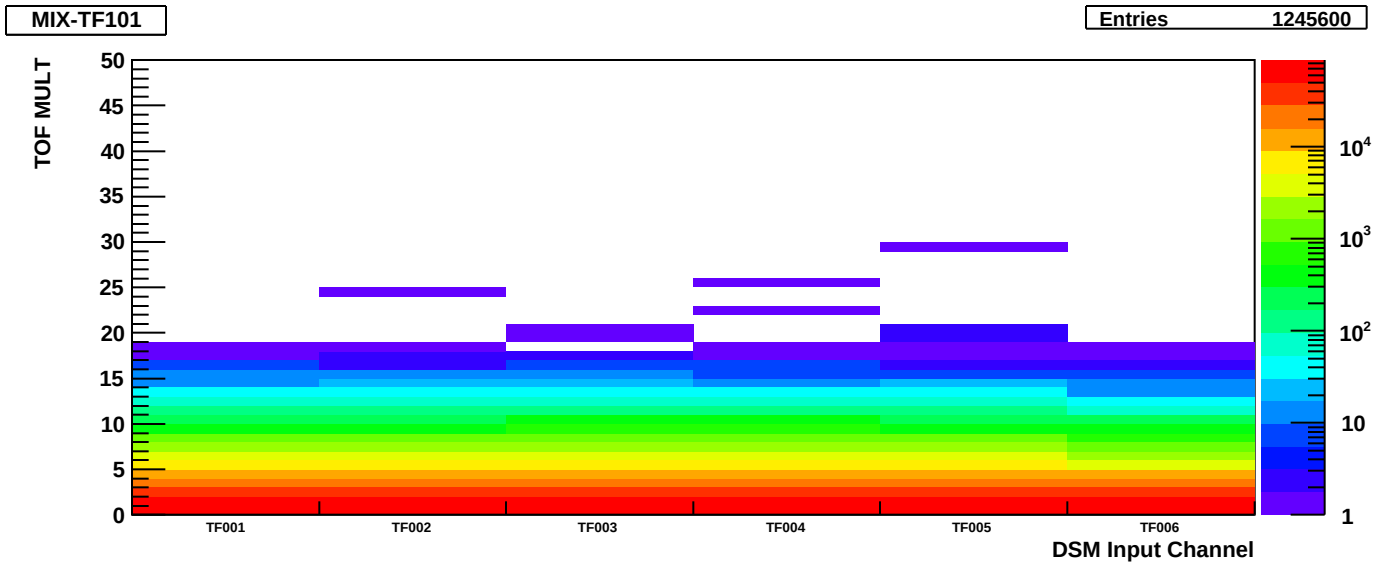

MIX-TF002
Entries 8719200
TOF MULT
TOF tray

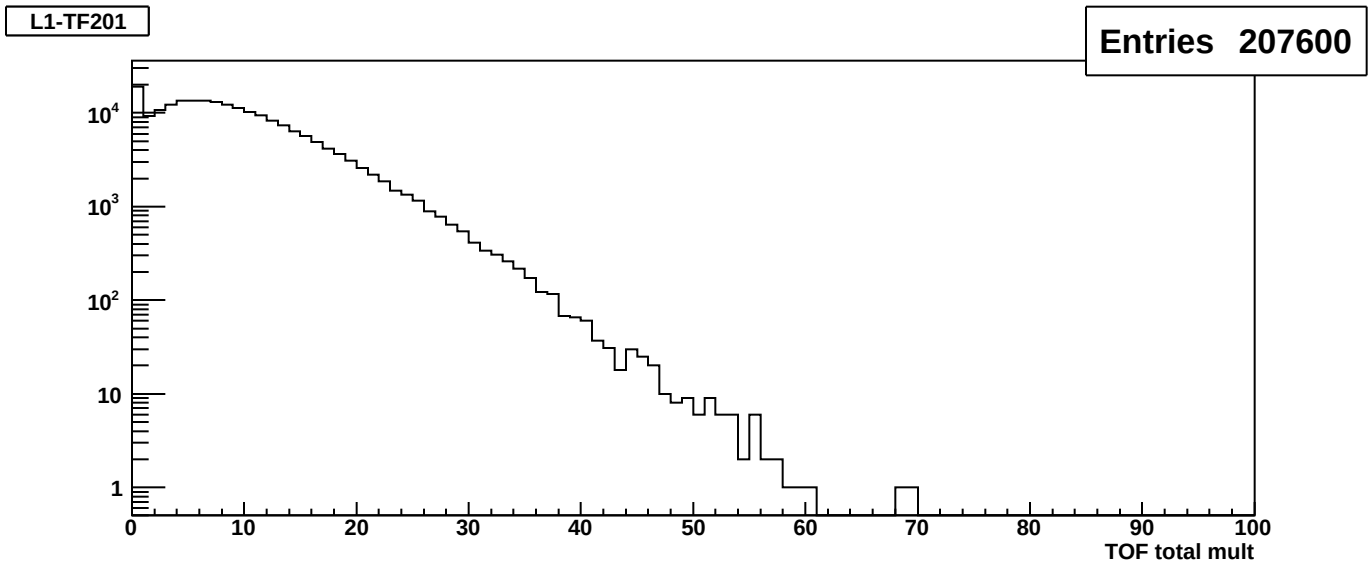

MIX-TF003
Entries 8719200
TOF MULT
TOF tray

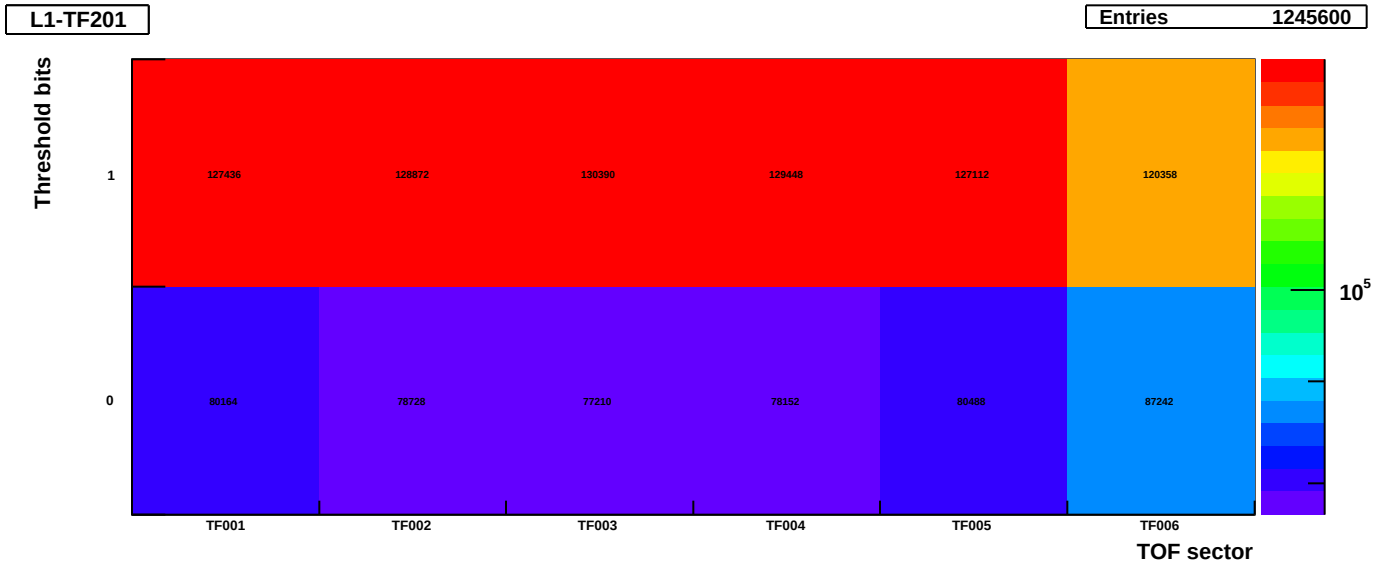

MIX-TF004
Entries 8719200
TOF MULT
TOF tray

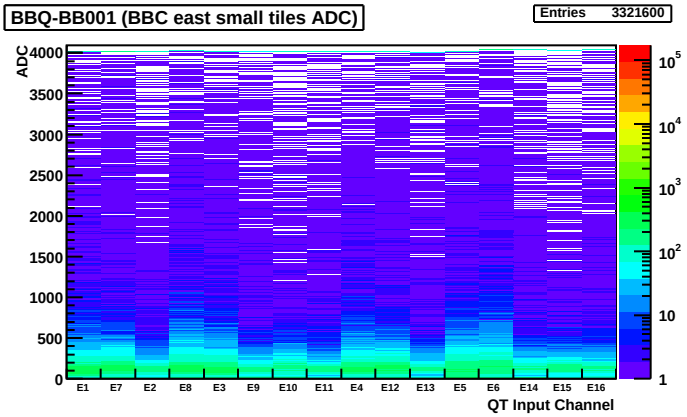

MIX-TF005
Entries 8719200
TOF MULT
TOF tray

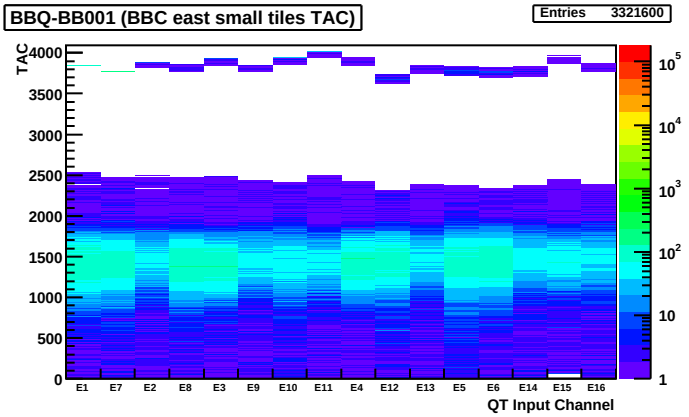

MIX-TF006
Entries 8719200
TOF MULT
TOF tray

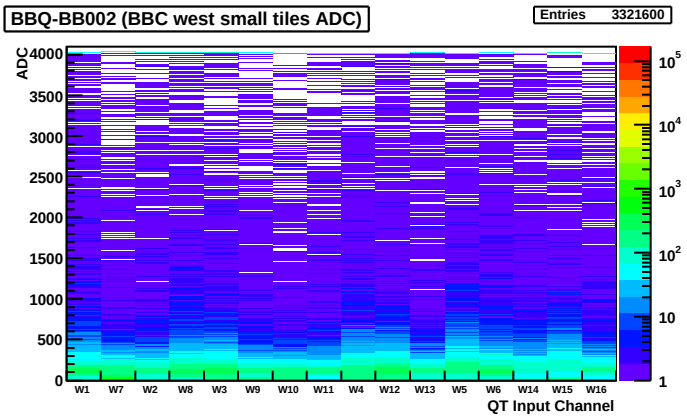

MIX-TF101
Entries 1245600
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

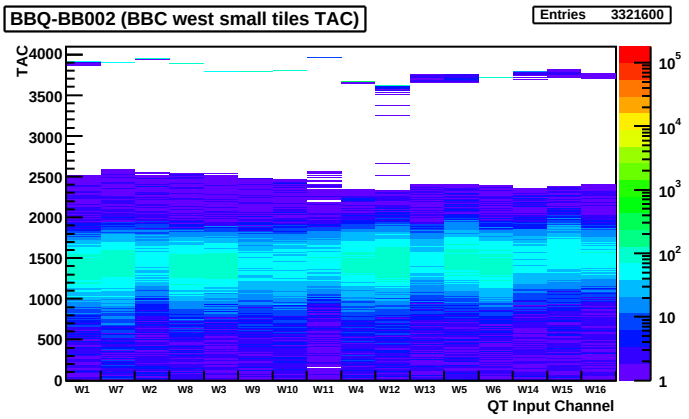

L1-TF201
Entries 207600
TOF total mult

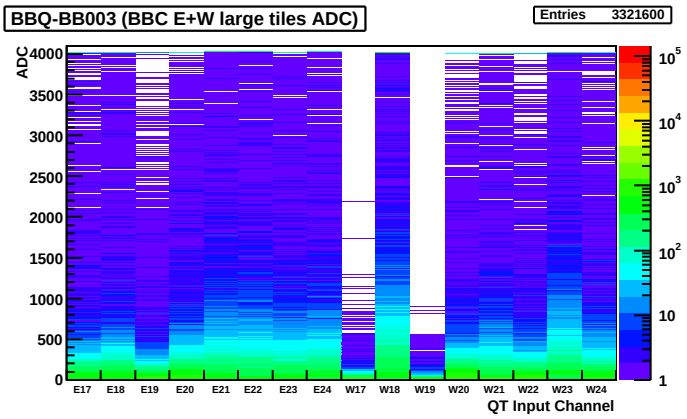

L1-TF201
Entries 1245600
Threshold bits
127436
128872
130390
129448
127112
120358
80164
78728
77210
78152
80488
87242
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

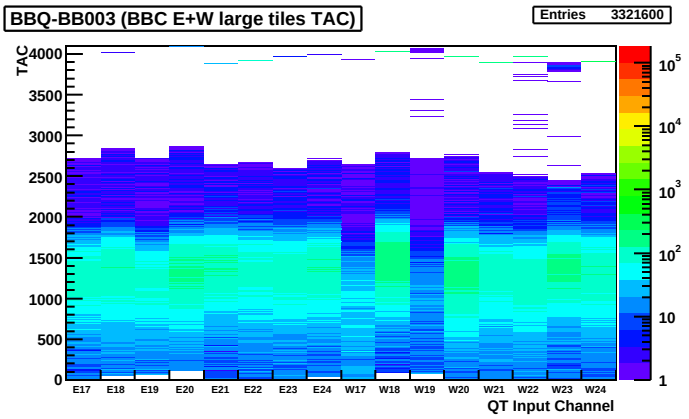

BBQ-BB001 (BBC east small tiles ADC)
Entries 3321600
ADC
QT Input Channel

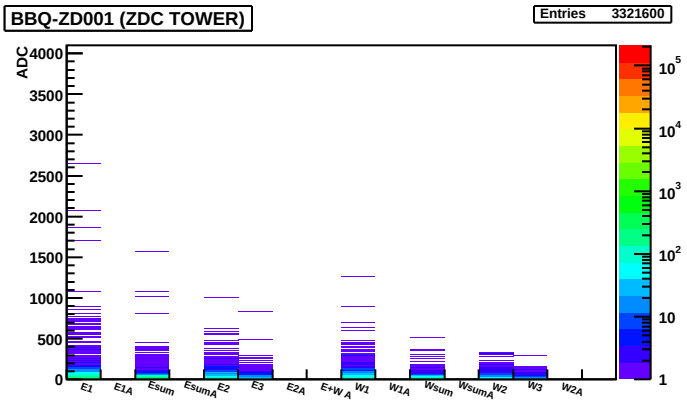

BBQ-BB001 (BBC east small tiles TAC)
Entries 3321600
TAC
QT Input Channel

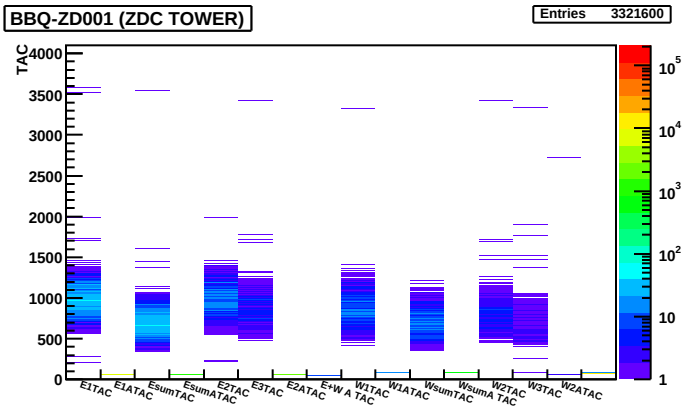

BBQ-BB002 (BBC west small tiles ADC)
Entries 3321600
ADC
QT Input Channel

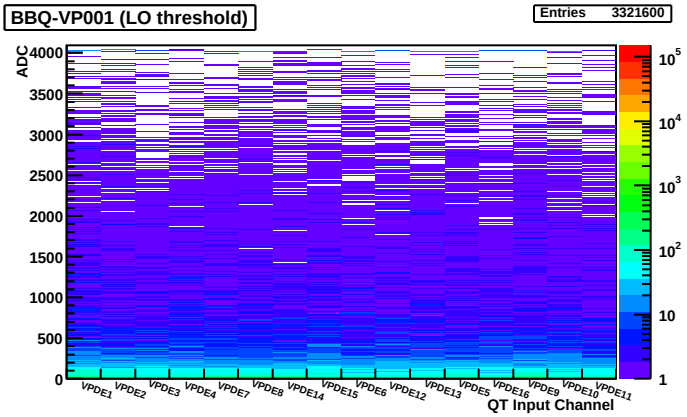

BBQ-BB002 (BBC west small tiles TAC)
Entries 3321600
TAC
QT Input Channel

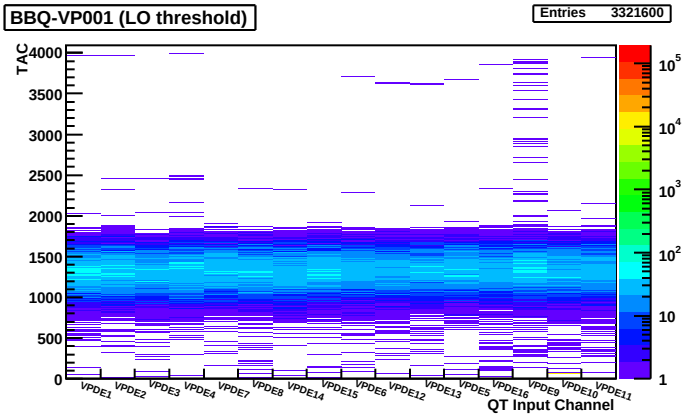

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3321600
ADC
QT Input Channel

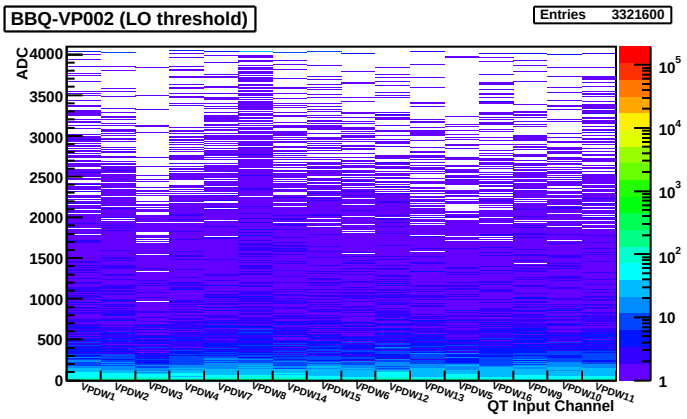

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3321600
TAC
QT Input Channel

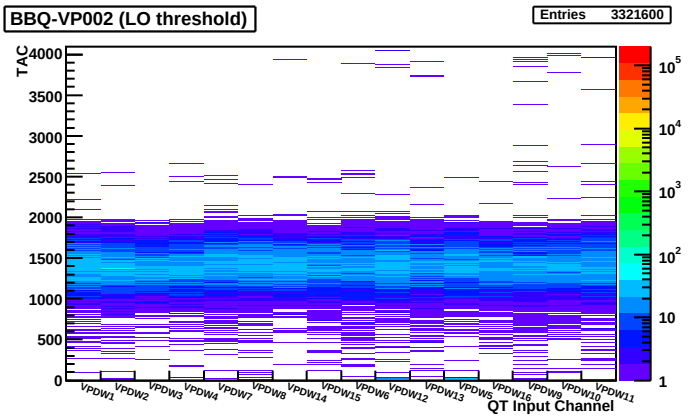

BBQ-ZD001 (ZDC TOWER)
Entries 3321600
ADC

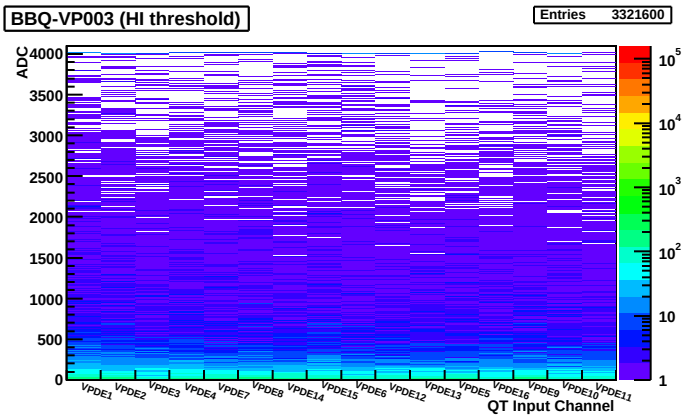

BBQ-ZD001 (ZDC TOWER)
Entries 3321600
TAC

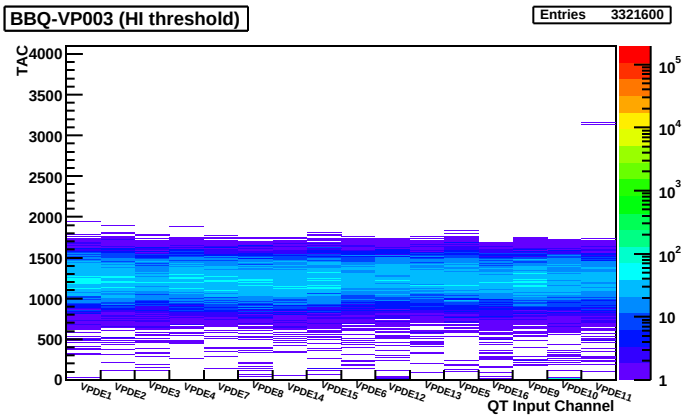

BBQ-VP001 (LO threshold)
Entries 3321600
ADC
QT Input Channel

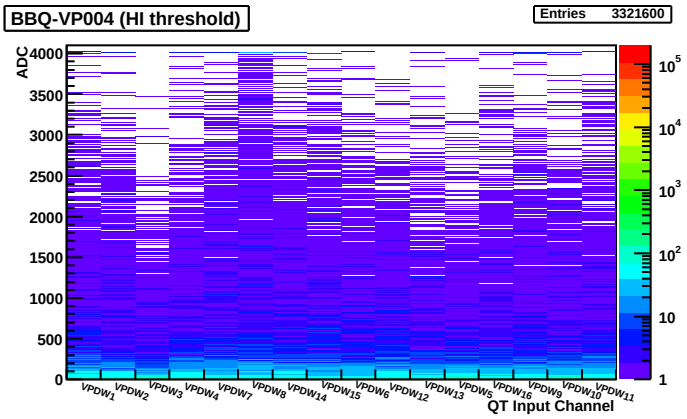

BBQ-VP001 (LO threshold)
Entries 3321600
TAC
QT Input Channel

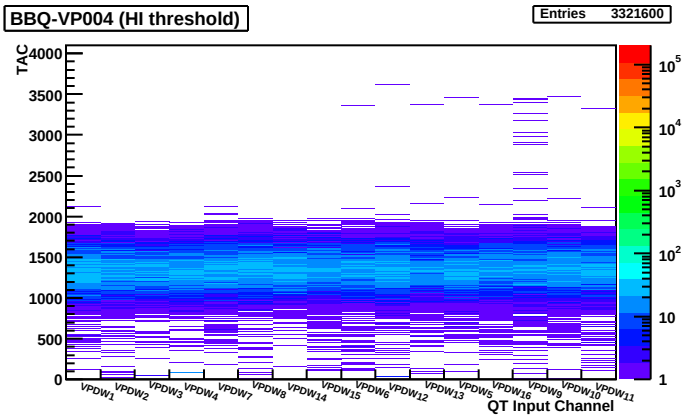

BBQ-VP002 (LO threshold)
Entries 3321600
ADC
QT Input Channel

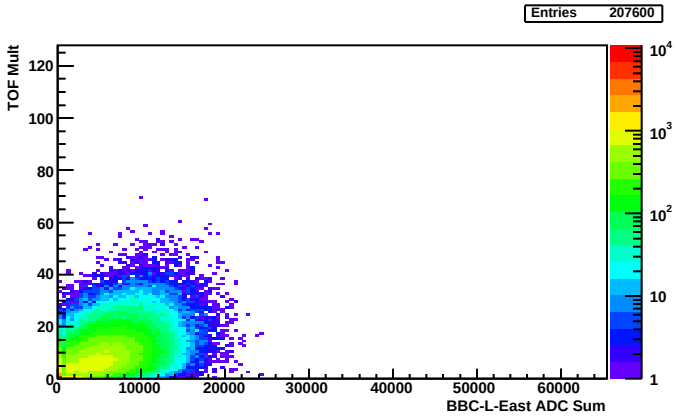

BBQ-VP002 (LO threshold)
Entries 3321600
TAC
QT Input Channel

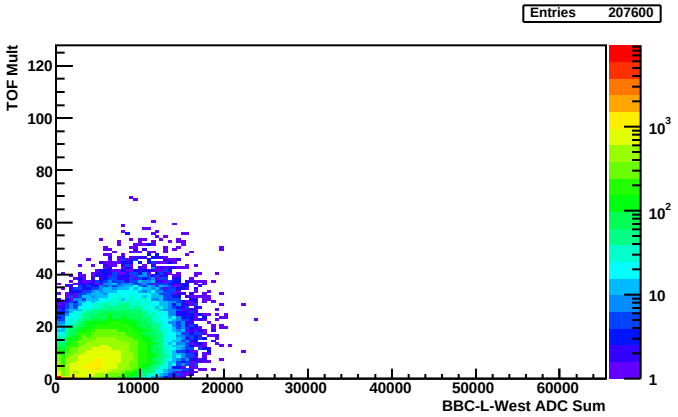

BBQ-VP003 (HI threshold)
Entries 3321600
ADC
QT Input Channel

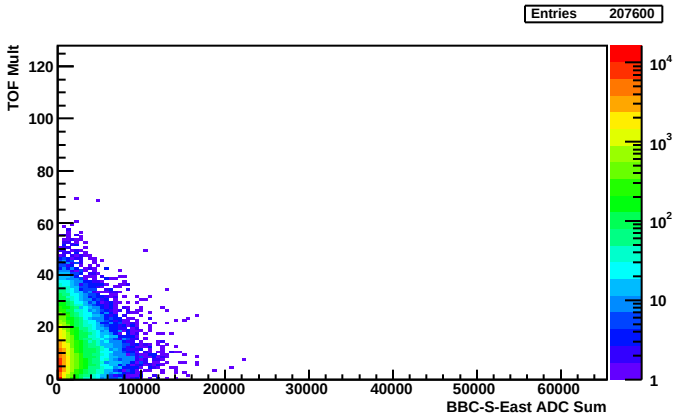

BBQ-VP003 (HI threshold)
Entries 3321600
TAC
QT Input Channel

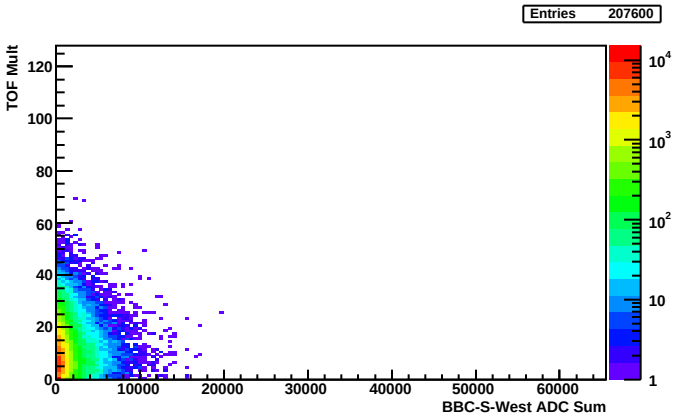

BBQ-VP004 (HI threshold)
Entries 3321600
ADC
QT Input Channel

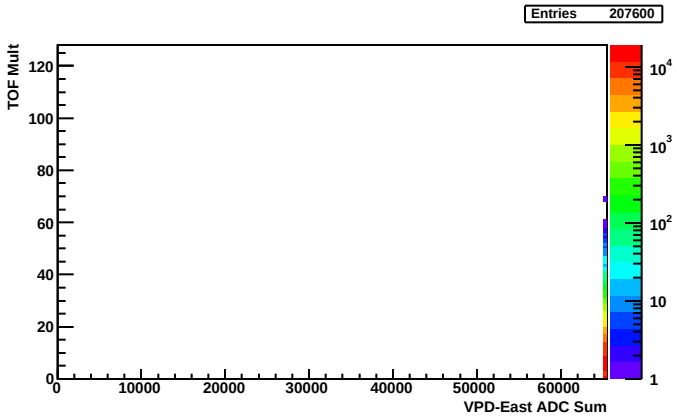

BBQ-VP004 (HI threshold)
Entries 3321600
TAC
QT Input Channel

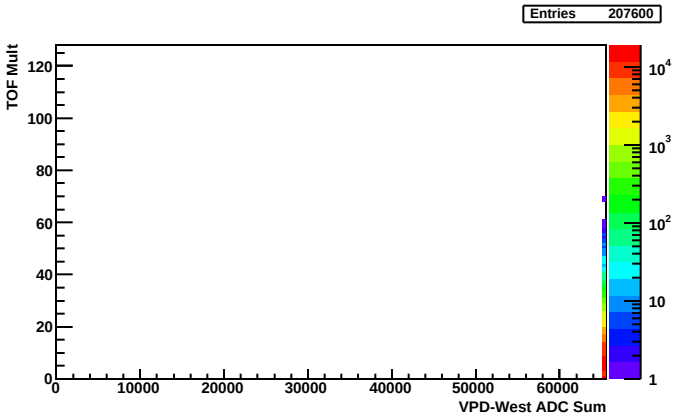

Entries 207600
TOF Mult
BBC-L-East ADC Sum

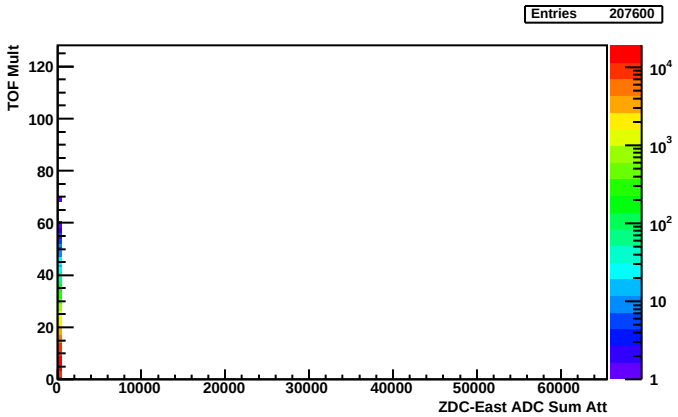

Entries 207600
TOF Mult
BBC-L-West ADC Sum

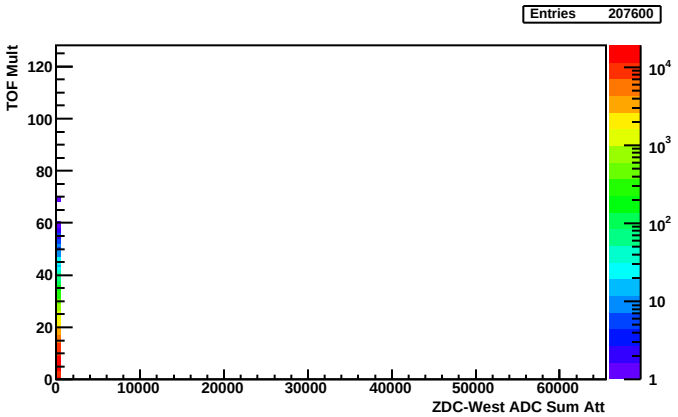

Entries 207600
TOF Mult
BBC-S-East ADC Sum

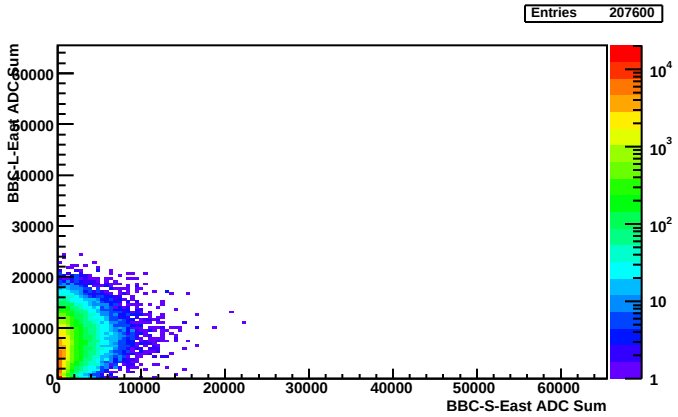

Entries 207600
TOF Mult
BBC-S-West ADC Sum

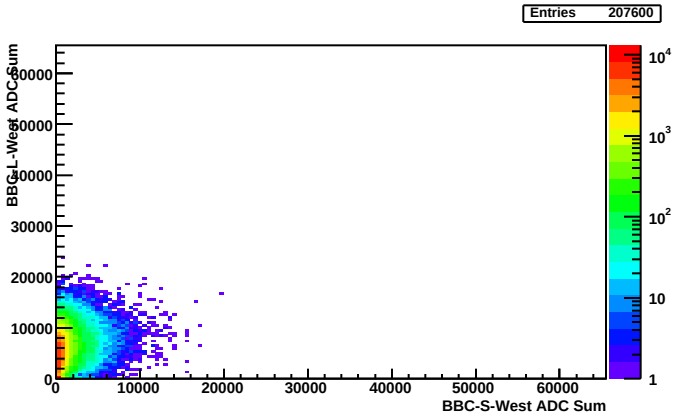

Entries 207600
TOF Mult
VPD-East ADC Sum

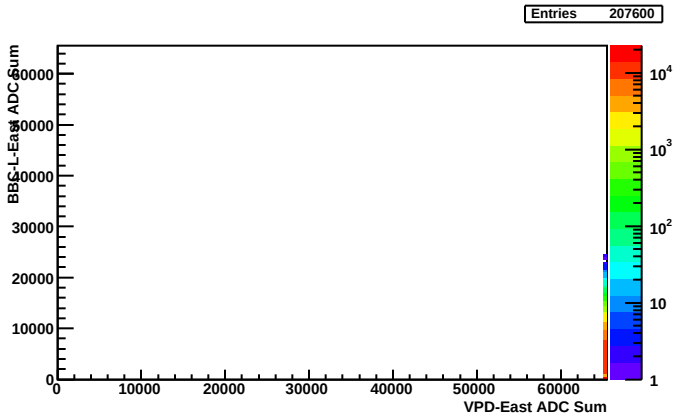

Entries 207600
TOF Mult
VPD-West ADC Sum

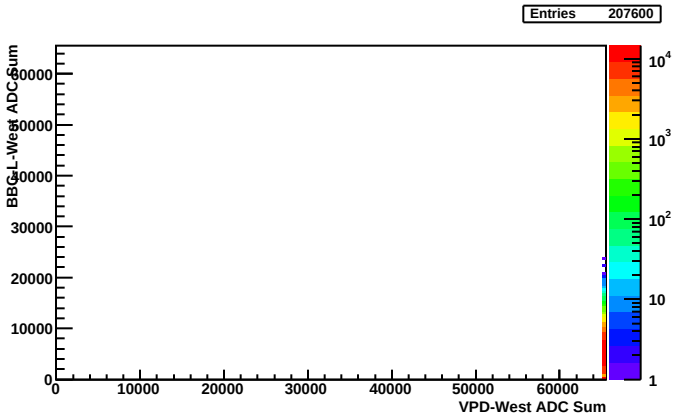

Entries 207600
TOF Mult
ZDC-East ADC Sum Att

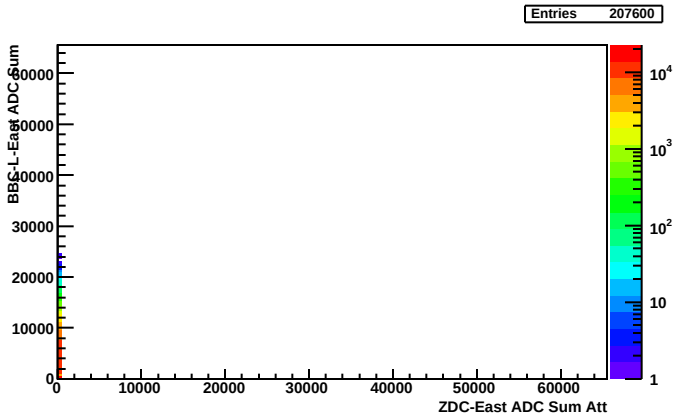

Entries 207600
TOF Mult
ZDC-West ADC Sum Att

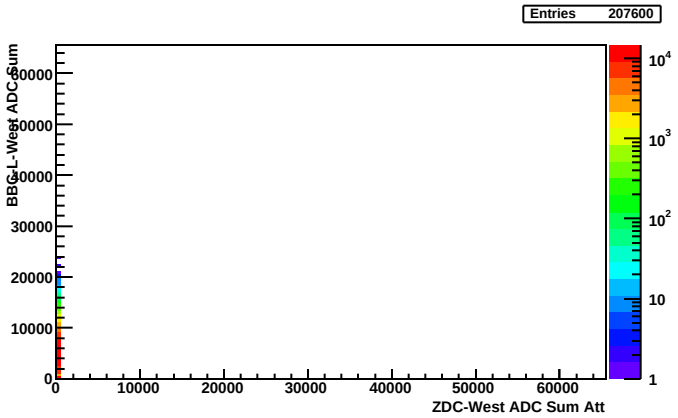

Entries 207600
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 207600
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 207600
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 207600
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 207600
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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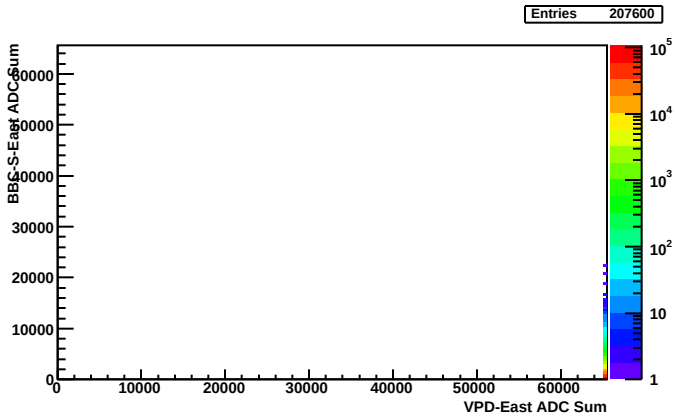
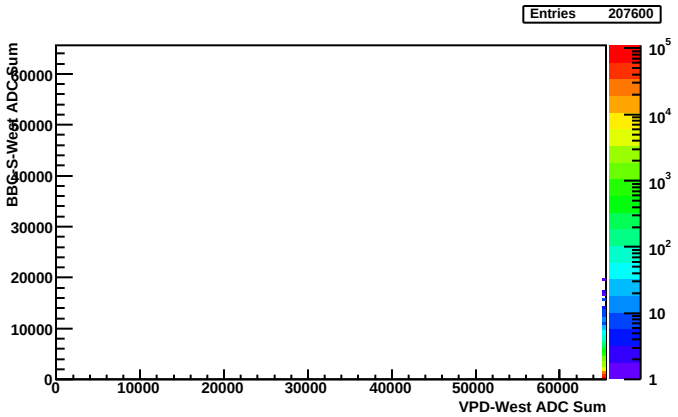
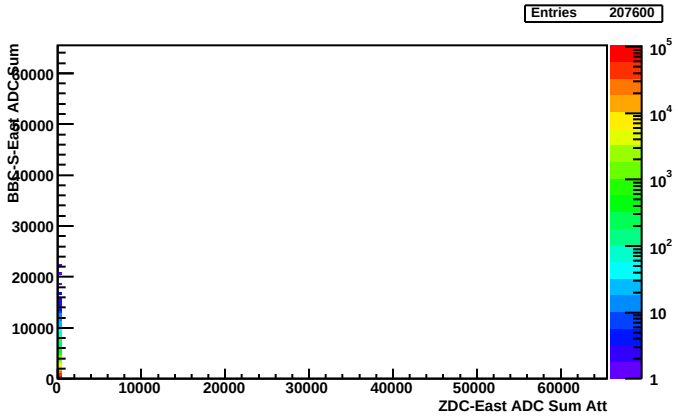
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 207600
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 207600
BBC-S-West ADC Sum
VPD-West ADC Sum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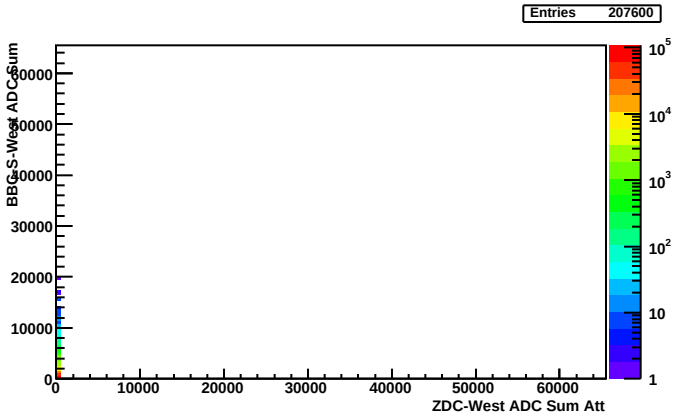
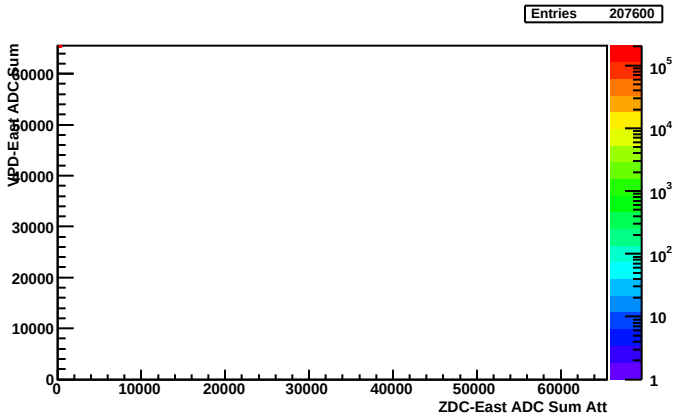
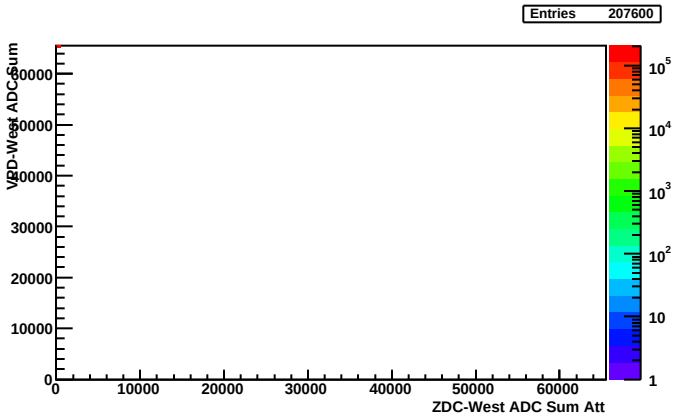
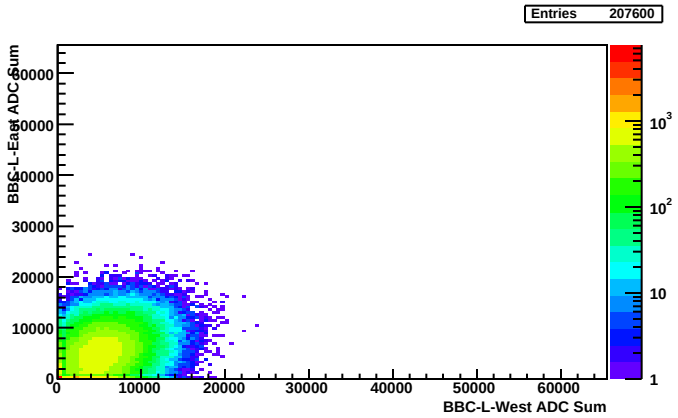
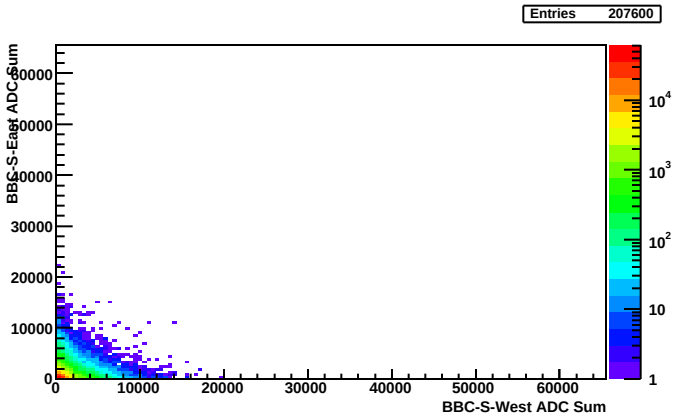
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

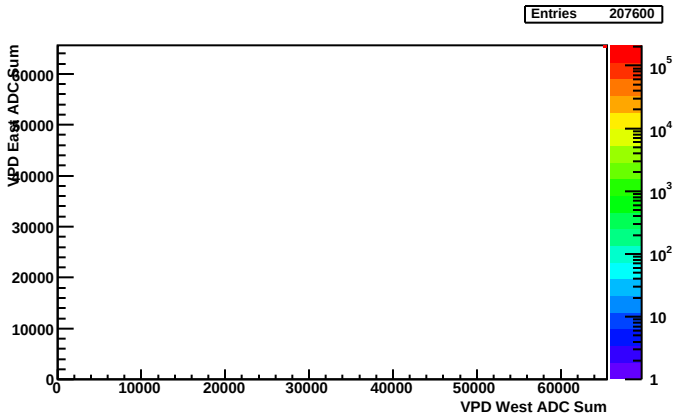

Entries 207600
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

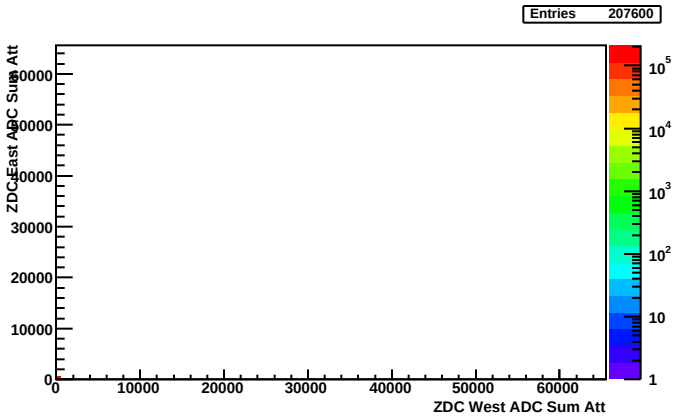

Entries 207600
VPD-East ADC Sum
ZDC-East ADC Sum Att

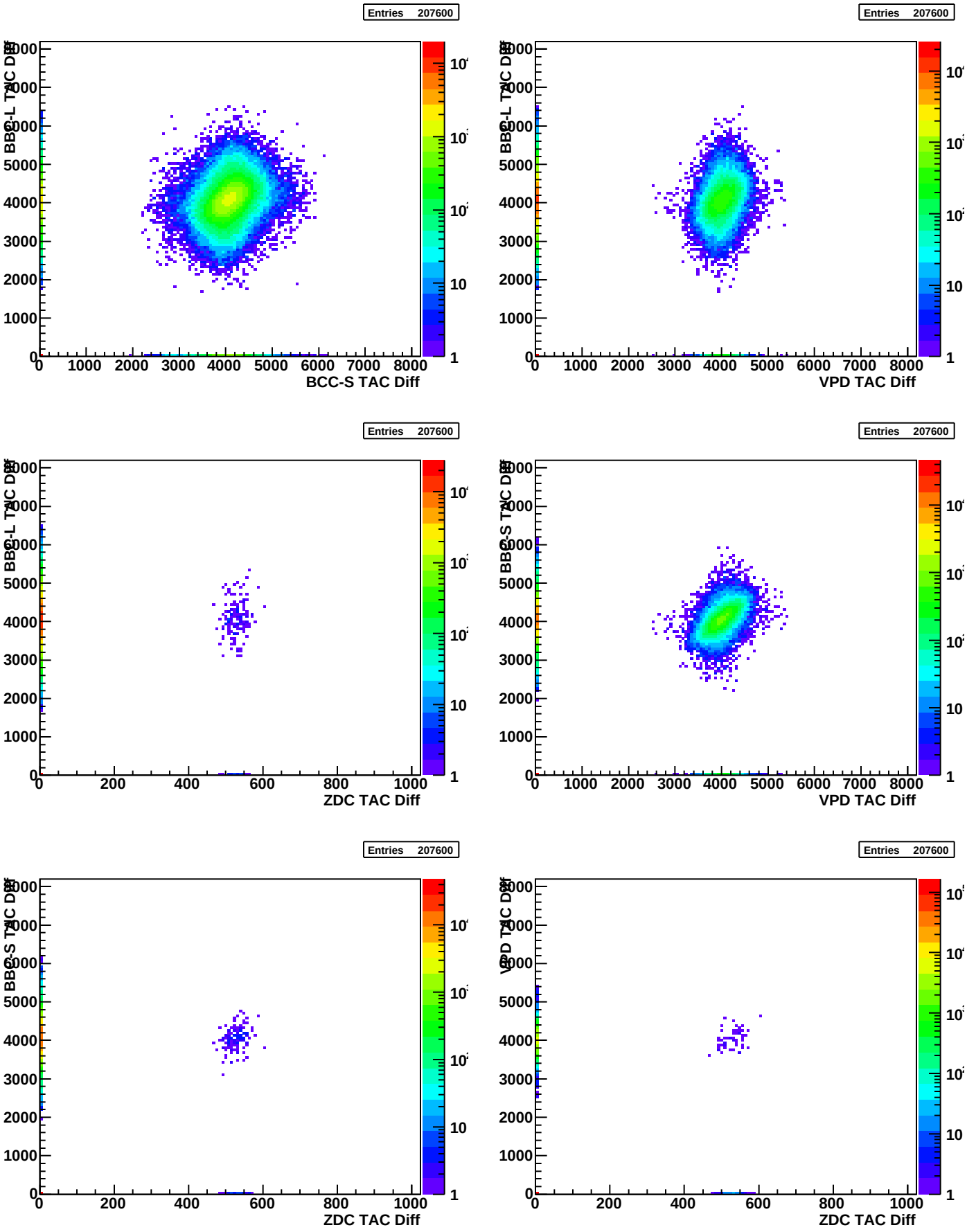

Entries 207600
VPD-West ADC Sum
ZDC-West ADC Sum Att

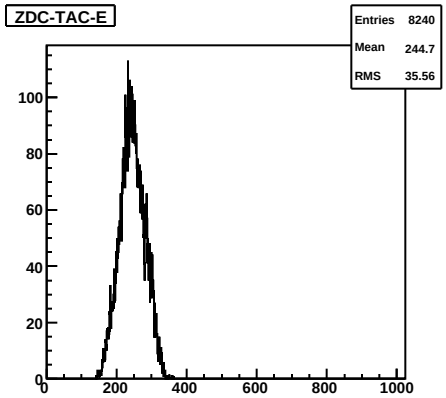

Entries 207600
BBC-L-East ADC Sum
BBC-L-West ADC Sum

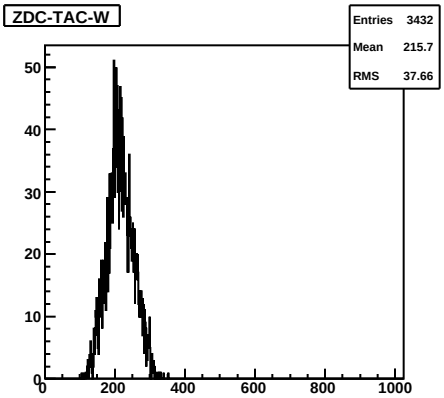

Entries 207600
BBC-S-East ADC Sum
BBC-S-West ADC Sum

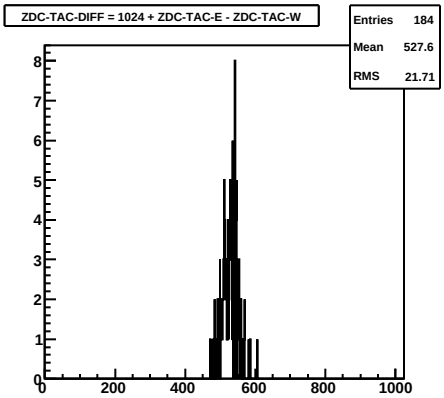

Entries 207600
VPD East ADC Sum
VPD West ADC Sum

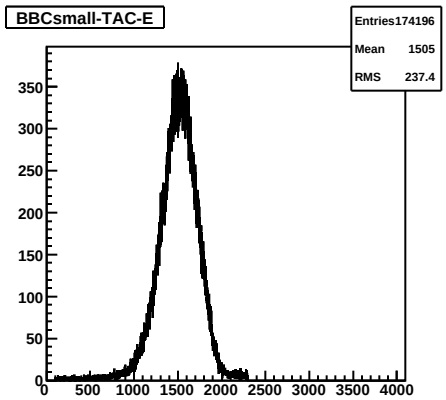

Entries 207600
ZDC East ADC Sum Att
ZDC West ADC Sum Att

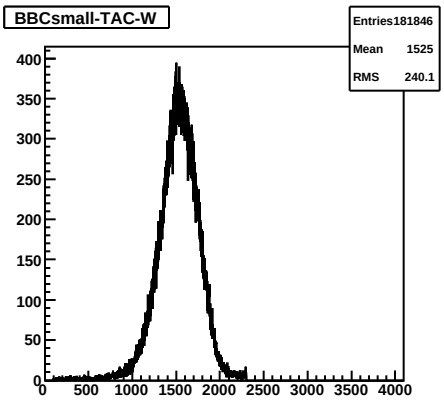

Entries 207600
BBC-L TAC Diff
BCC-S TAC Diff
Entries 207600
BBC-L TAC Diff
VPD TAC Diff
Entries 207600
BBC-L TAC Diff
ZDC TAC Diff
Entries 207600
BBC-S TAC Diff
VPD TAC Diff
Entries 207600
BBC-S TAC Diff
ZDC TAC Diff
Entries 207600
VPD TAC Diff
ZDC TAC Diff

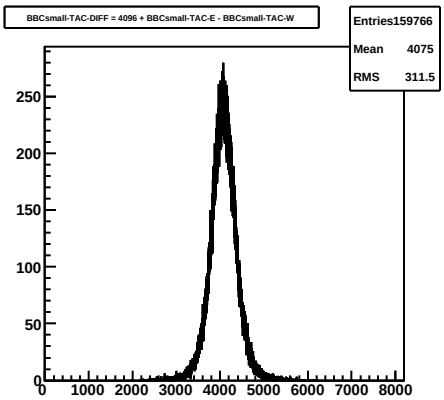

ZDC-TAC-E
Entries 8240
Mean 244.7
RMS 35.56

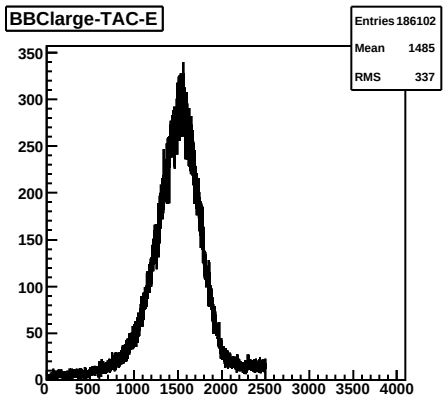

ZDC-TAC-W
Entries 3432
Mean 215.7
RMS 37.66

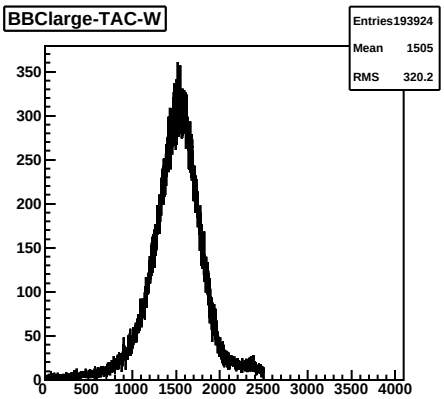

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 184
Mean 527.6
RMS 21.71

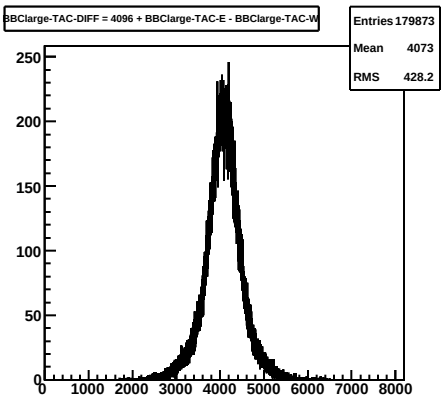

BBCsmall-TAC-E
Entries174196
Mean 1505
RMS 237.4

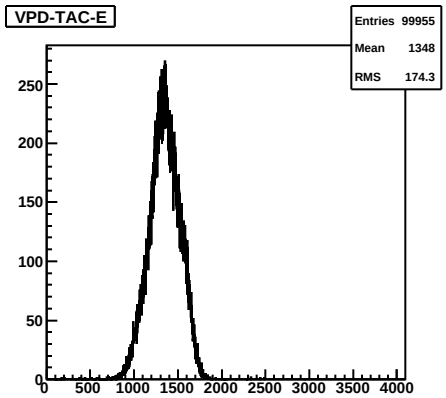

BBCsmall-TAC-W
Entries181846
Mean 1525
RMS 240.1

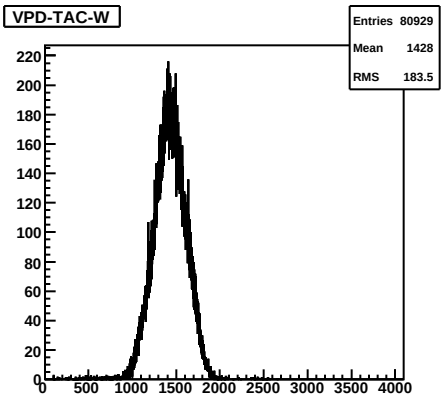

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries159766
Mean 4075
RMS 311.5

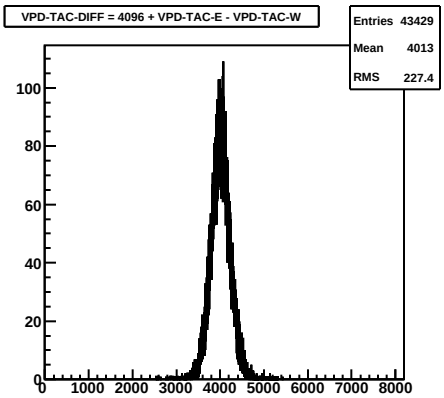

BBClarge-TAC-E
Entries186102
Mean 1485
RMS 337

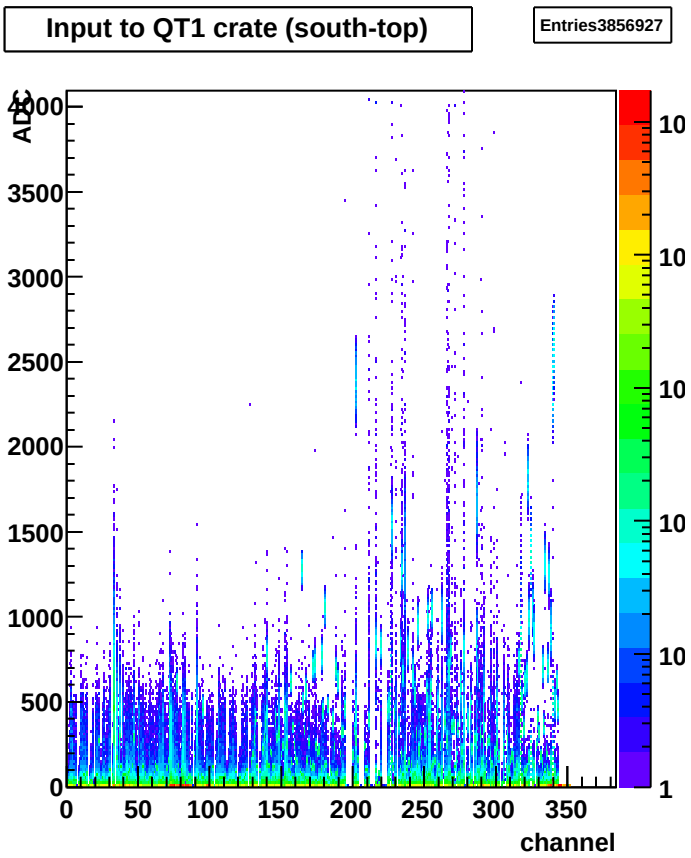

BBClarge-TAC-W
Entries193924
Mean 1505
RMS 320.2

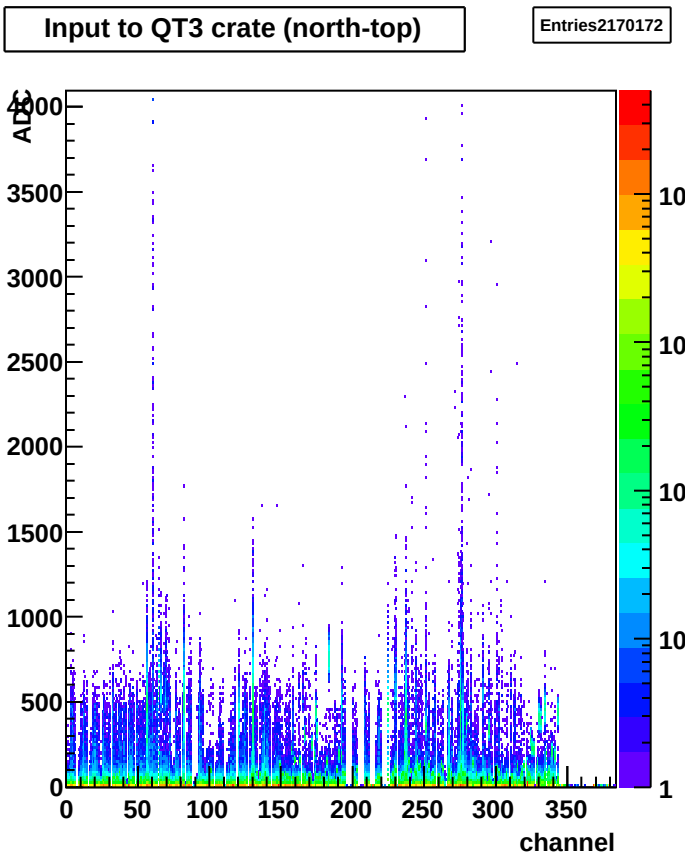

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries179873
Mean 4073
RMS 428.2

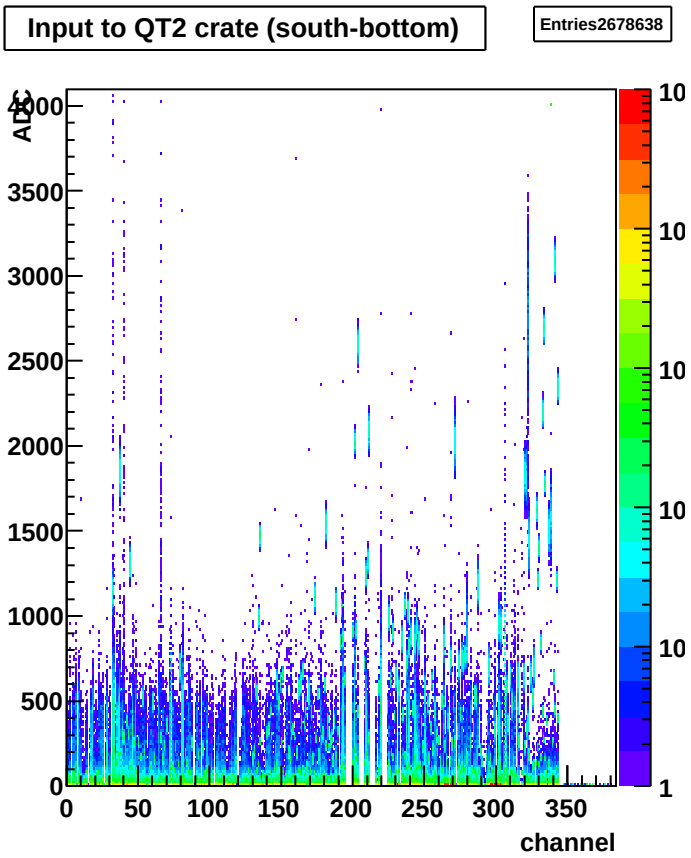

VPD-TAC-E
Entries 99955
Mean 1348
RMS 174.3

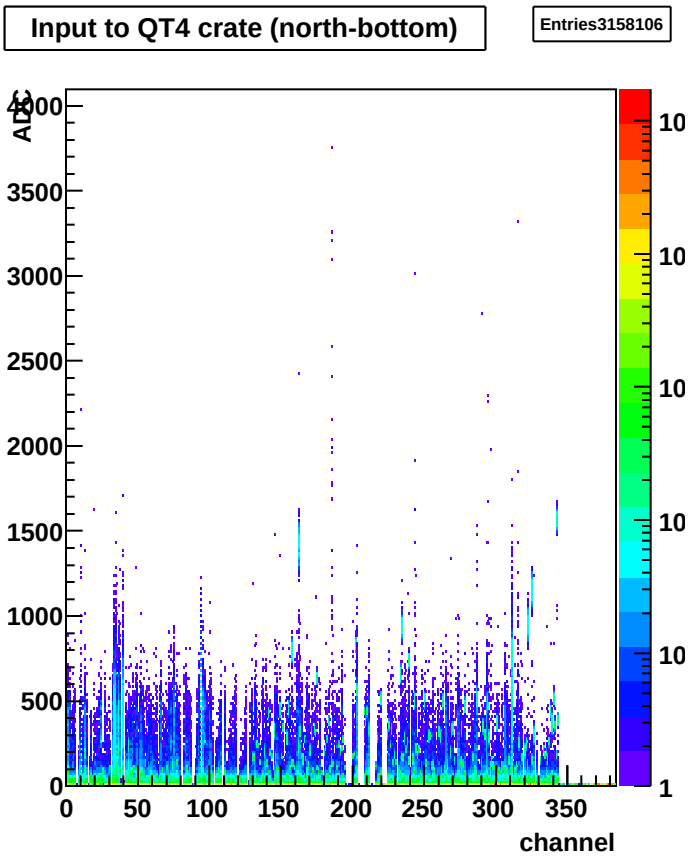

VPD-TAC-W
Entries 80929
Mean 1428
RMS 183.5

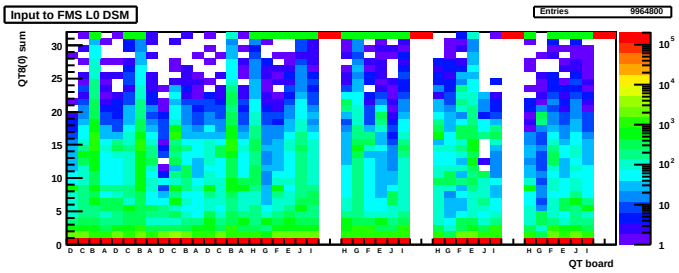

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 43429
Mean 4013
RMS 227.4

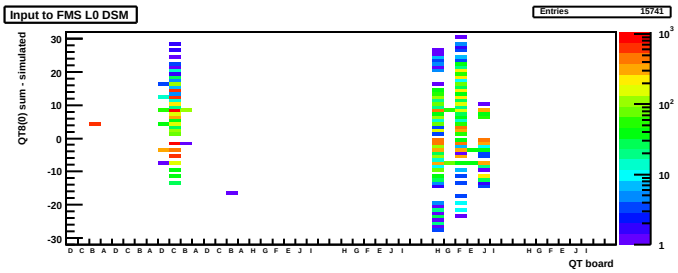

Input to QT1 crate (south-top)
Entries3856927
ADC
channel

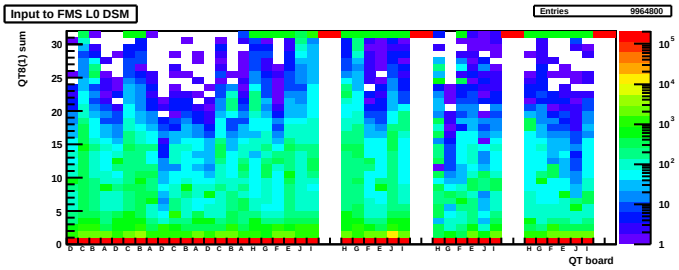

Input to QT3 crate (north-top)
Entries2170172
ADC
channel

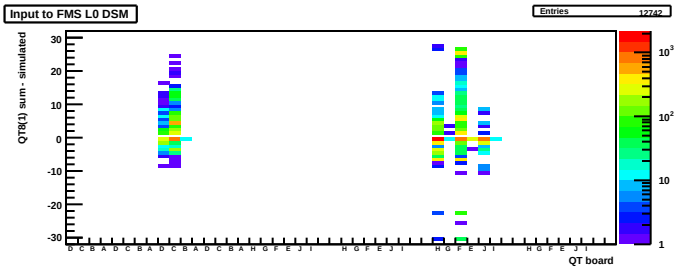

Input to QT2 crate (south-bottom)
Entries2678638
ADC
channel

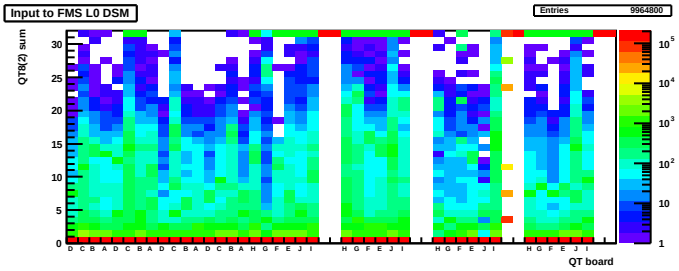

Input to QT4 crate (north-bottom)
Entries3158106
ADC
channel

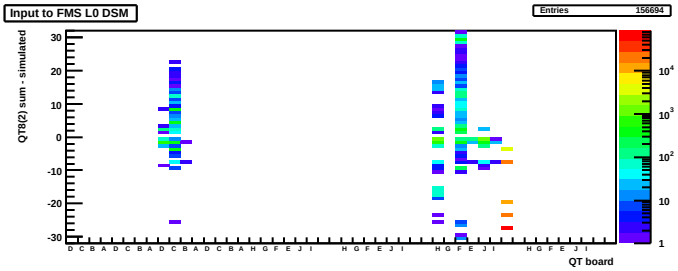

Input to FMS L0 DSM
Entries 9964800
QT8(0) sum
QT board

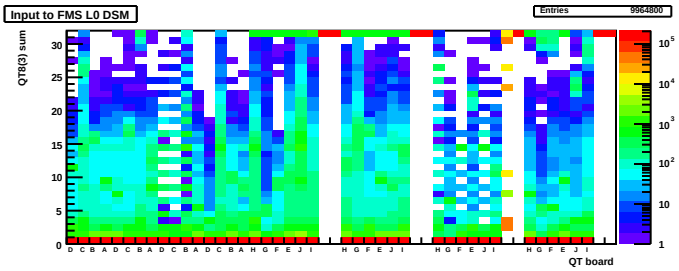

Input to FMS L0 DSM
Entries 15741
QT8(0) sum - simulated
QT board

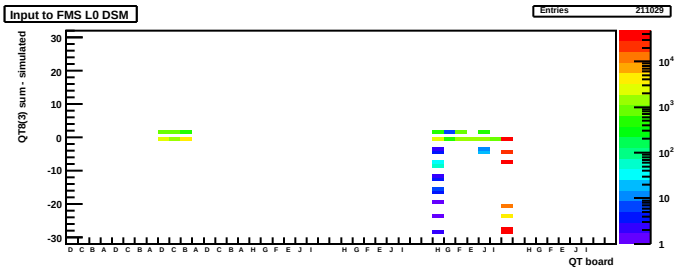

Input to FMS L0 DSM
Entries 9964800
QT8(1) sum
QT board

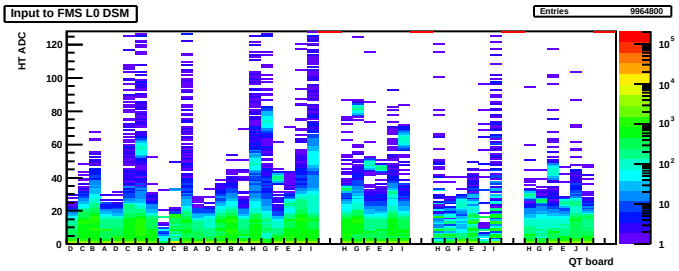

Input to FMS L0 DSM
Entries 12742
QT8(1) sum - simulated
QT board

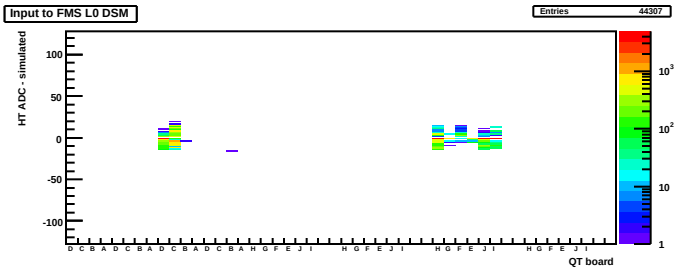

Input to FMS L0 DSM
Entries 9964800
QT8(2) sum
QT board

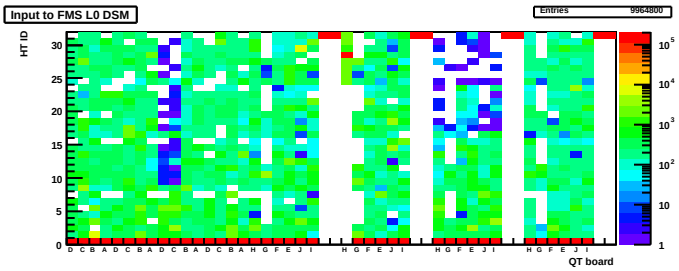

Input to FMS L0 DSM
Entries 156694
QT8(2) sum - simulated
QT board

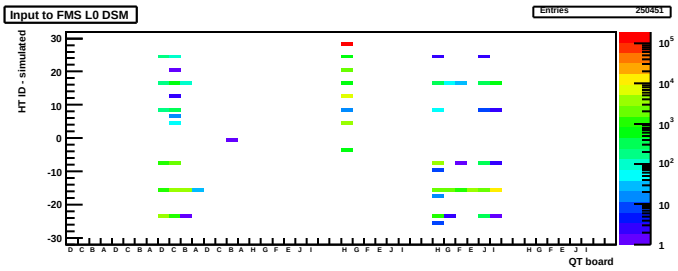

Input to FMS L0 DSM
Entries 9964800
QT8(3) sum
QT board

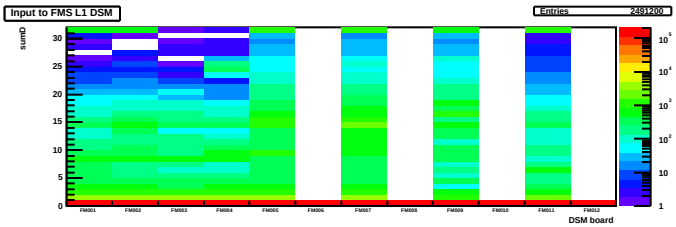

Input to FMS L0 DSM
Entries 211029
QT8(3) sum - simulated
QT board

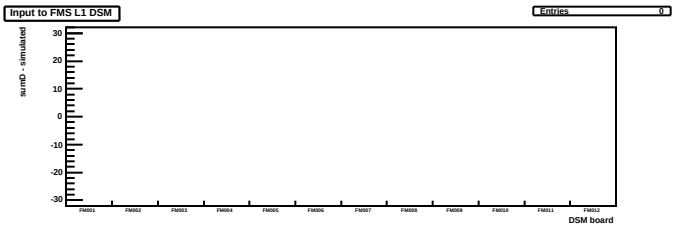

Input to FMS L0 DSM
Entries 9964800
HT ADC
QT board

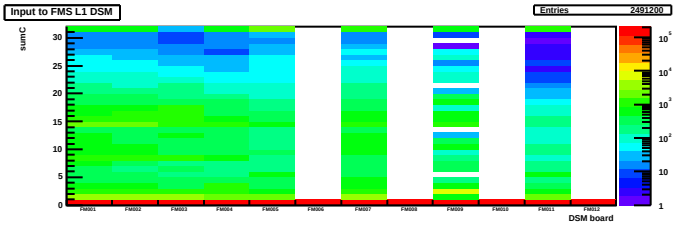

Input to FMS L0 DSM
Entries 44307
HT ADC - simulated
QT board

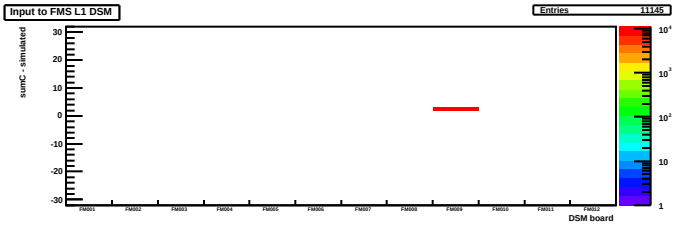

Input to FMS L0 DSM
Entries 9964800
HT ID
QT board

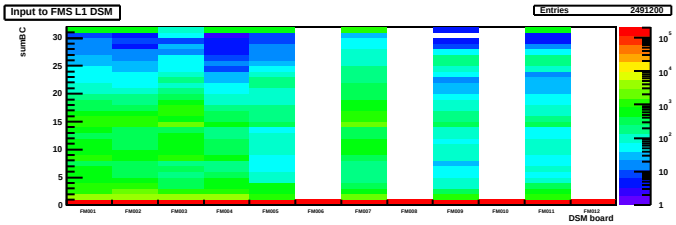

Input to FMS L0 DSM
Entries 250451
HT ID - simulated
QT board

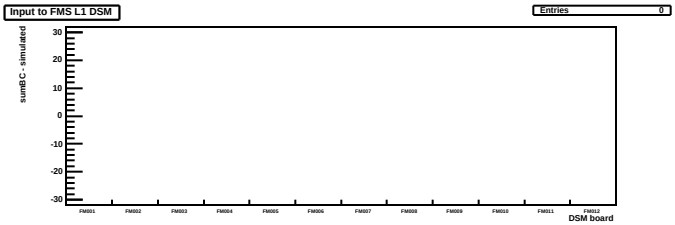

Input to FMS L1 DSM
Entries 2491200
sumD
DSM board

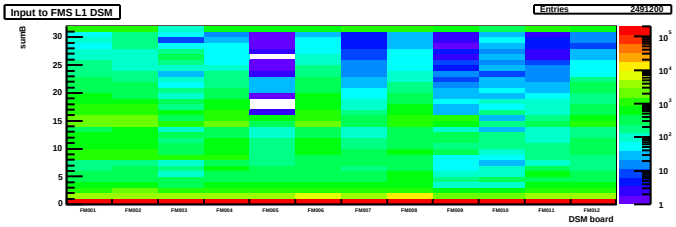

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

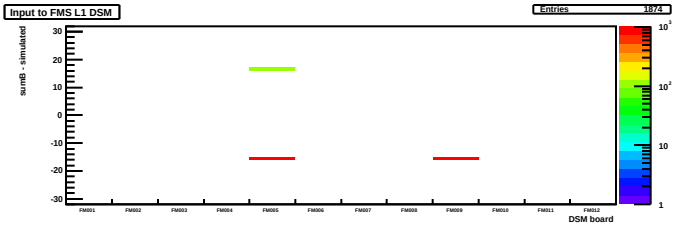

Input to FMS L1 DSM
Entries 2491200
sumC
DSM board

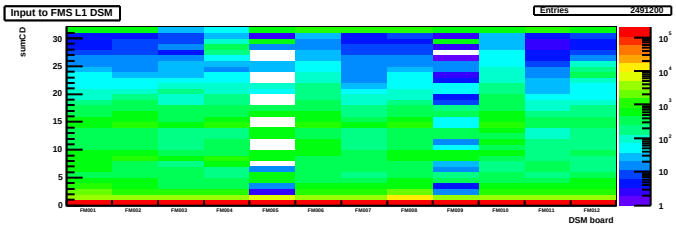

Input to FMS L1 DSM
Entries 11145
sumC - simulated
DSM board

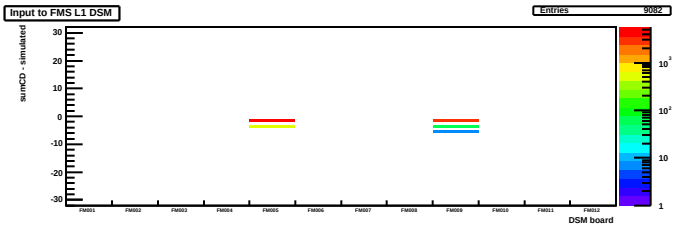

Input to FMS L1 DSM
Entries 2491200
sumBC
DSM board

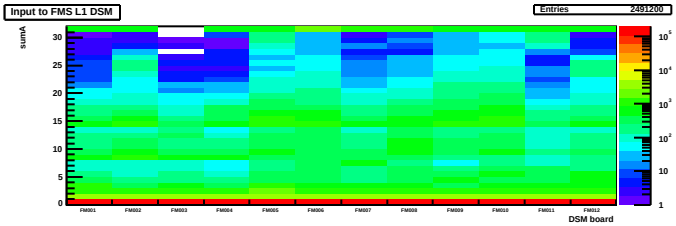

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

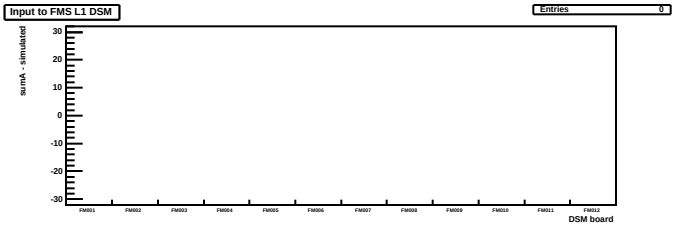

Input to FMS L1 DSM
Entries 2491200
sumB
DSM board

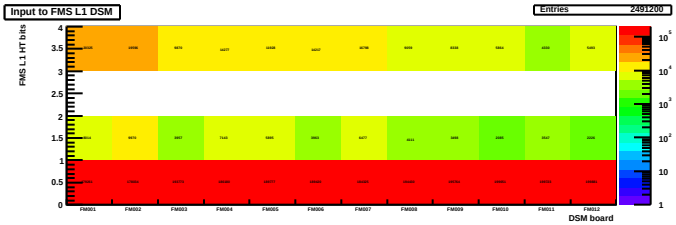

Input to FMS L1 DSM
Entries 1874
sumB - simulated
DSM board

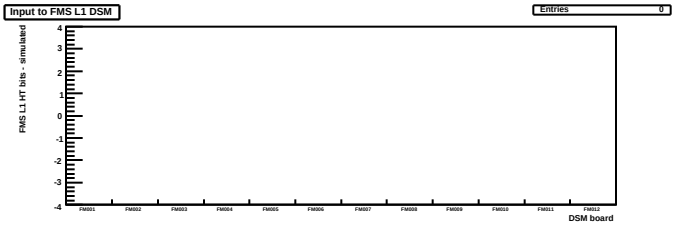

Input to FMS L1 DSM
Entries 2491200
sumCD
DSM board

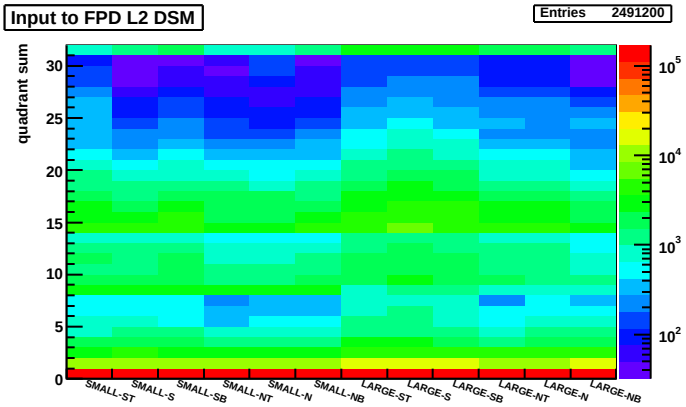

Input to FMS L1 DSM
Entries 9082
sumCD - simulated
DSM board

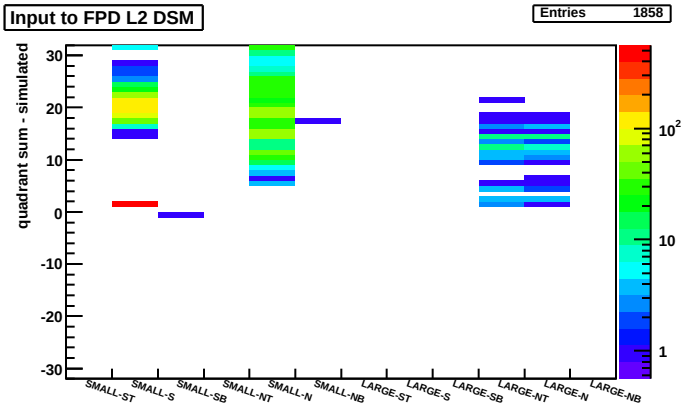

Input to FMS L1 DSM
Entries 2491200
sumA
DSM board

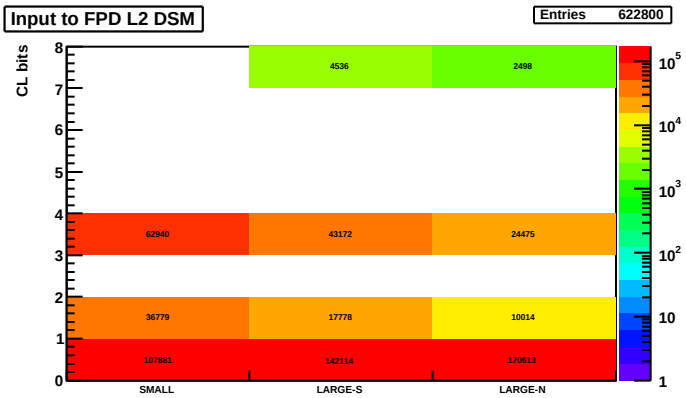

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

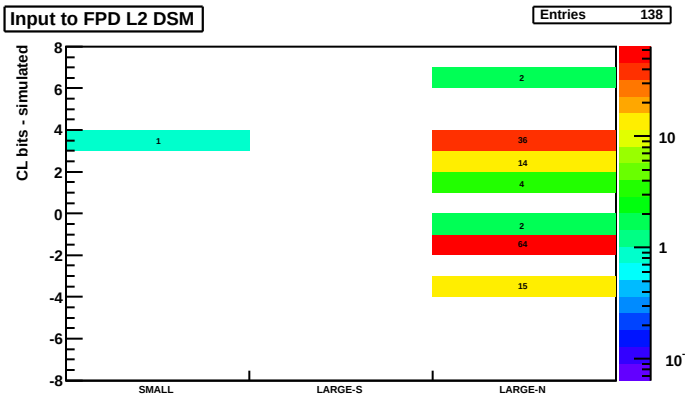

Input to FMS L1 DSM
Entries 2491200
FMS L1 HT bits
DSM board

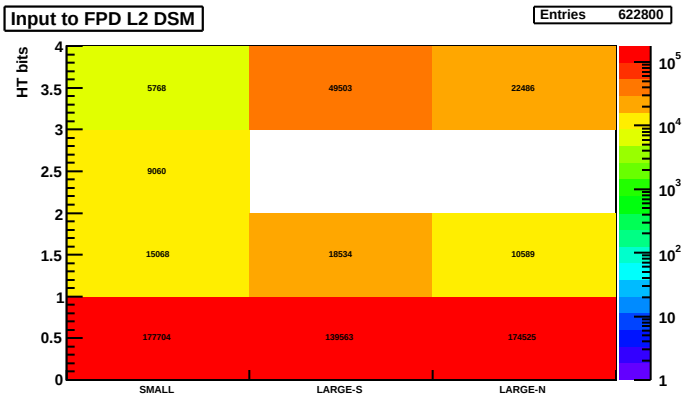

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

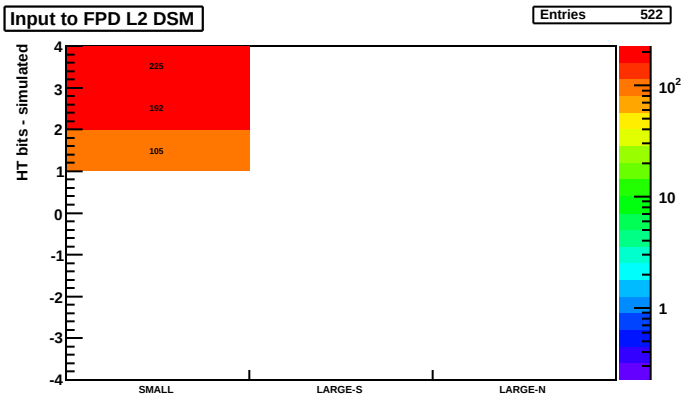

Input to FPD L2 DSM
Entries 2491200
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

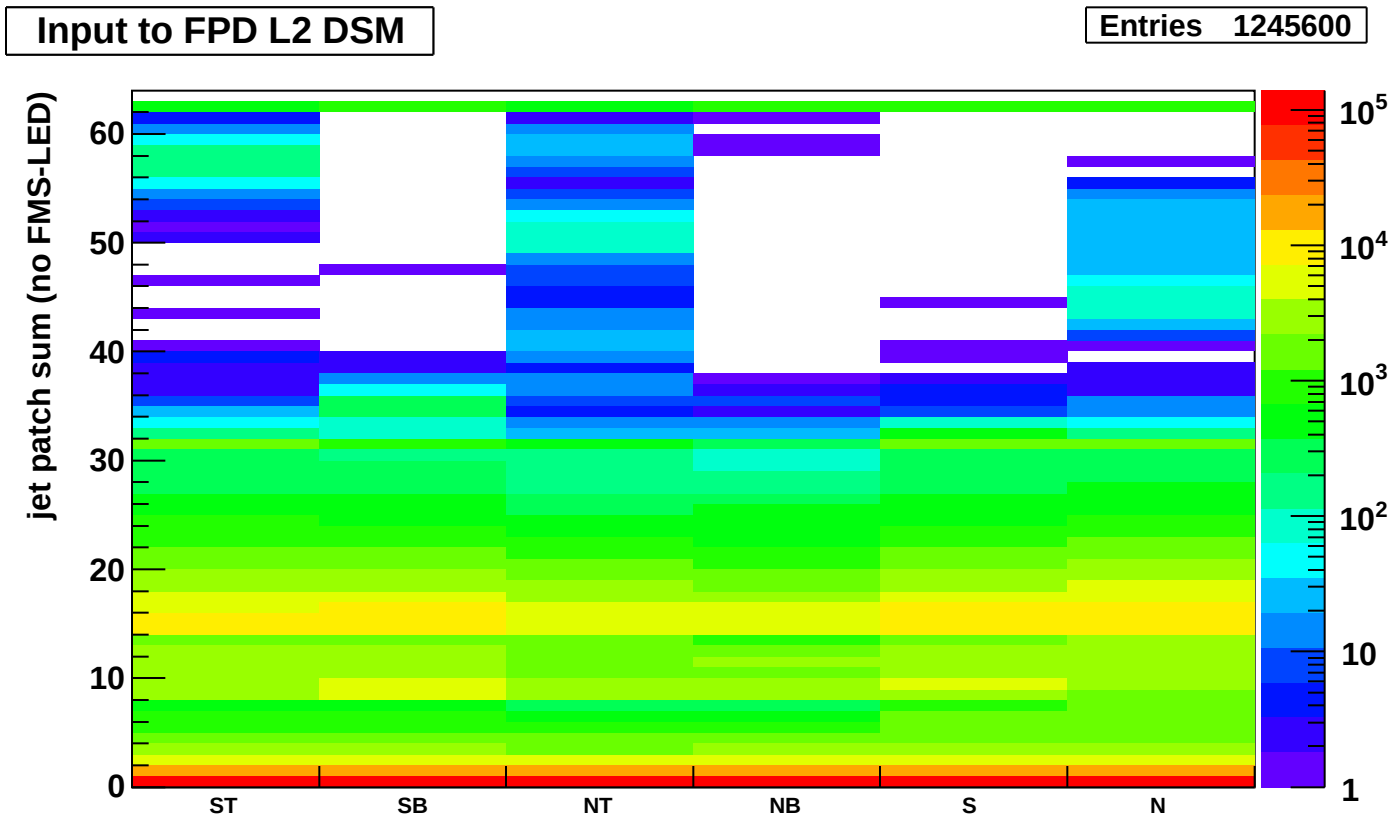

Input to FPD L2 DSM
Entries 1858
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

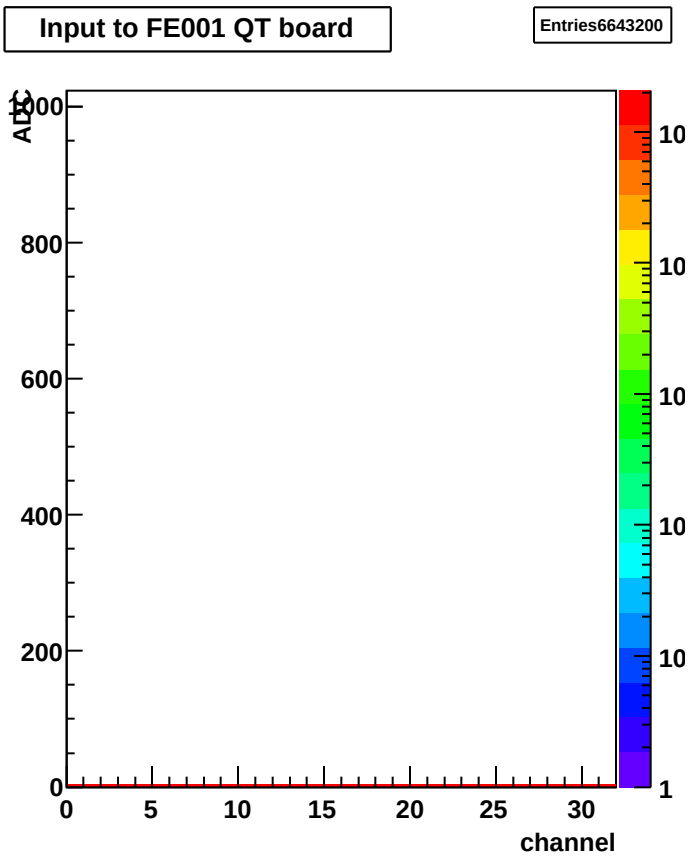

Input to FPD L2 DSM
Entries 622800
CL bits
4536
2498
62940
43172
24475
36779
17778
10014
107881
142114
170613
SMALL
LARGE-S
LARGE-N

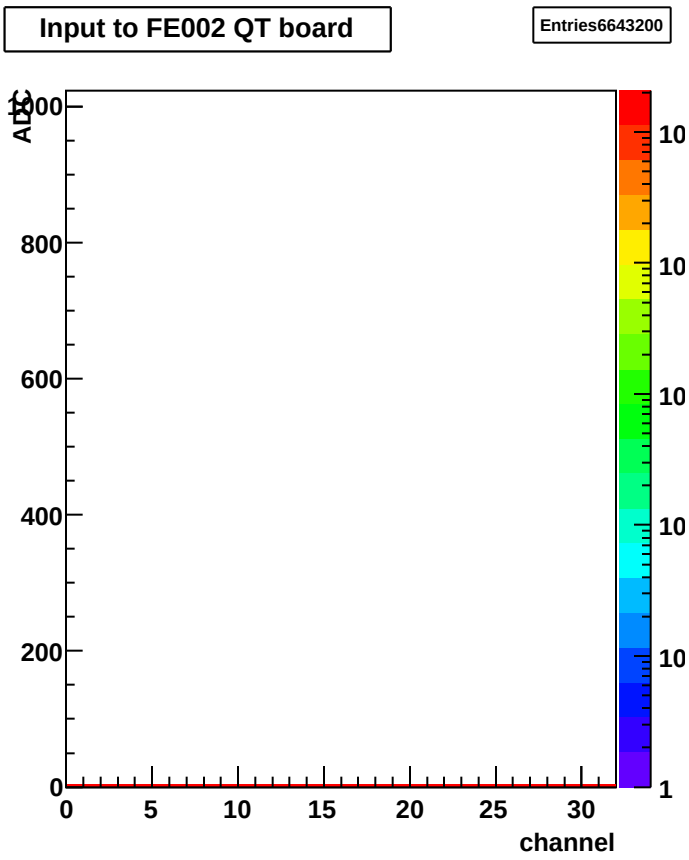

Input to FPD L2 DSM
Entries 138
CL bits - simulated
1
2
36
14
4
2
64
15
SMALL
LARGE-S
LARGE-N

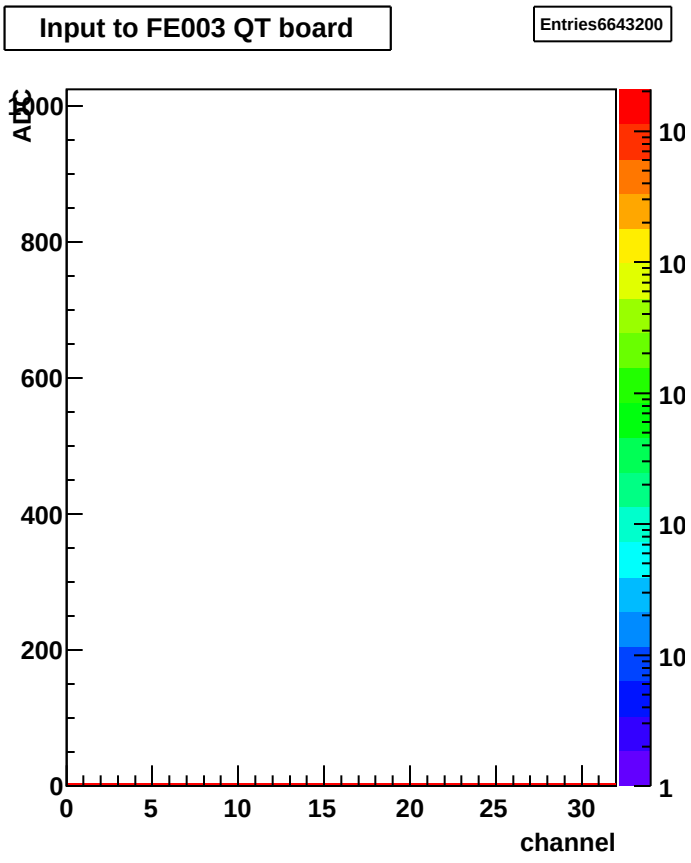

Input to FPD L2 DSM
Entries 622800
HT bits
5768
49503
22486
9060
15068
18534
10589
177704
139563
174525
SMALL
LARGE-S
LARGE-N

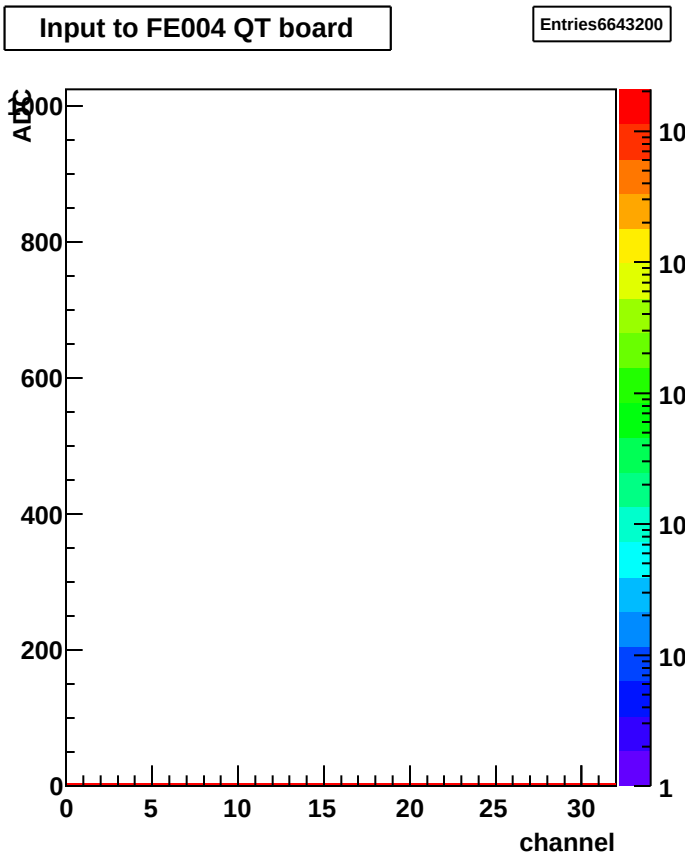

Input to FPD L2 DSM
Entries 522
HT bits - simulated
225
192
105
SMALL
LARGE-S
LARGE-N

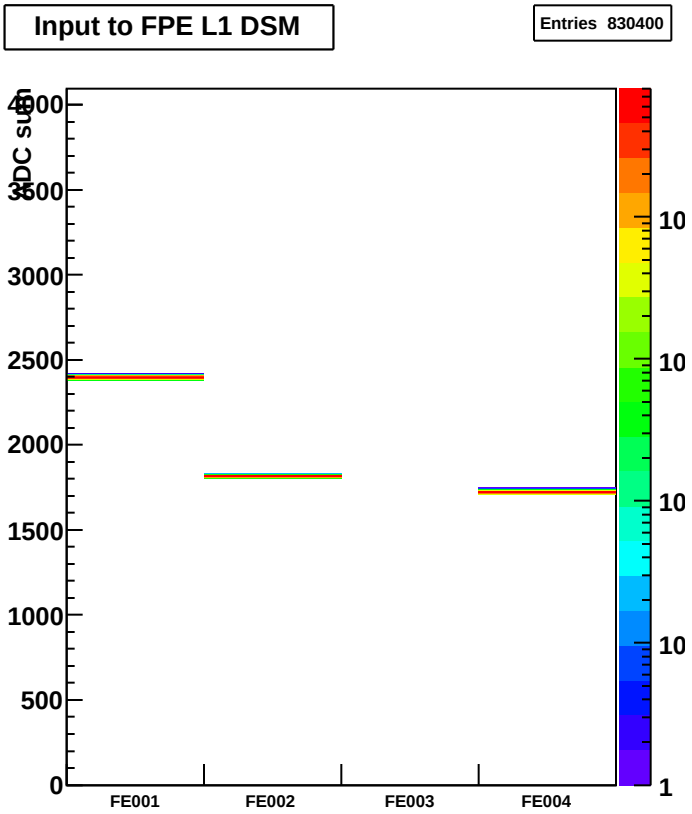

Input to FPD L2 DSM
Entries 1245600
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

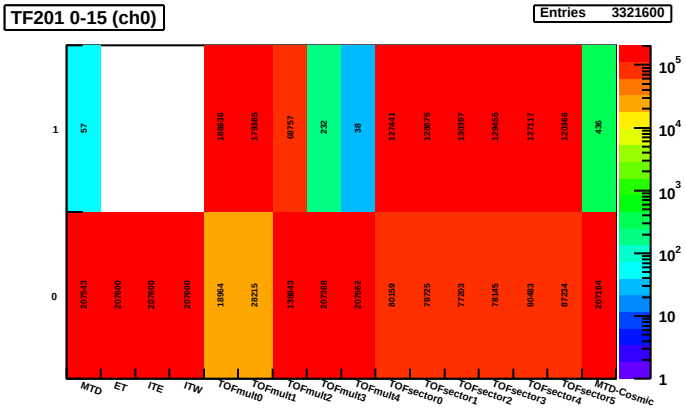

Input to FE001 QT board
Entries6643200
ADC
channel

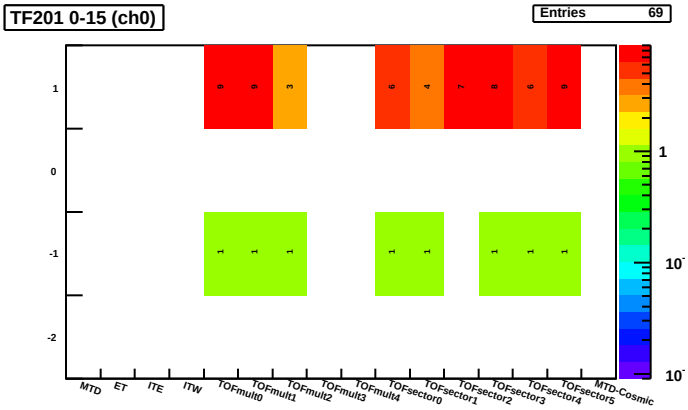

Input to FE002 QT board
Entries6643200
ADC
channel

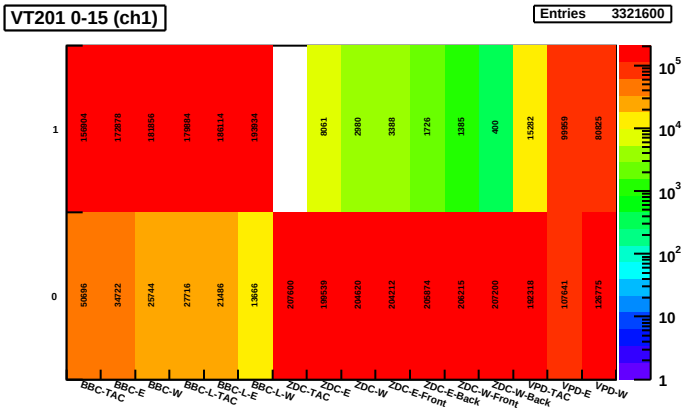

Input to FE003 QT board
Entries6643200
ADC
channel

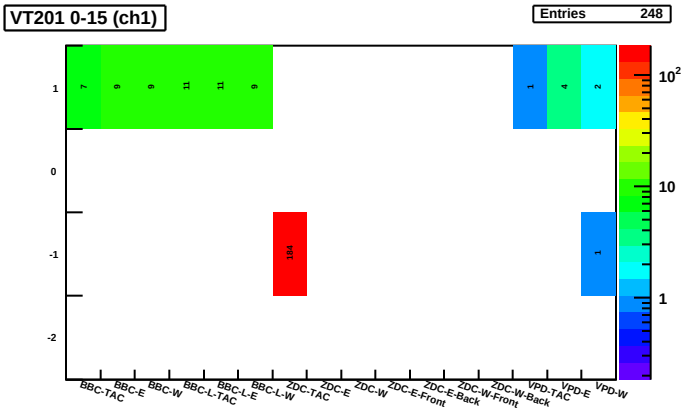

Input to FE004 QT board
Entries6643200
ADC
channel

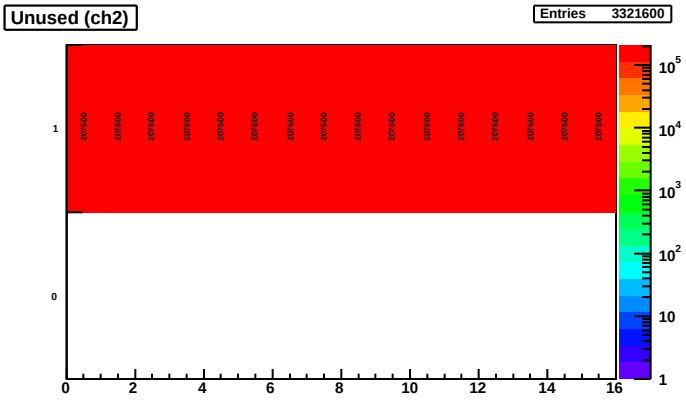

Input to FPE L1 DSM
Entries 830400
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

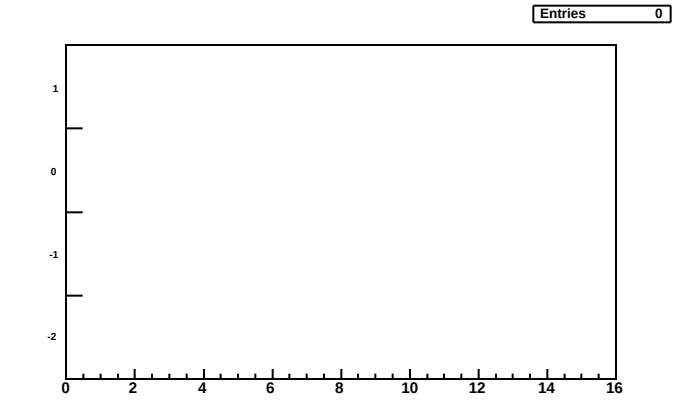

TF201 0-15 (ch0)
Entries 3321600
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

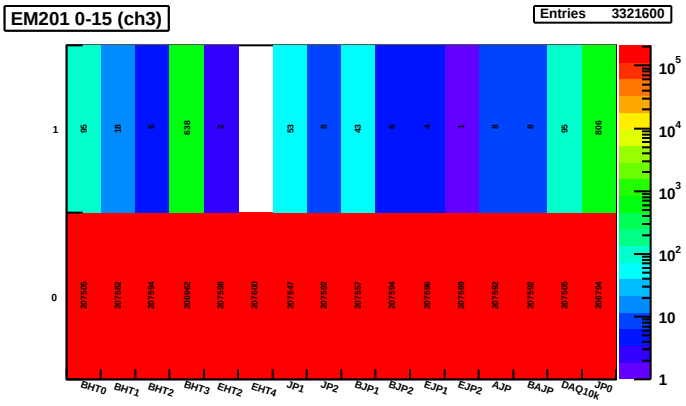

TF201 0-15 (ch0)
Entries 69
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3321600
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 248
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3321600


Entries 0


EM201 0-15 (ch3)
Entries 3321600
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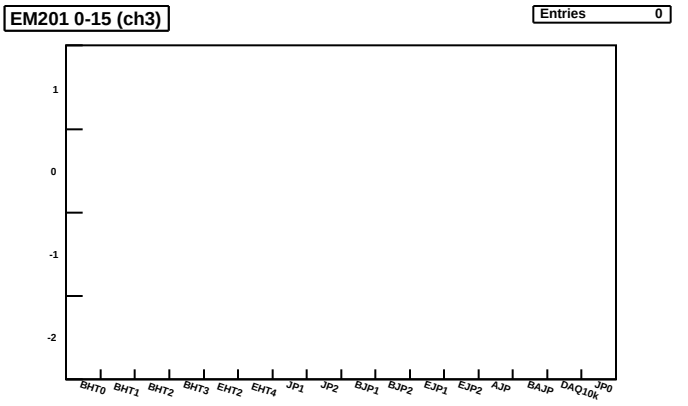
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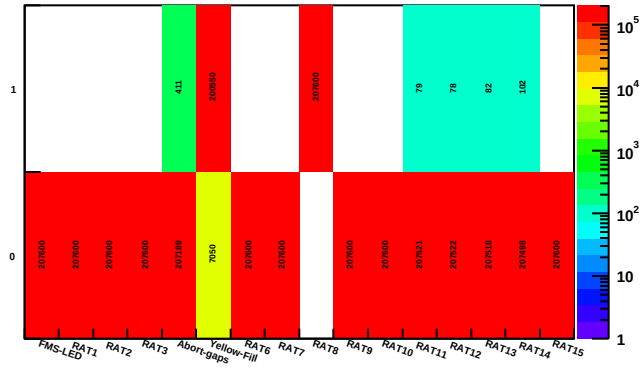
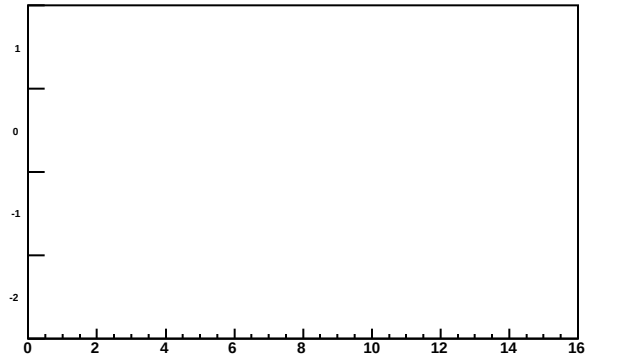
RAT board (ch4)





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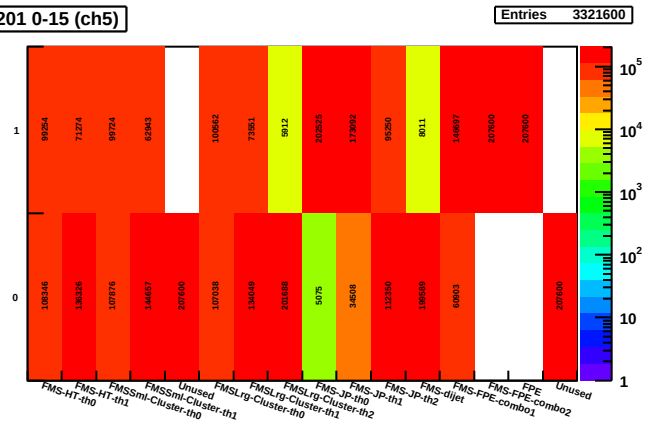

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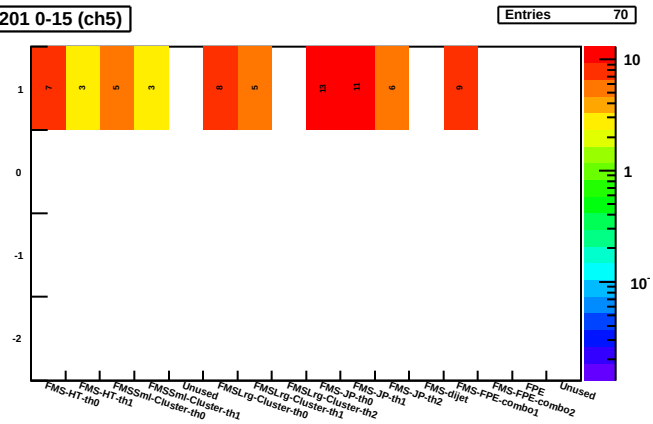

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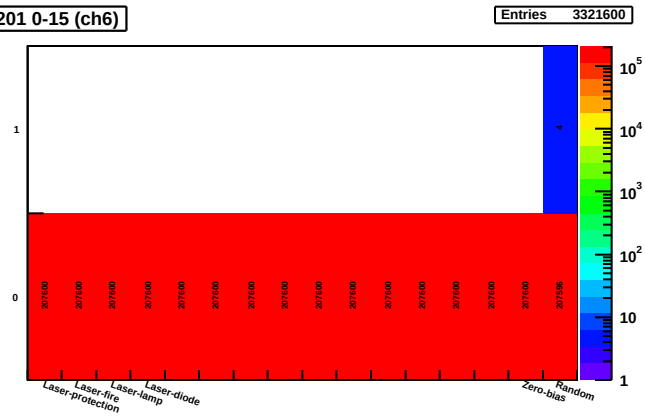

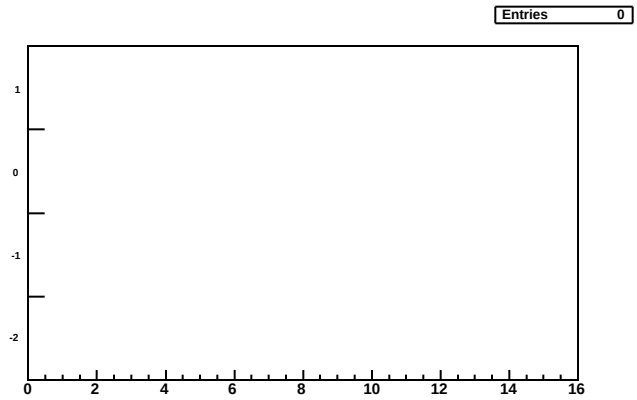
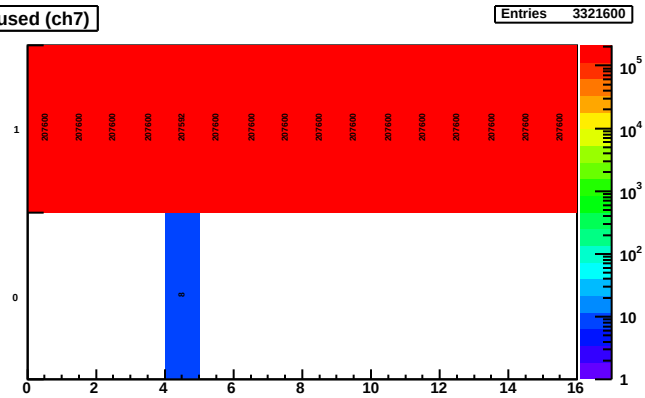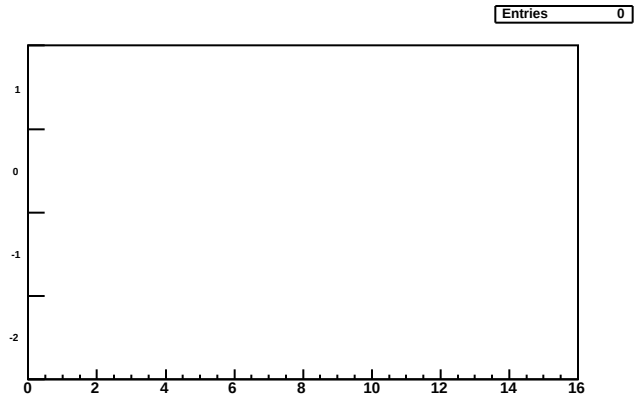
Unused (ch7)