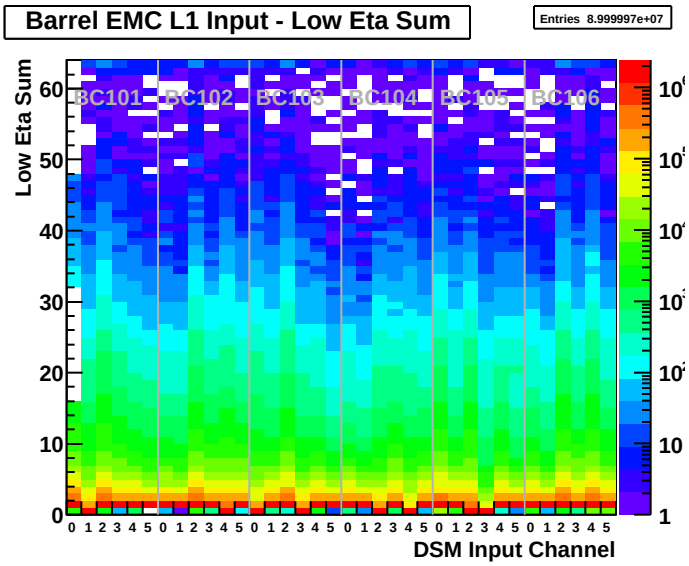
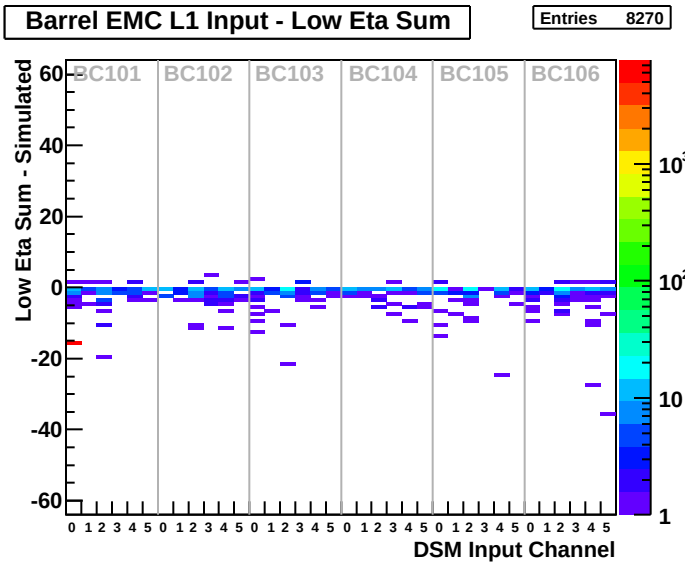
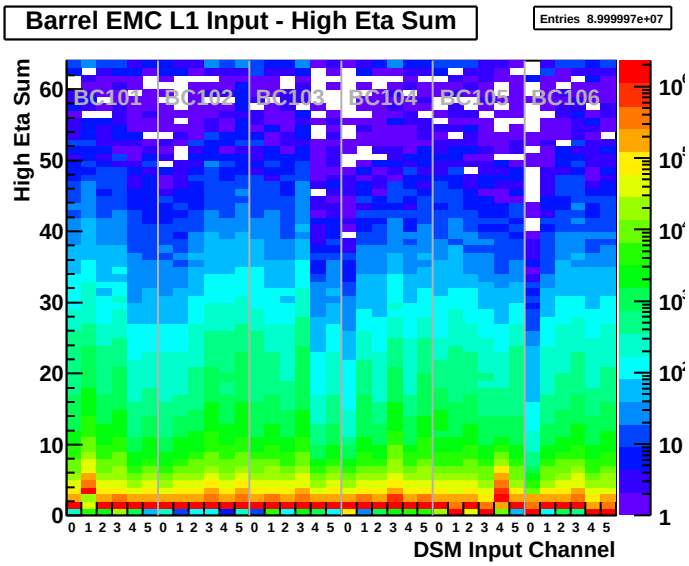
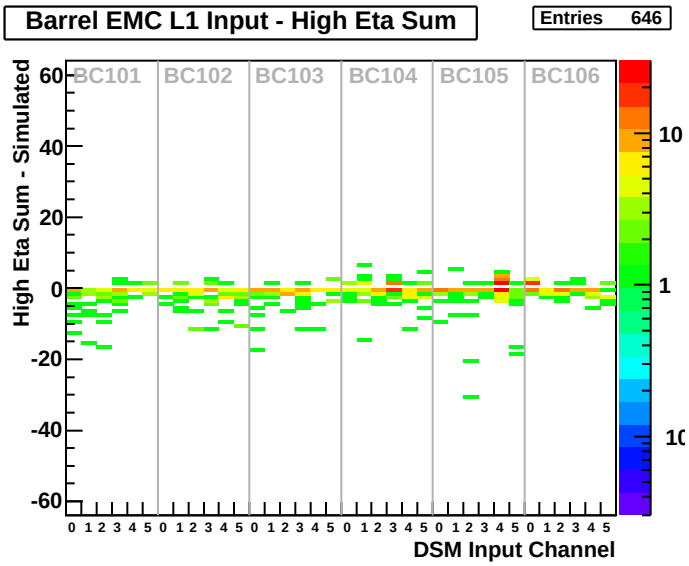
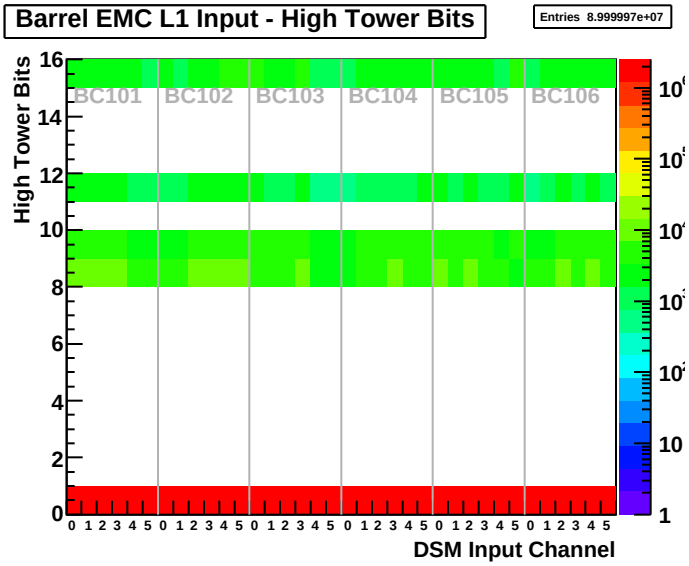
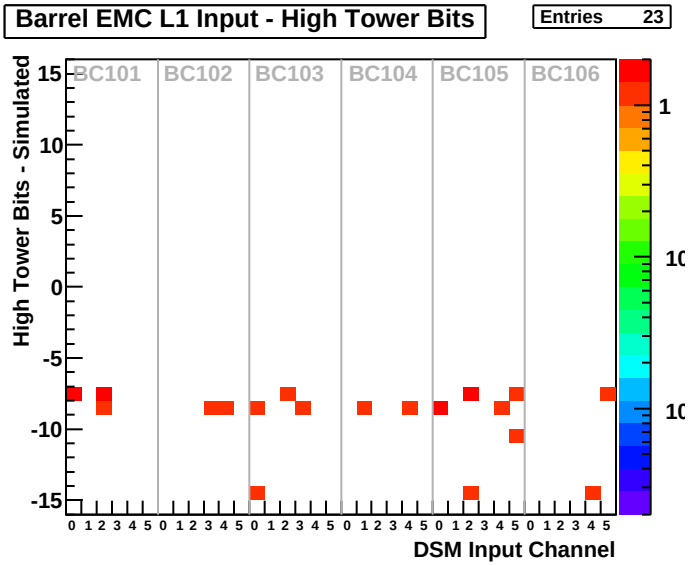

Barrel EMC L1 Input - Low Eta Sum
Entries 8.999997e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

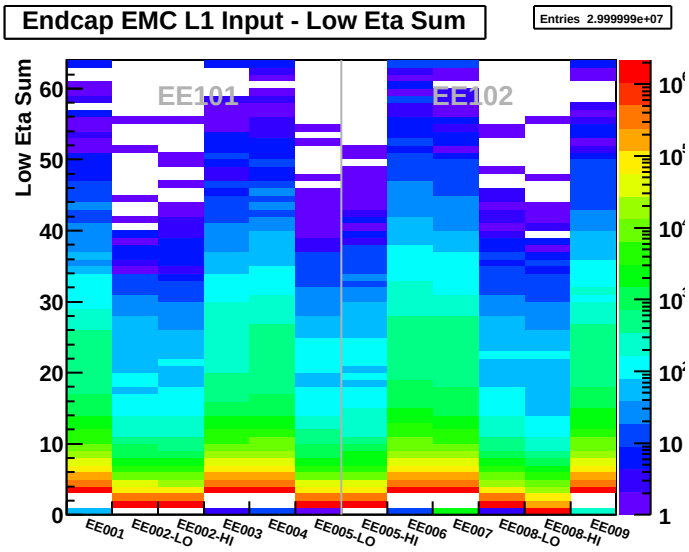

Barrel EMC L1 Input - Low Eta Sum
Entries 8270
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

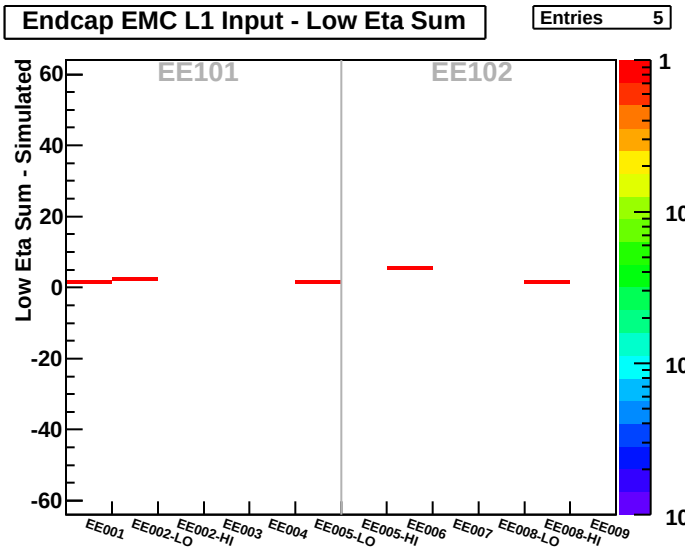

Barrel EMC L1 Input - High Eta Sum
Entries 8.999997e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

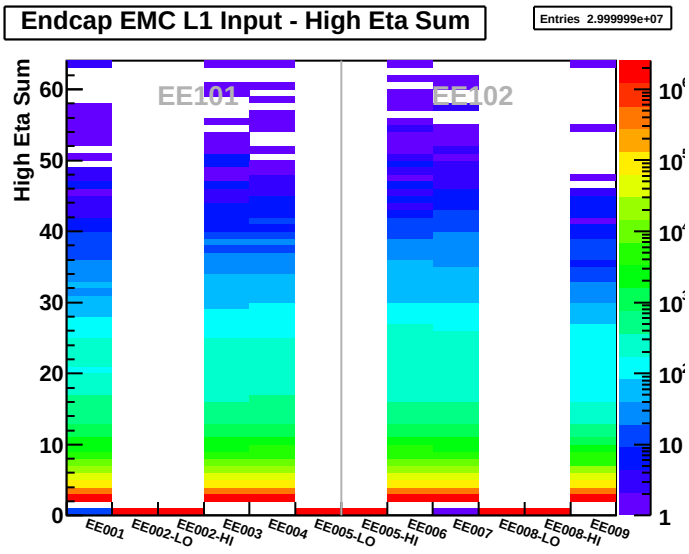

Barrel EMC L1 Input - High Eta Sum
Entries 646
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

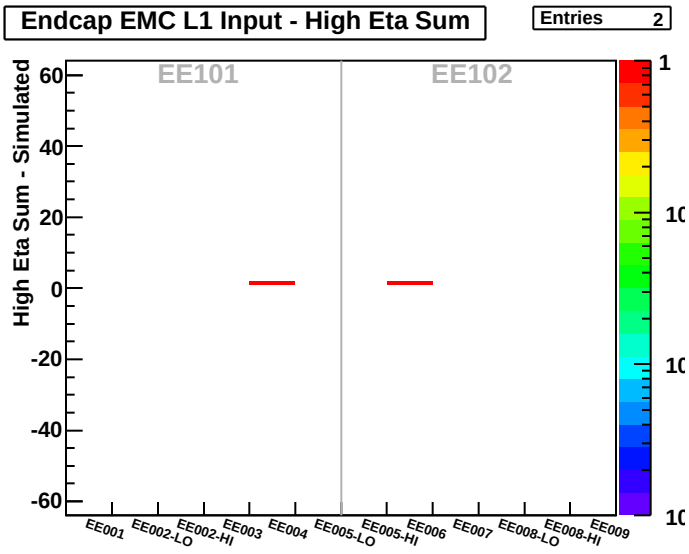

Barrel EMC L1 Input - High Tower Bits
Entries 8.999997e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

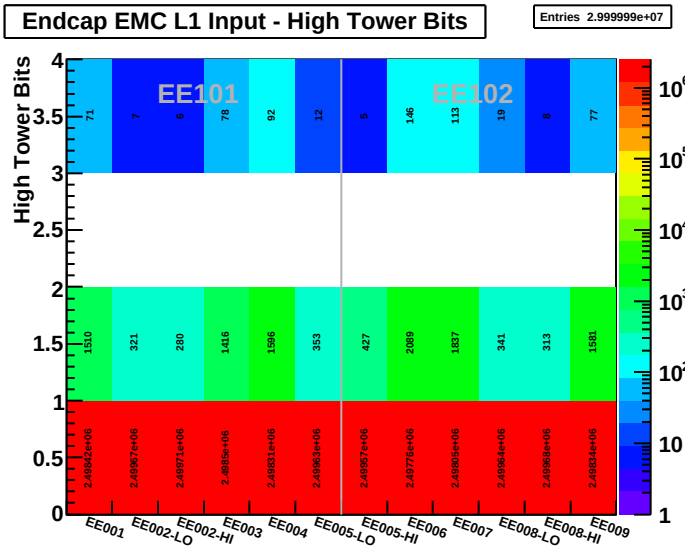

Barrel EMC L1 Input - High Tower Bits
Entries 23
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

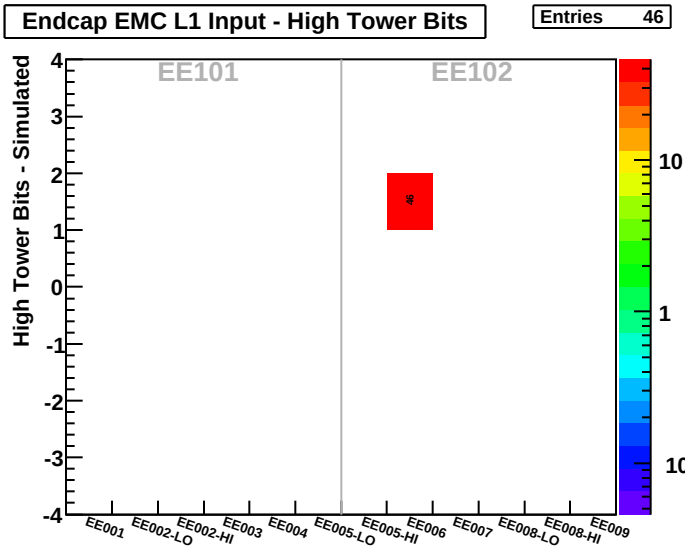

Endcap EMC L1 Input - Low Eta Sum
Entries 2.999999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

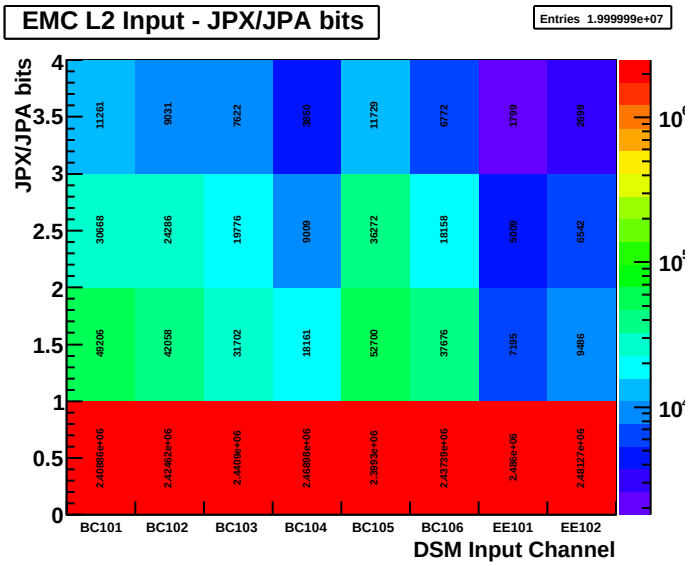

Endcap EMC L1 Input - Low Eta Sum
Entries 5
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

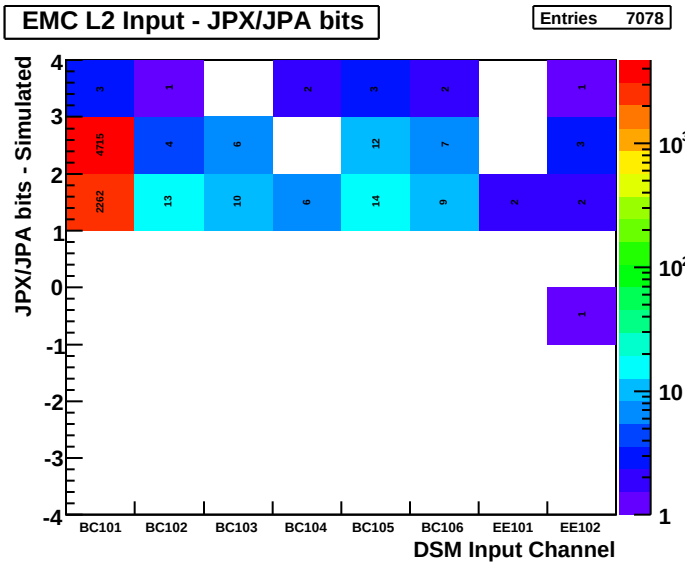

Endcap EMC L1 Input - High Eta Sum
Entries 2.999999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

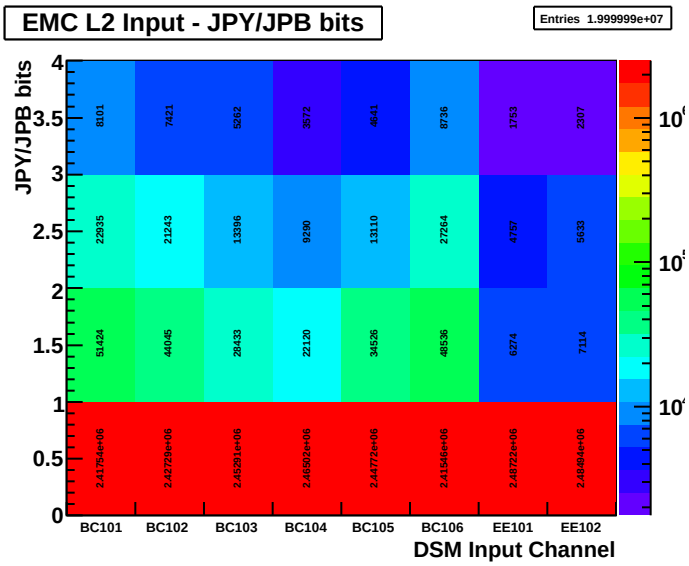

Endcap EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

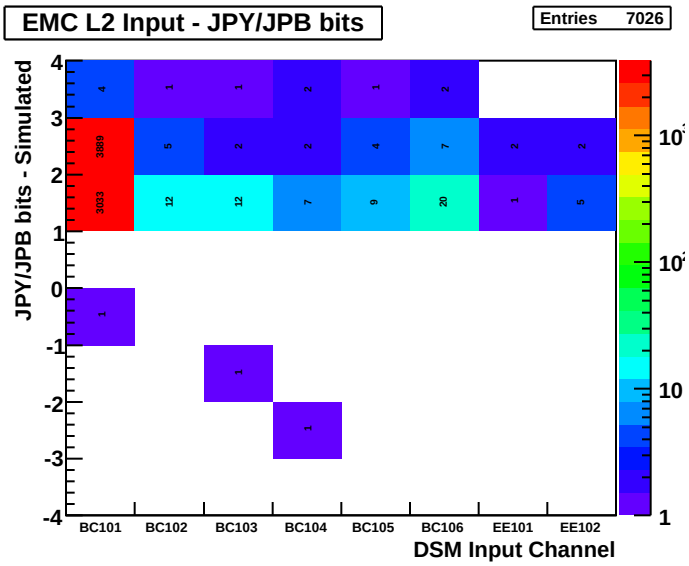

Endcap EMC L1 Input - High Tower Bits
Entries 2.999999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

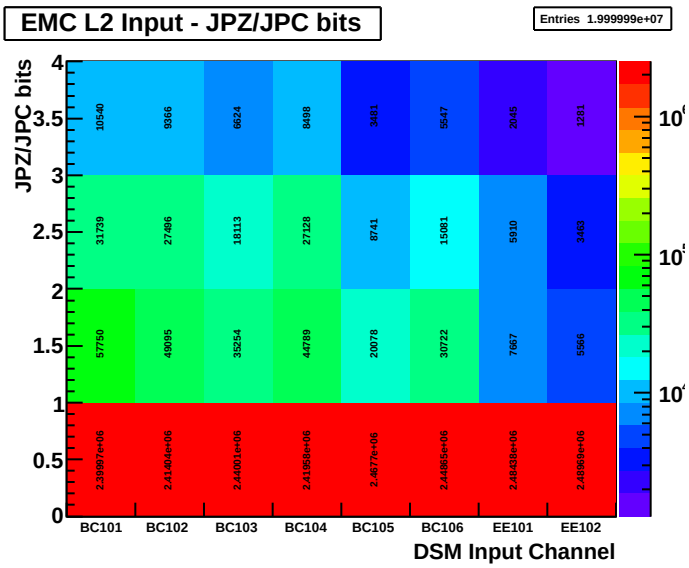

Endcap EMC L1 Input - High Tower Bits
Entries 46
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

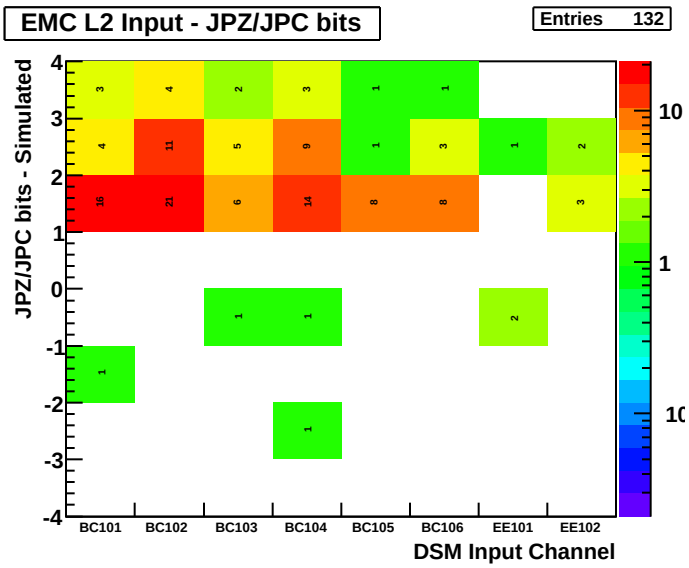

EMC L2 Input - JPX/JPA bits
Entries 1.999999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
11261 9031 7622 3850 11729 6772 1799 2699
30668 24286 19776 9009 36272 18158 5009 6542
49206 42058 31702 18161 52700 37676 7195 9486
2.40886e+06 2.42462e+06 2.4409e+06 2.46808e+06 2.3993e+06 2.43739e+06 2.49e+06 2.48127e+06

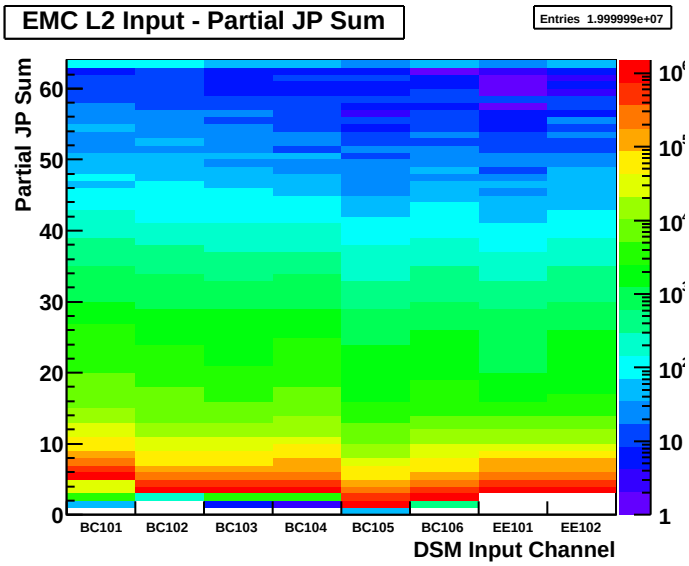

EMC L2 Input - JPX/JPA bits
Entries 7078
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 1 2 3 2 1
4715 4 6 12 7 3
2262 13 10 6 14 9 2 2
1

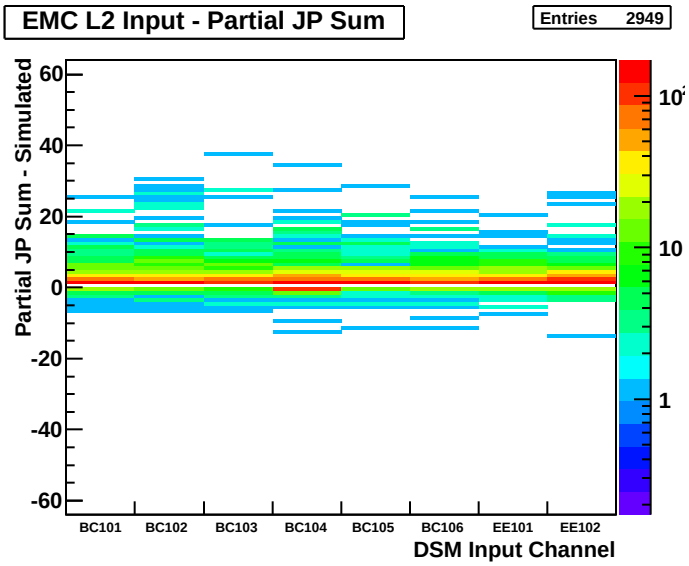

EMC L2 Input - JPY/JPB bits
Entries 1.999999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8101 7421 5262 3572 4641 8736 1753 2307
22935 21243 13396 9290 13110 27264 4757 5633
51424 44045 28433 22120 34526 46536 6274 7114
2.41754e+06 2.42729e+06 2.45281e+06 2.46502e+06 2.44772e+06 2.41546e+06 2.48722e+06 2.49484e+06

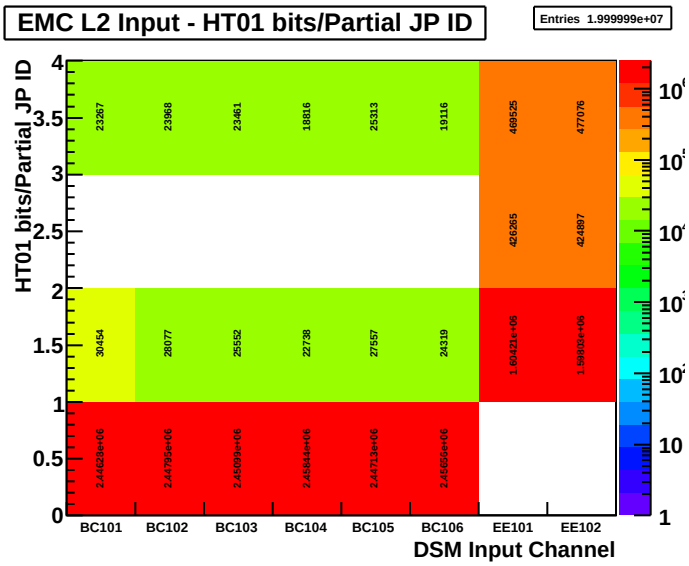

EMC L2 Input - JPY/JPB bits
Entries 7026
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4 1 1 2 1 2
3889 5 2 2 4 7 2 2
3033 12 12 7 9 20 1 5
1
1
1

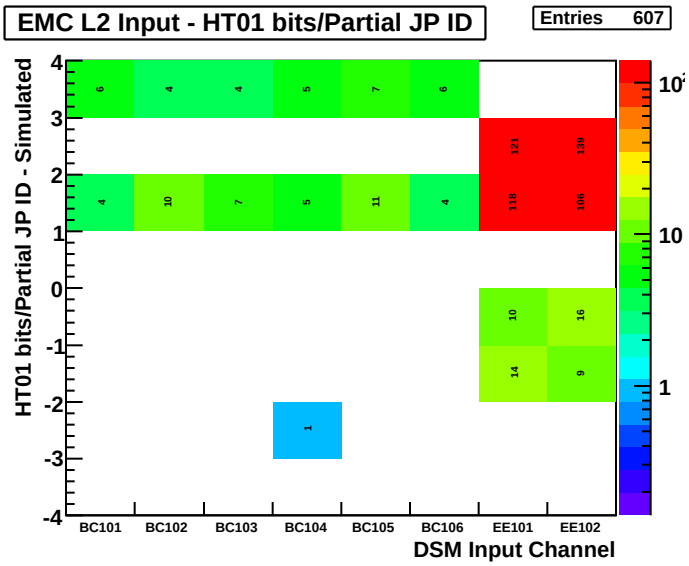

EMC L2 Input - JPZ/JPC bits
Entries 1.999999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10540 9366 6624 8498 3481 5547 2045 1281
31739 27496 18113 27128 8741 15081 5910 3463
57750 49095 35254 44789 20078 30722 7667 5566
2.39997e+06 2.41404e+06 2.44031e+06 2.41958e+06 2.46776e+06 2.44865e+06 2.48438e+06 2.48969e+06

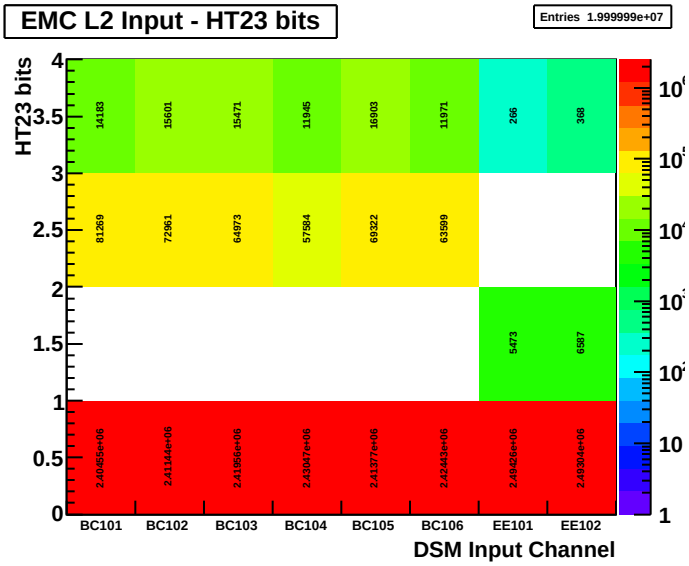

EMC L2 Input - JPZ/JPC bits
Entries 132
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 4 2 3 1 1
4 11 5 9 1 3 1 2
16 21 6 14 8 8 3
1 1 2
1
1

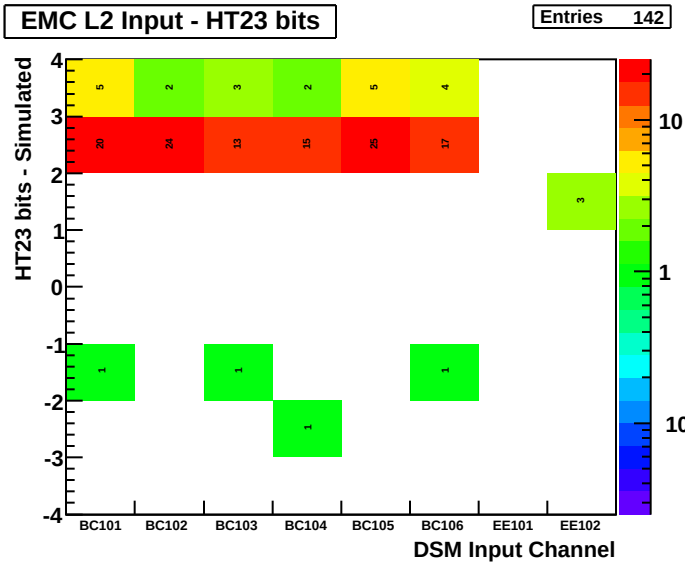

EMC L2 Input - Partial JP Sum
Entries 1.999999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

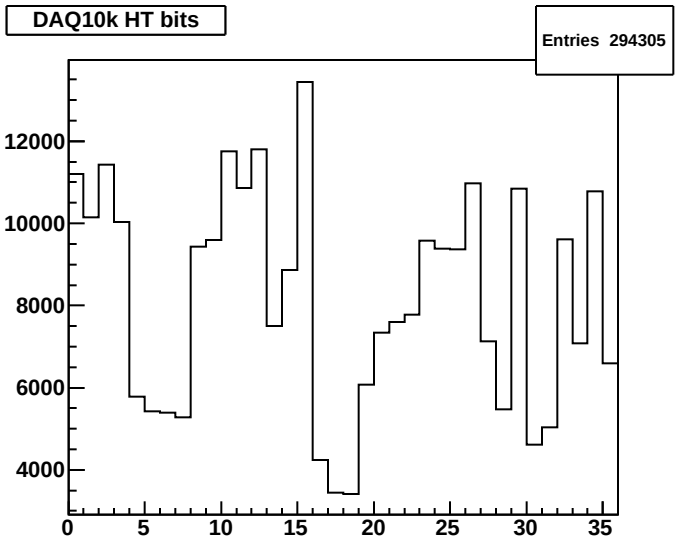

EMC L2 Input - Partial JP Sum
Entries 2949
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

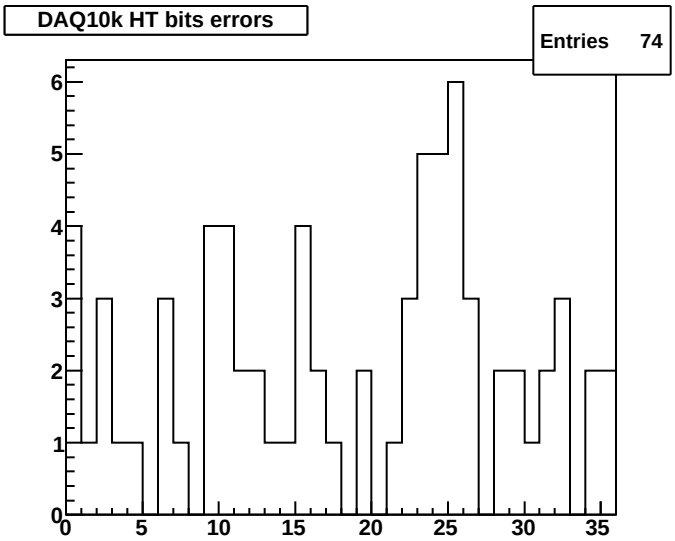

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.999999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

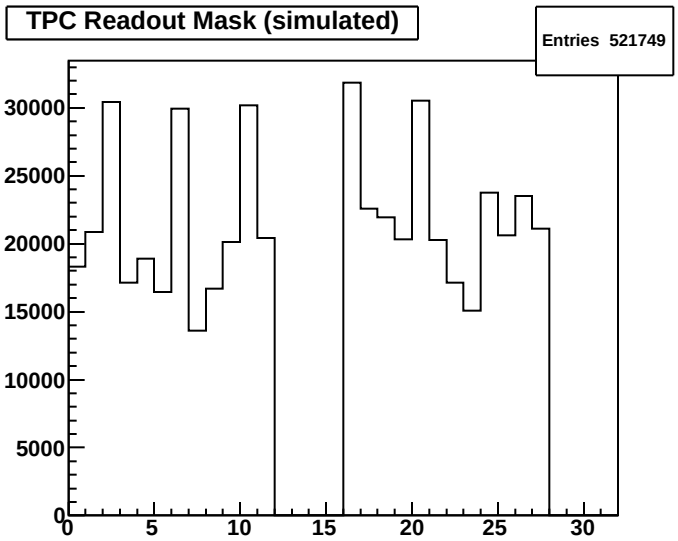

EMC L2 Input - HT01 bits/Partial JP ID
Entries 607
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

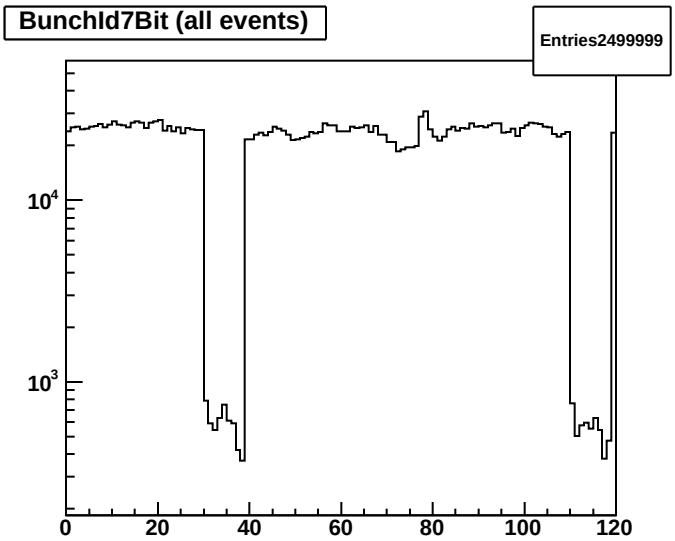

EMC L2 Input - HT23 bits
Entries 1.999999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

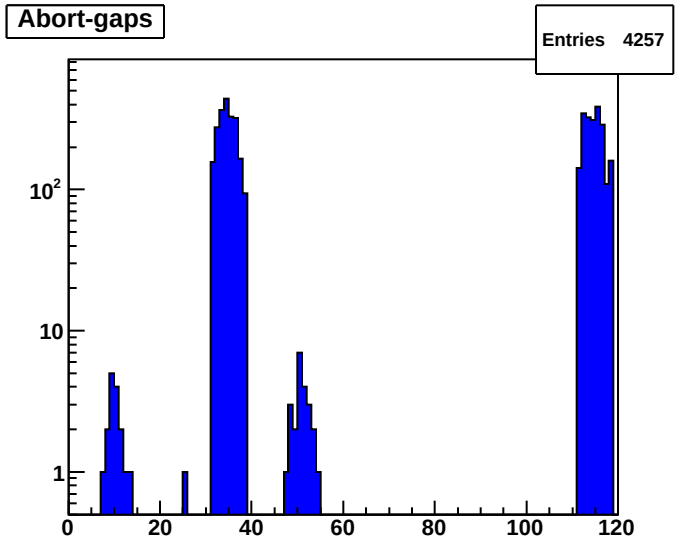

EMC L2 Input - HT23 bits
Entries 142
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

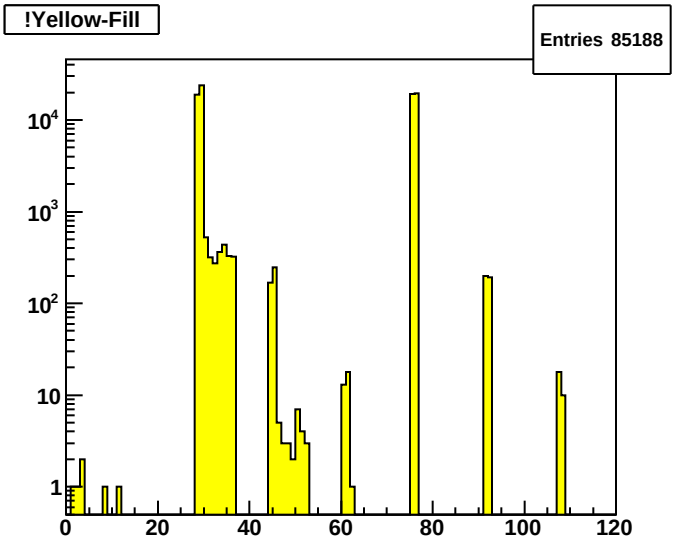

DAQ10k HT bits
Entries 294305

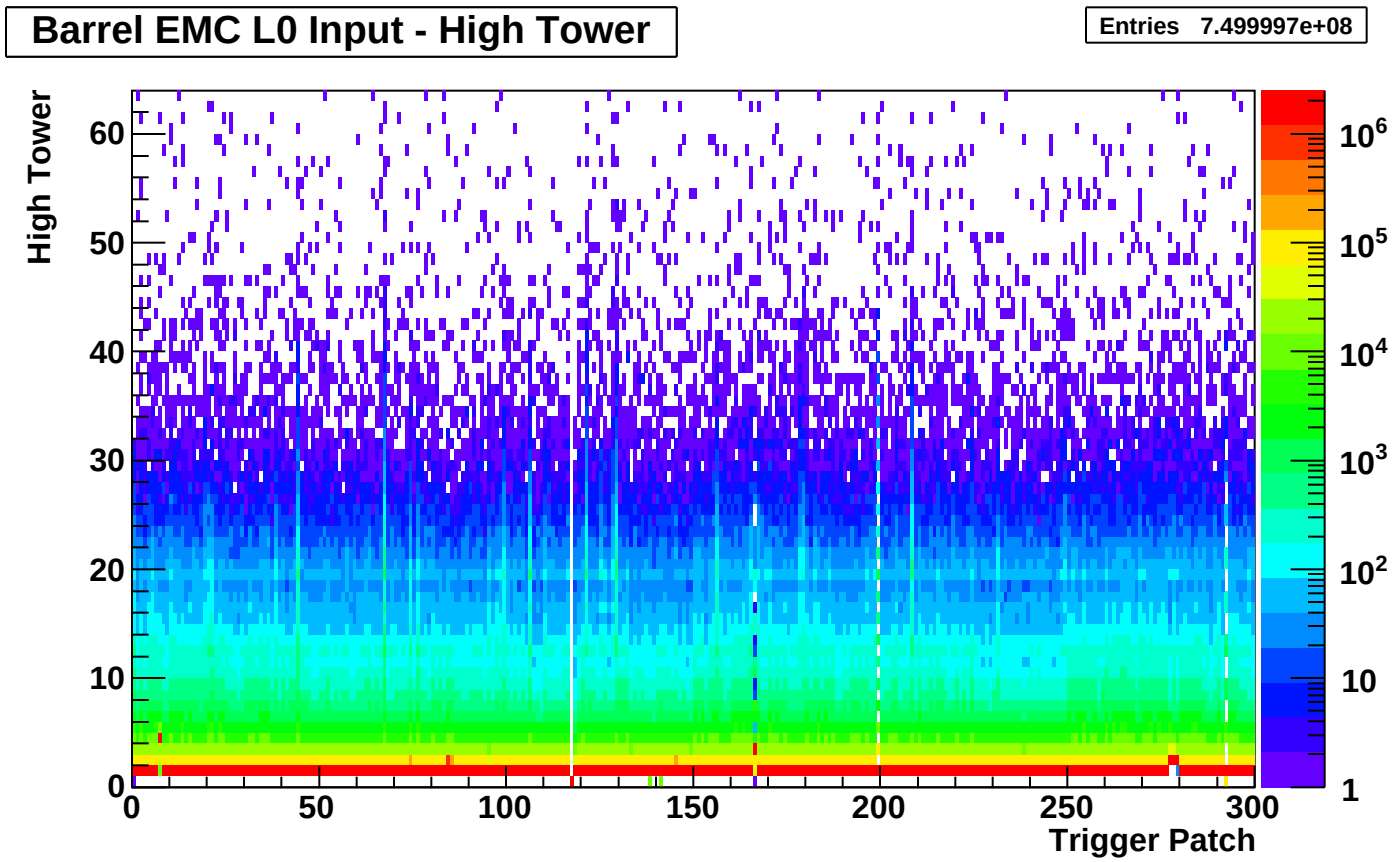

DAQ10k HT bits errors
Entries 74

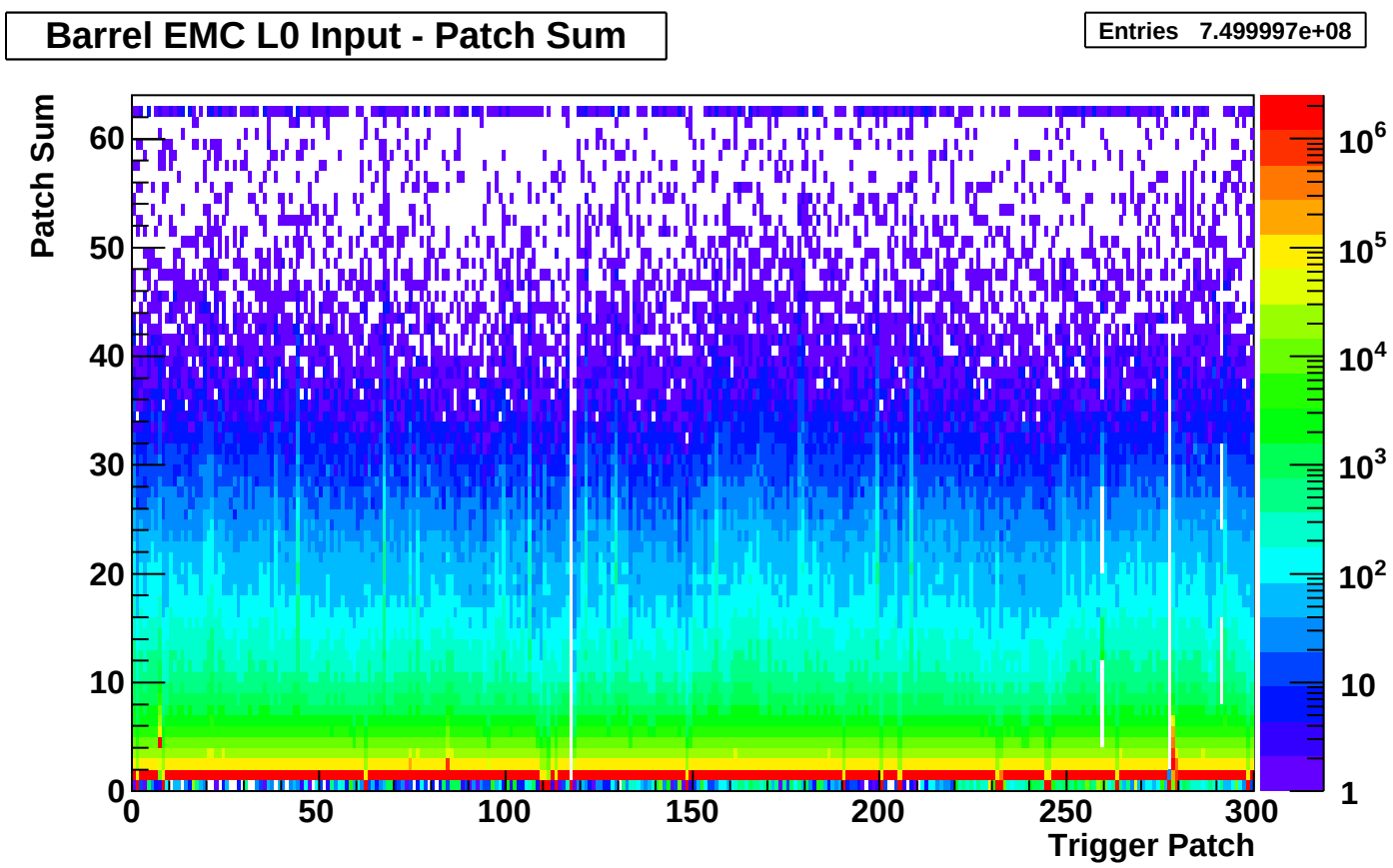

TPC Readout Mask (simulated)
Entries 521749

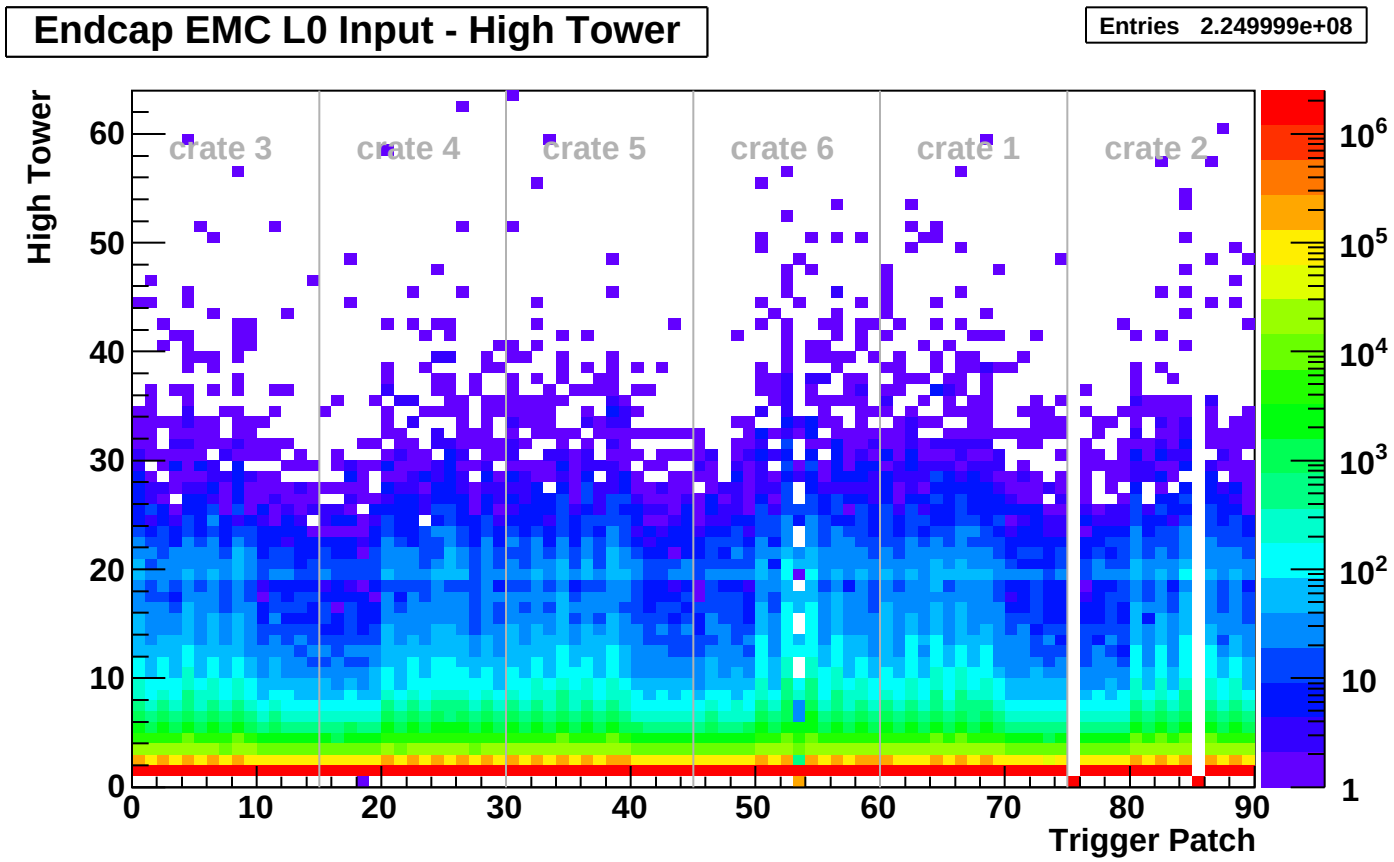

BunchId7Bit (all events)
Entries2499999

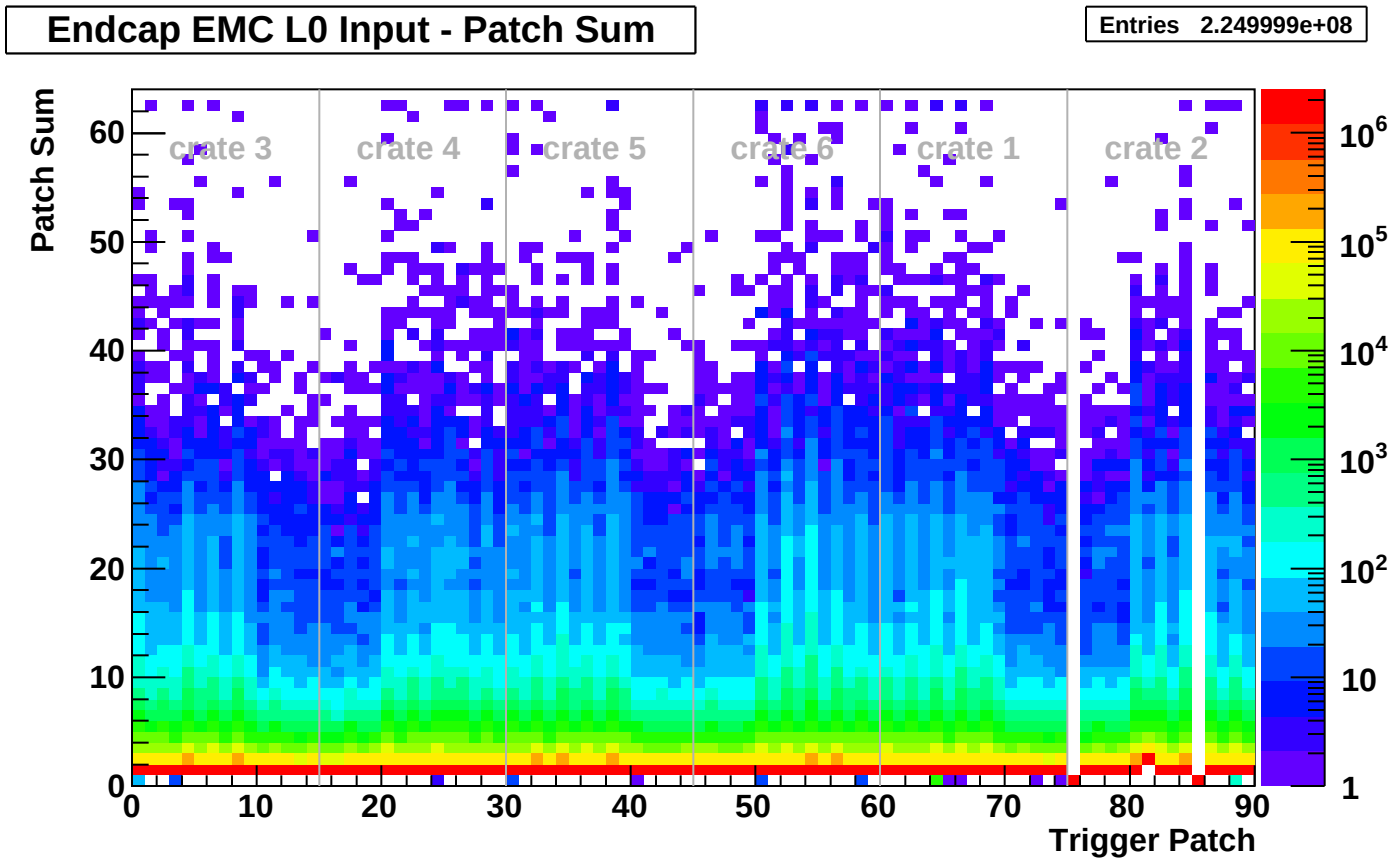

Abort-gaps
Entries 4257

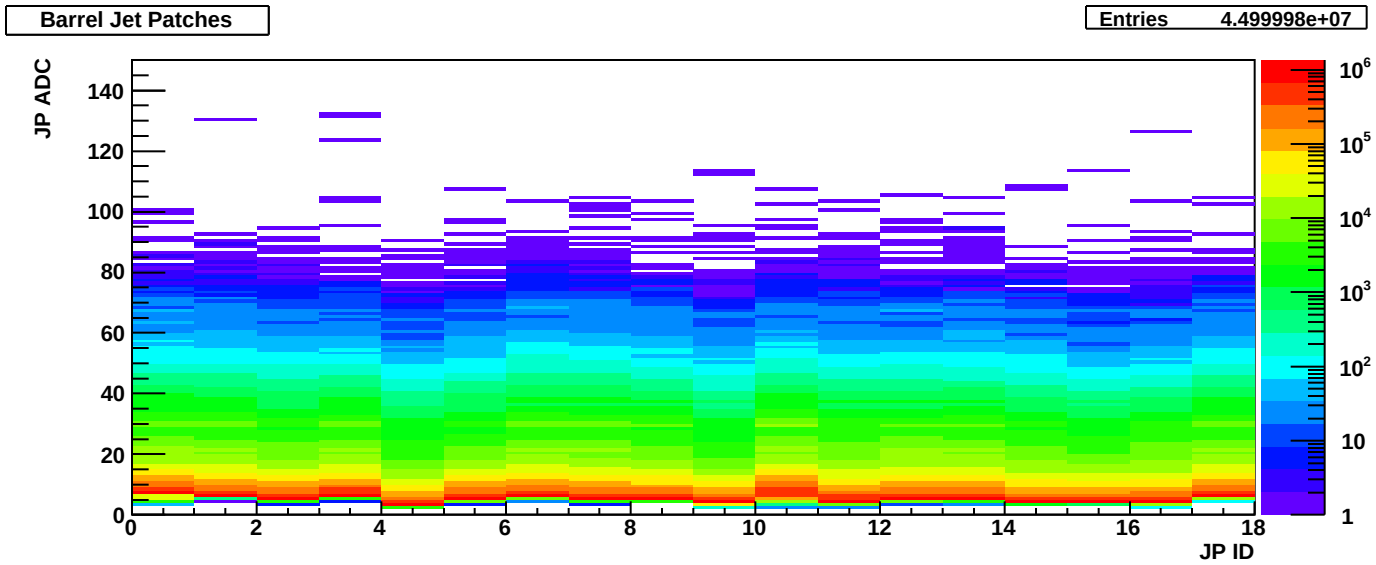

!Yellow-Fill
Entries 85188

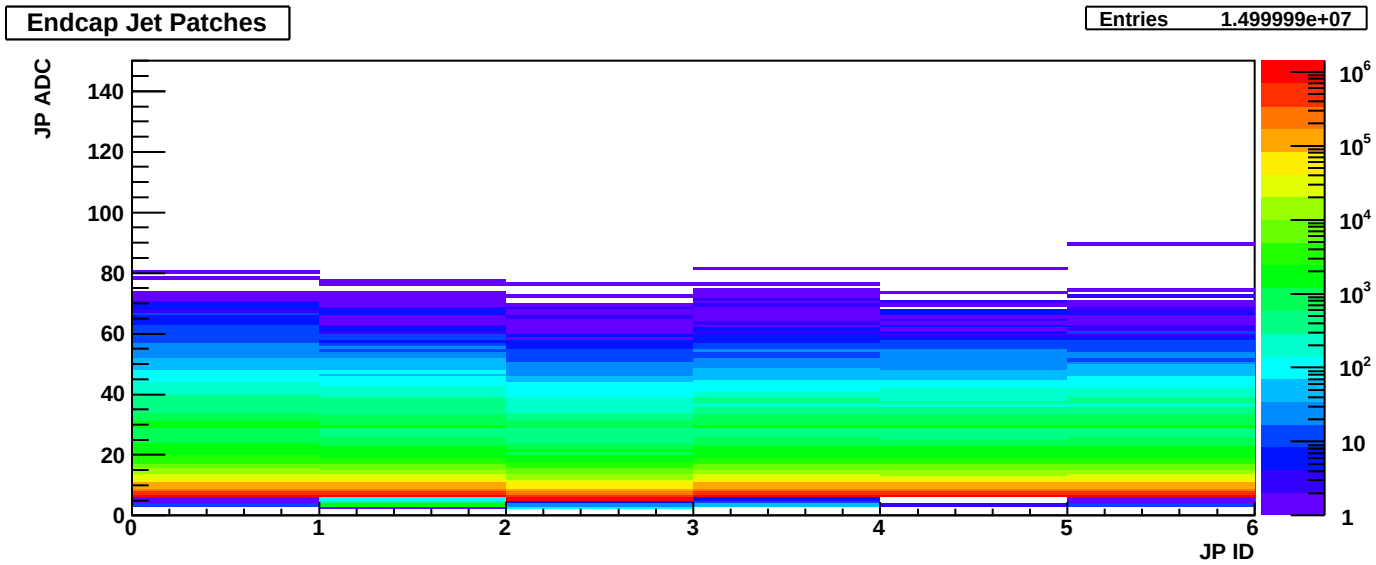

Barrel EMC L0 Input - High Tower
Entries 7.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

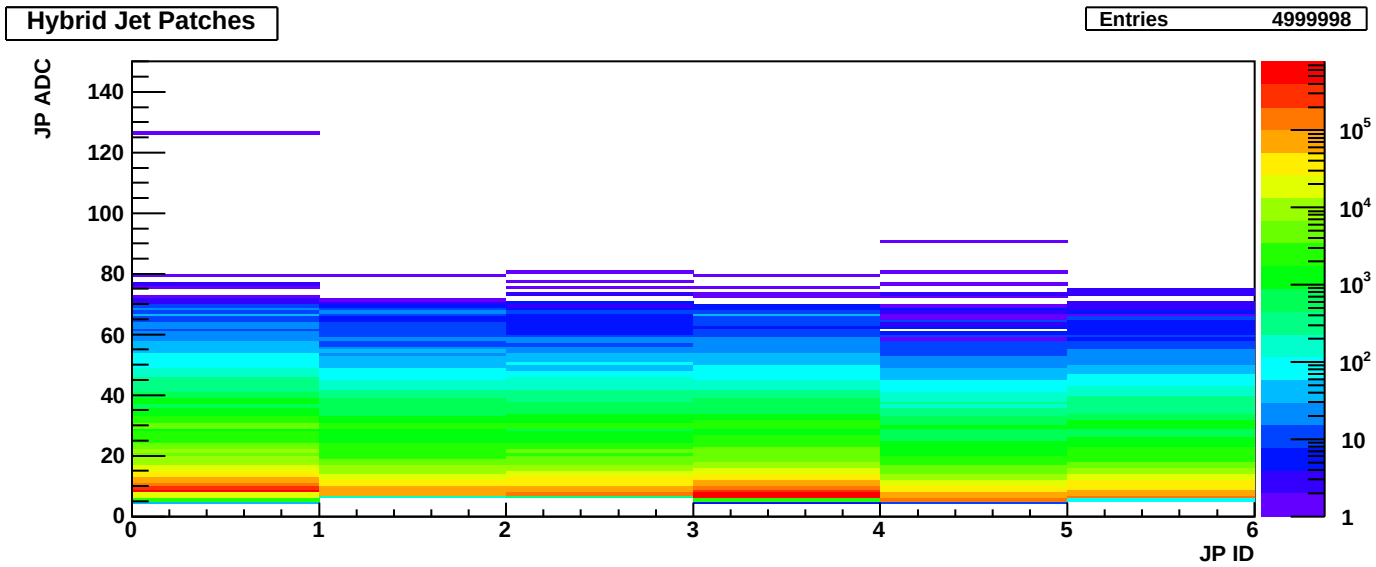

Barrel EMC L0 Input - Patch Sum
Entries 7.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

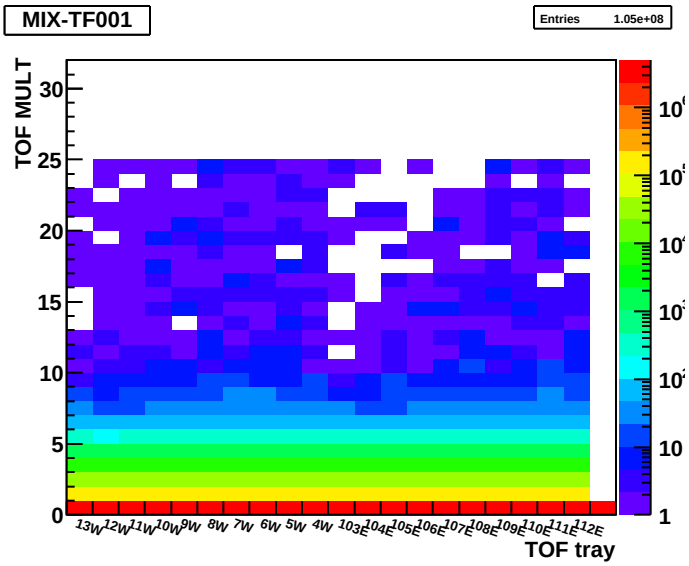

Endcap EMC L0 Input - High Tower
Entries 2.249999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

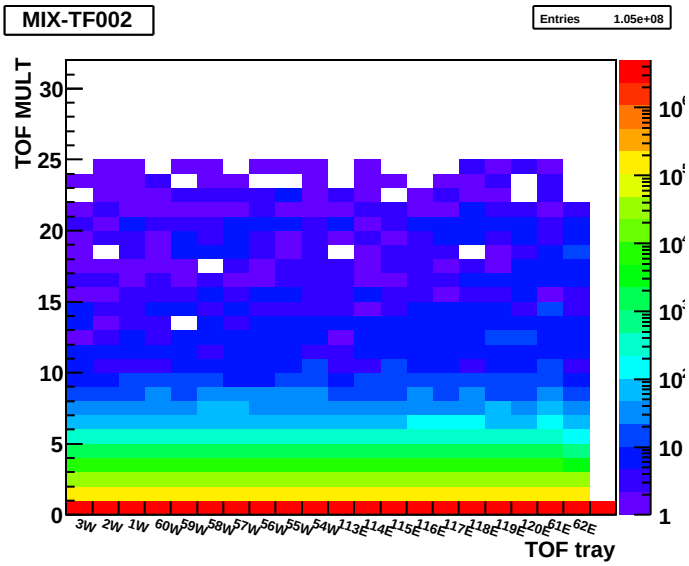

Endcap EMC L0 Input - Patch Sum
Entries 2.249999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

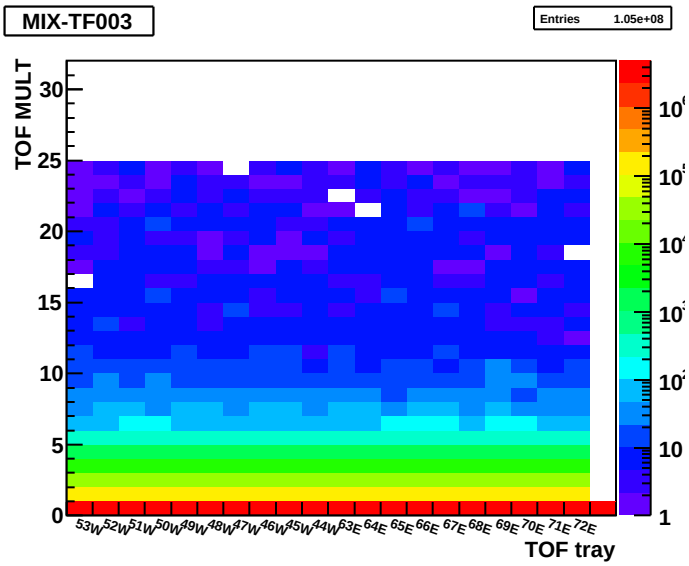

Barrel Jet Patches
Entries 4.499998e+07
JP ADC
JP ID

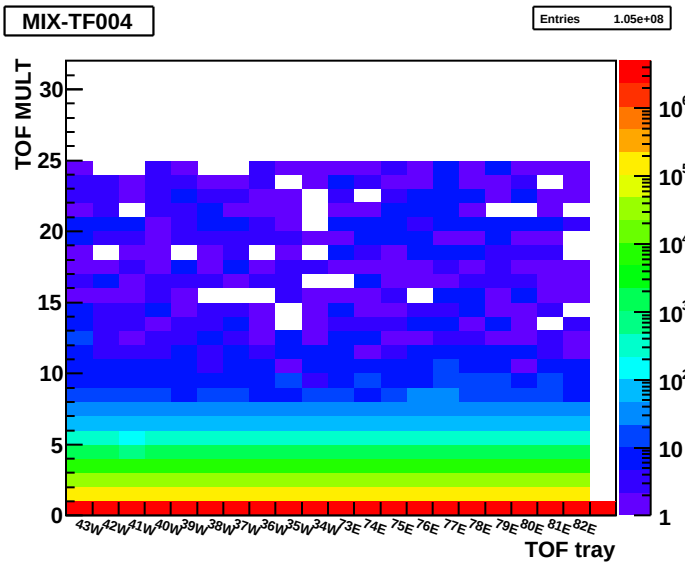

Endcap Jet Patches
Entries 1.499999e+07
JP ADC
JP ID

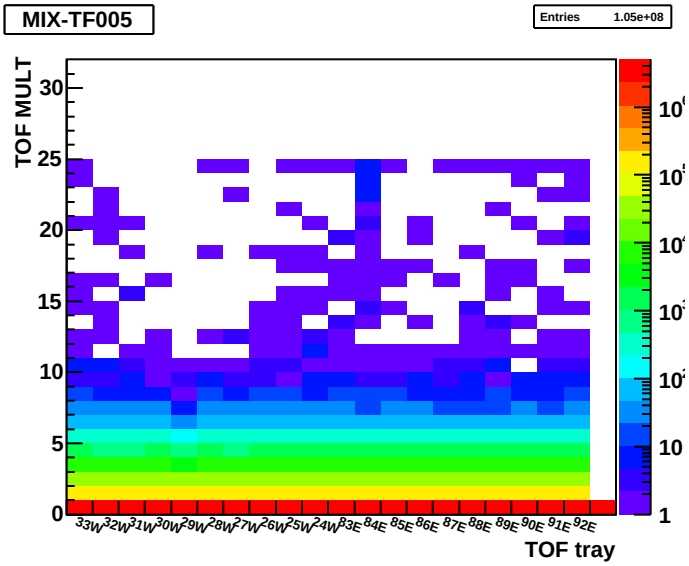

Hybrid Jet Patches
Entries 4999998
JP ADC
JP ID

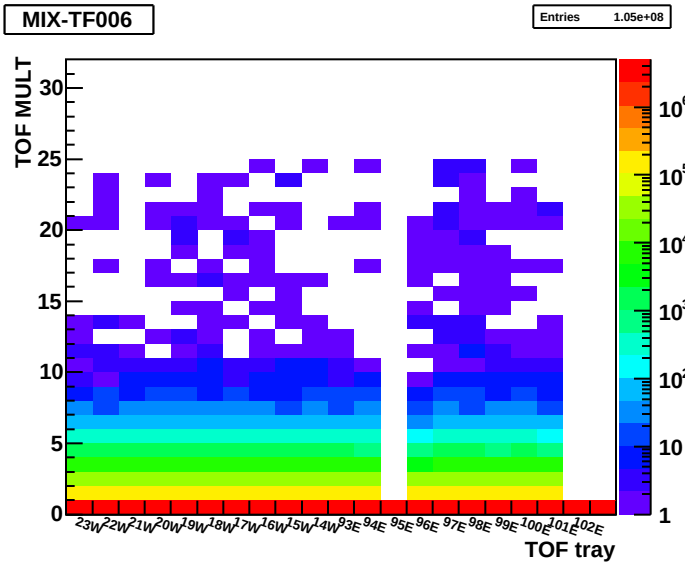

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

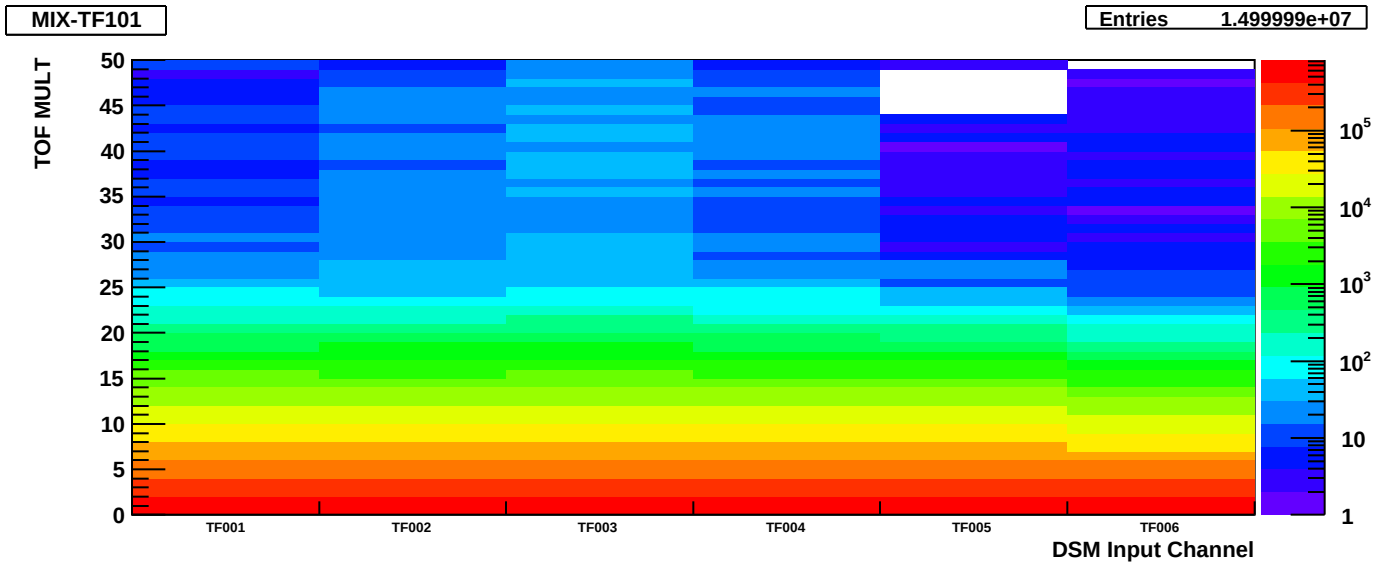

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

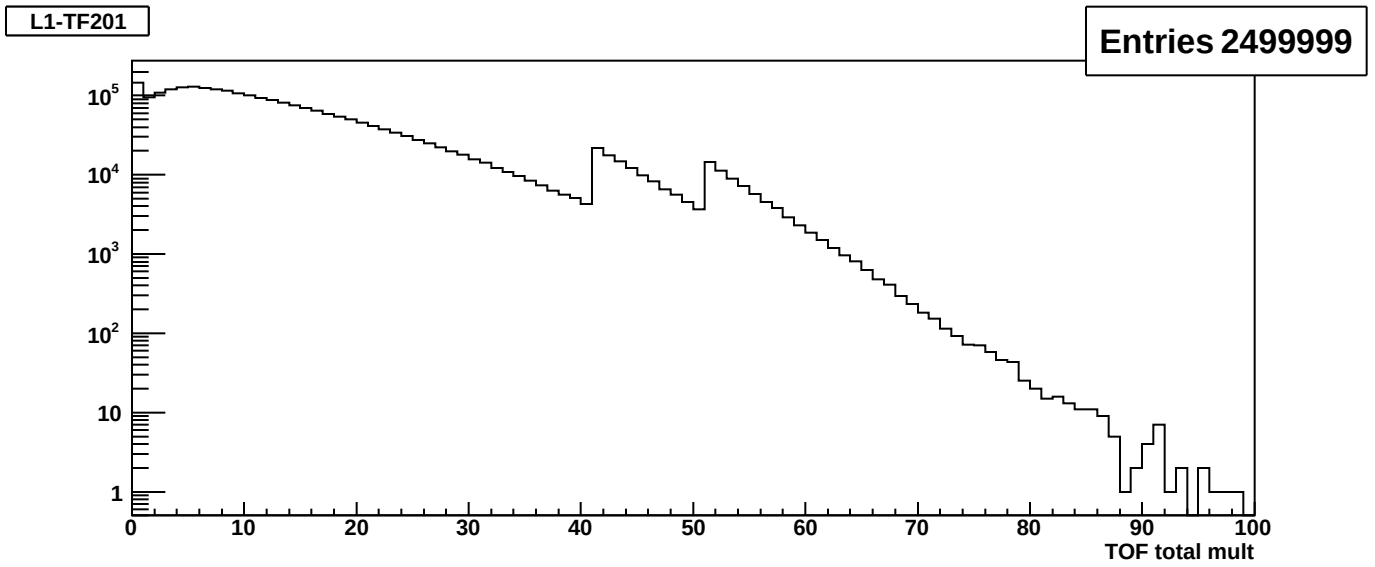

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

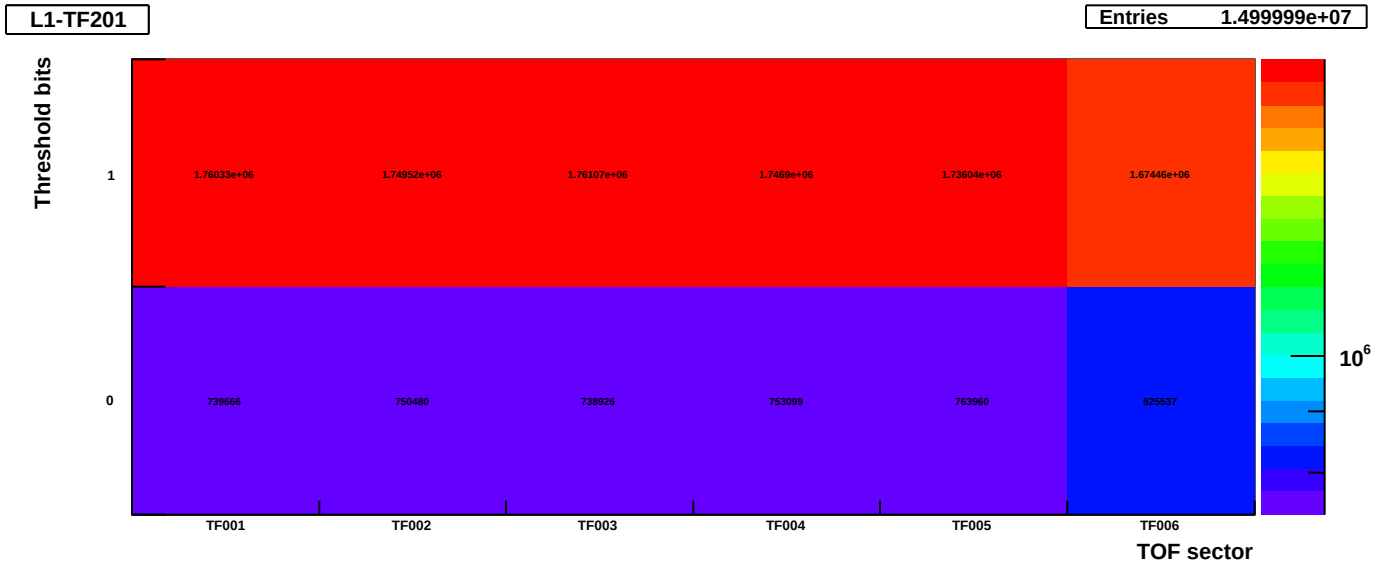

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

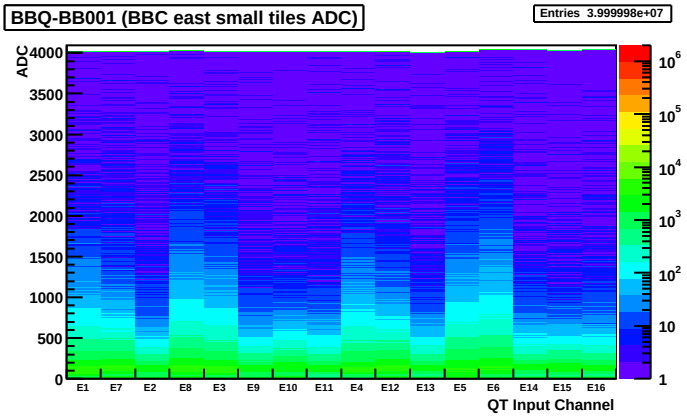

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

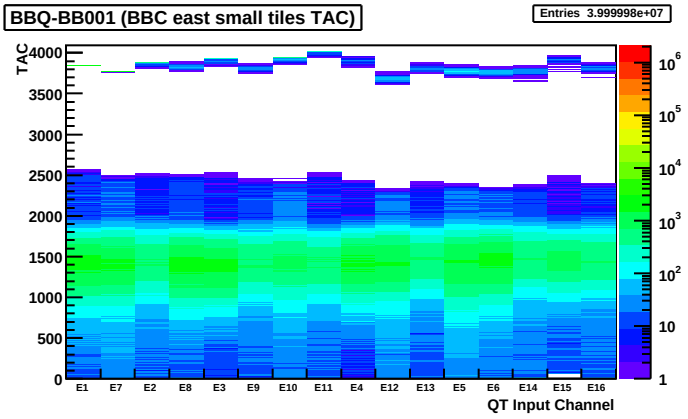

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

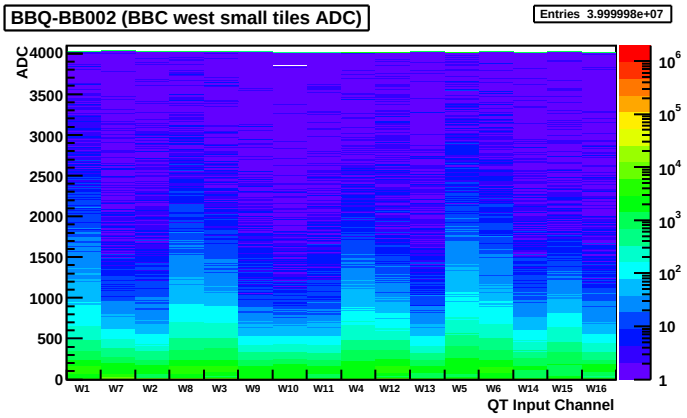

MIX-TF101
Entries 1.499999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

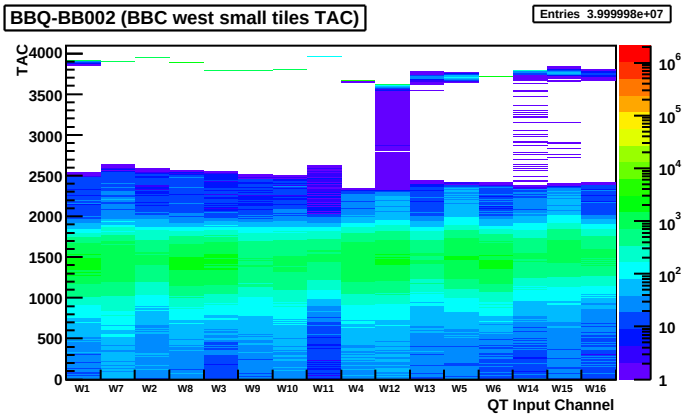

L1-TF201
Entries 2499999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

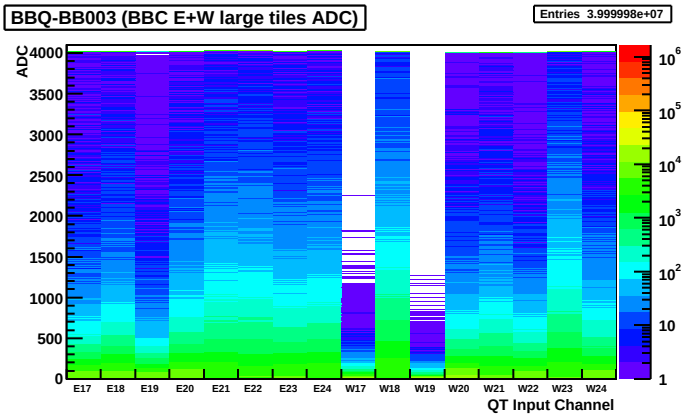

L1-TF201
Entries 1.499999e+07
Threshold bits
1
0
1.76033e+06
1.74952e+06
1.76107e+06
1.7469e+06
1.73604e+06
1.67446e+06
739666
750480
738926
753099
763960
825537
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

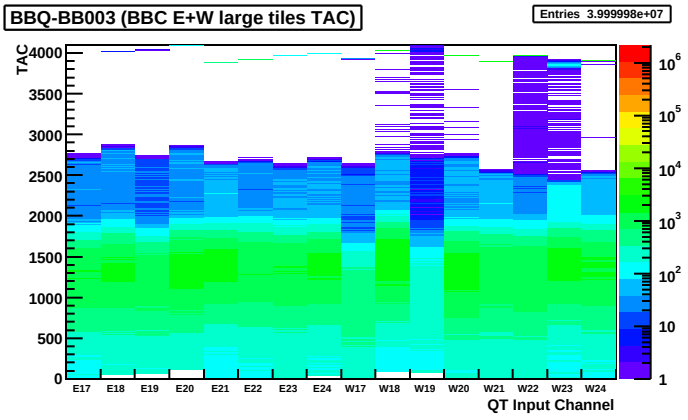

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

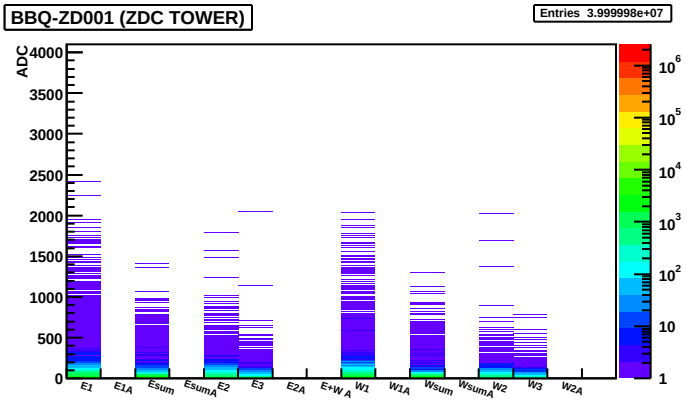

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

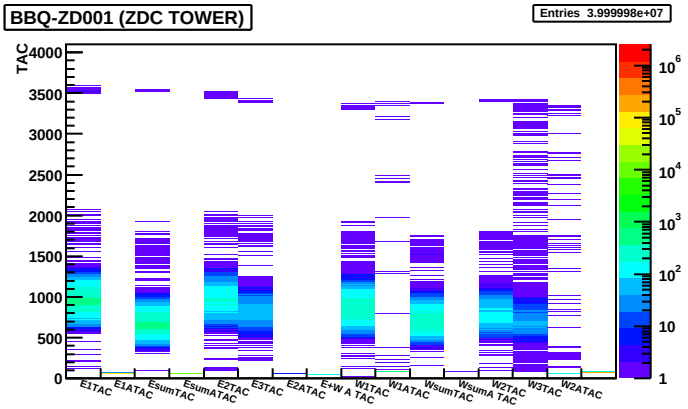

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

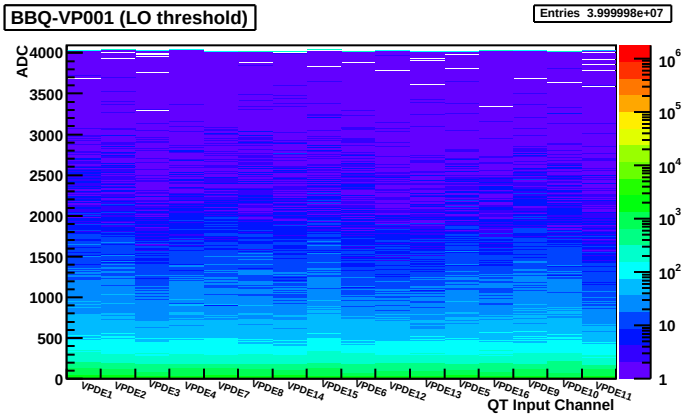

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

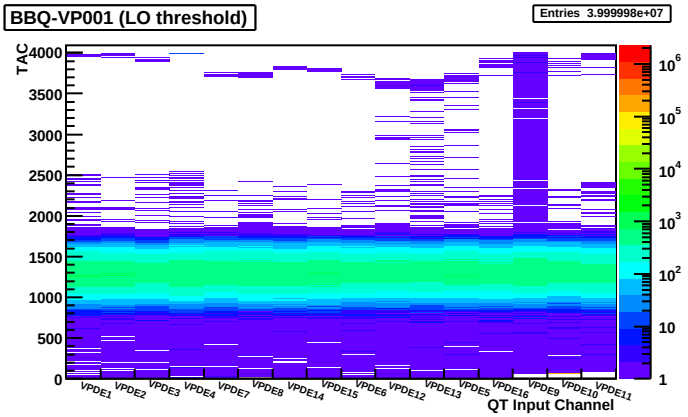

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

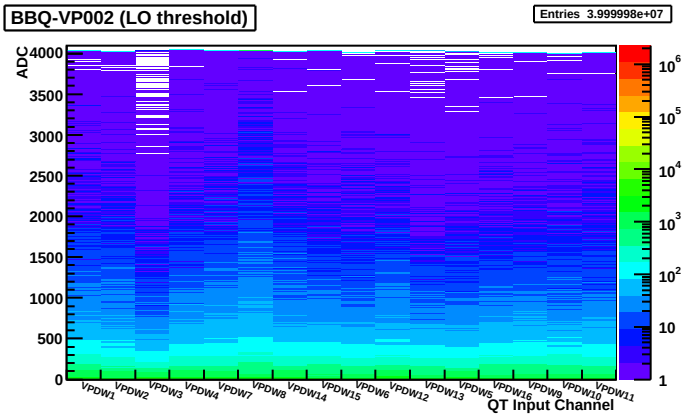

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

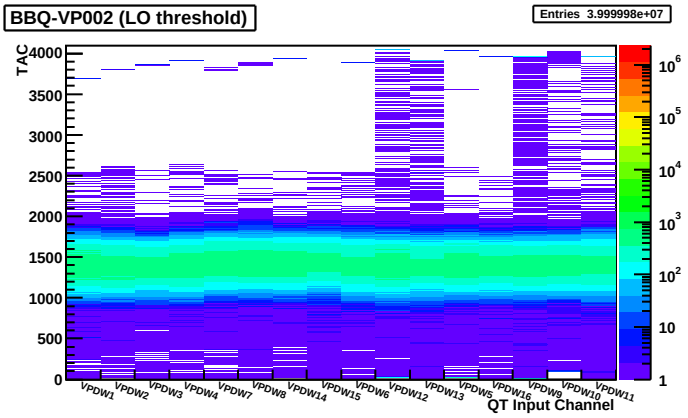

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
ADC

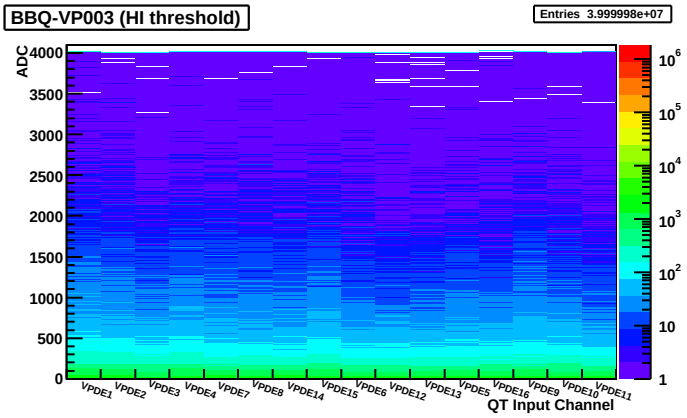

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
TAC

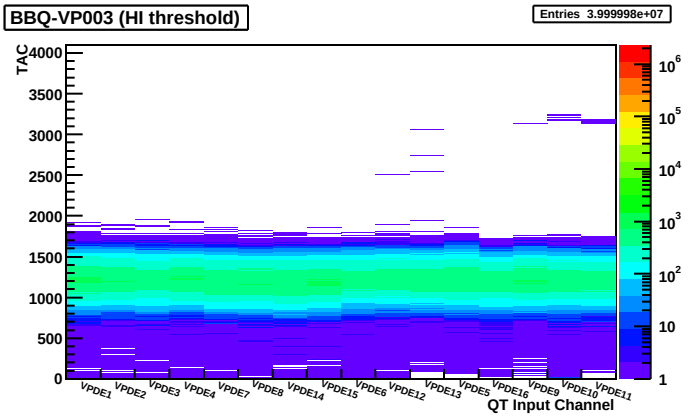

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

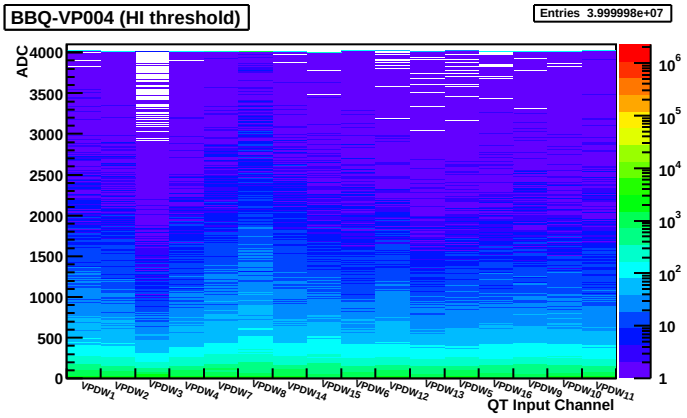

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

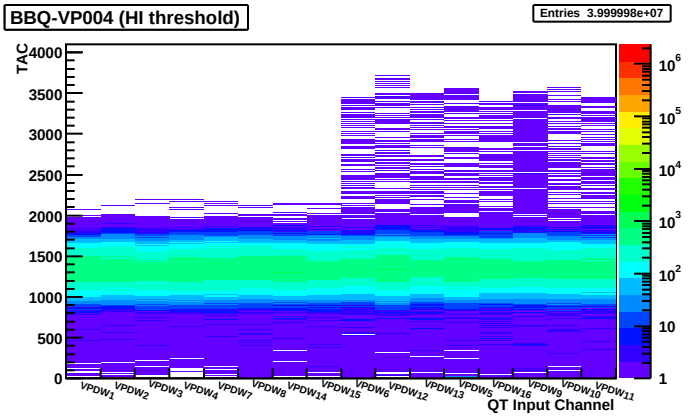

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

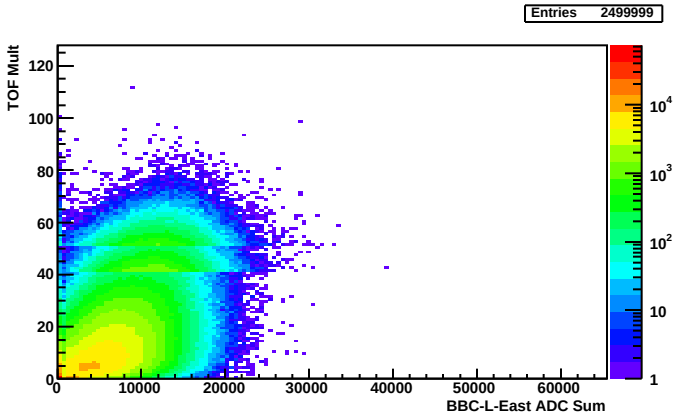

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

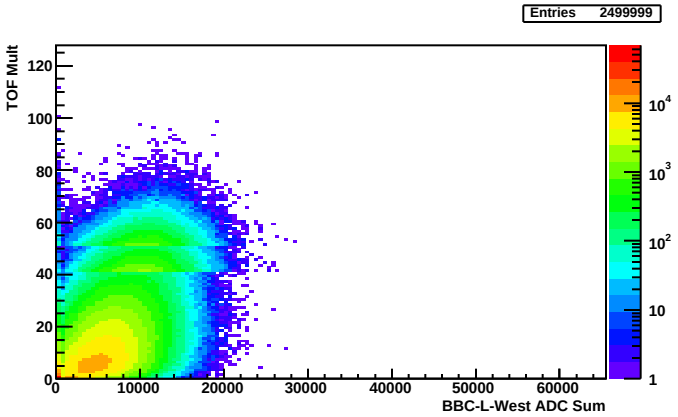

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

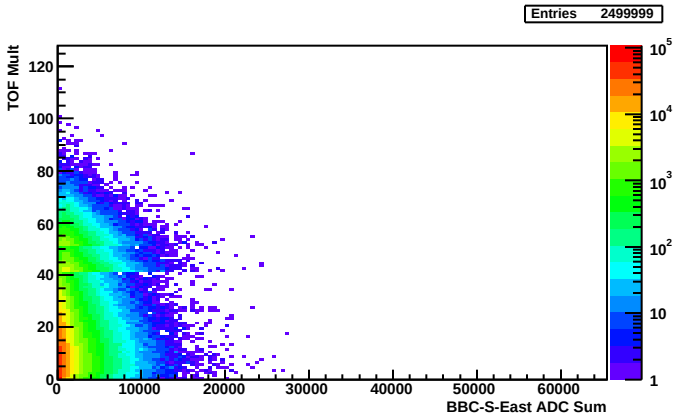

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

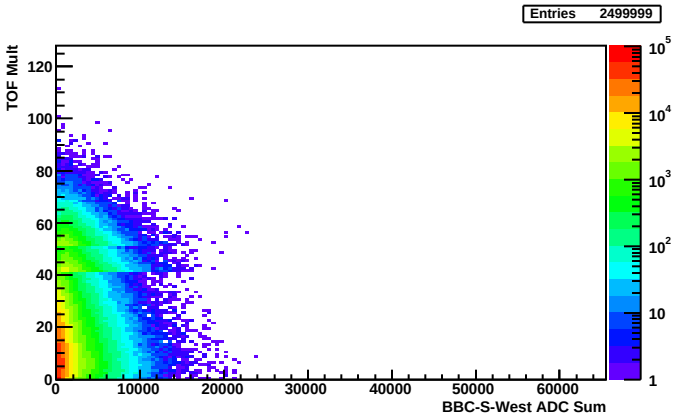

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

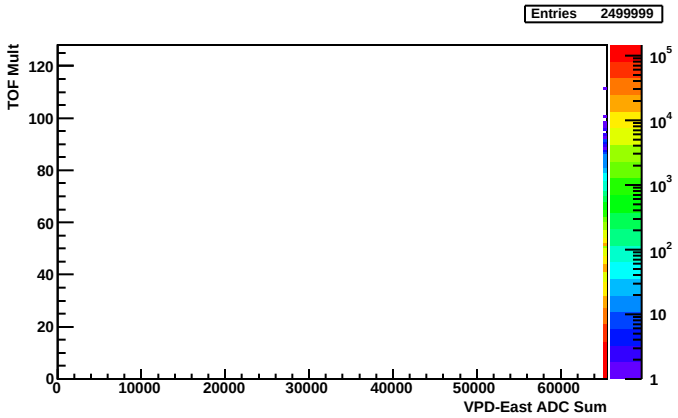

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

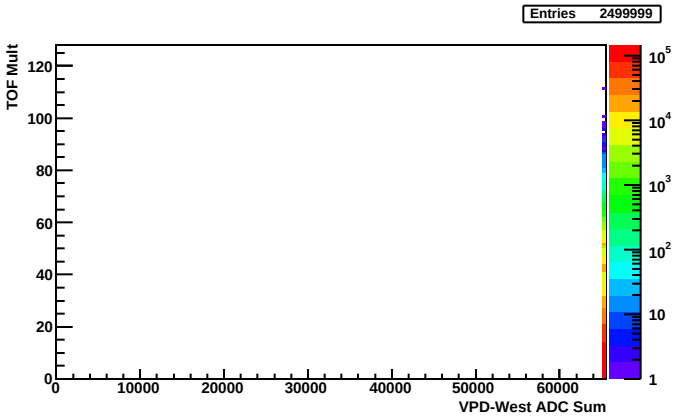

Entries 2499999
TOF Mult
BBC-L-East ADC Sum

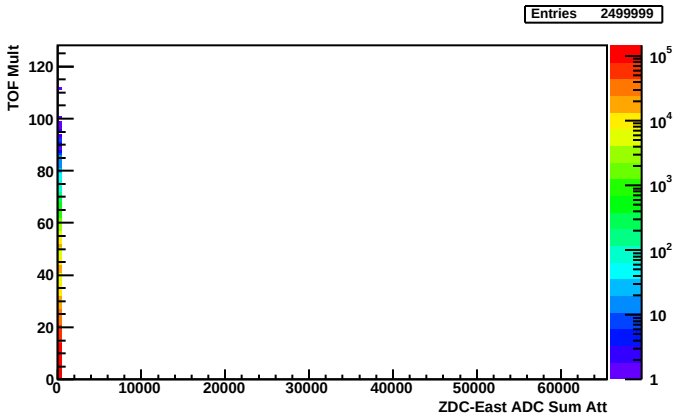

Entries 2499999
TOF Mult
BBC-L-West ADC Sum

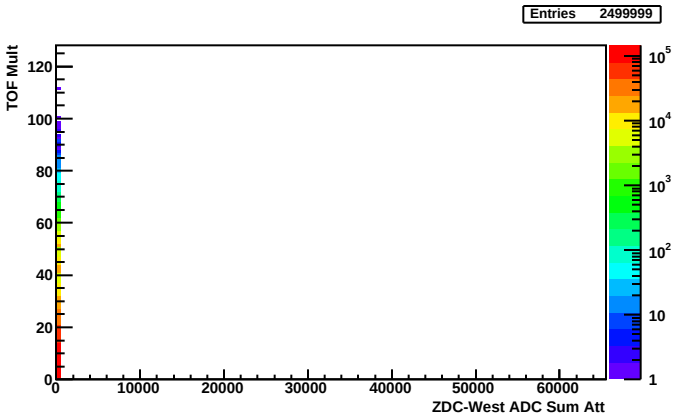

Entries 2499999
TOF Mult
BBC-S-East ADC Sum

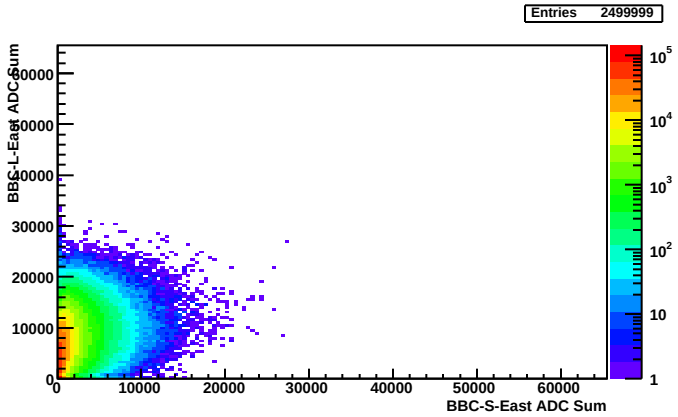

Entries 2499999
TOF Mult
BBC-S-West ADC Sum

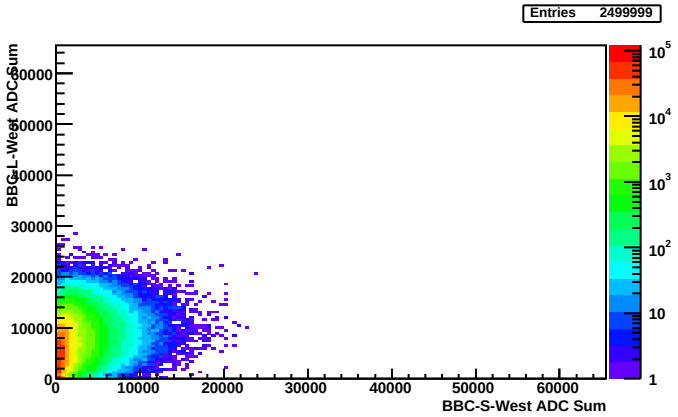

Entries 2499999
TOF Mult
VPD-East ADC Sum

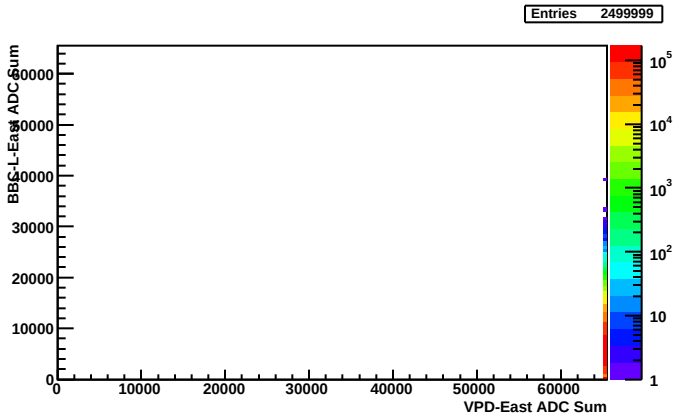

Entries 2499999
TOF Mult
VPD-West ADC Sum

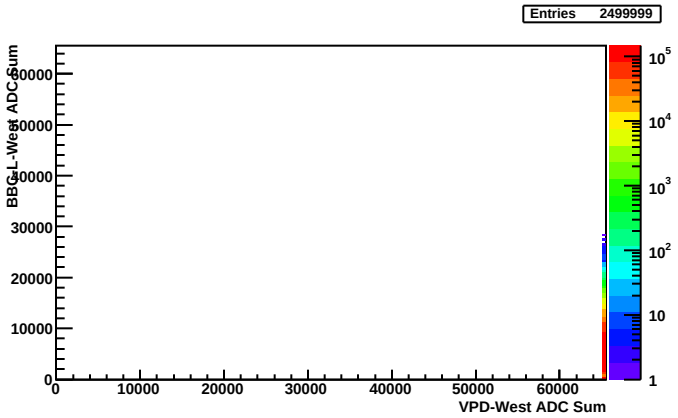

Entries 2499999
TOF Mult
ZDC-East ADC Sum Att

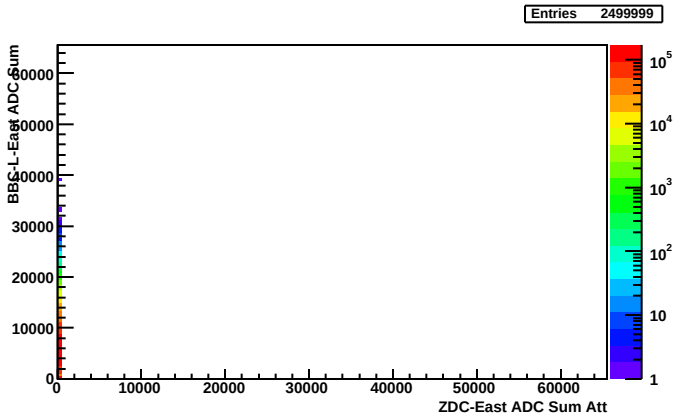

Entries 2499999
TOF Mult
ZDC-West ADC Sum Att

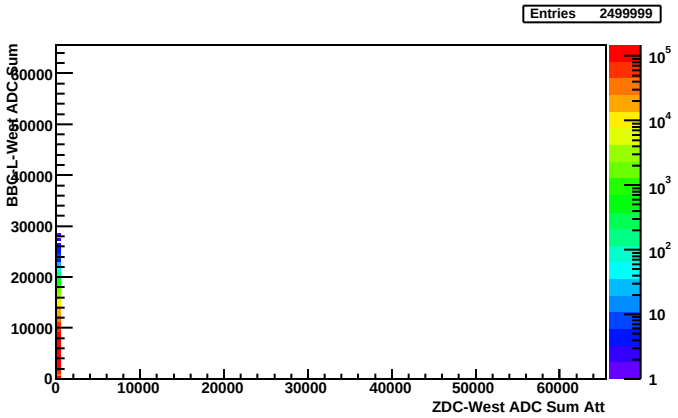

Entries 2499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

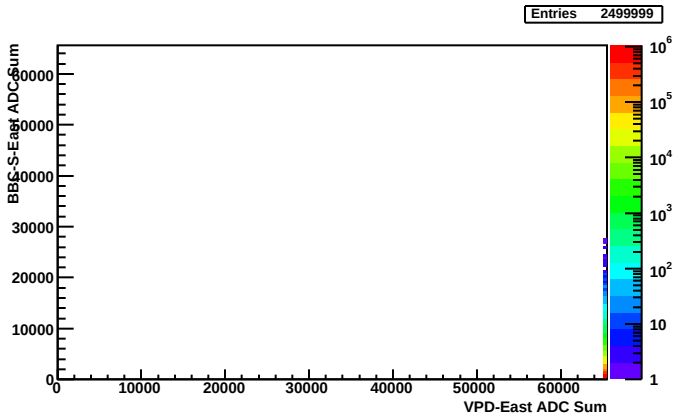

Entries 2499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

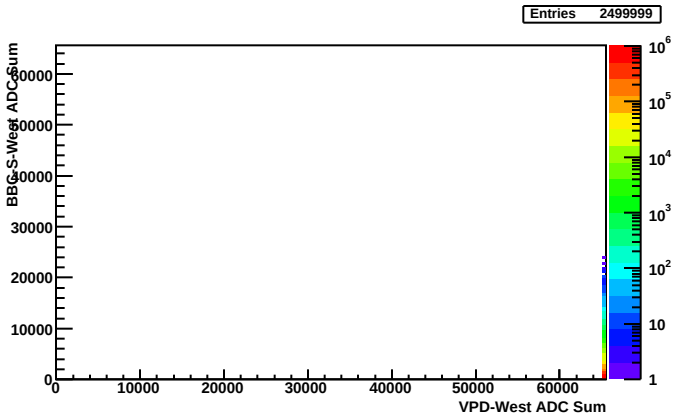

Entries 2499999
BBC-L-East ADC Sum
VPD-East ADC Sum

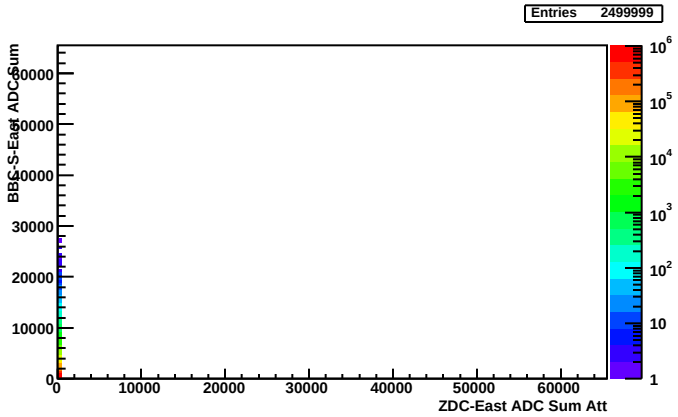

Entries 2499999
BBC-L-West ADC Sum
VPD-West ADC Sum

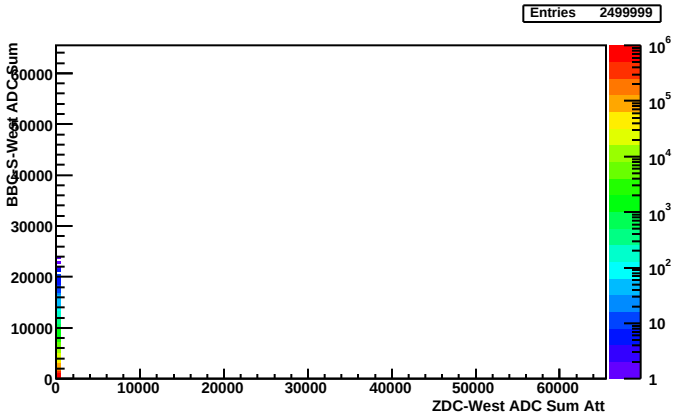

Entries 2499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

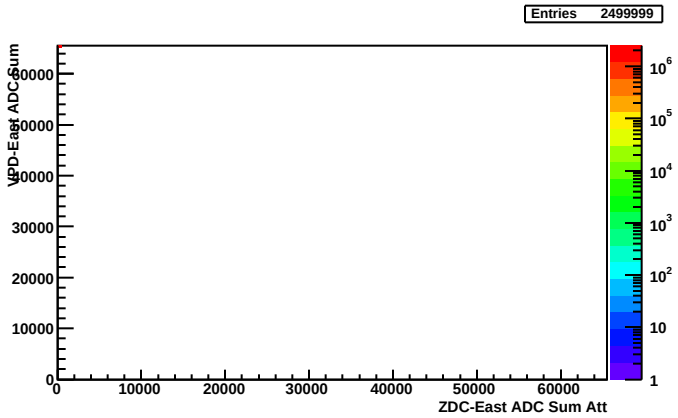

Entries 2499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

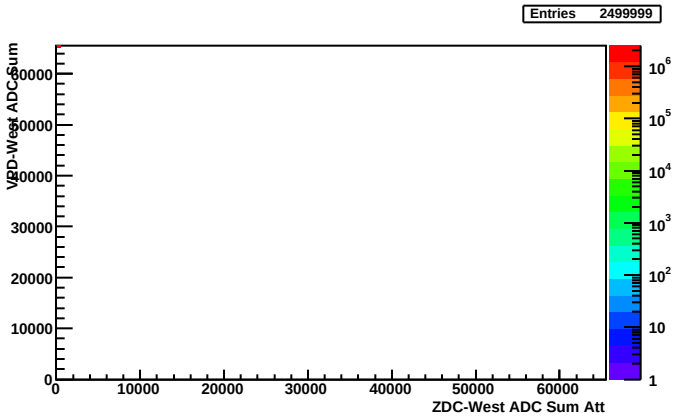

Entries 2499999
BBC-S-East ADC Sum
VPD-East ADC Sum

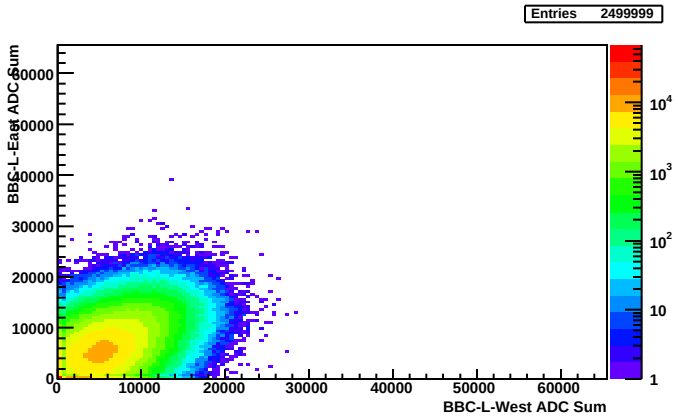

Entries 2499999
BBC-S-West ADC Sum
VPD-West ADC Sum

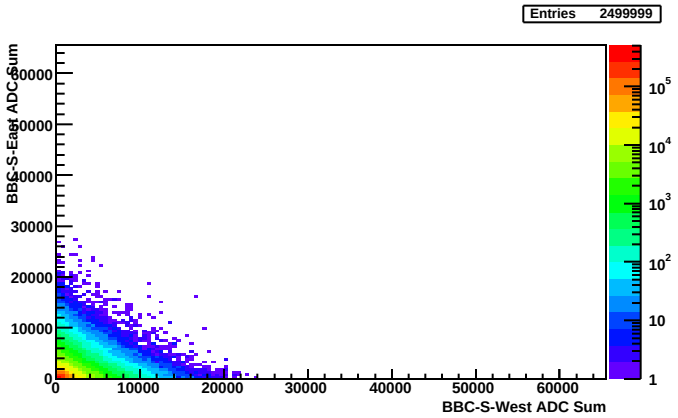

Entries 2499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

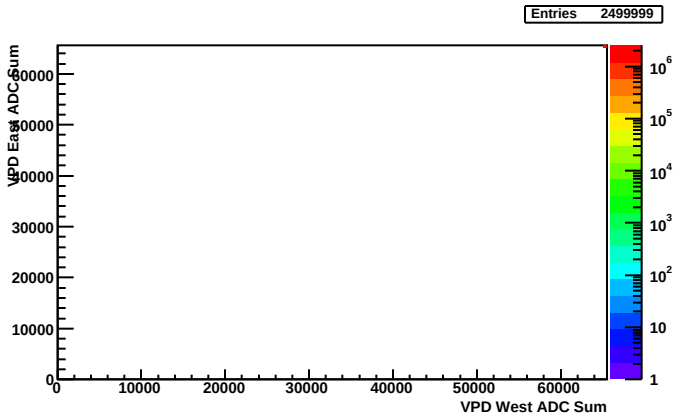

Entries 2499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

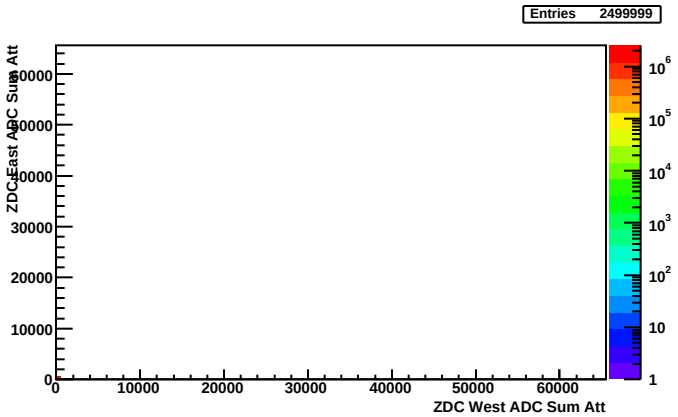

Entries 2499999
VPD-East ADC Sum
ZDC-East ADC Sum Att

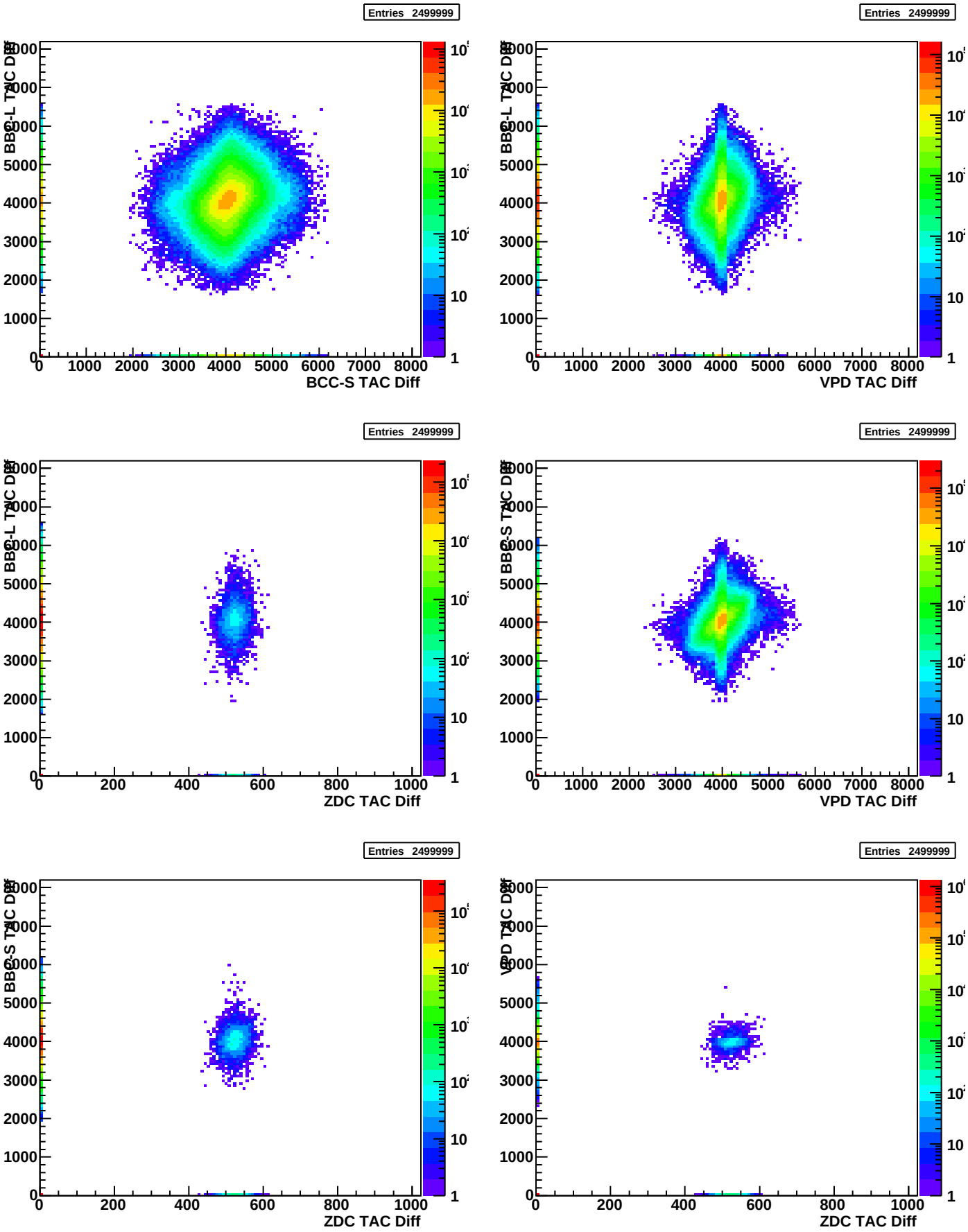

Entries 2499999
VPD-West ADC Sum
ZDC-West ADC Sum Att

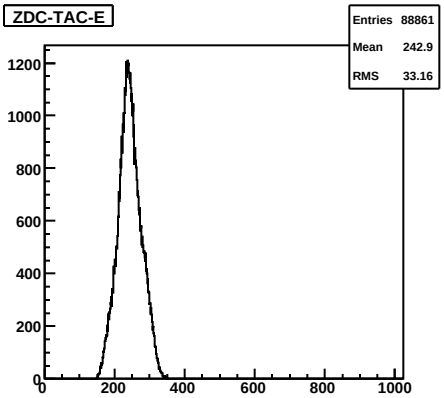

Entries 2499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

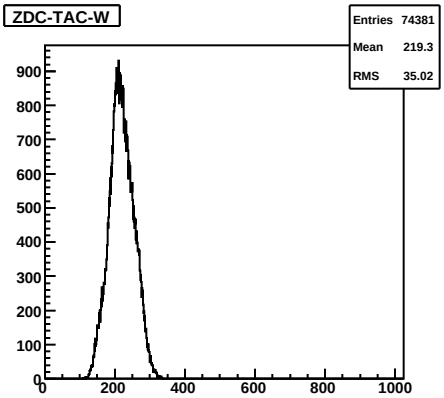

Entries 2499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

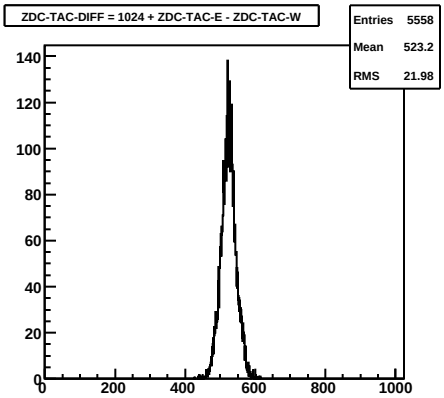

Entries 2499999
VPD East ADC Sum
VPD West ADC Sum

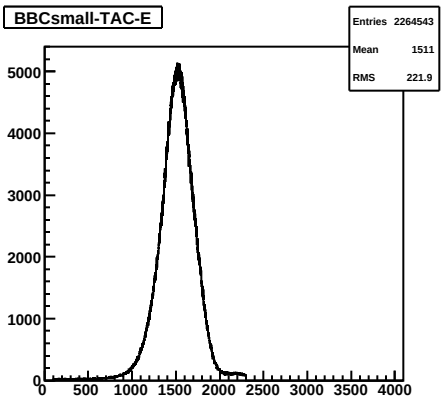

Entries 2499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

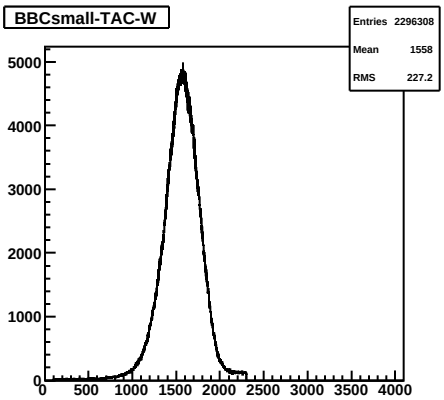

Entries 2499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2499999
BBC-L TAC Diff
VPD TAC Diff
Entries 2499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2499999
BBC-S TAC Diff
VPD TAC Diff
Entries 2499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2499999
VPD TAC Diff
ZDC TAC Diff

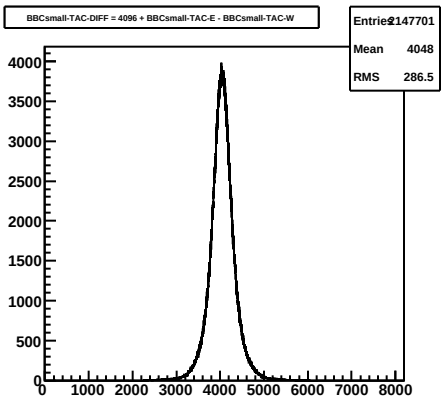

ZDC-TAC-E
Entries 88861
Mean 242.9
RMS 33.16

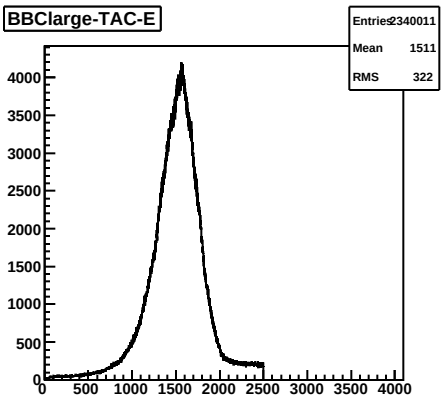

ZDC-TAC-W
Entries 74381
Mean 219.3
RMS 35.02

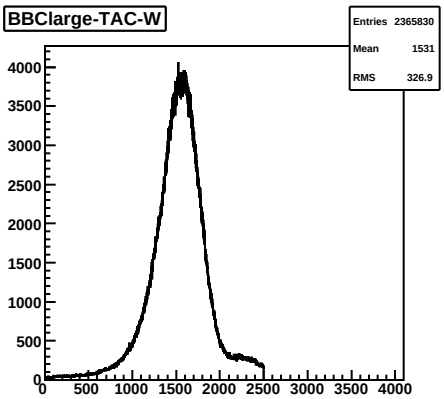

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5558
Mean 523.2
RMS 21.98

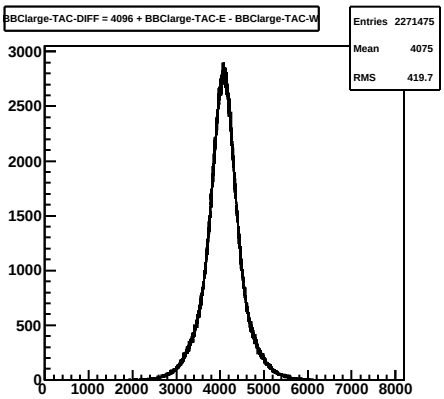

BBCsmall-TAC-E
Entries 2264543
Mean 1511
RMS 221.9

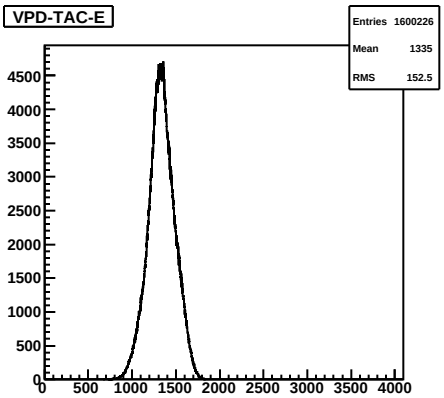

BBCsmall-TAC-W
Entries 2296308
Mean 1558
RMS 227.2

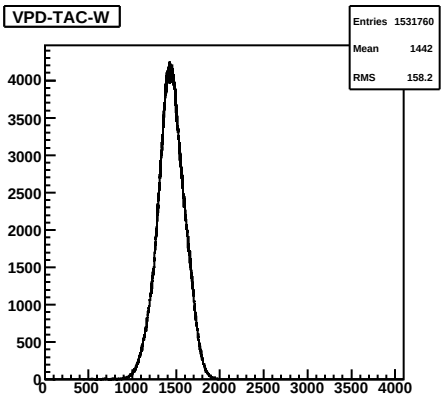

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2147701
Mean 4048
RMS 286.5

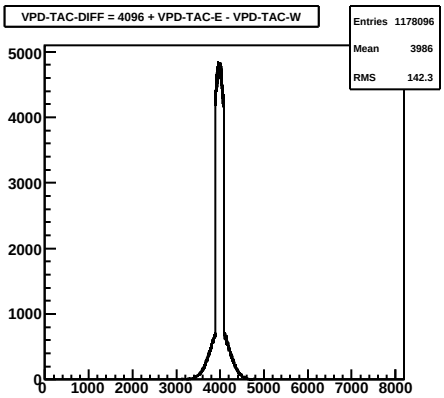

BBClarge-TAC-E
Entries 2340011
Mean 1511
RMS 322

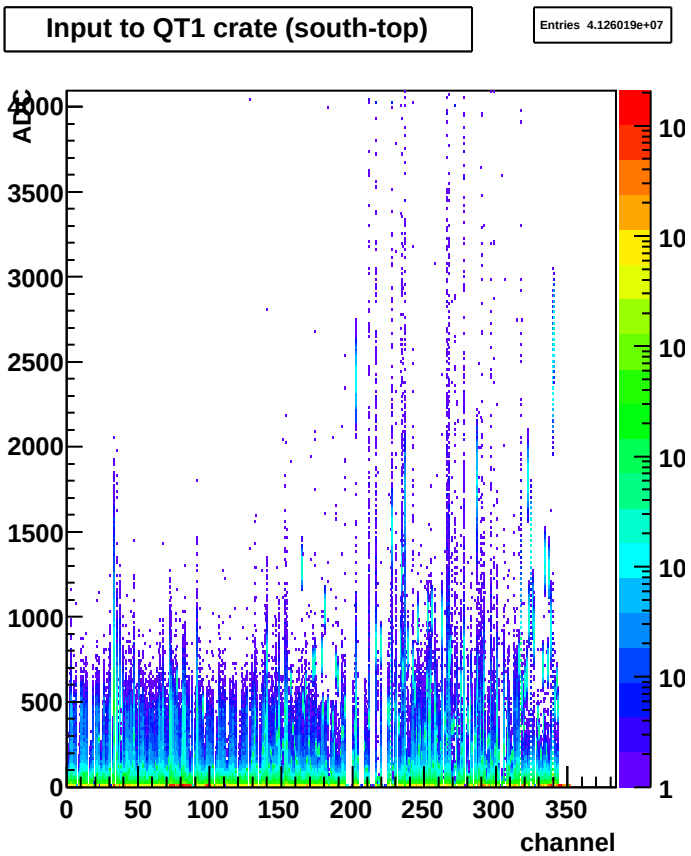

BBClarge-TAC-W
Entries 2365830
Mean 1531
RMS 326.9

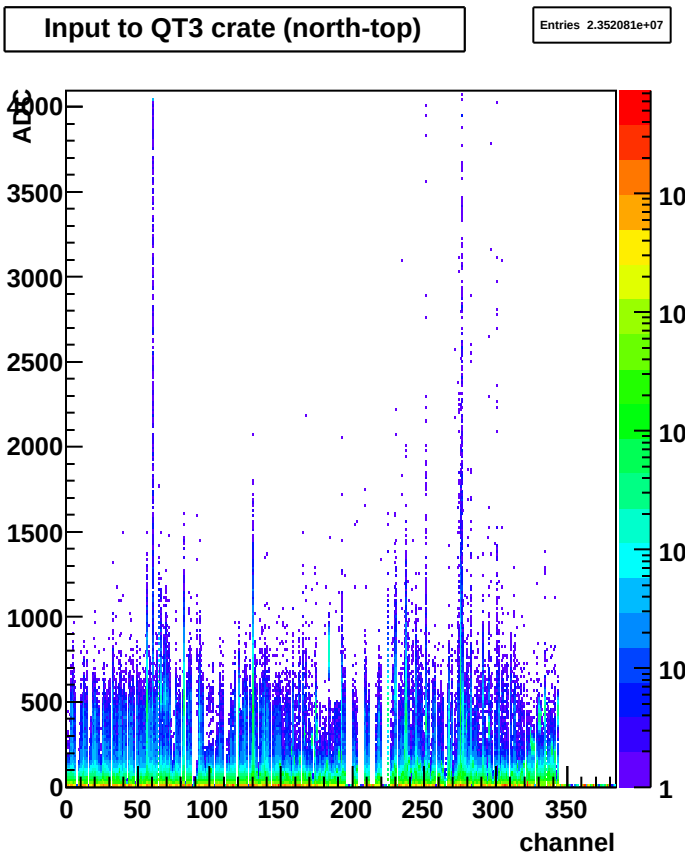

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2271475
Mean 4075
RMS 419.7

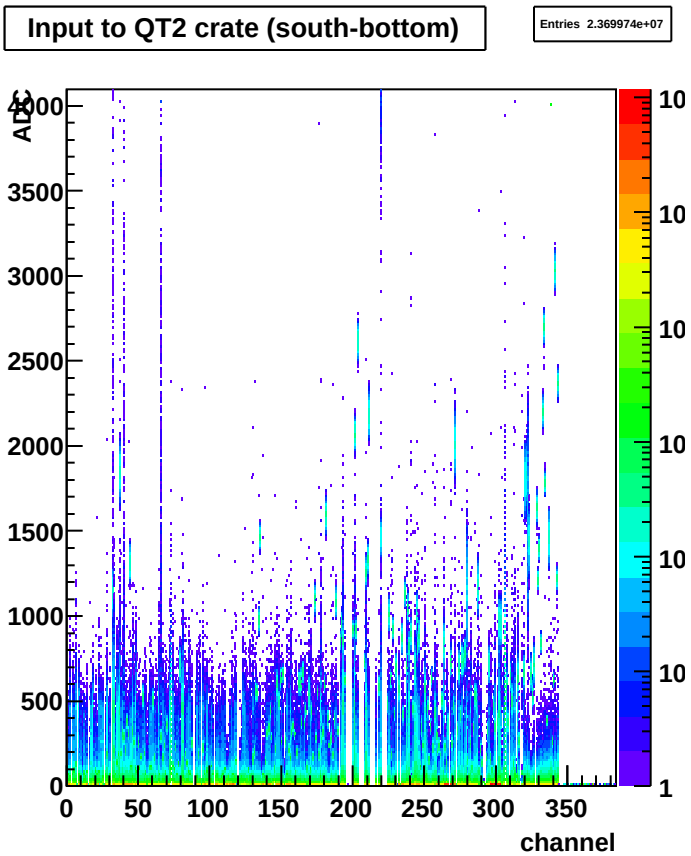

VPD-TAC-E
Entries 1600226
Mean 1335
RMS 152.5

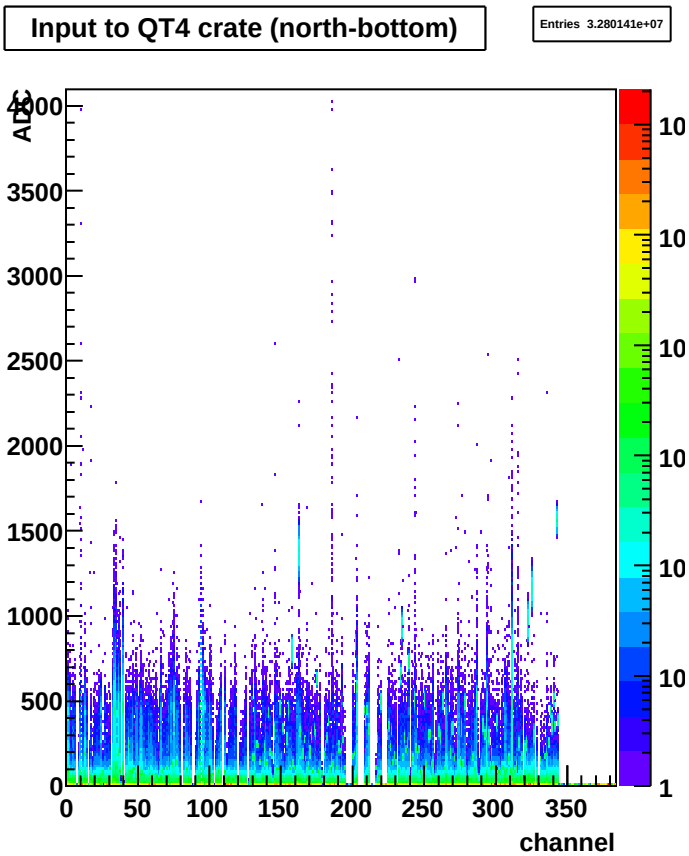

VPD-TAC-W
Entries 1531760
Mean 1442
RMS 158.2

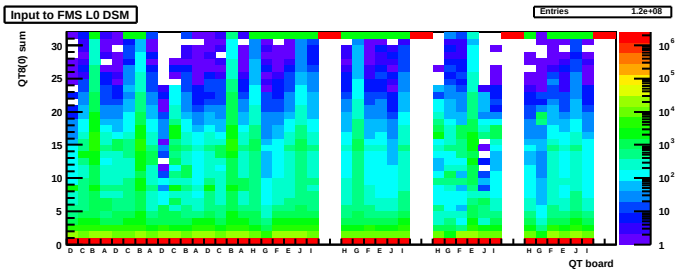

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1178096
Mean 3986
RMS 142.3

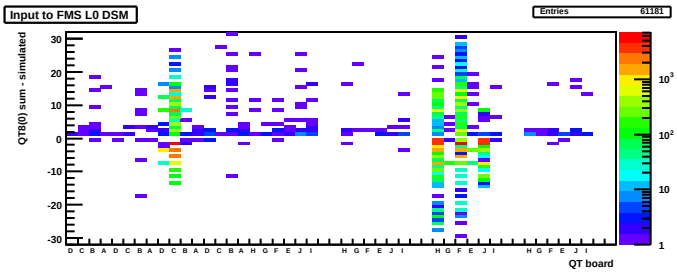

Input to QT1 crate (south-top)
Entries 4.126019e+07
ADC
channel

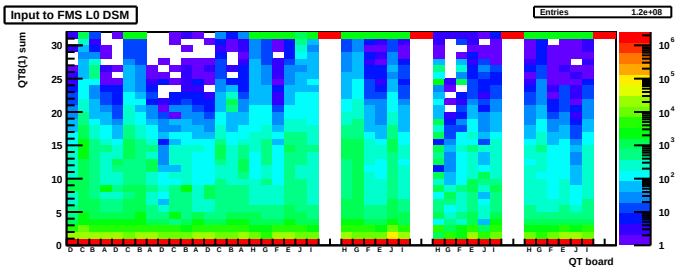

Input to QT3 crate (north-top)
Entries 2.352081e+07
ADC
channel

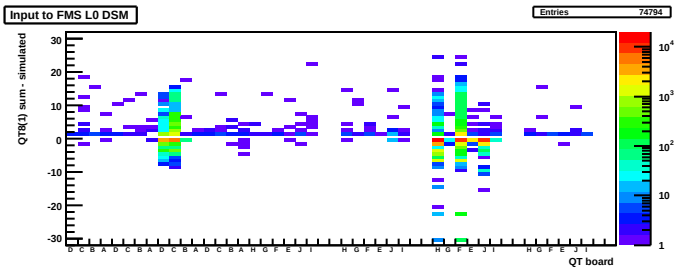

Input to QT2 crate (south-bottom)
Entries 2.369974e+07
ADC
channel

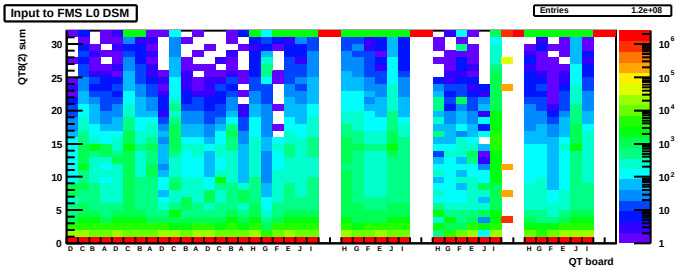

Input to QT4 crate (north-bottom)
Entries 3.280141e+07
ADC
channel

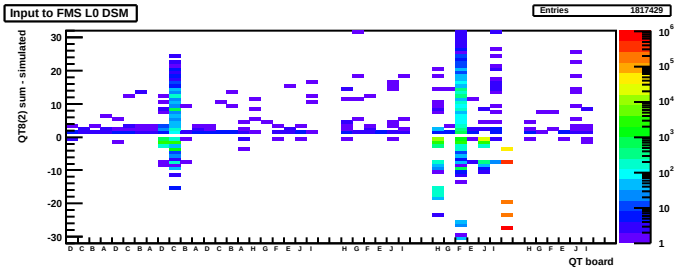

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

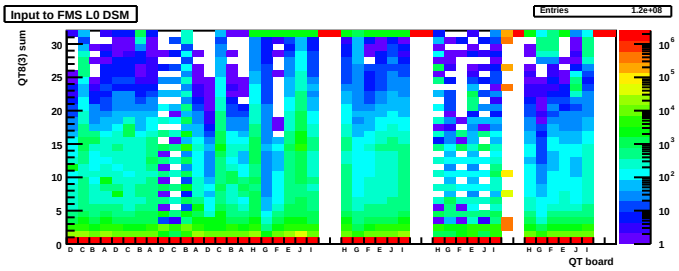

Input to FMS L0 DSM
Entries 61181
QT8(0) sum - simulated
QT board

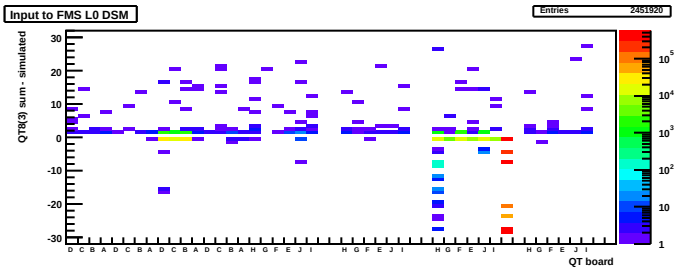

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

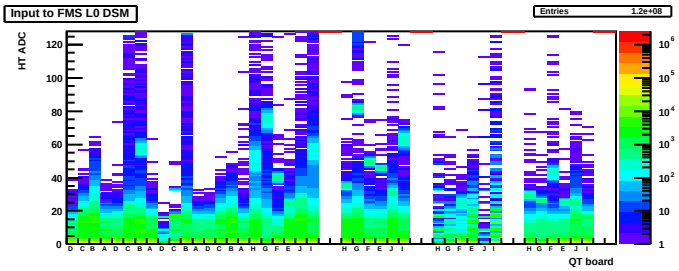

Input to FMS L0 DSM
Entries 74794
QT8(1) sum - simulated
QT board

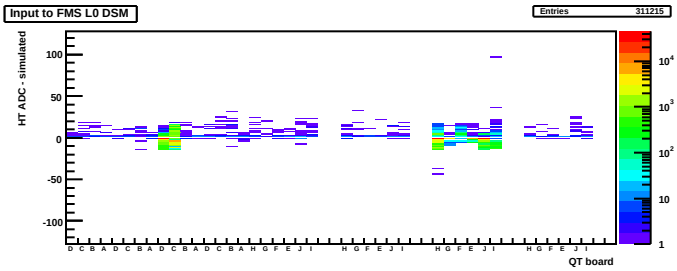

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

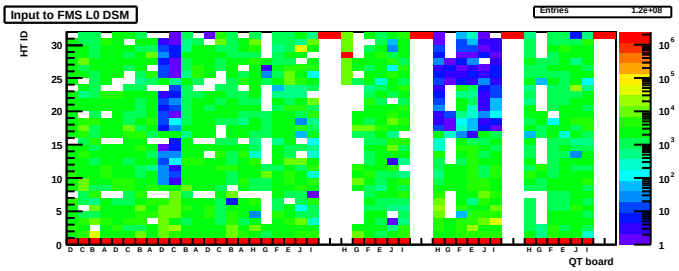

Input to FMS L0 DSM
Entries 1817429
QT8(2) sum - simulated
QT board

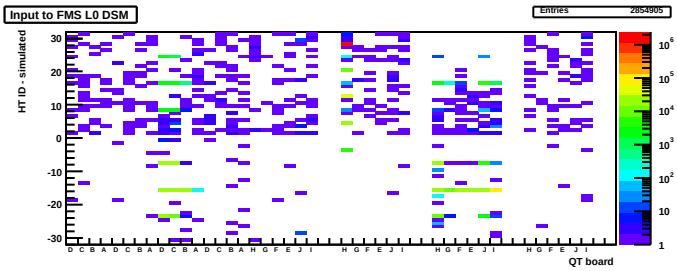

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

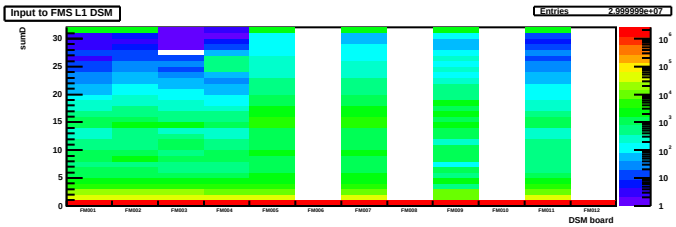

Input to FMS L0 DSM
Entries 2451920
QT8(3) sum - simulated
QT board

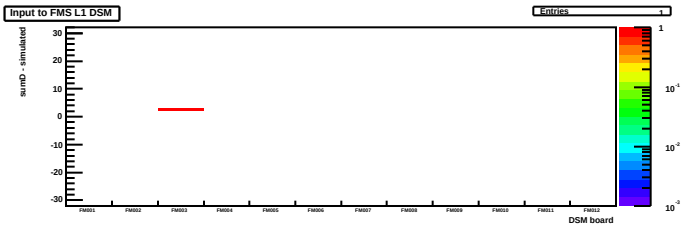

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

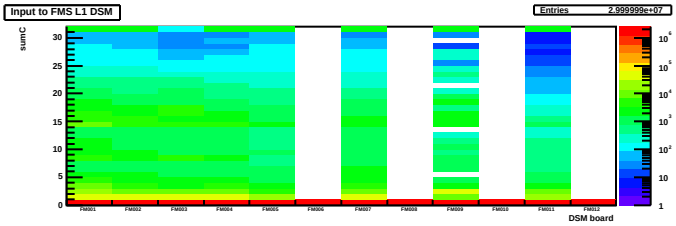

Input to FMS L0 DSM
Entries 311215
HT ADC - simulated
QT board

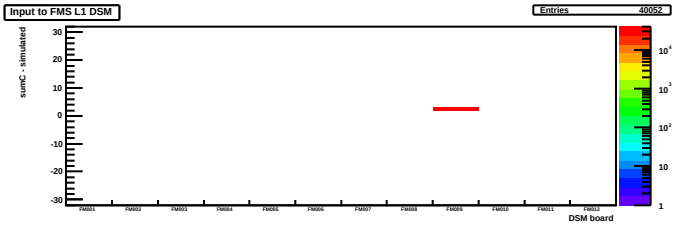

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

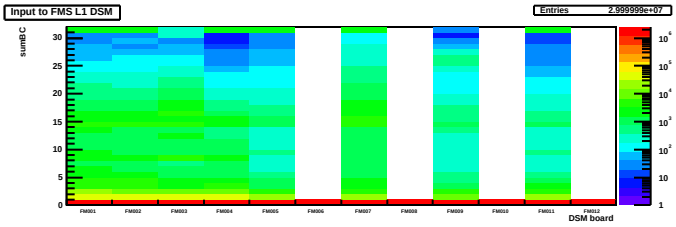

Input to FMS L0 DSM
Entries 2854905
HT ID - simulated
QT board

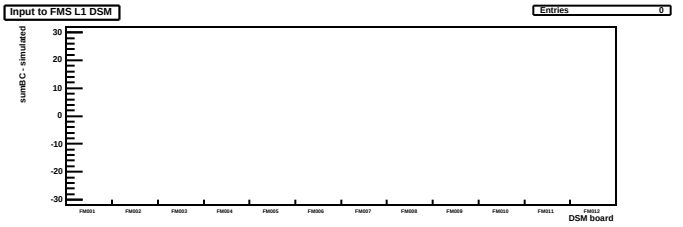

Input to FMS L1 DSM
Entries 2.999999e+07
sumD
DSM board

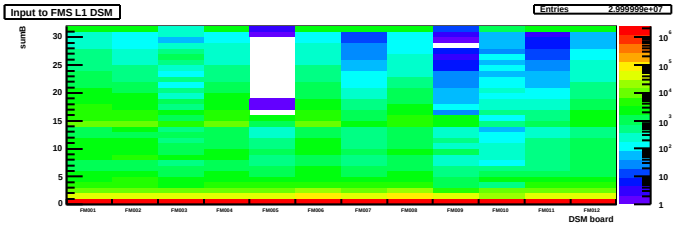

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

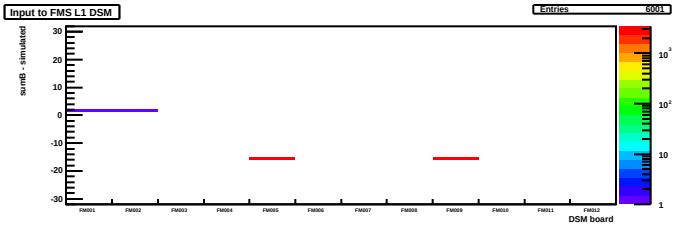

Input to FMS L1 DSM
Entries 2.999999e+07
sumC
DSM board

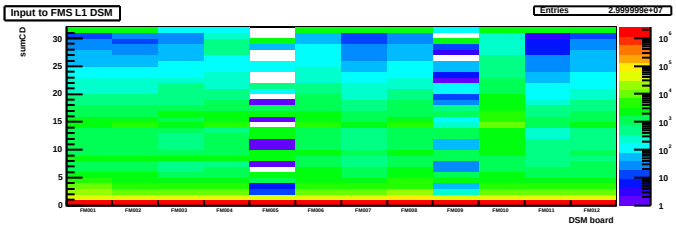

Input to FMS L1 DSM
Entries 40052
sumC - simulated
DSM board

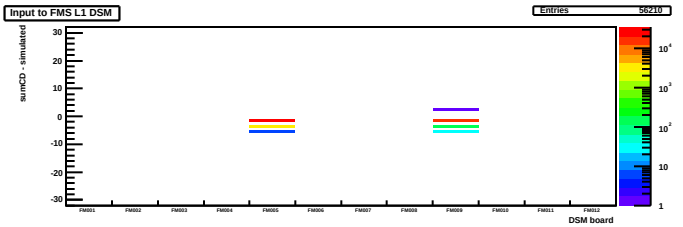

Input to FMS L1 DSM
Entries 2.999999e+07
sumBC
DSM board

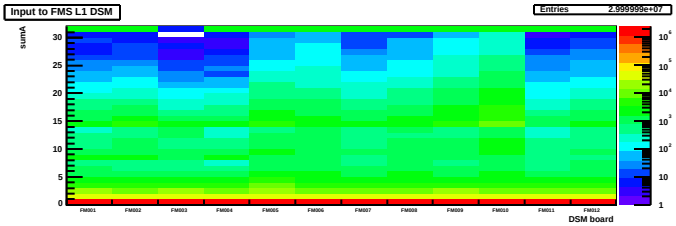

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

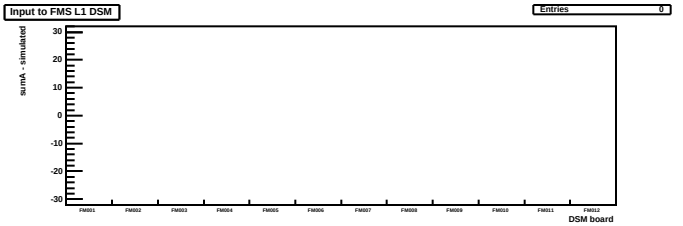

Input to FMS L1 DSM
Entries 2.999999e+07
sumB
DSM board

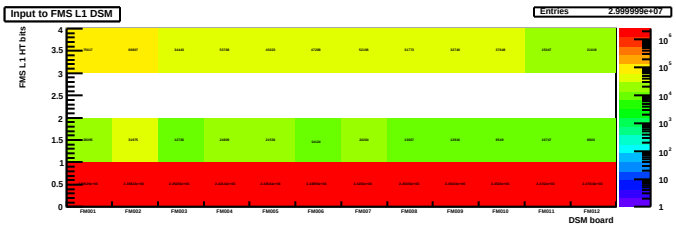

Input to FMS L1 DSM
Entries 6001
sumB - simulated
DSM board

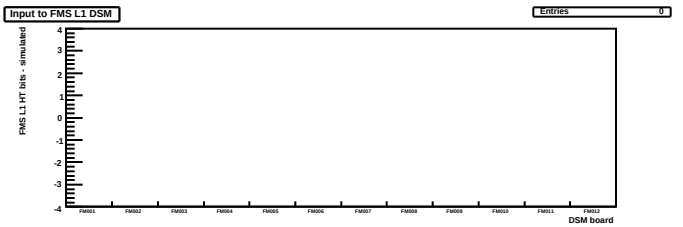

Input to FMS L1 DSM
Entries 2.999999e+07
sumCD
DSM board

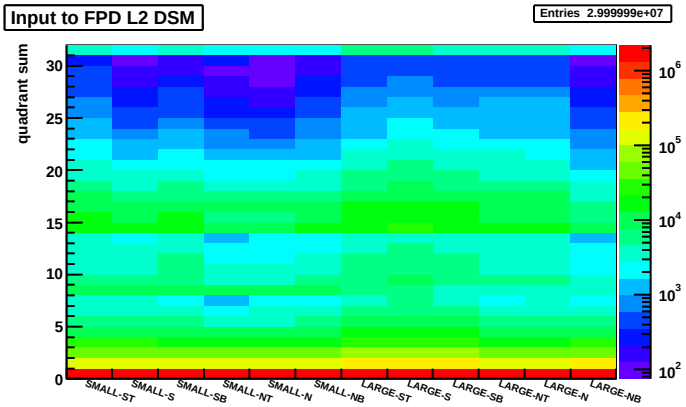

Input to FMS L1 DSM
Entries 56210
sumCD - simulated
DSM board

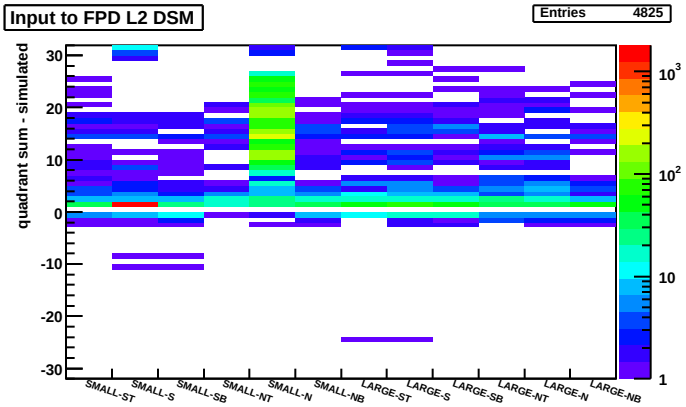

Input to FMS L1 DSM
Entries 2.999999e+07
sumA
DSM board

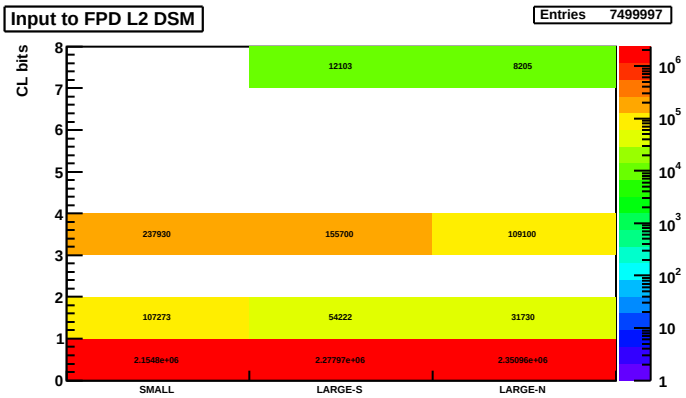

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

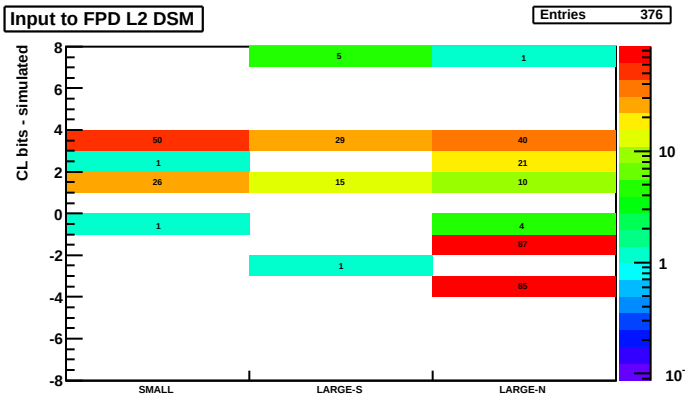

Input to FMS L1 DSM
Entries 2.999999e+07
FMS L1 HT bits
DSM board

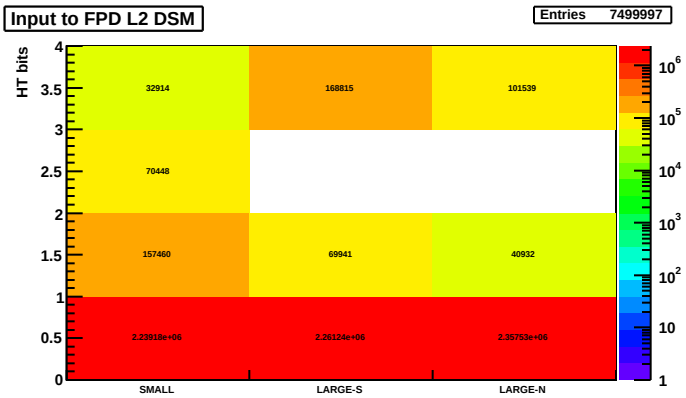

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

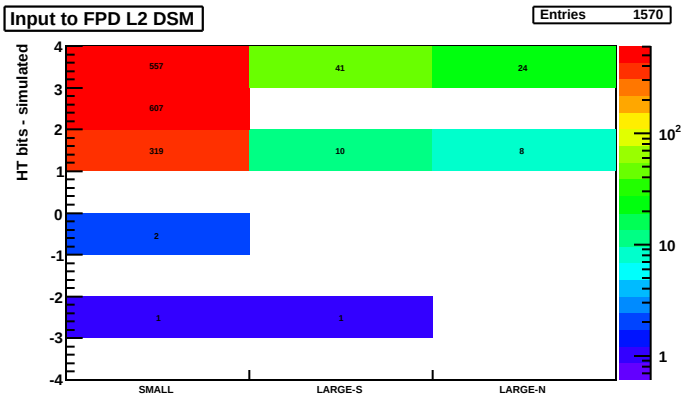

Input to FPD L2 DSM
Entries 2.999999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

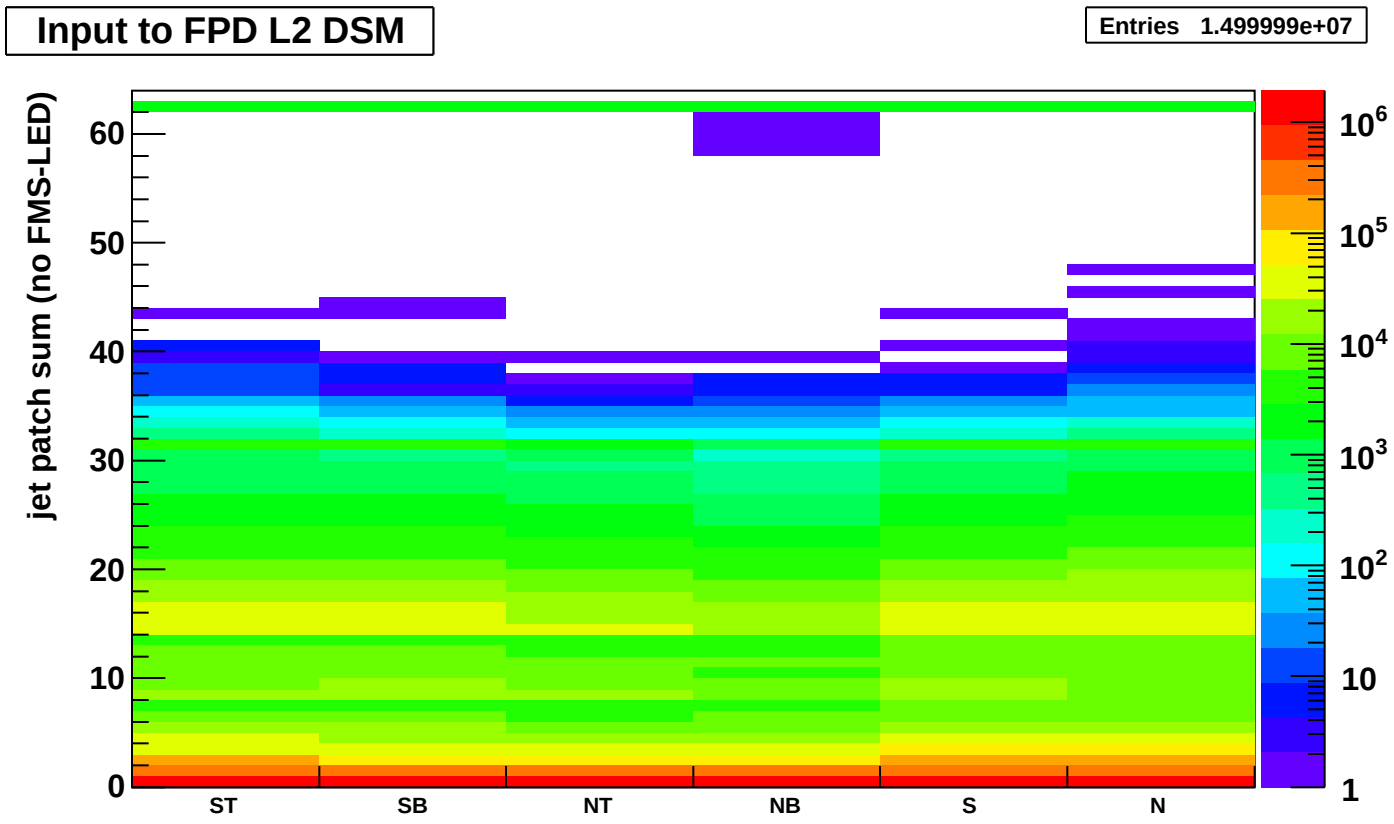

Input to FPD L2 DSM
Entries 4825
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

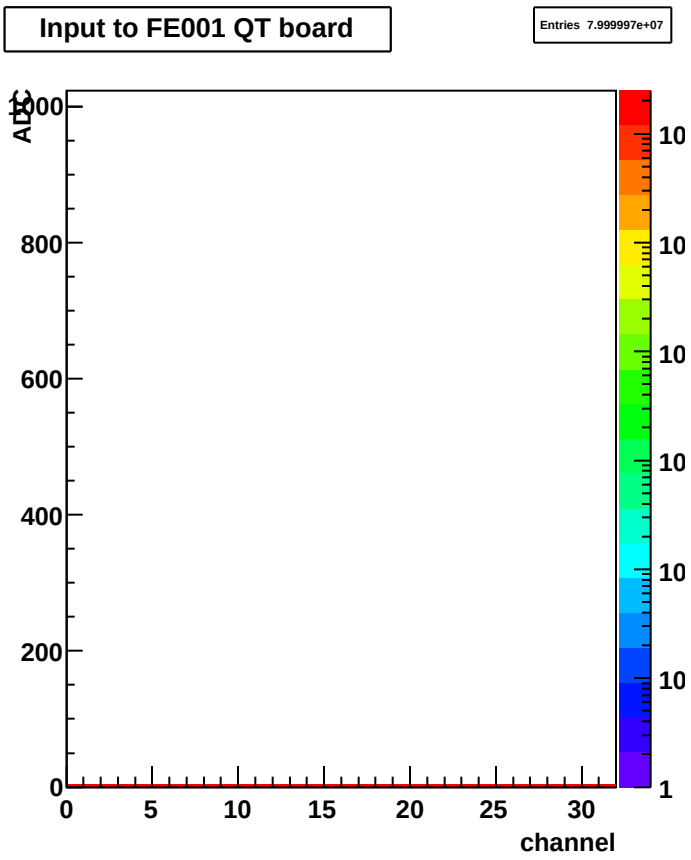

Input to FPD L2 DSM
Entries 7499997
CL bits
12103
8205
237930
155700
109100
107273
54222
31730
2.1548e+06
2.27797e+06
2.35096e+06
SMALL
LARGE-S
LARGE-N

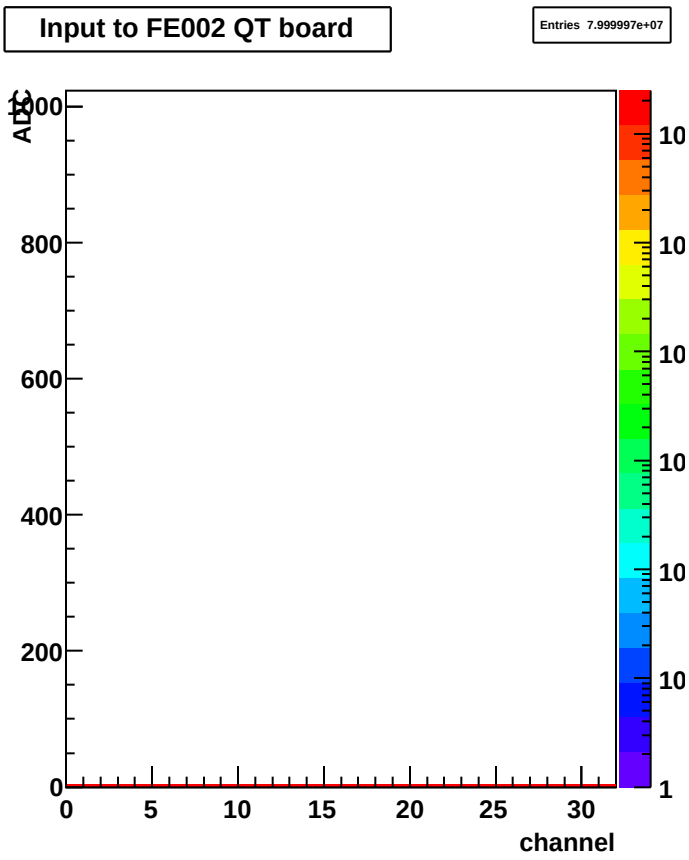

Input to FPD L2 DSM
Entries 376
CL bits - simulated
5
1
50
29
40
1
21
26
15
10
1
4
87
1
85
SMALL
LARGE-S
LARGE-N

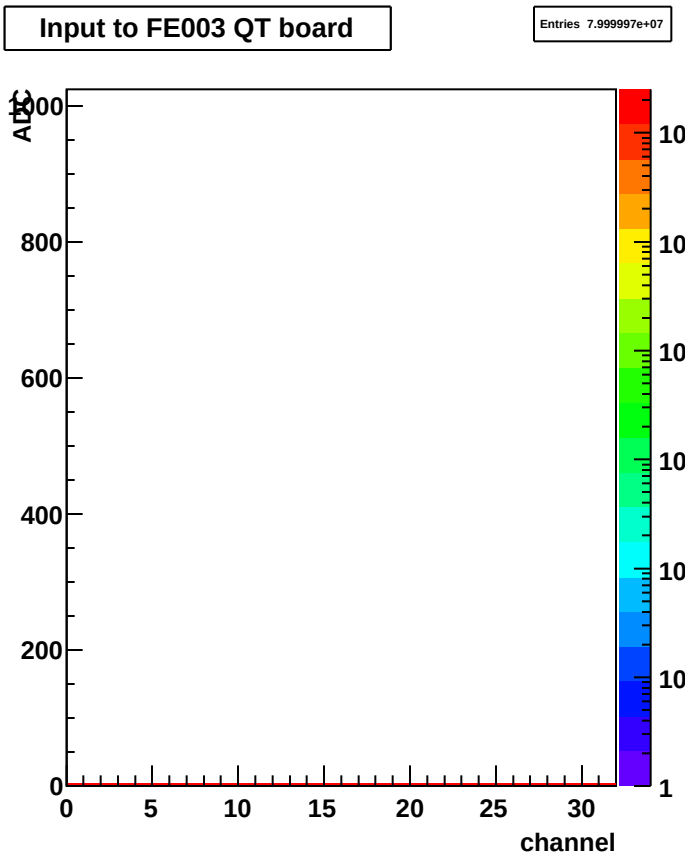

Input to FPD L2 DSM
Entries 7499997
HT bits
32914
168815
101539
70448
157460
69941
40932
2.23918e+06
2.26124e+06
2.35753e+06
SMALL
LARGE-S
LARGE-N

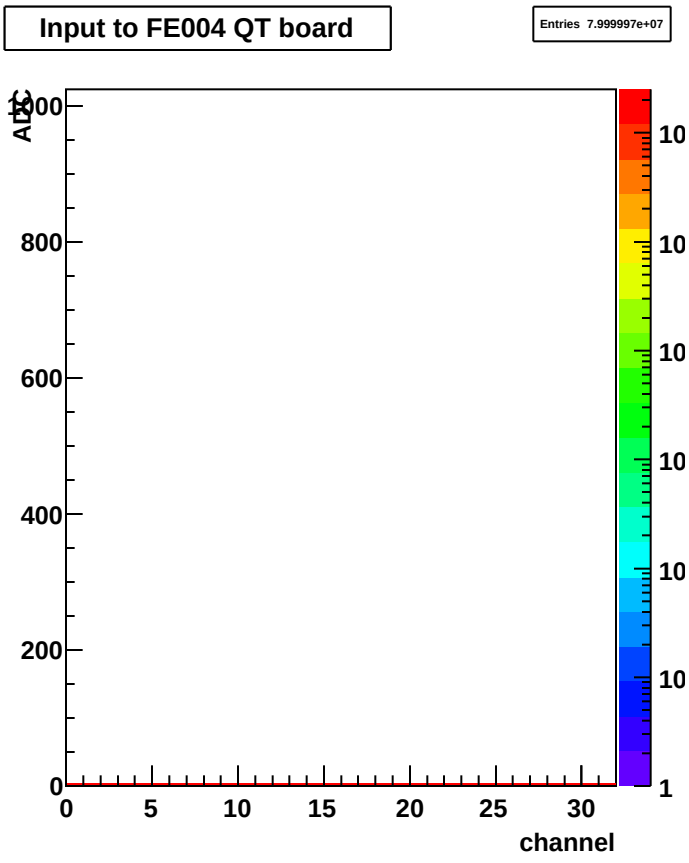

Input to FPD L2 DSM
Entries 1570
HT bits - simulated
557
41
24
607
319
10
8
2
1
1
SMALL
LARGE-S
LARGE-N

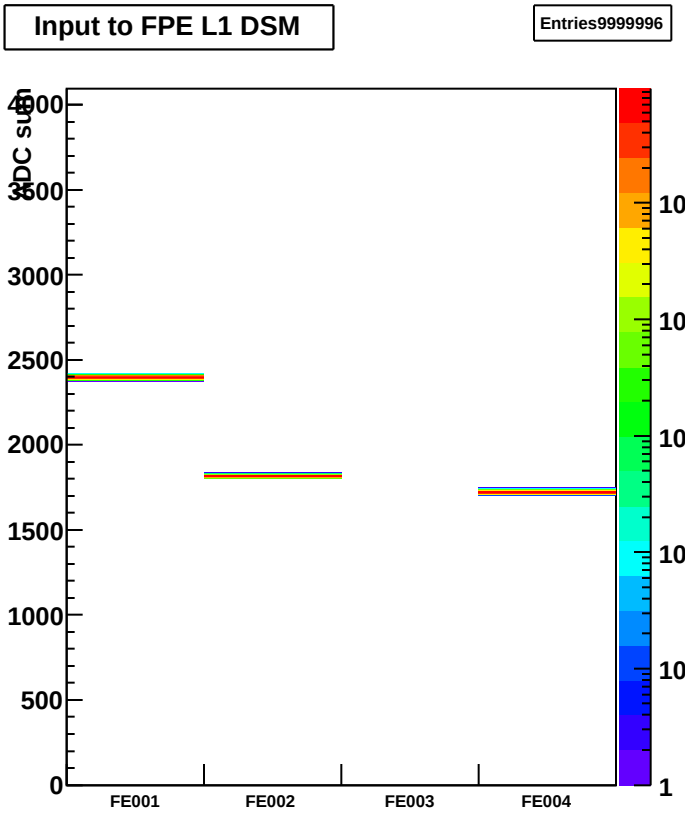

Input to FPD L2 DSM
Entries 1.499999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.999997e+07
ADC
channel

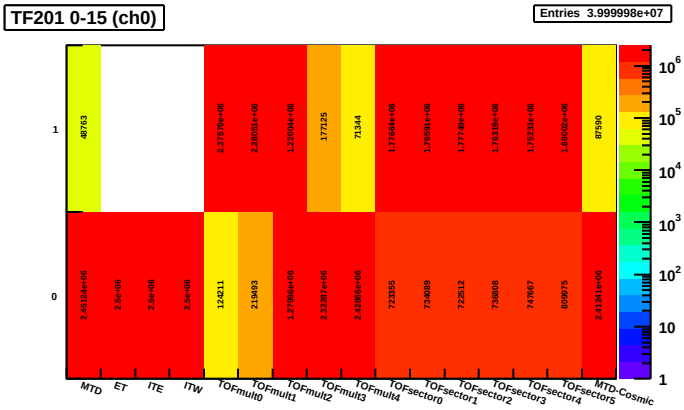

Input to FE002 QT board
Entries 7.999997e+07
ADC
channel


Input to FE003 QT board
Entries 7.999997e+07
ADC
channel

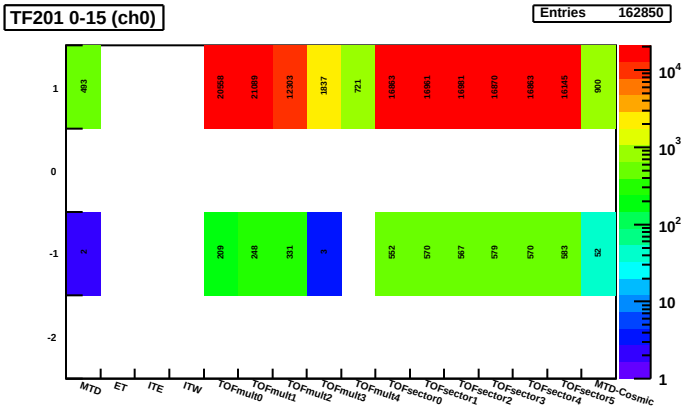

Input to FE004 QT board
Entries 7.999997e+07
ADC
channel


Input to FPE L1 DSM
Entries9999996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

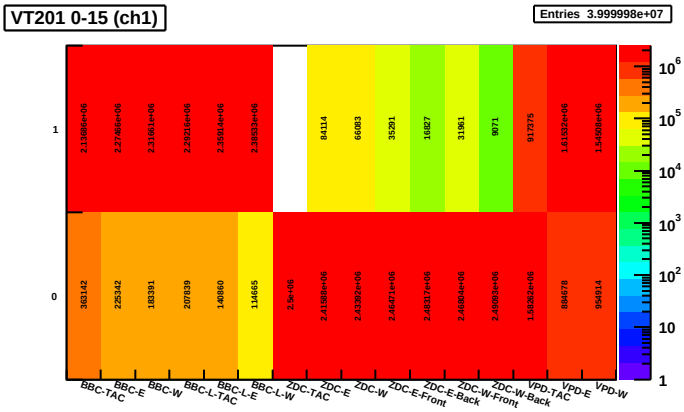
TF201 0-15 (ch0)



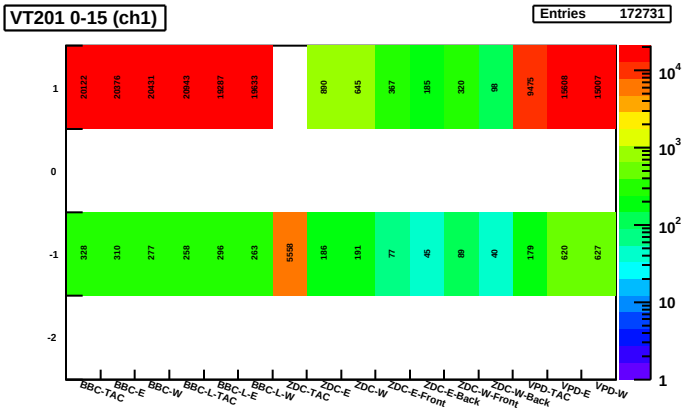
TF201 0-15 (ch0)



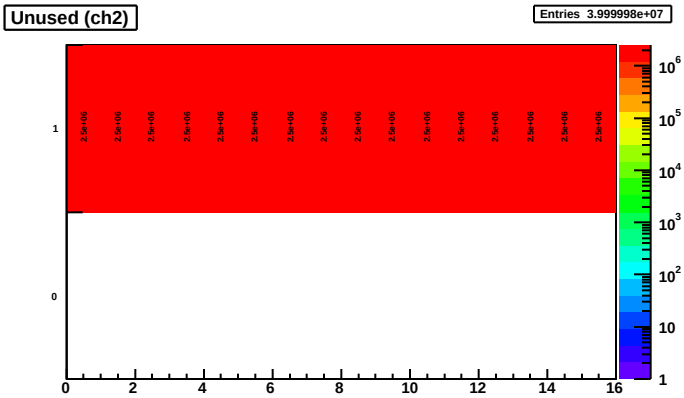
VT201 0-15 (ch1)

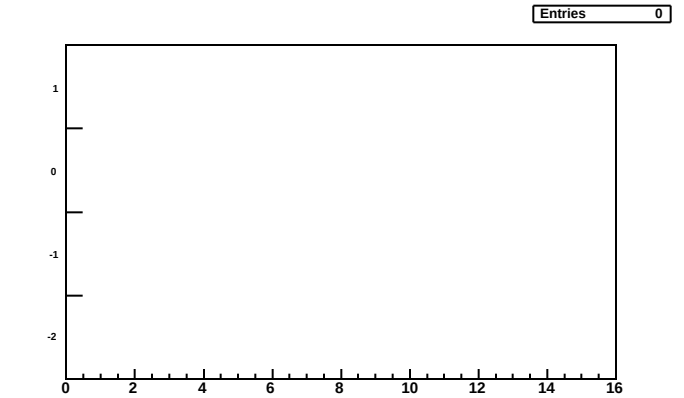


VT201 0-15 (ch1)

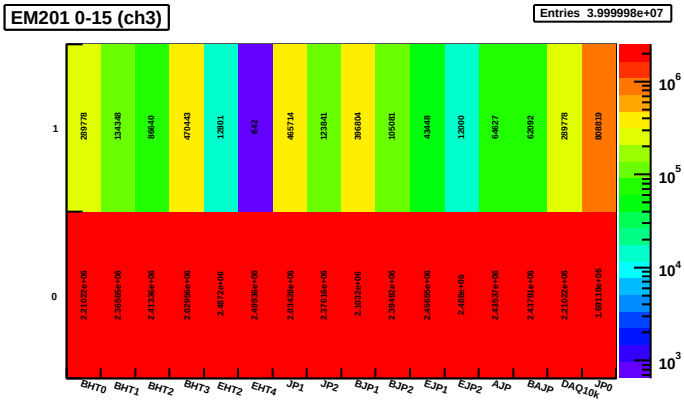


Unused (ch2)

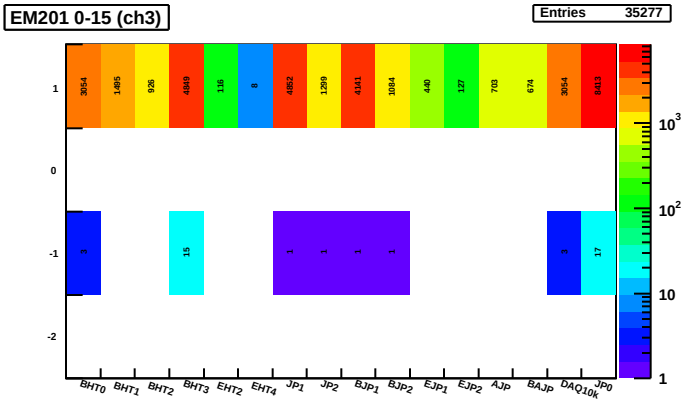




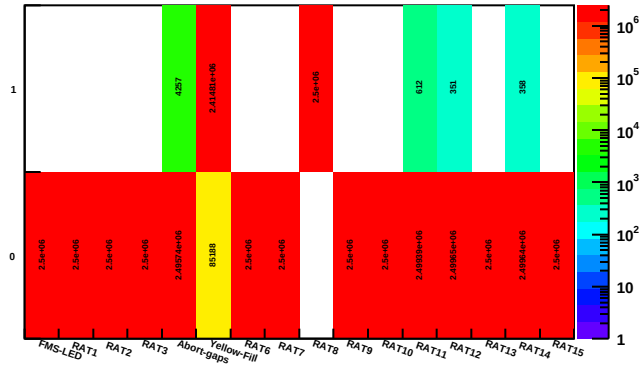
EM201 0-15 (ch3)

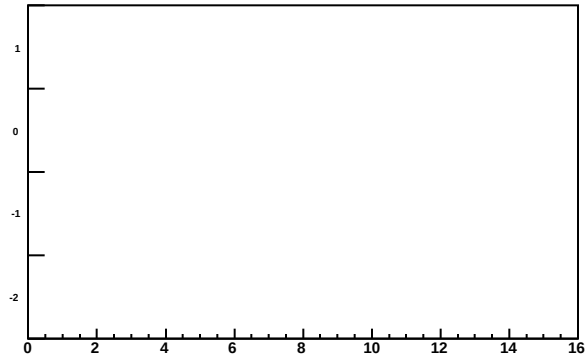


EM201 0-15 (ch3)

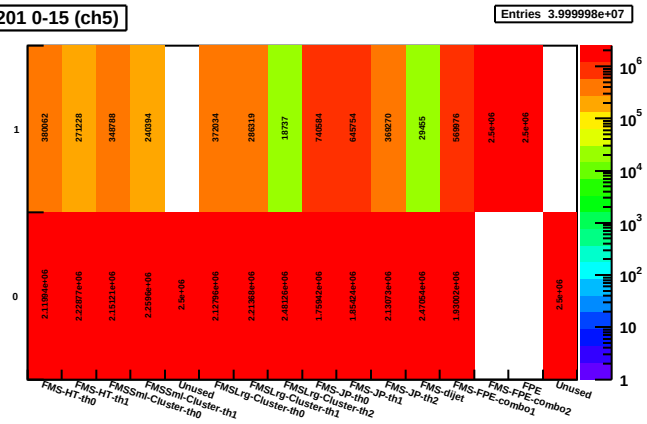


RAT board (ch4)

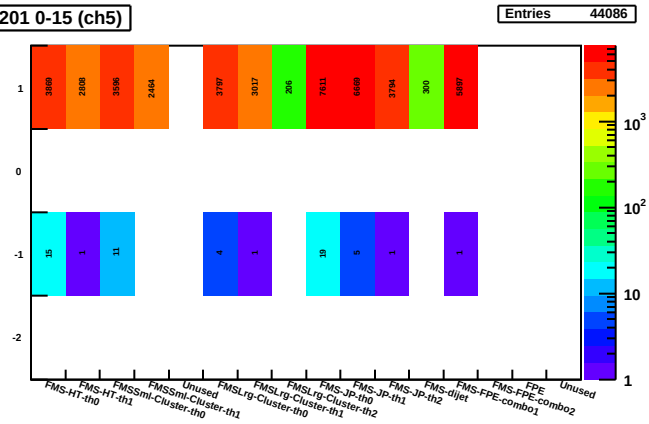




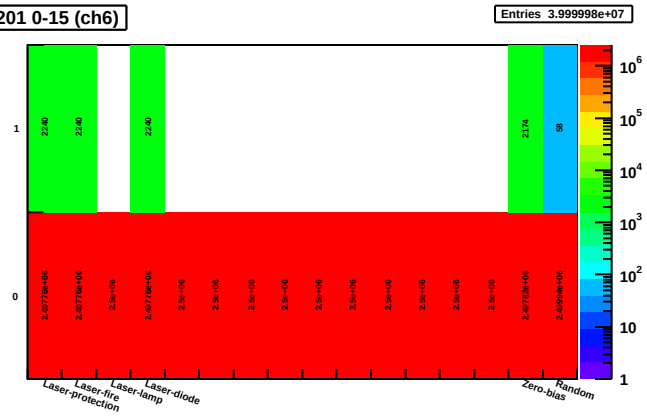
FP201 0-15 (ch5)

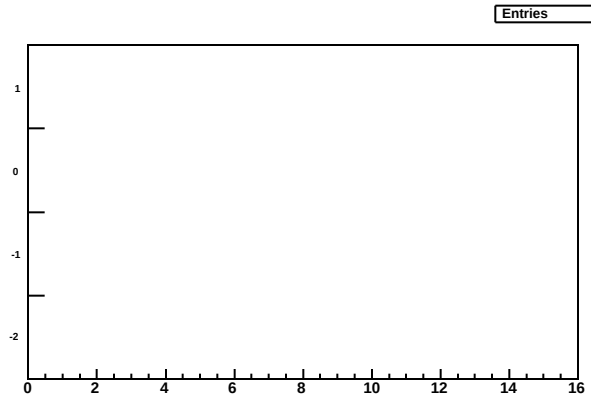
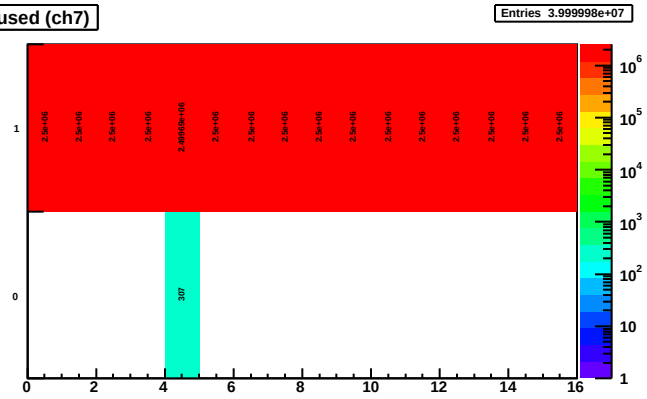
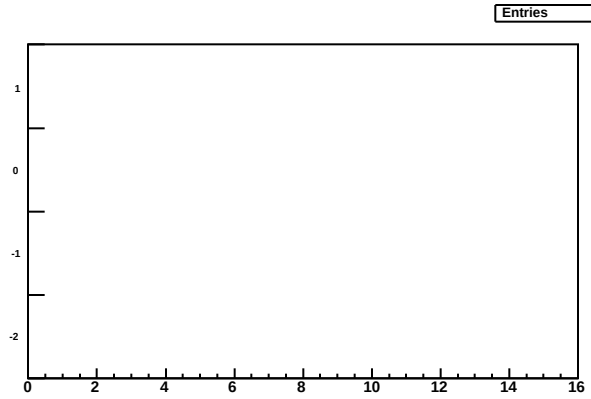


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)