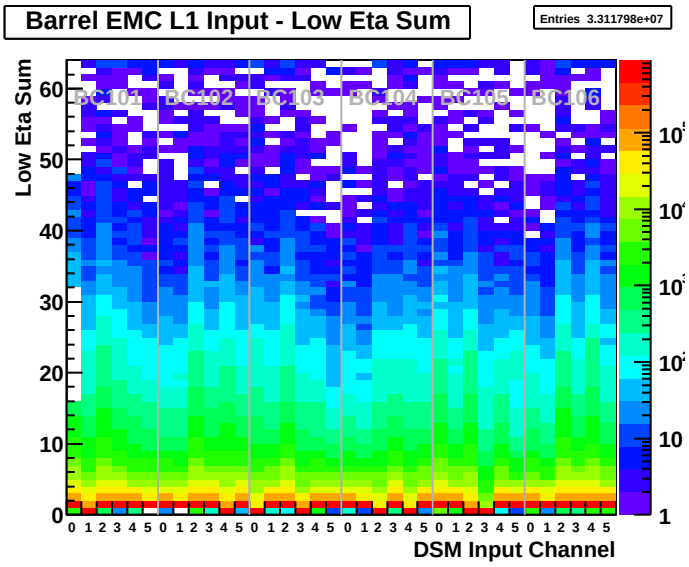
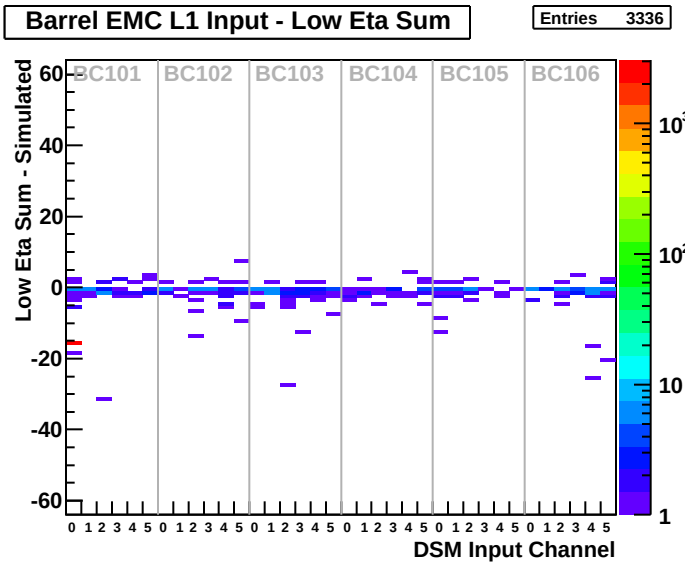
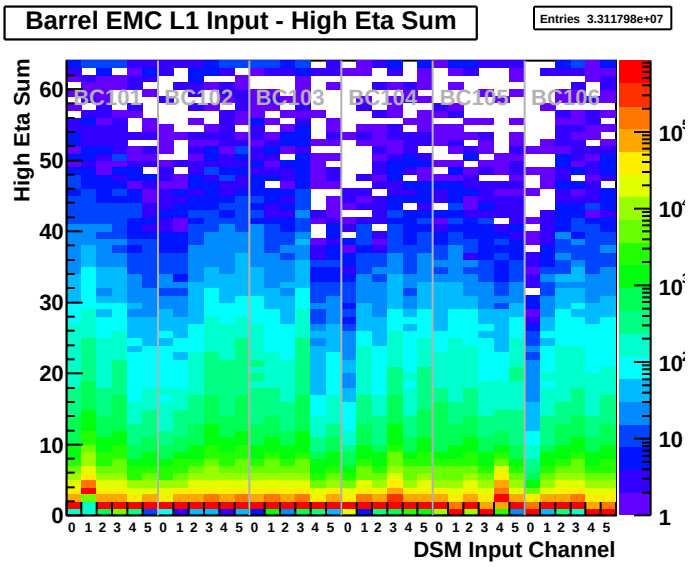
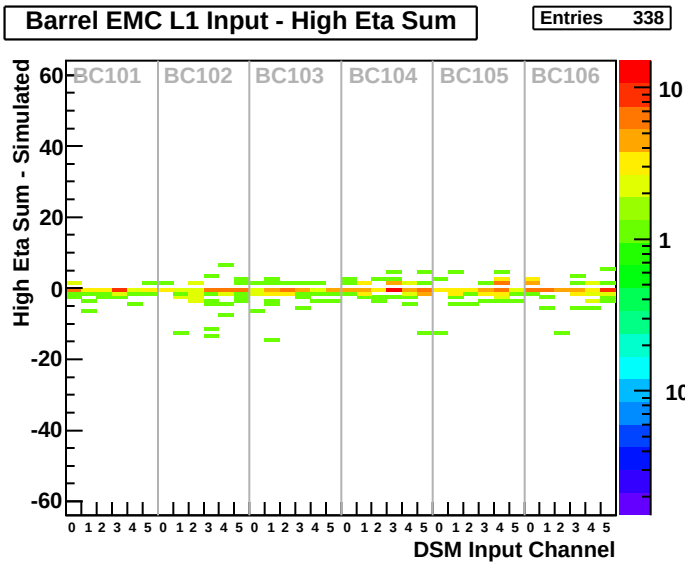
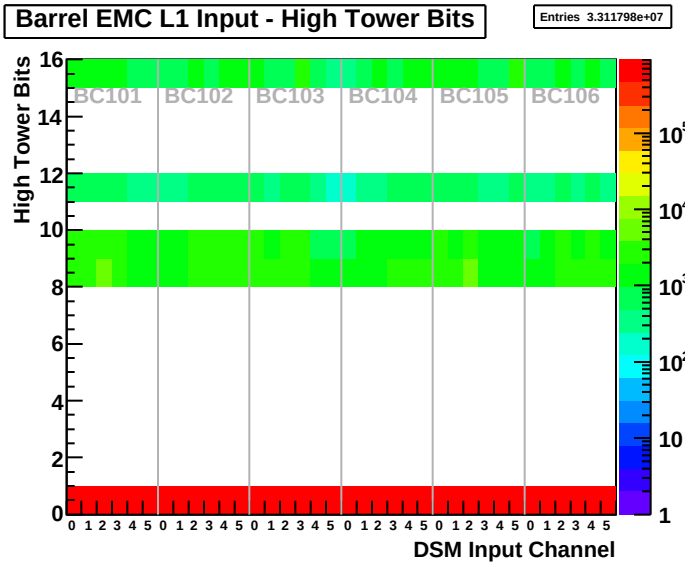
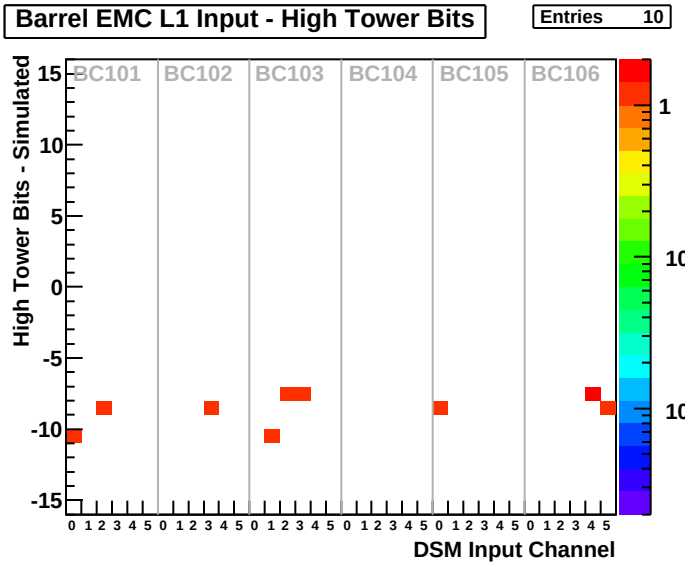

Barrel EMC L1 Input - Low Eta Sum
Entries 3.311798e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

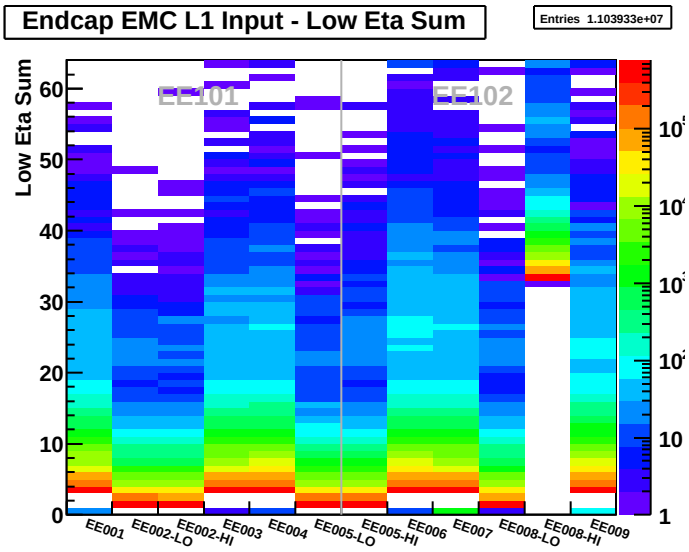

Barrel EMC L1 Input - Low Eta Sum
Entries 3336
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

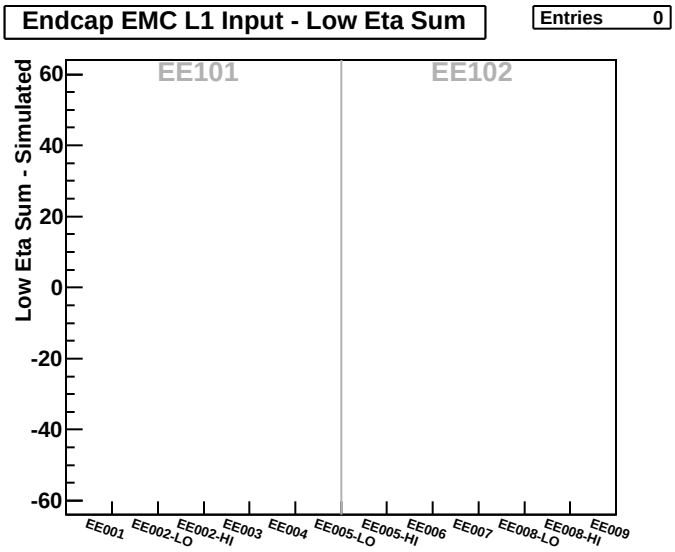

Barrel EMC L1 Input - High Eta Sum
Entries 3.311798e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

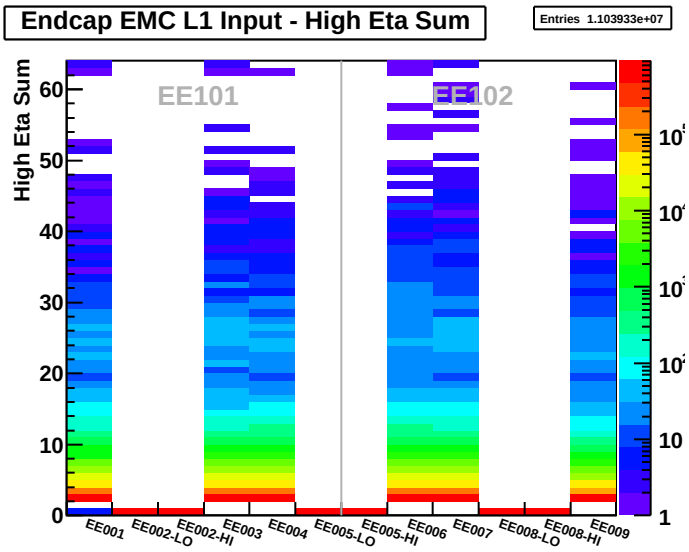

Barrel EMC L1 Input - High Eta Sum
Entries 338
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

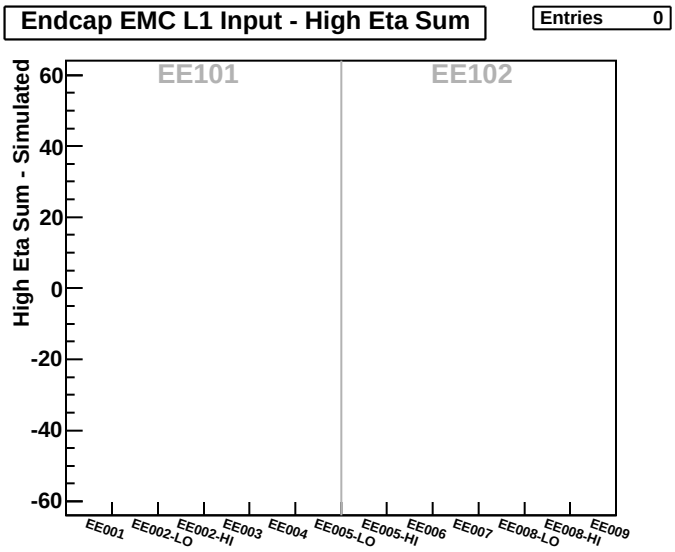

Barrel EMC L1 Input - High Tower Bits
Entries 3.311798e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

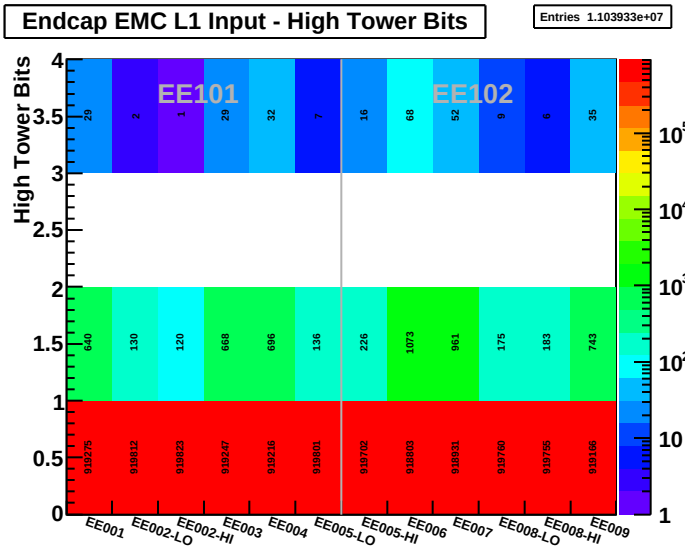

Barrel EMC L1 Input - High Tower Bits
Entries 10
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

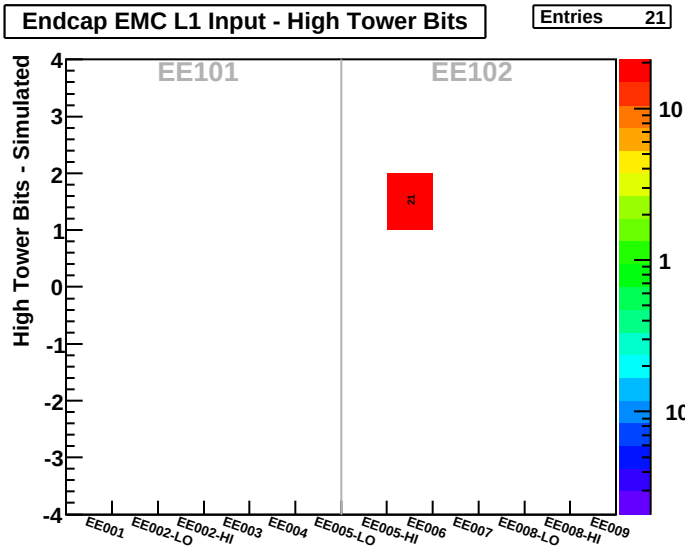

Endcap EMC L1 Input - Low Eta Sum
Entries 1.103933e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

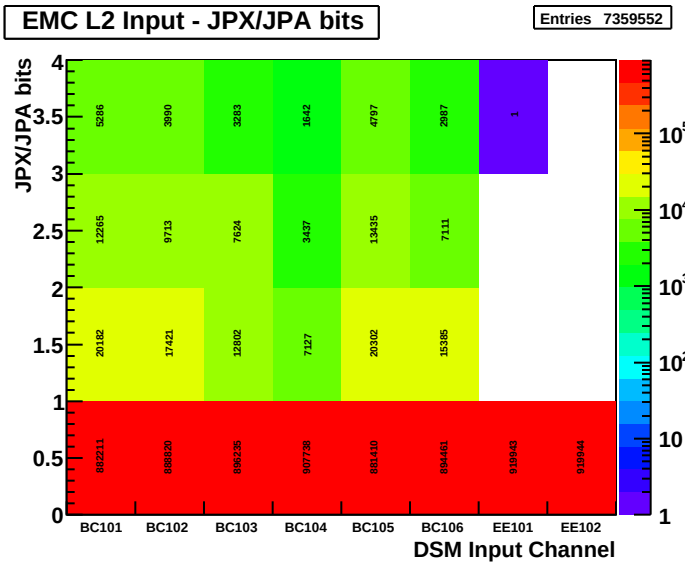

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

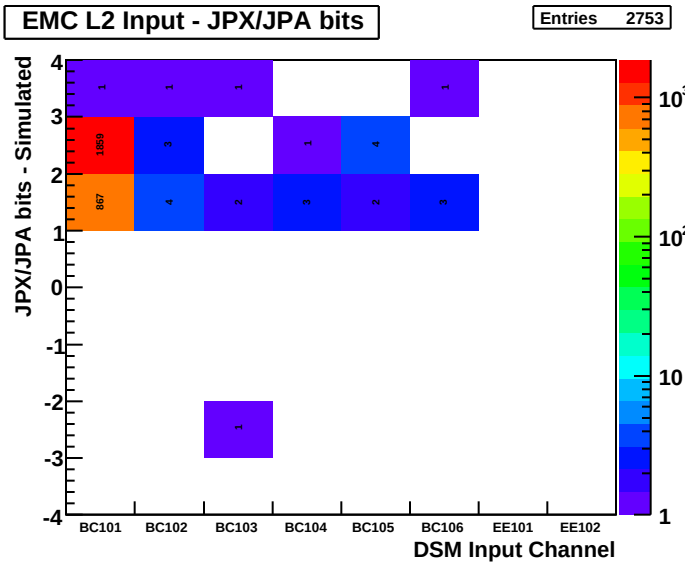

Endcap EMC L1 Input - High Eta Sum
Entries 1.103933e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

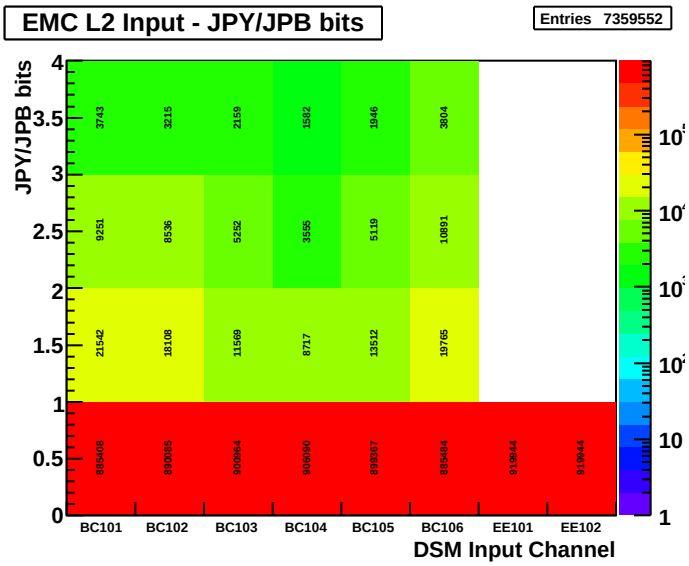

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

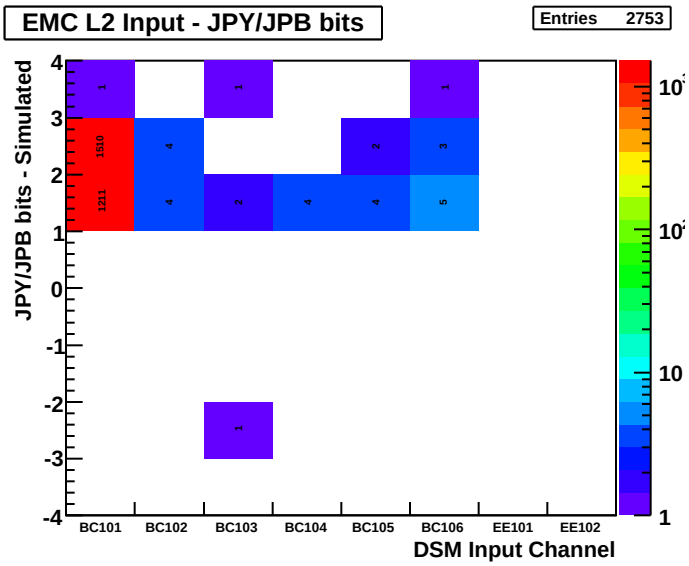

Endcap EMC L1 Input - High Tower Bits
Entries 1.103933e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

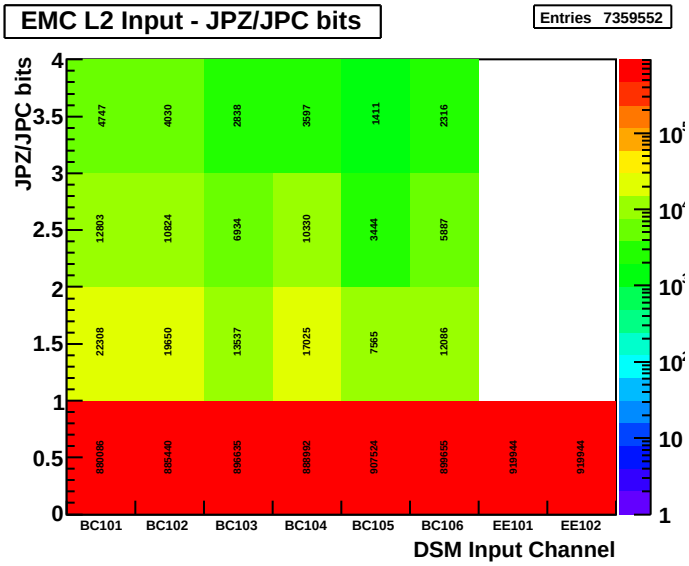

Endcap EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

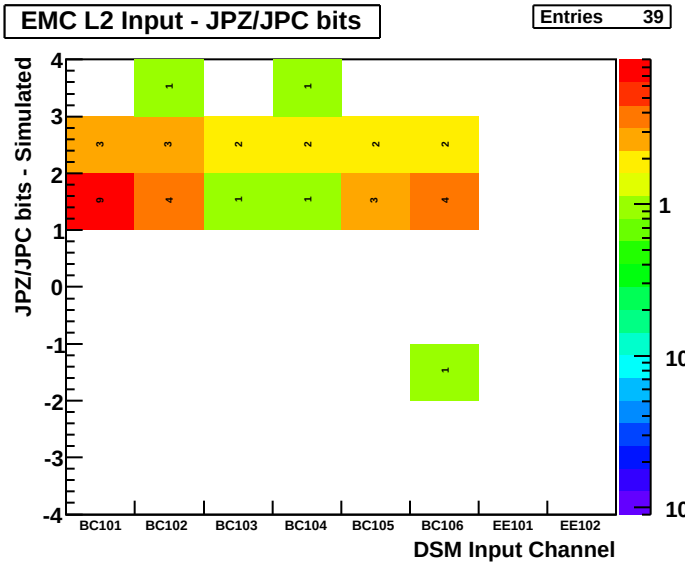

EMC L2 Input - JPX/JPA bits
Entries 7359552
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

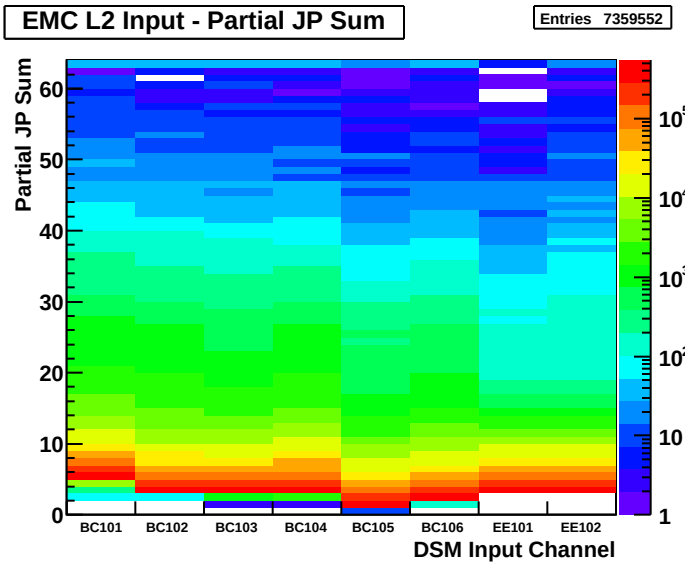

EMC L2 Input - JPX/JPA bits
Entries 2753
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

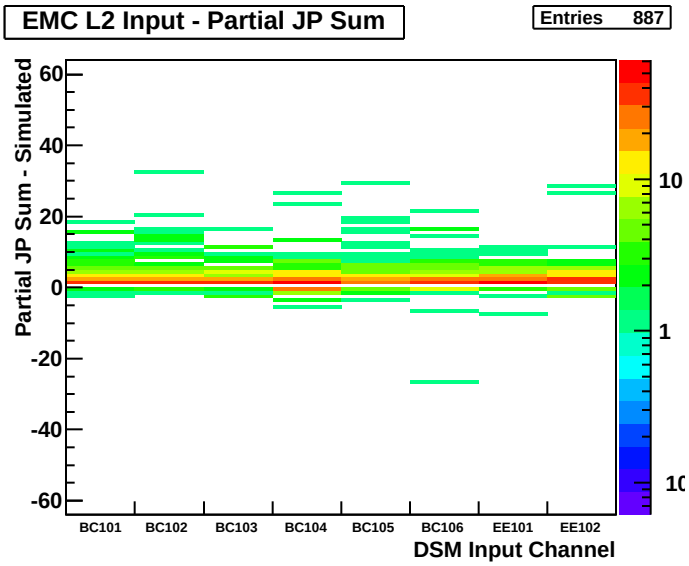

EMC L2 Input - JPY/JPB bits
Entries 7359552
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

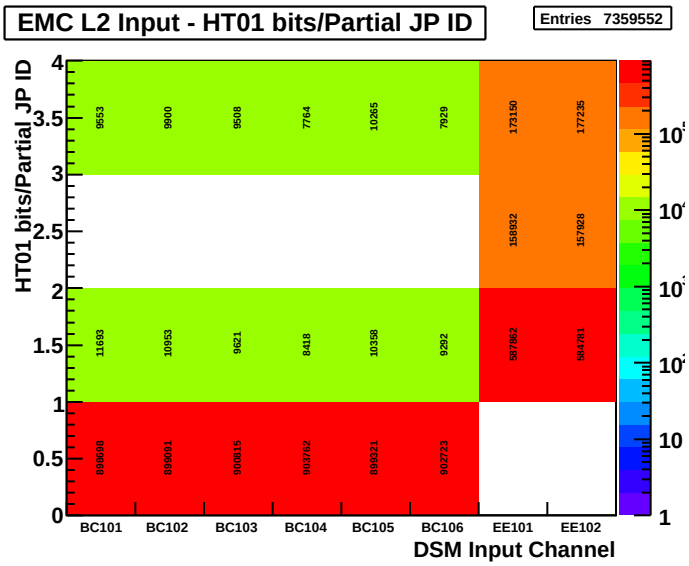

EMC L2 Input - JPY/JPB bits
Entries 2753
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

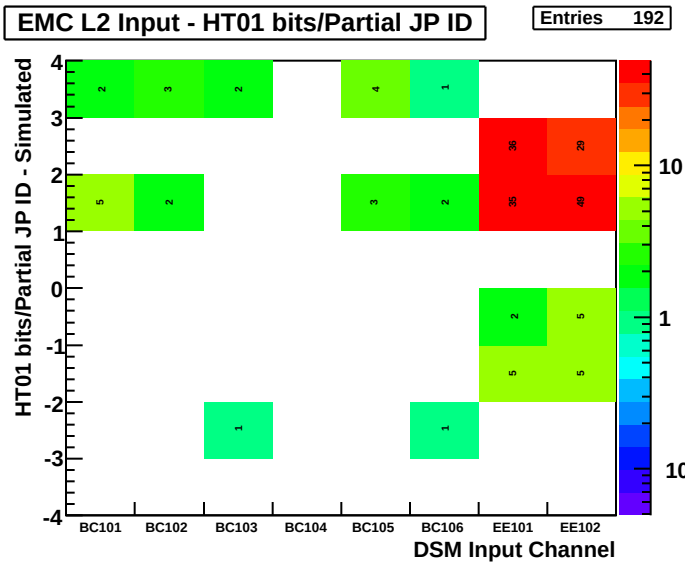

EMC L2 Input - JPZ/JPC bits
Entries 7359552
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

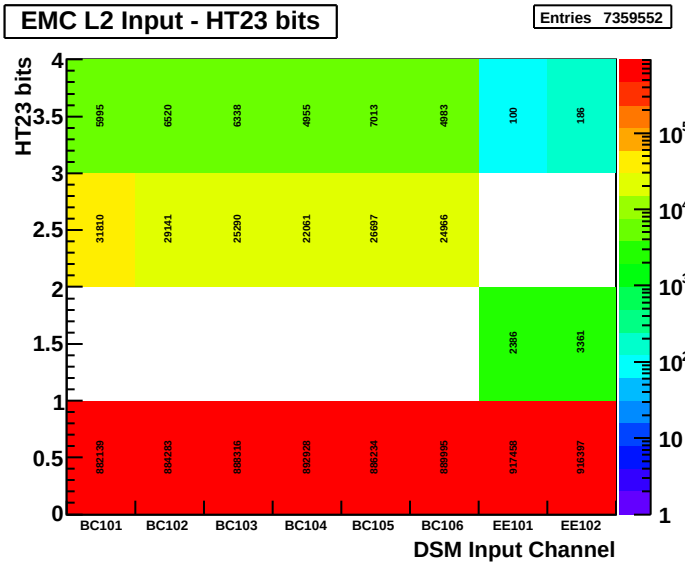

EMC L2 Input - JPZ/JPC bits
Entries 39
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

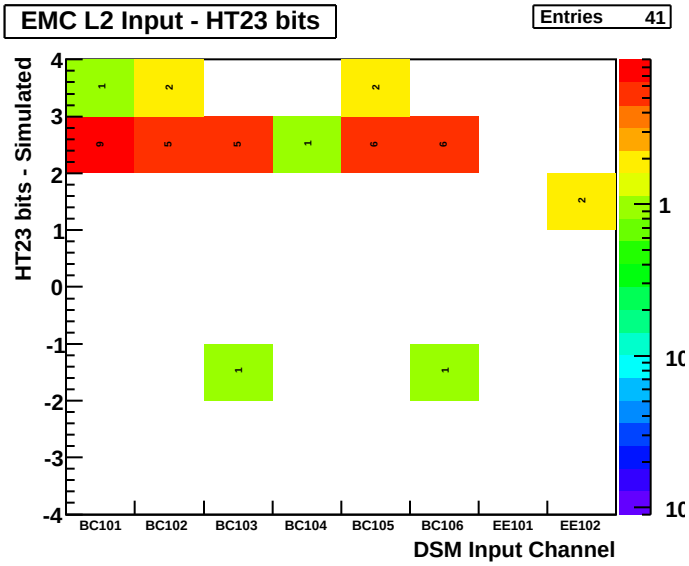

EMC L2 Input - Partial JP Sum
Entries 7359552
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

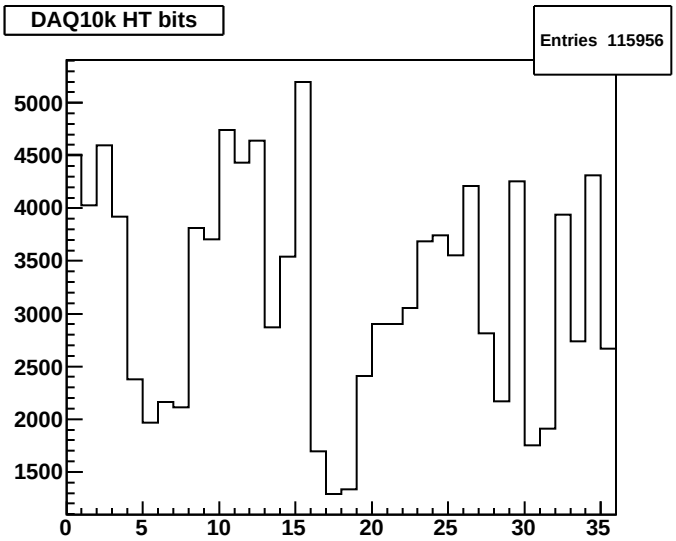

EMC L2 Input - Partial JP Sum
Entries 887
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

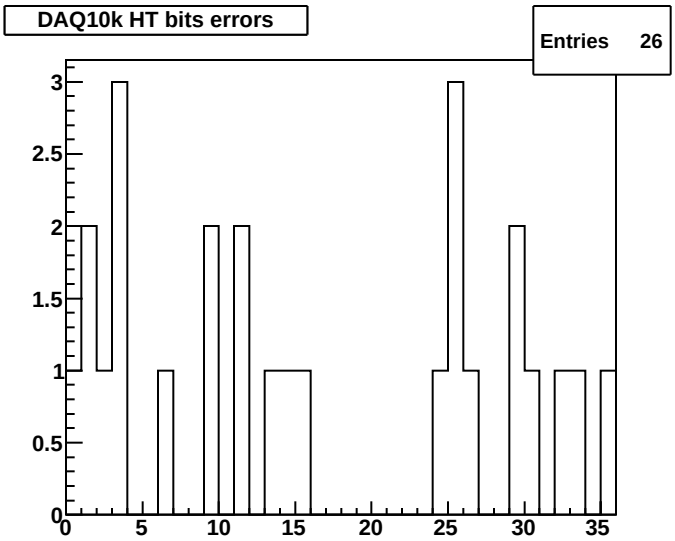

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7359552
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

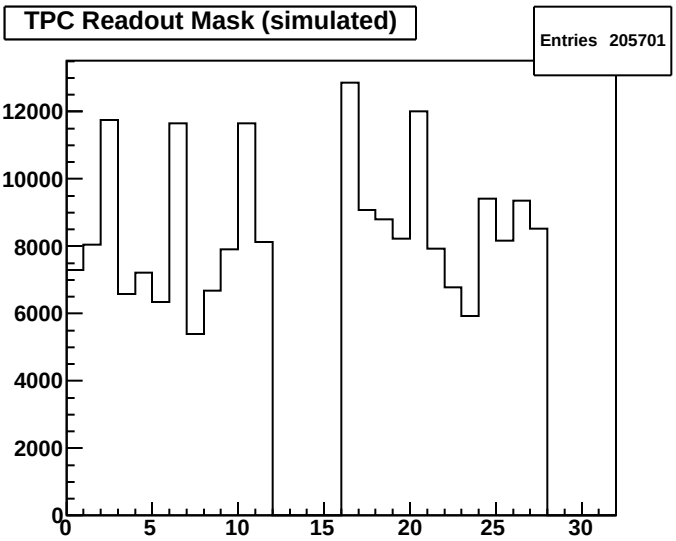

EMC L2 Input - HT01 bits/Partial JP ID
Entries 192
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

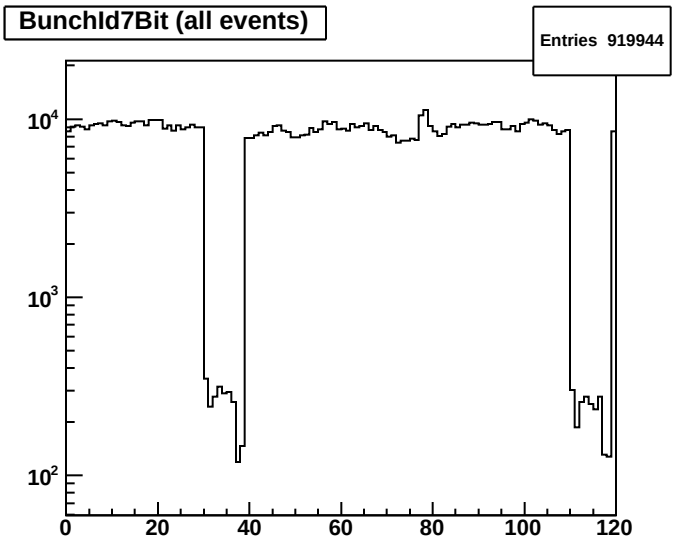

EMC L2 Input - HT23 bits
Entries 7359552
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

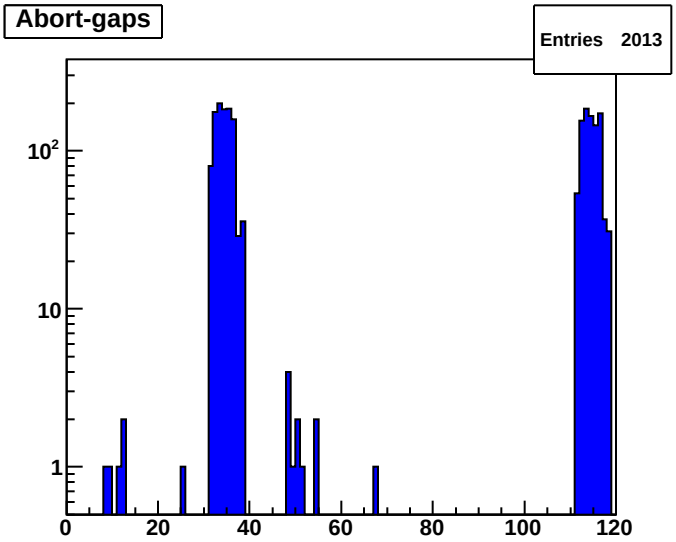

EMC L2 Input - HT23 bits
Entries 41
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

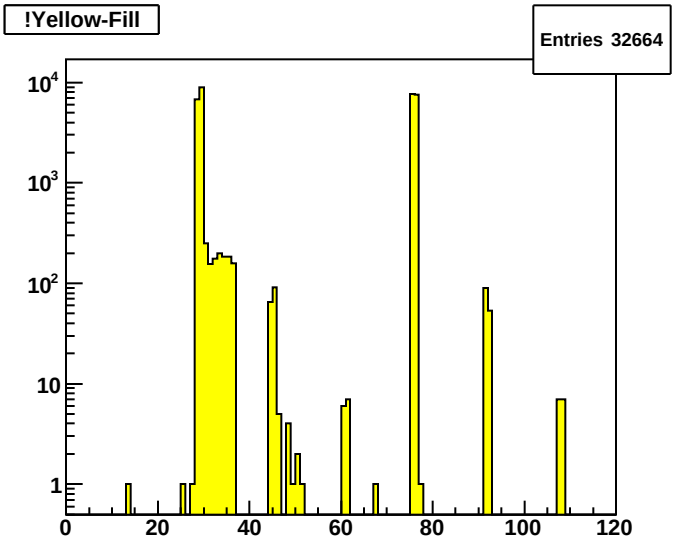

DAQ10k HT bits
Entries 115956

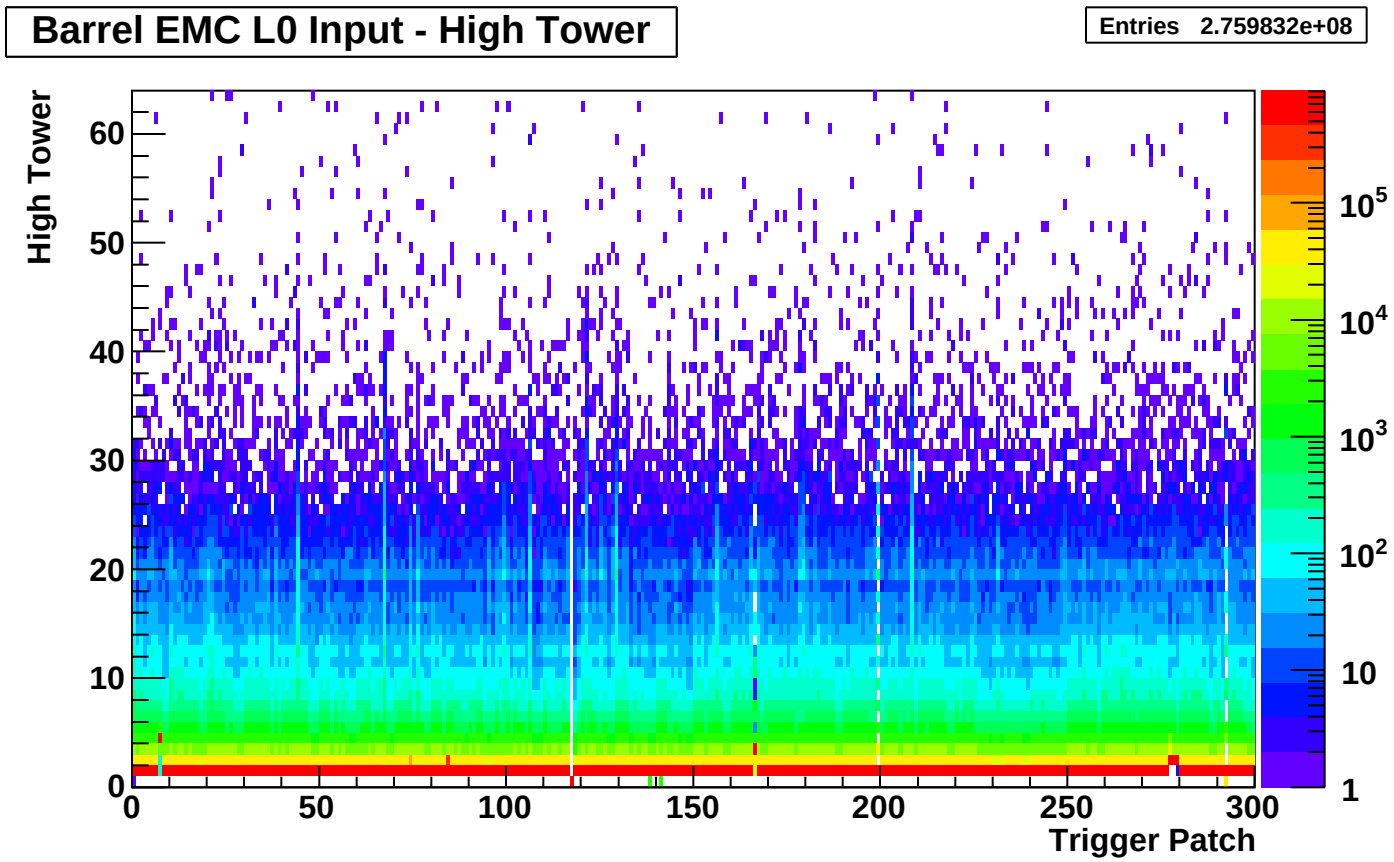

DAQ10k HT bits errors
Entries 26

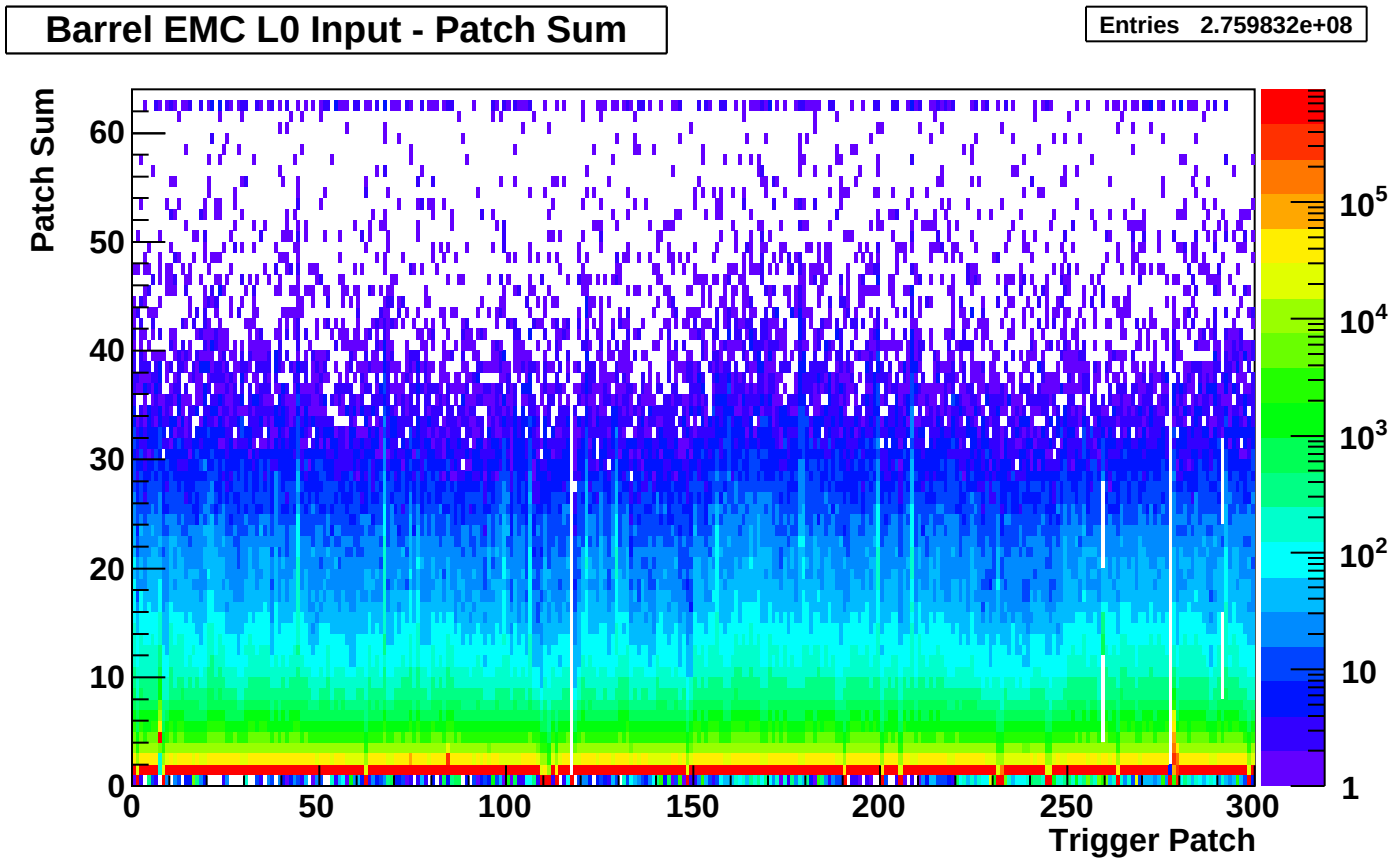

TPC Readout Mask (simulated)
Entries 205701

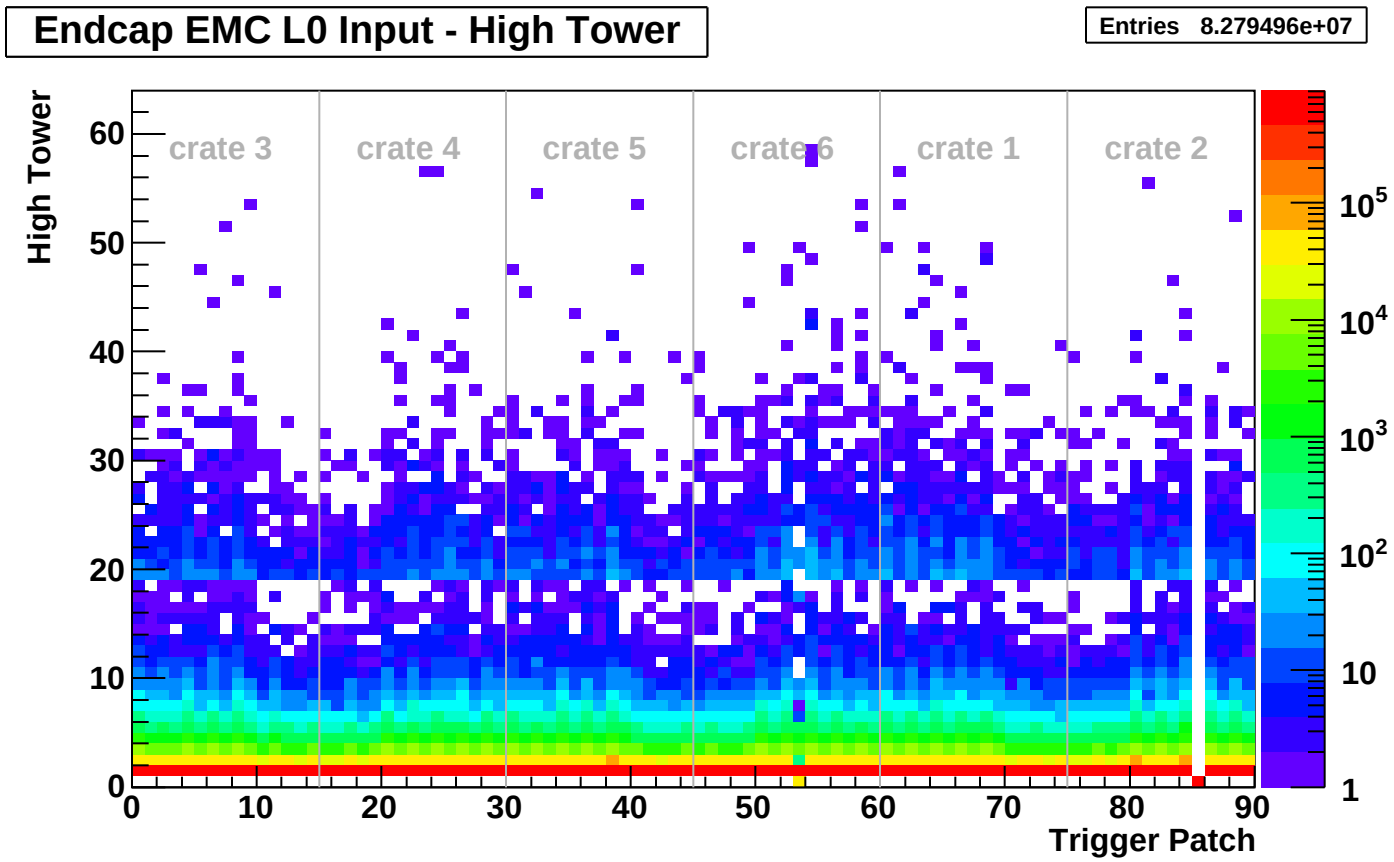

BunchId7Bit (all events)
Entries 919944

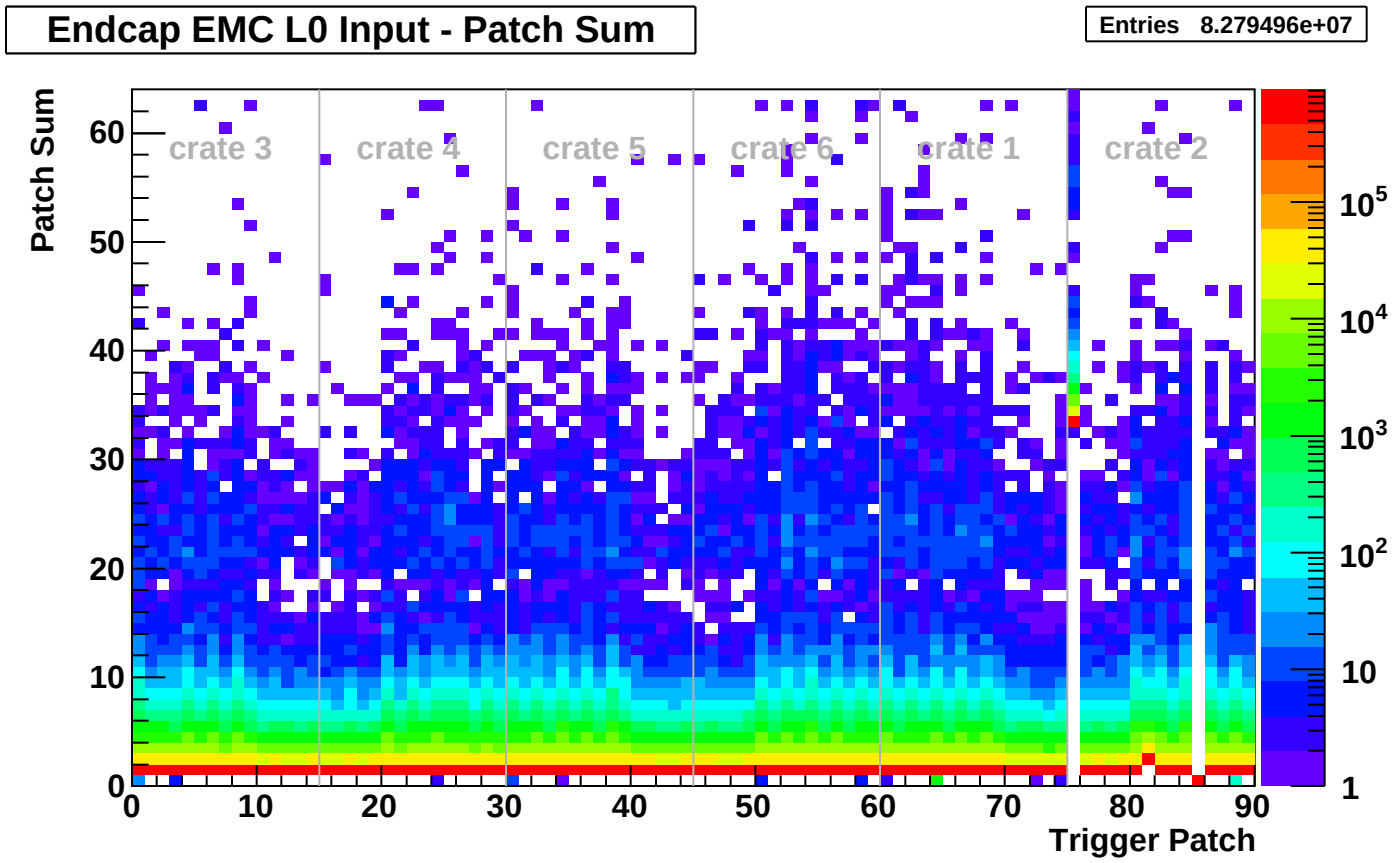

Abort-gaps
Entries 2013

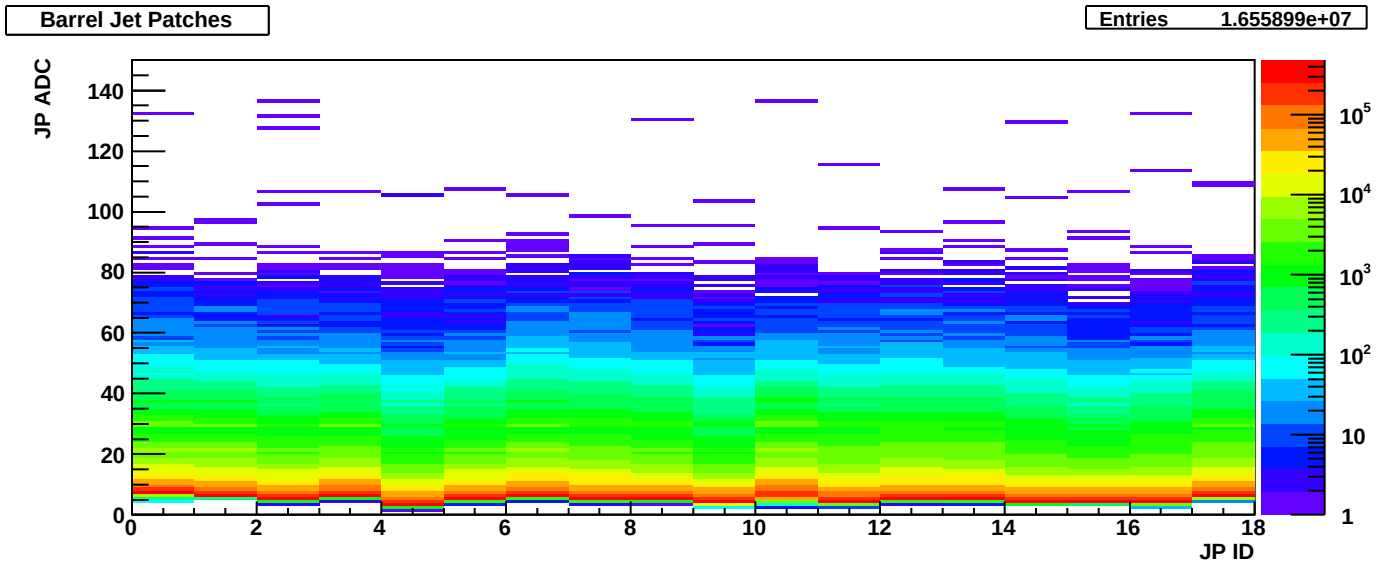

!Yellow-Fill
Entries 32664

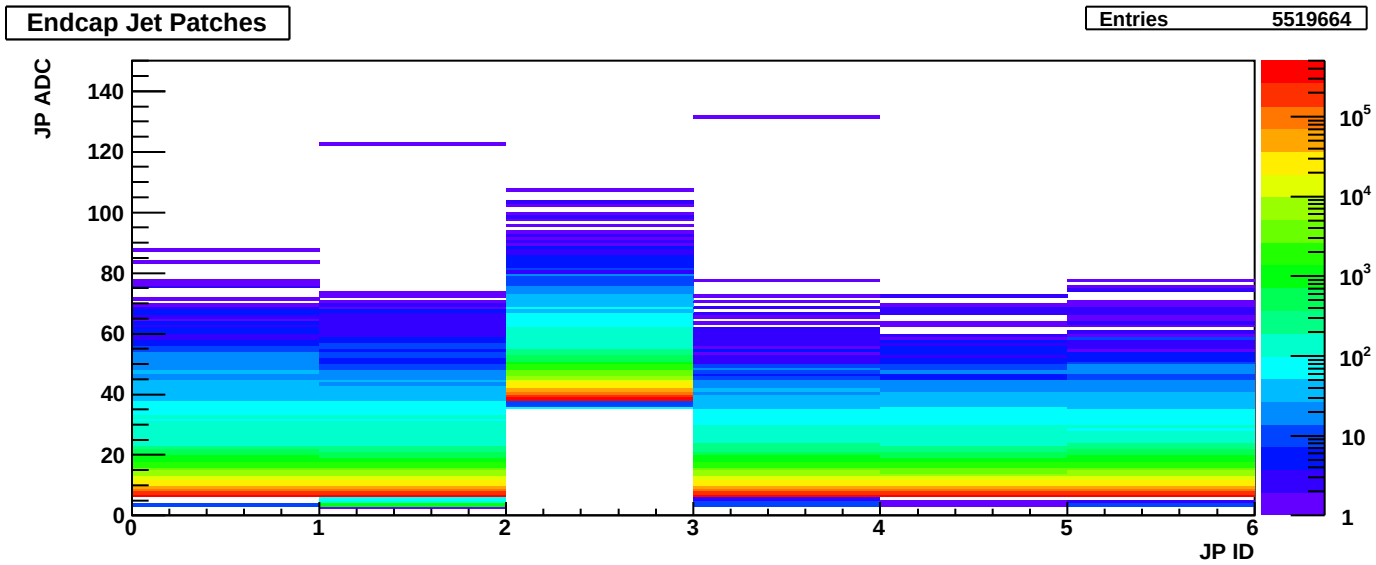

Barrel EMC L0 Input - High Tower
Entries 2.759832e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

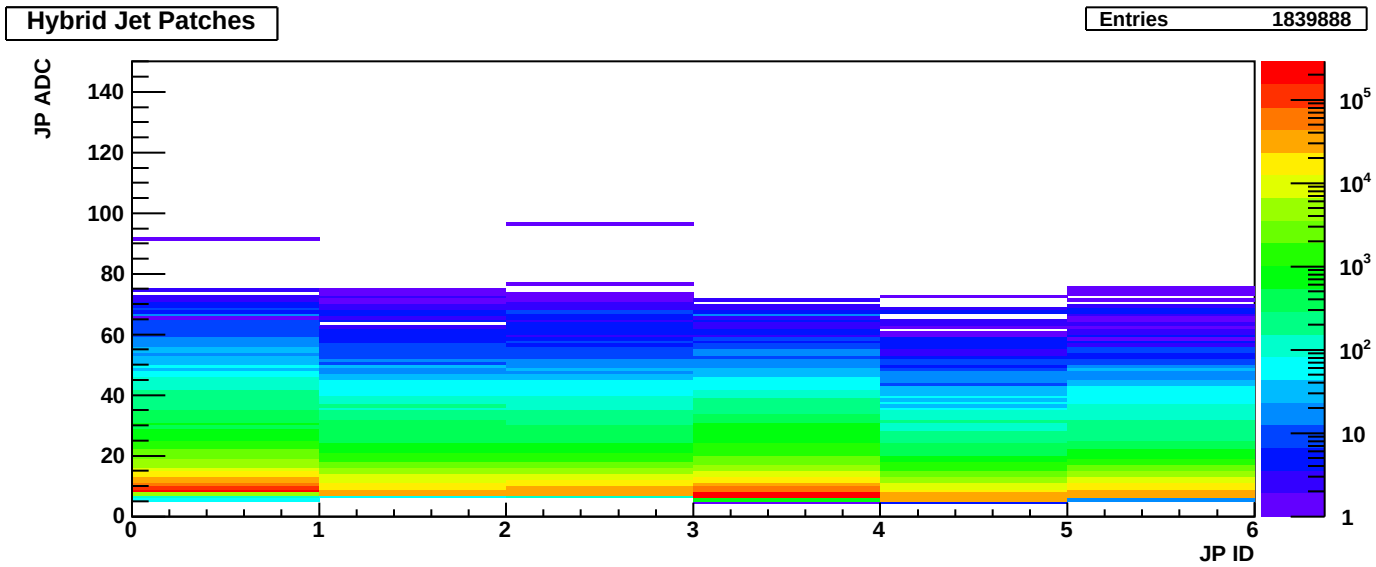

Barrel EMC L0 Input - Patch Sum
Entries 2.759832e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

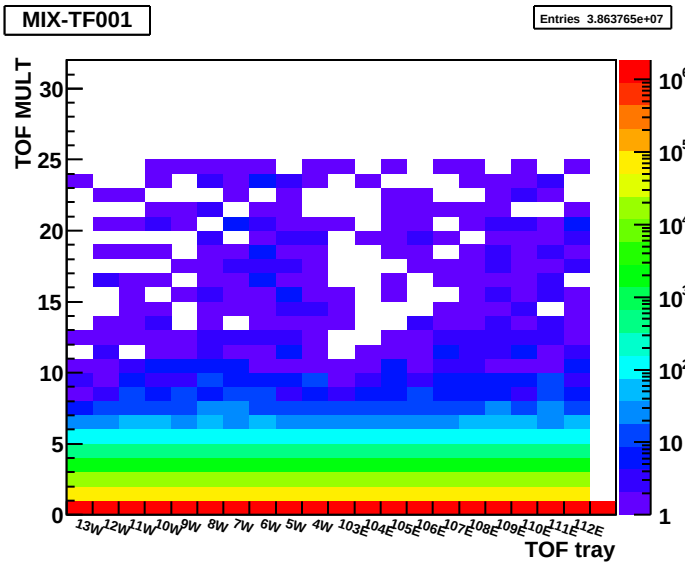

Endcap EMC L0 Input - High Tower
Entries 8.279496e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

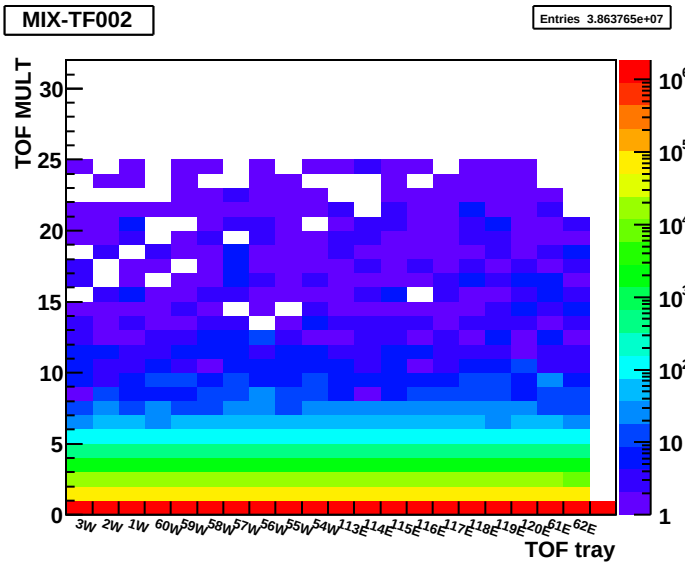

Endcap EMC L0 Input - Patch Sum
Entries 8.279496e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

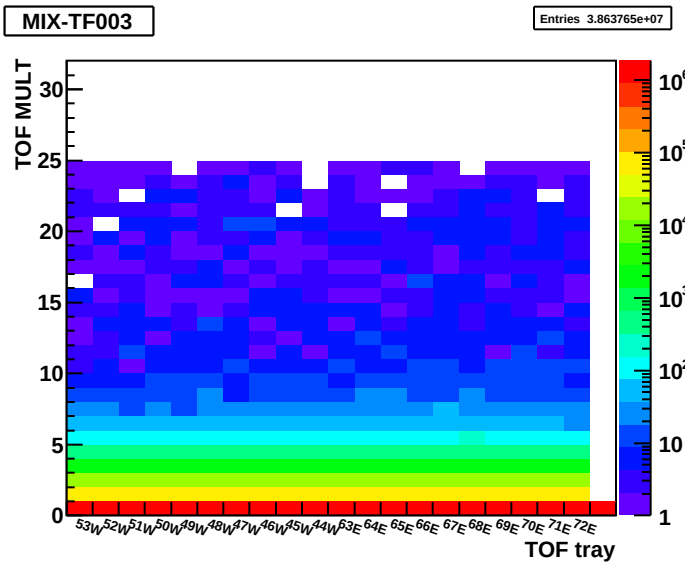

Barrel Jet Patches
Entries 1.655899e+07
JP ADC
JP ID

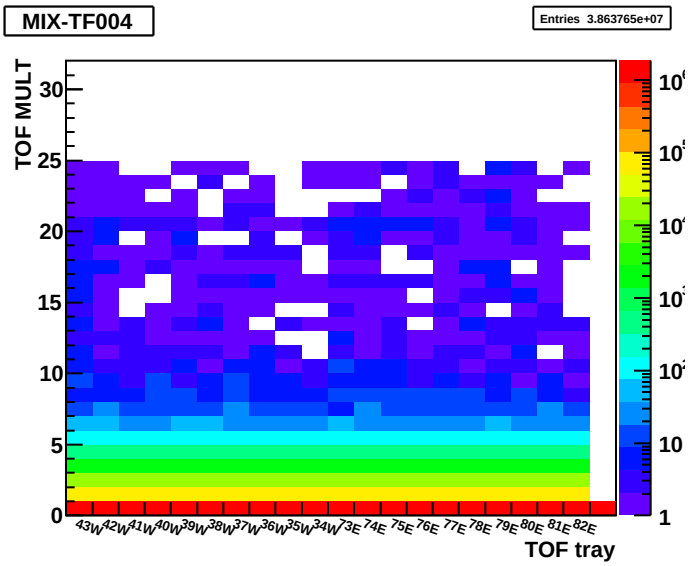

Endcap Jet Patches
Entries 5519664
JP ADC
JP ID

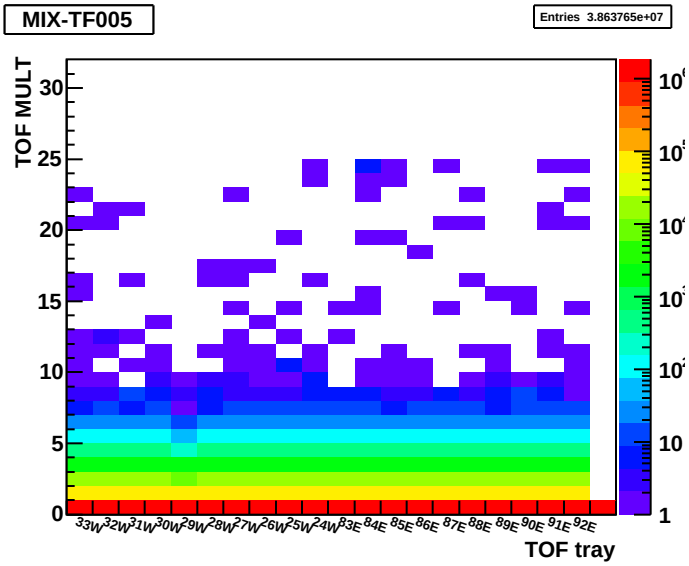

Hybrid Jet Patches
Entries 1839888
JP ADC
JP ID

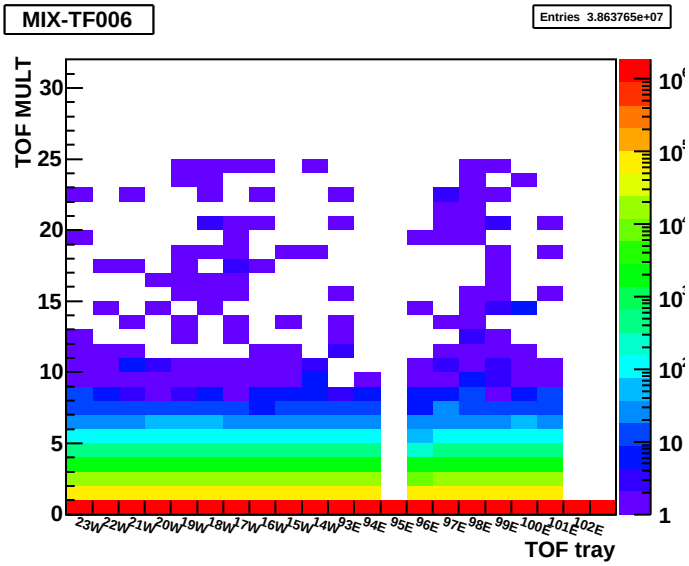

MIX-TF001
Entries 3.863765e+07
TOF MULT
TOF tray

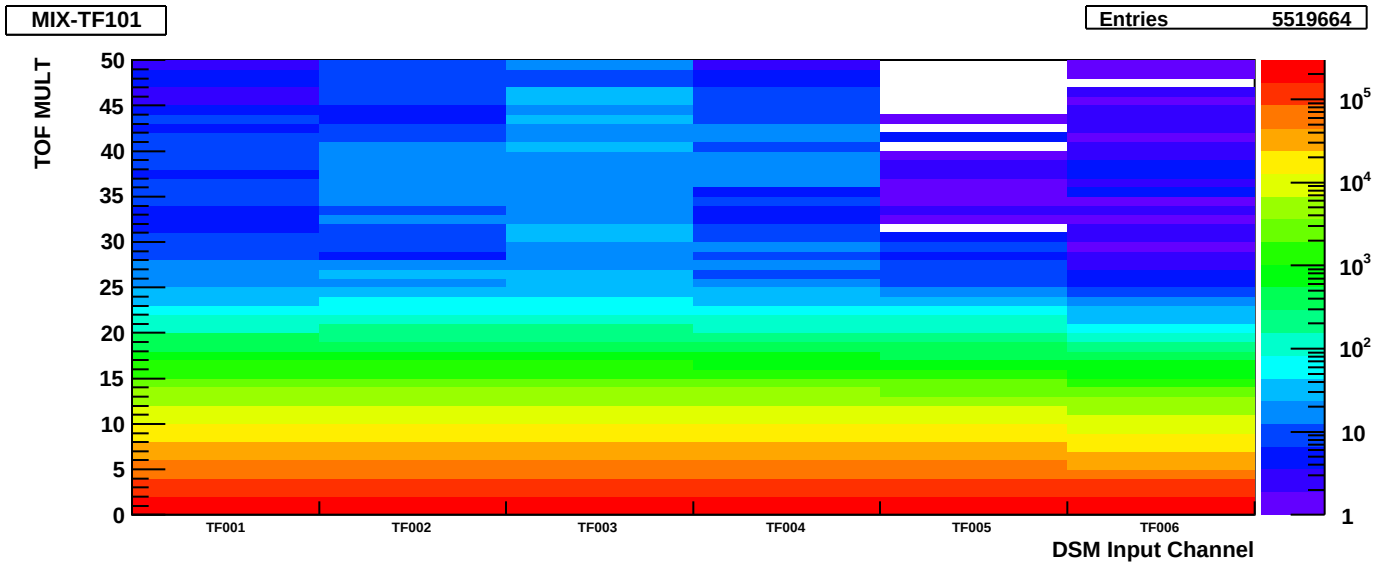

MIX-TF002
Entries 3.863765e+07
TOF MULT
TOF tray

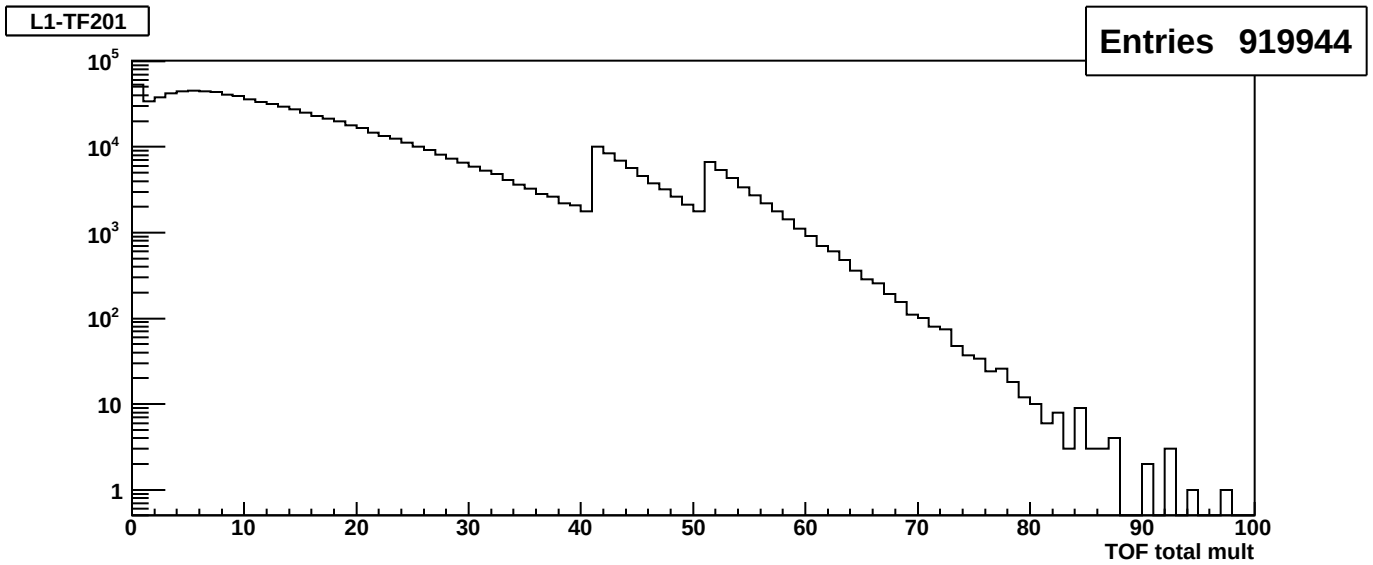

MIX-TF003
Entries 3.863765e+07
TOF MULT
TOF tray

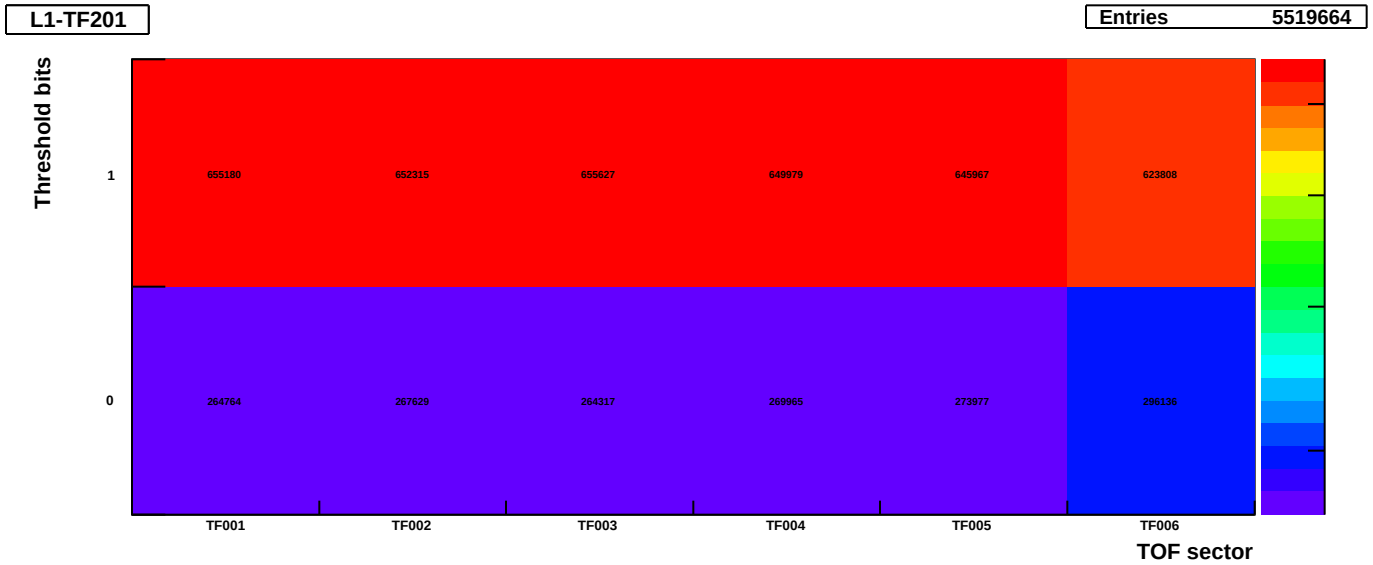

MIX-TF004
Entries 3.863765e+07
TOF MULT
TOF tray

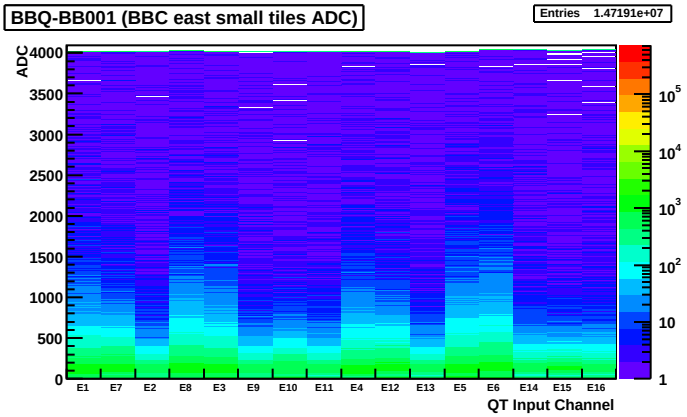

MIX-TF005
Entries 3.863765e+07
TOF MULT
TOF tray

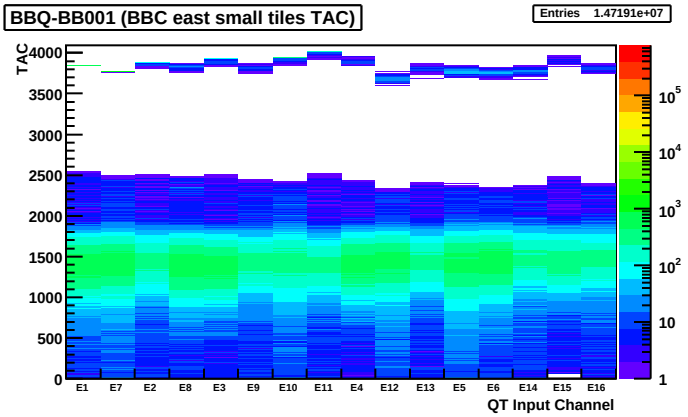

MIX-TF006
Entries 3.863765e+07
TOF MULT
TOF tray

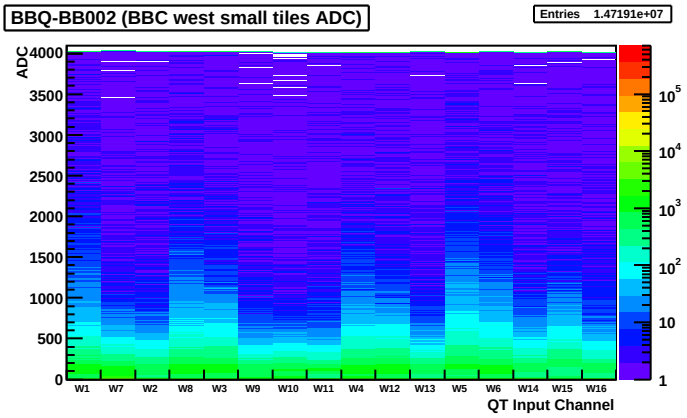

MIX-TF101
Entries 5519664
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

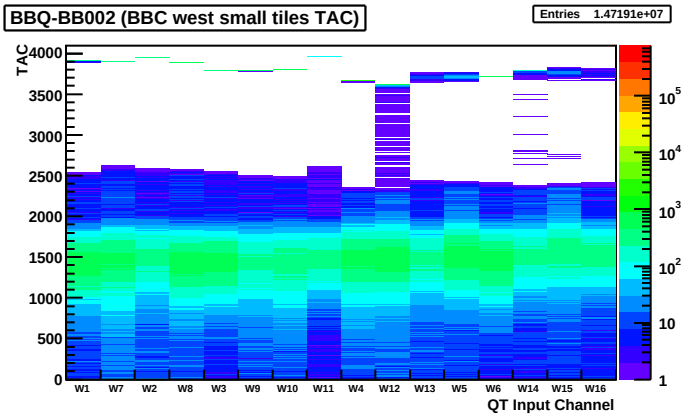

L1-TF201
Entries 919944
TOF total mult

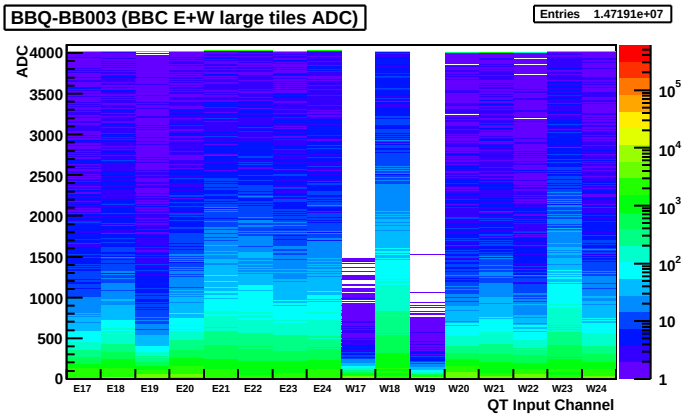

L1-TF201
Entries 5519664
Threshold bits
655180
652315
655627
649979
645967
623808
264764
267629
264317
269965
273977
296136
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

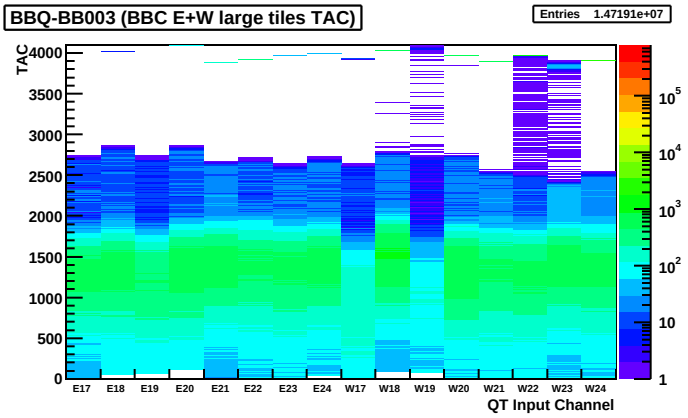

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.47191e+07
ADC
QT Input Channel

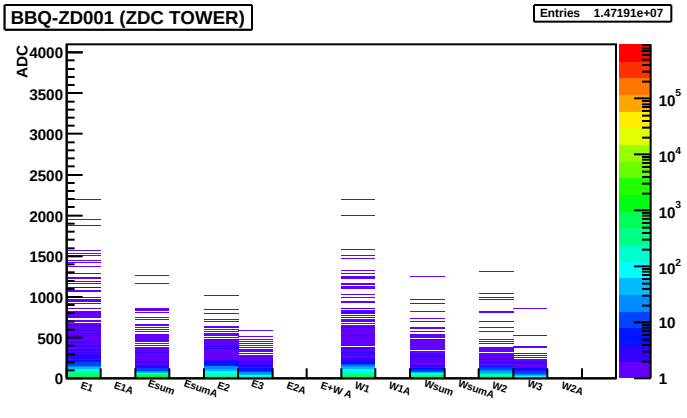

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.47191e+07
TAC
QT Input Channel

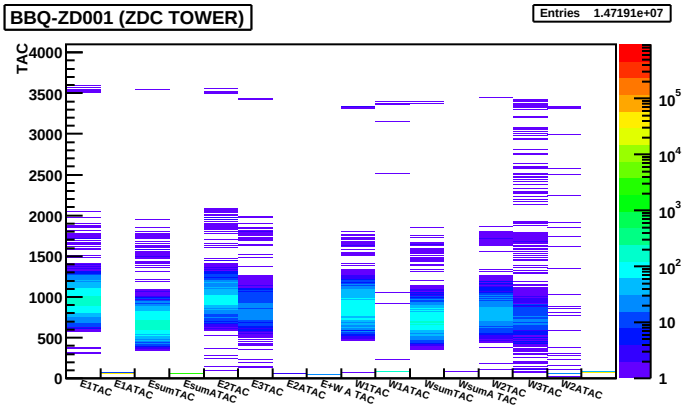

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.47191e+07
ADC
QT Input Channel

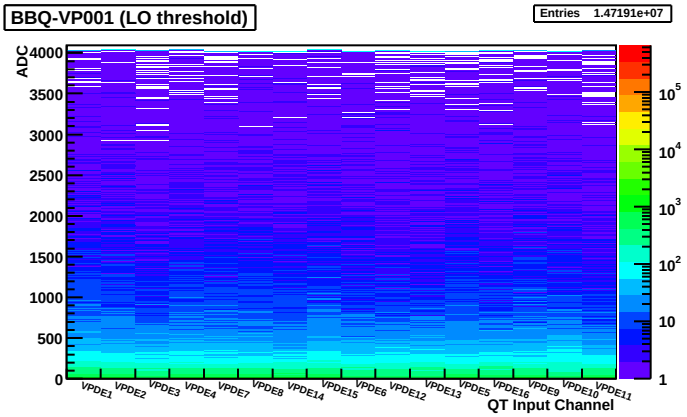

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.47191e+07
TAC
QT Input Channel

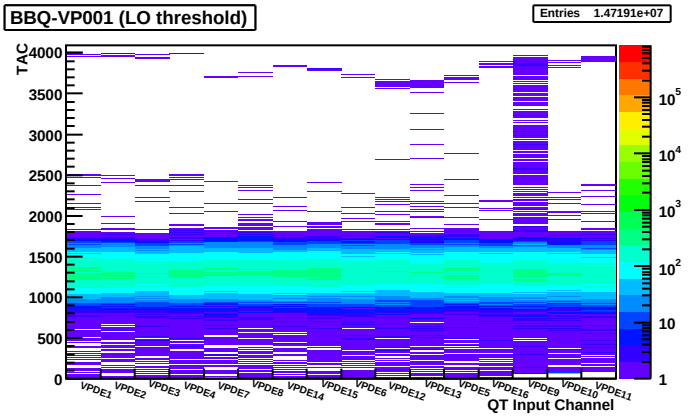

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.47191e+07
ADC
QT Input Channel

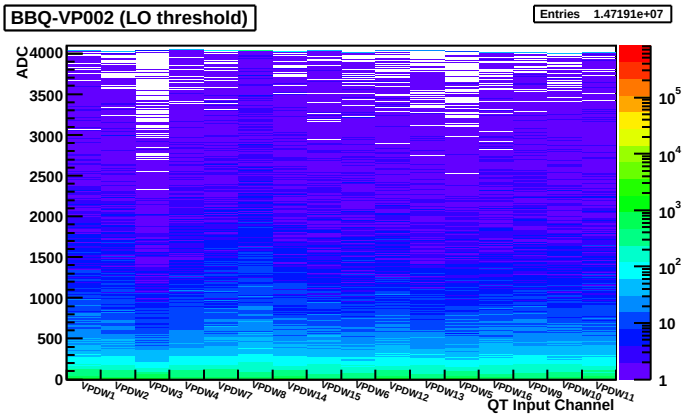

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.47191e+07
TAC
QT Input Channel

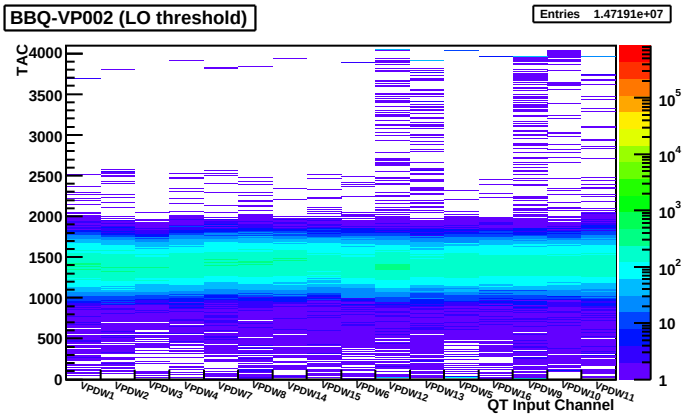

BBQ-ZD001 (ZDC TOWER)
Entries 1.47191e+07
ADC

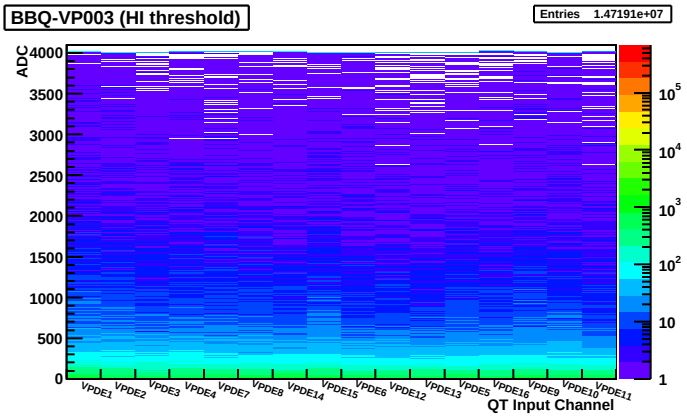

BBQ-ZD001 (ZDC TOWER)
Entries 1.47191e+07
TAC

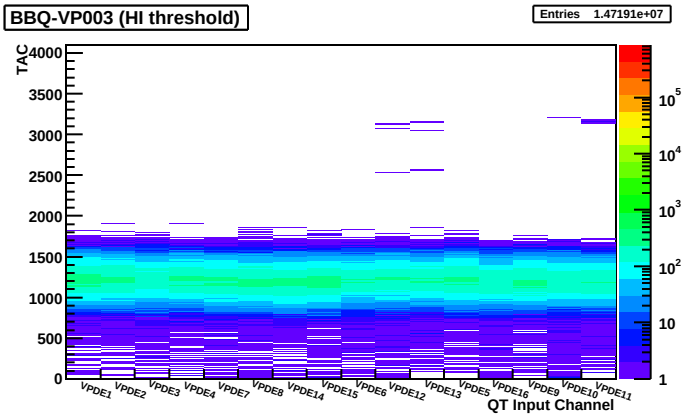

BBQ-VP001 (LO threshold)
Entries 1.47191e+07
ADC
QT Input Channel

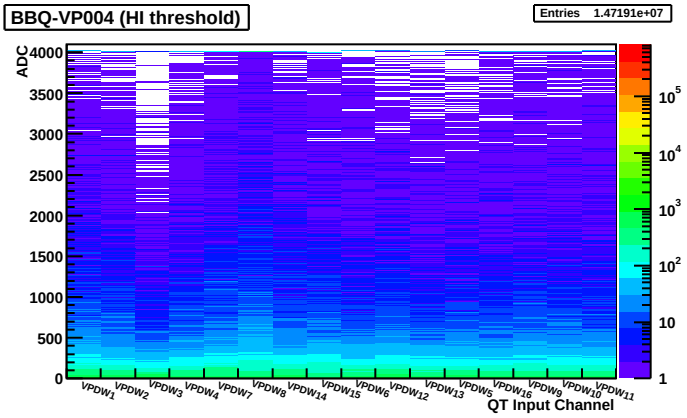

BBQ-VP001 (LO threshold)
Entries 1.47191e+07
TAC
QT Input Channel

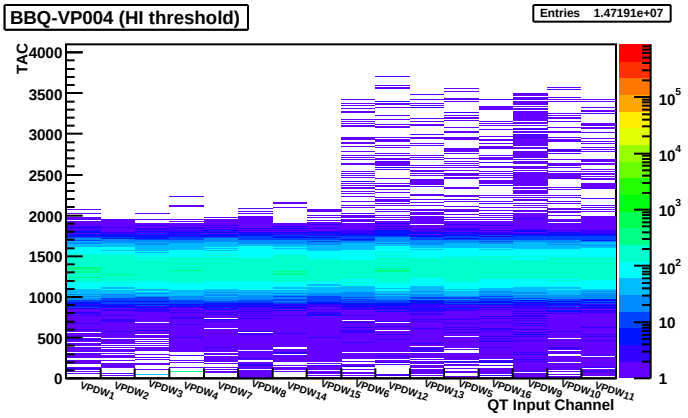

BBQ-VP002 (LO threshold)
Entries 1.47191e+07
ADC
QT Input Channel

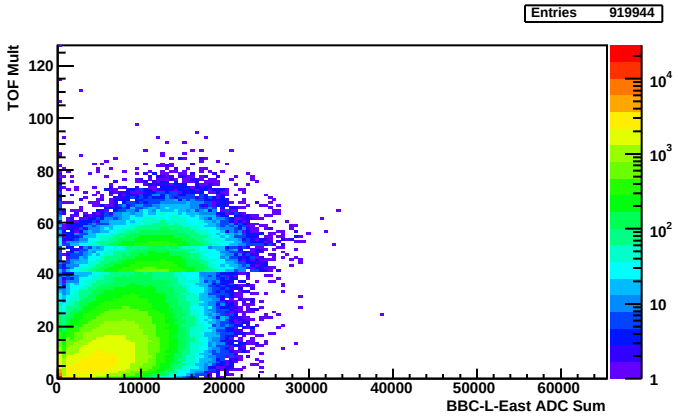

BBQ-VP002 (LO threshold)
Entries 1.47191e+07
TAC
QT Input Channel

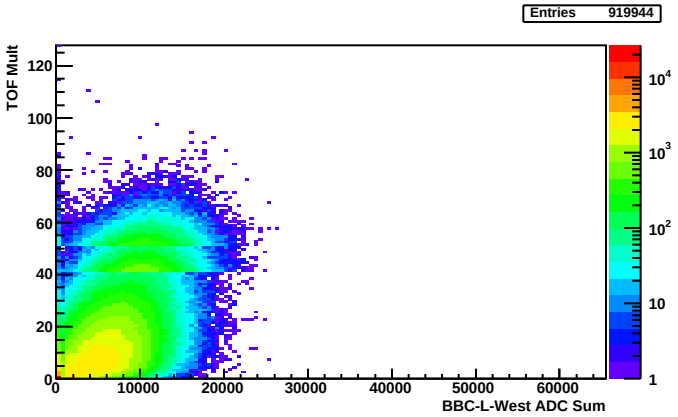

BBQ-VP003 (HI threshold)
Entries 1.47191e+07
ADC
QT Input Channel

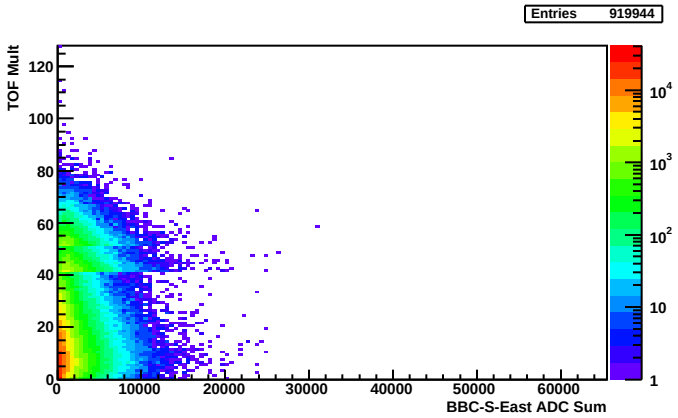

BBQ-VP003 (HI threshold)
Entries 1.47191e+07
TAC
QT Input Channel

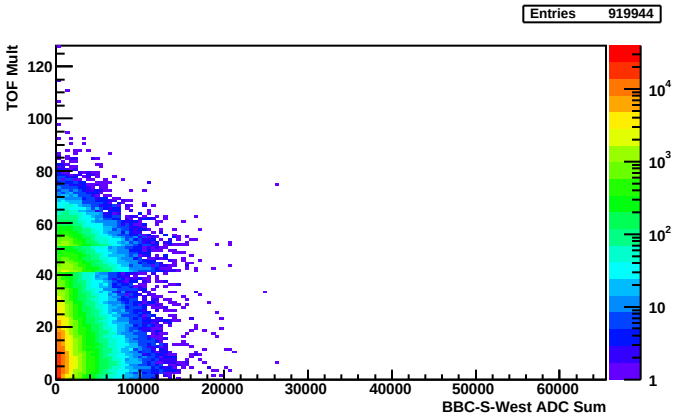

BBQ-VP004 (HI threshold)
Entries 1.47191e+07
ADC
QT Input Channel

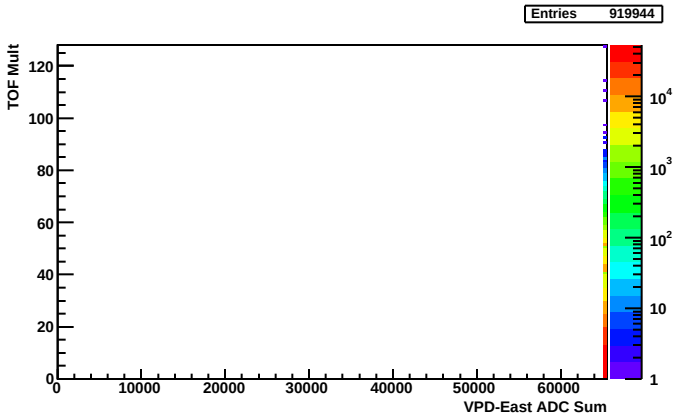

BBQ-VP004 (HI threshold)
Entries 1.47191e+07
TAC
QT Input Channel

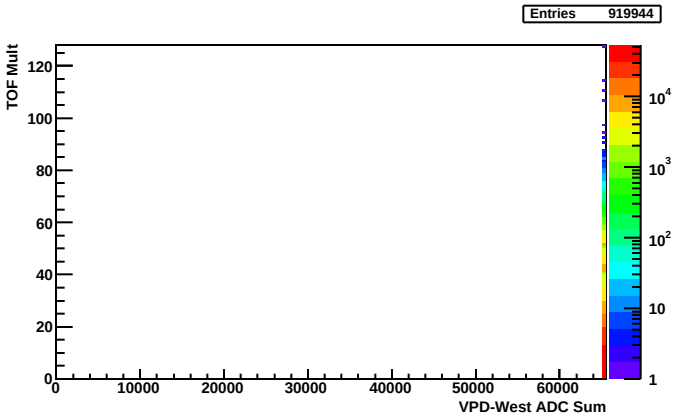

Entries 919944
TOF Mult
BBC-L-East ADC Sum

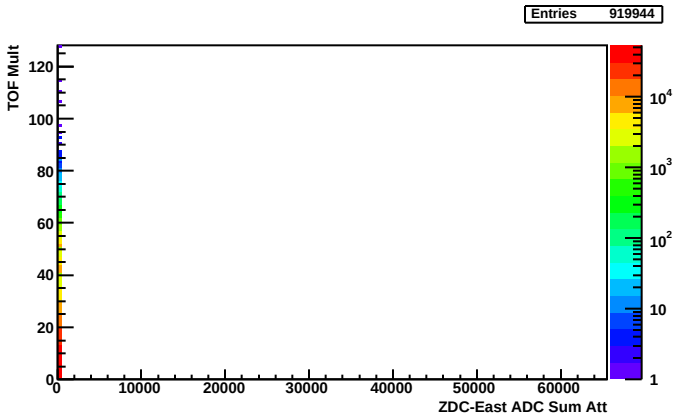

Entries 919944
TOF Mult
BBC-L-West ADC Sum

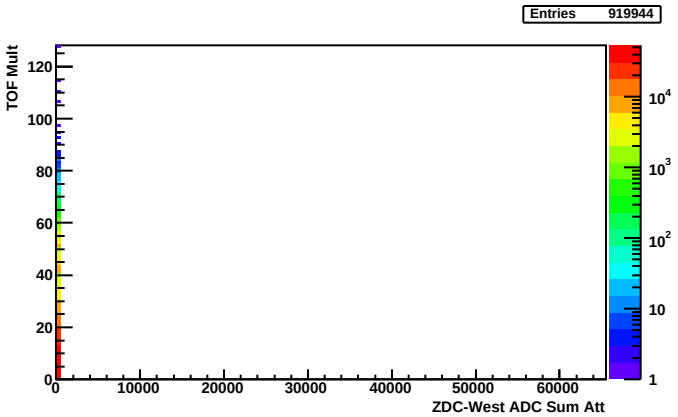

Entries 919944
TOF Mult
BBC-S-East ADC Sum

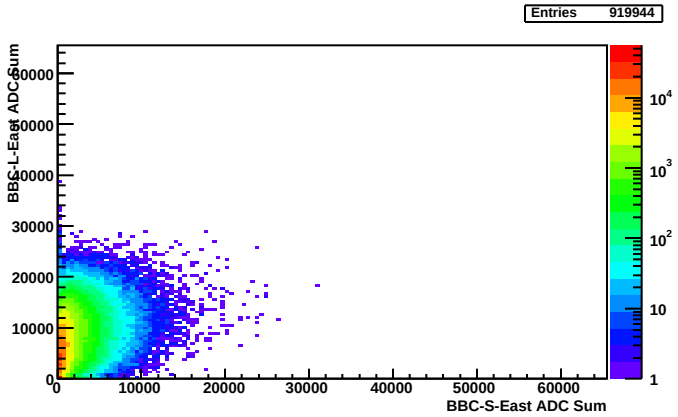

Entries 919944
TOF Mult
BBC-S-West ADC Sum

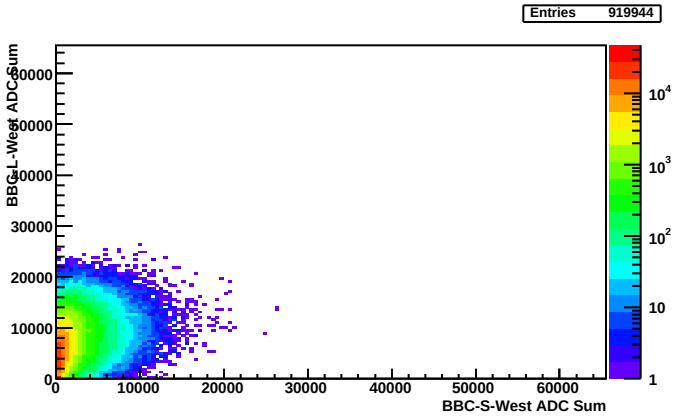

Entries 919944
TOF Mult
VPD-East ADC Sum

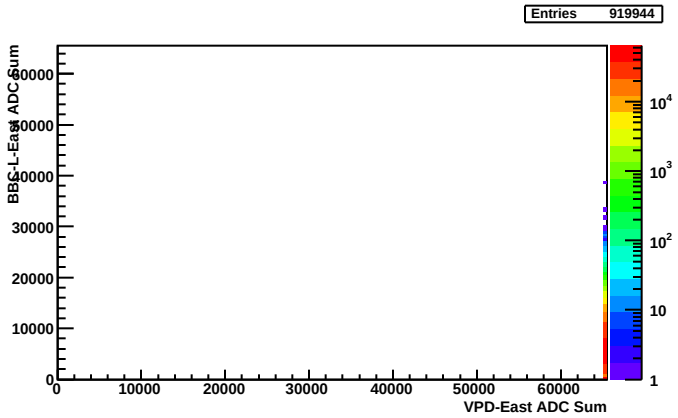

Entries 919944
TOF Mult
VPD-West ADC Sum


Entries 919944
TOF Mult
ZDC-East ADC Sum Att


Entries 919944
TOF Mult
ZDC-West ADC Sum Att


Entries 919944
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 919944
BBC-L-West ADC Sum
BBC-S-West ADC Sum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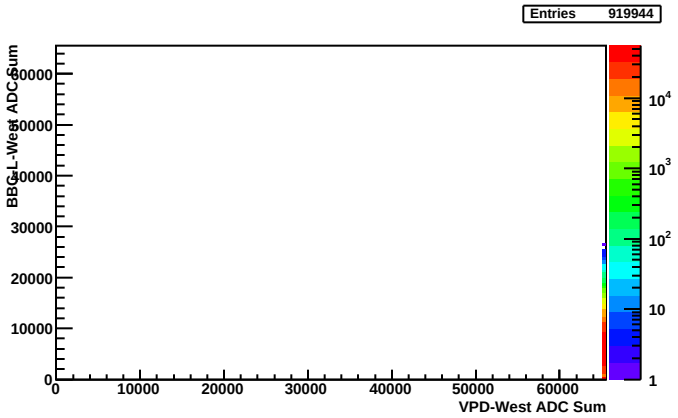
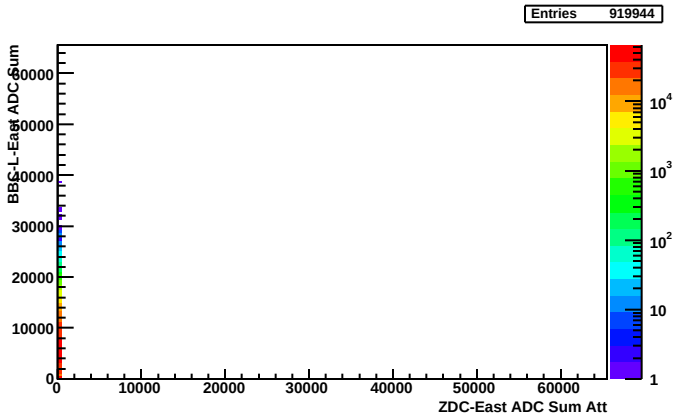
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 919944
BBC-L-West ADC Sum
VPD-West ADC Sum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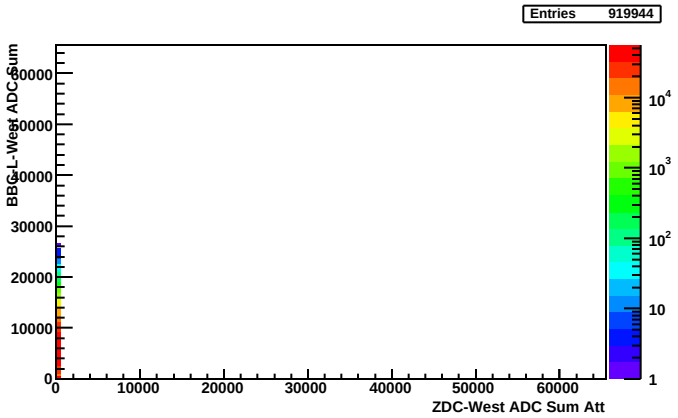
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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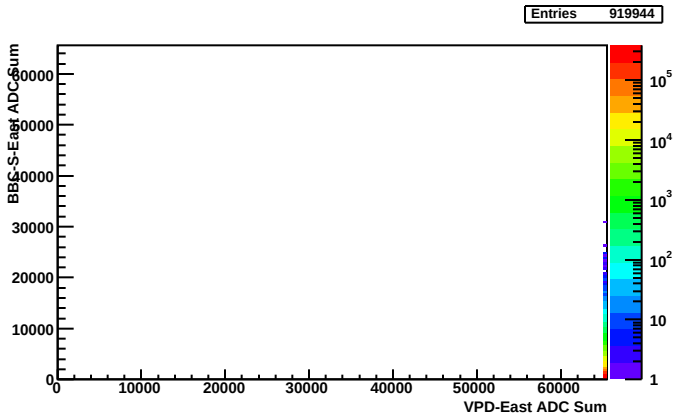
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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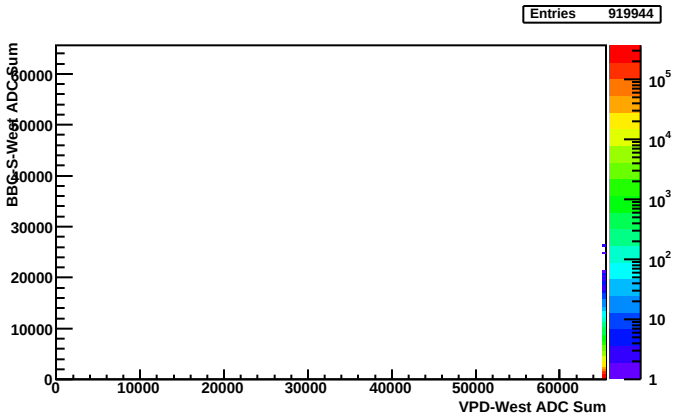
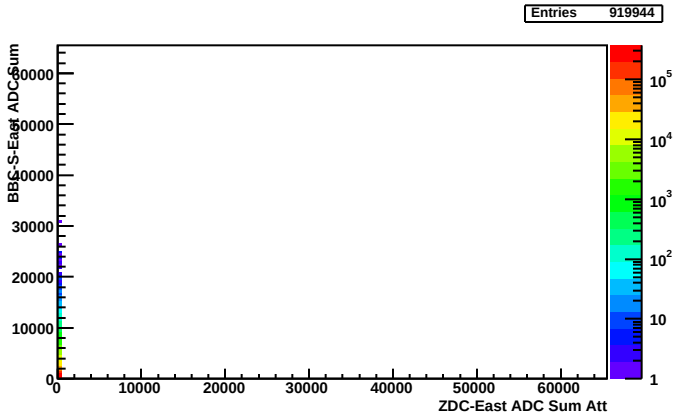
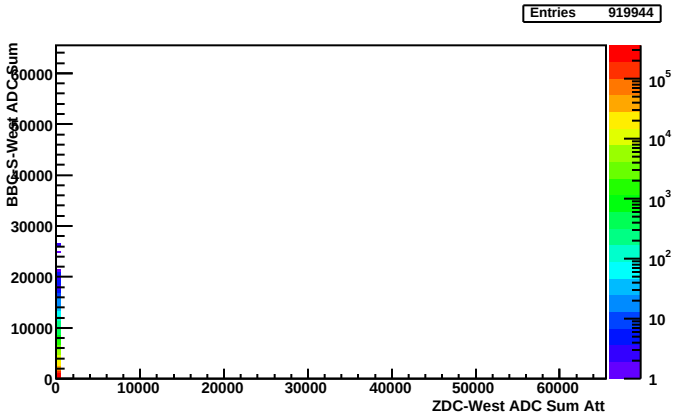
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 919944
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 919944
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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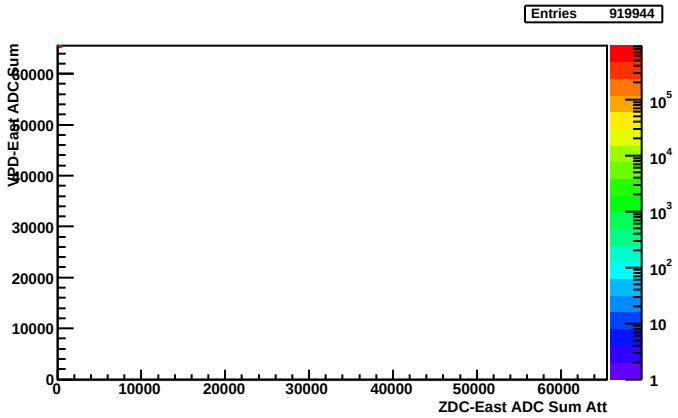
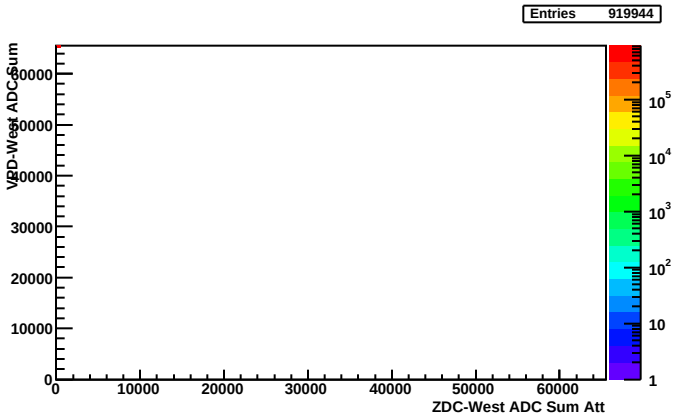
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 919944
VPD-East ADC Sum
ZDC-East ADC Sum Att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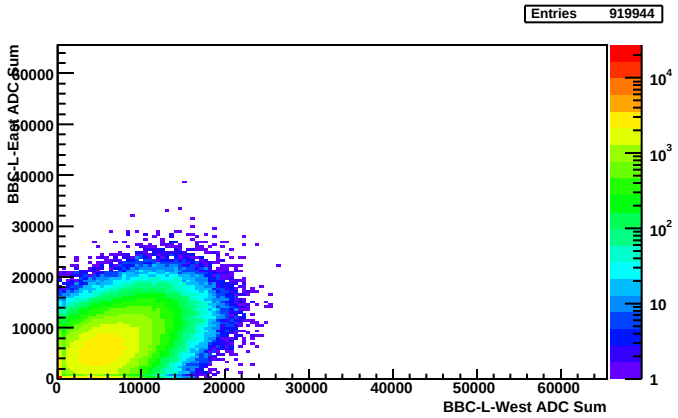
VPD-West ADC Sum
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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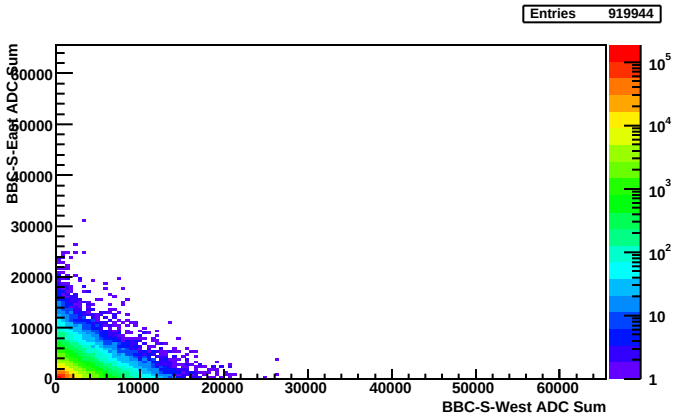
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BBC-S-East ADC Sum
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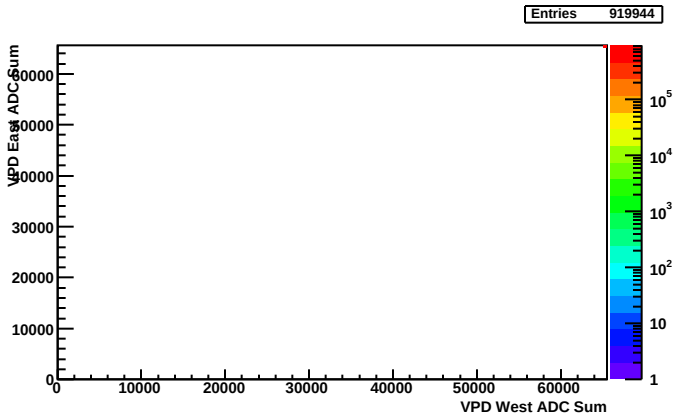
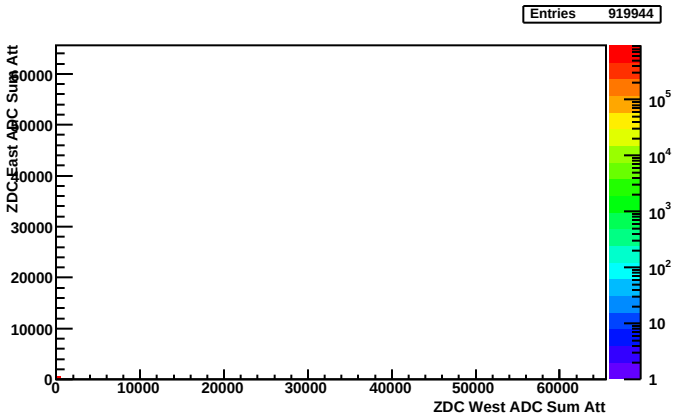
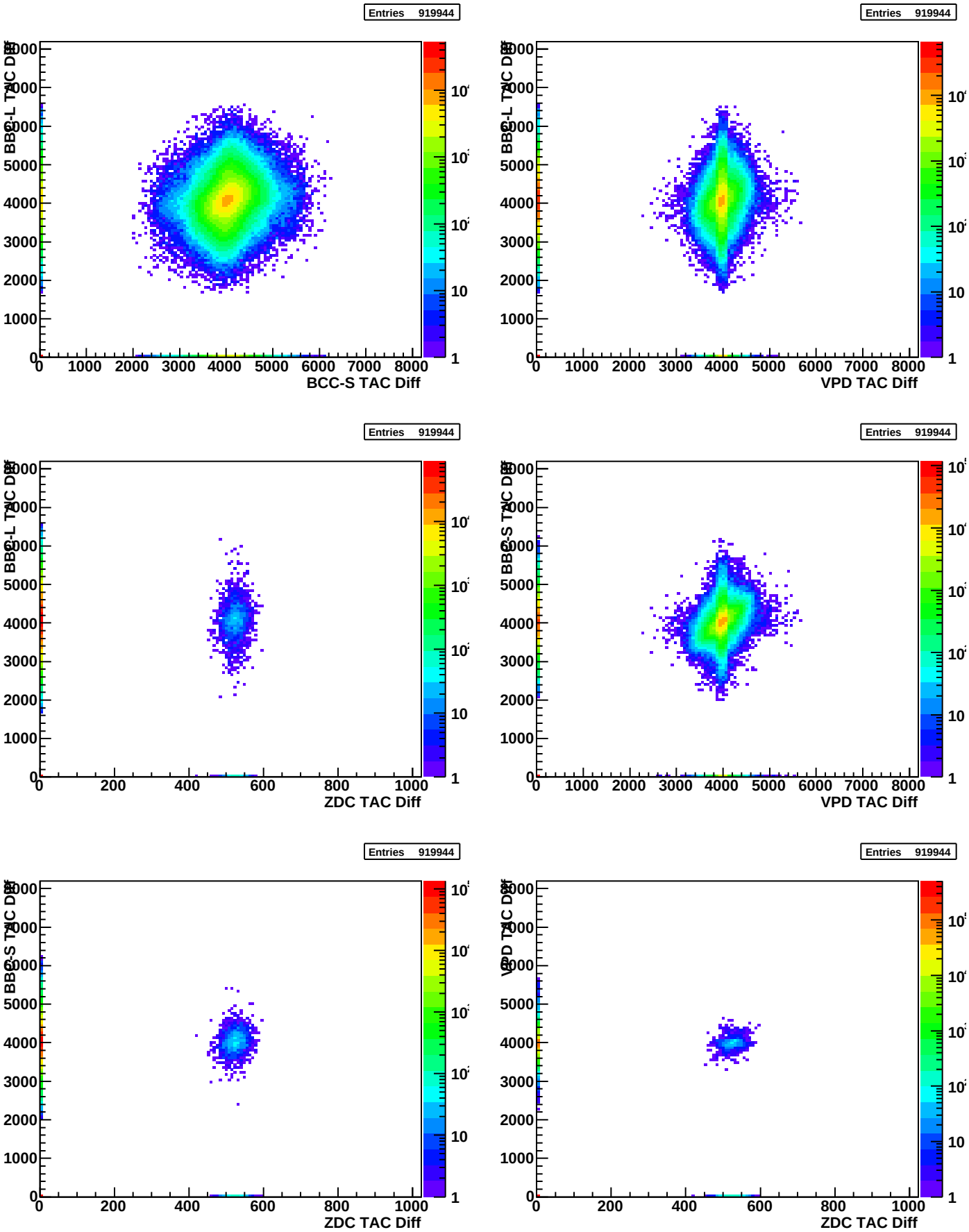
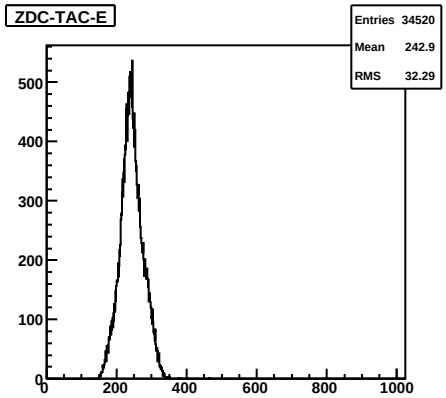
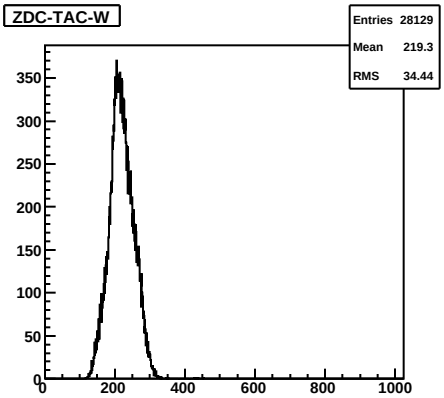
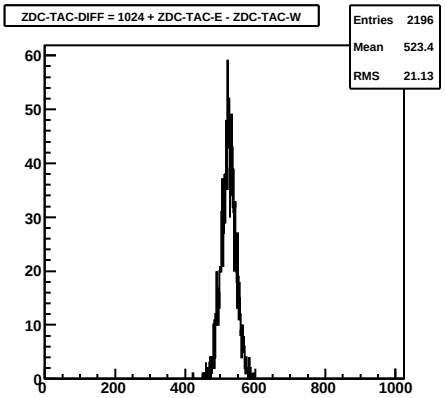

Entries 919944
VPD East ADC Sum
VPD West ADC Sum

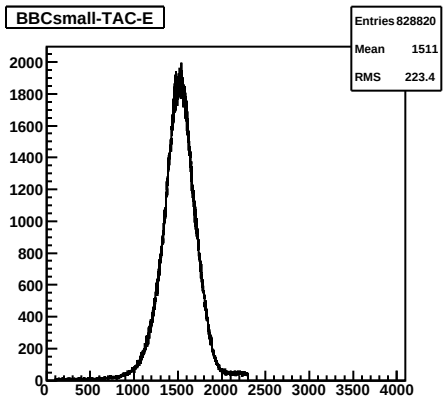

Entries 919944
ZDC East ADC Sum Att
ZDC West ADC Sum Att

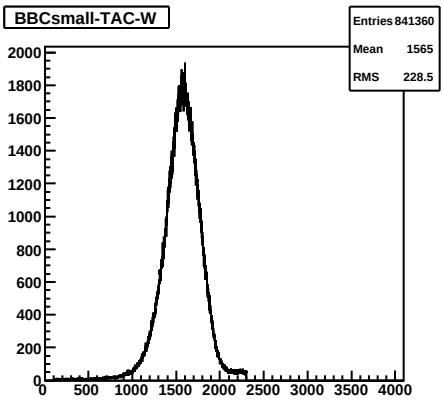

Entries 919944
BBC-L TAC Diff
BCC-S TAC Diff
Entries 919944
BBC-L TAC Diff
VPD TAC Diff
Entries 919944
BBC-L TAC Diff
ZDC TAC Diff
Entries 919944
BBC-S TAC Diff
VPD TAC Diff
Entries 919944
BBC-S TAC Diff
ZDC TAC Diff
Entries 919944
VPD TAC Diff
ZDC TAC Diff

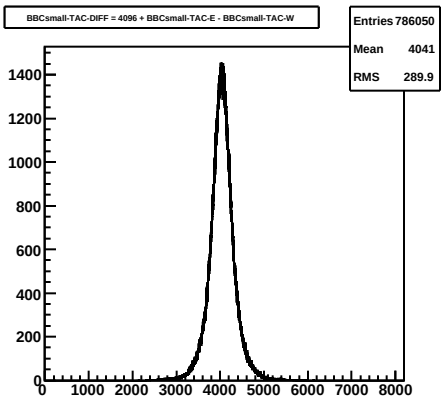

ZDC-TAC-E
Entries 34520
Mean 242.9
RMS 32.29

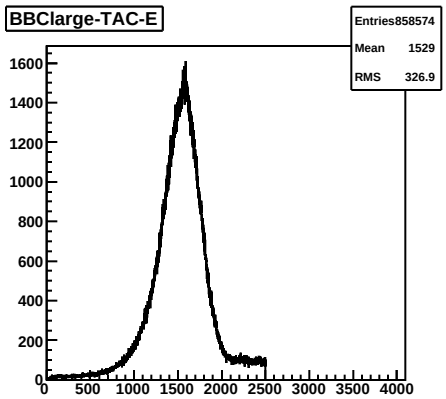

ZDC-TAC-W
Entries 28129
Mean 219.3
RMS 34.44

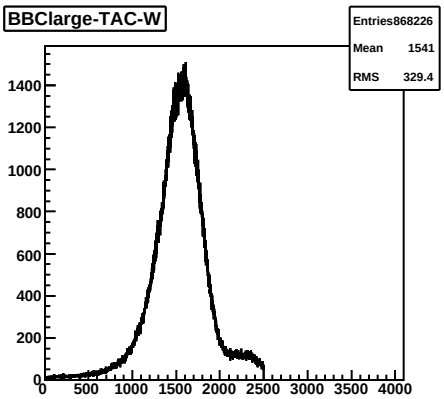

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2196
Mean 523.4
RMS 21.13

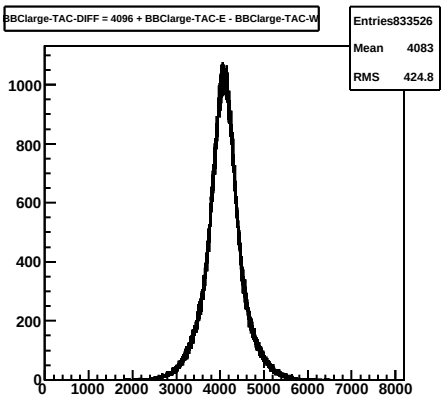

BBCsmall-TAC-E
Entries 828820
Mean 1511
RMS 223.4

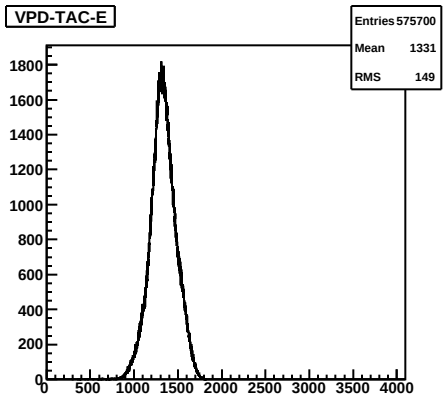

BBCsmall-TAC-W
Entries 841360
Mean 1565
RMS 228.5

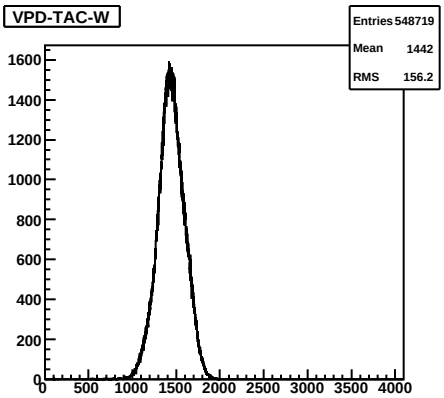

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 786050
Mean 4041
RMS 289.9

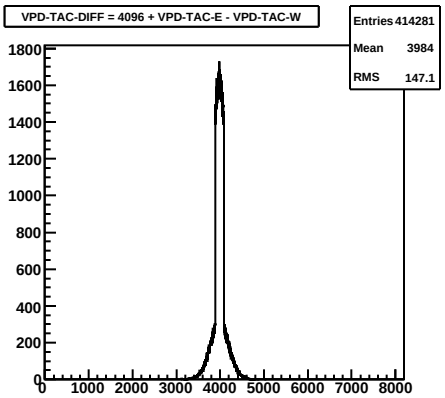

BBClarge-TAC-E
Entries 858574
Mean 1529
RMS 326.9

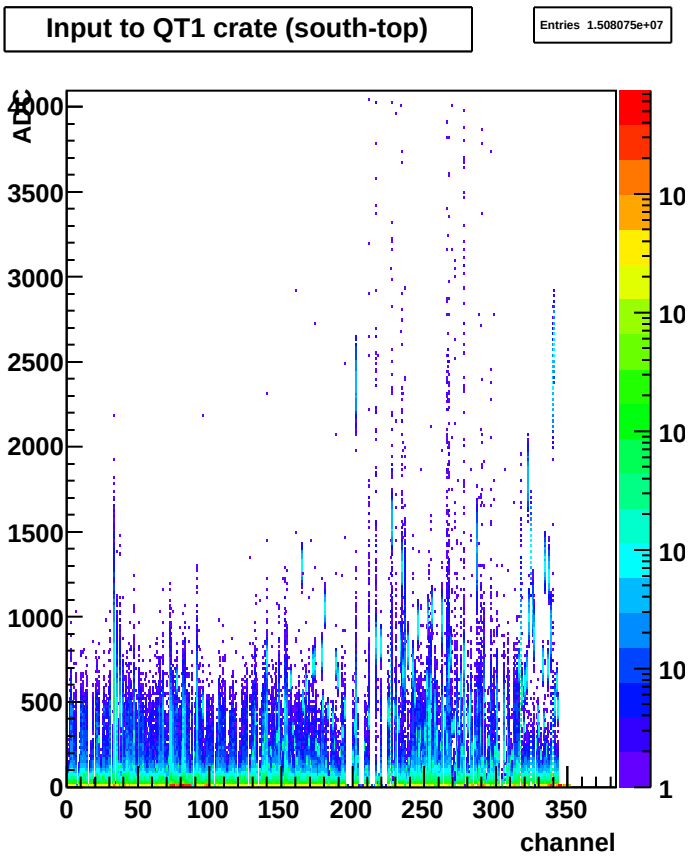

BBClarge-TAC-W
Entries 868226
Mean 1541
RMS 329.4

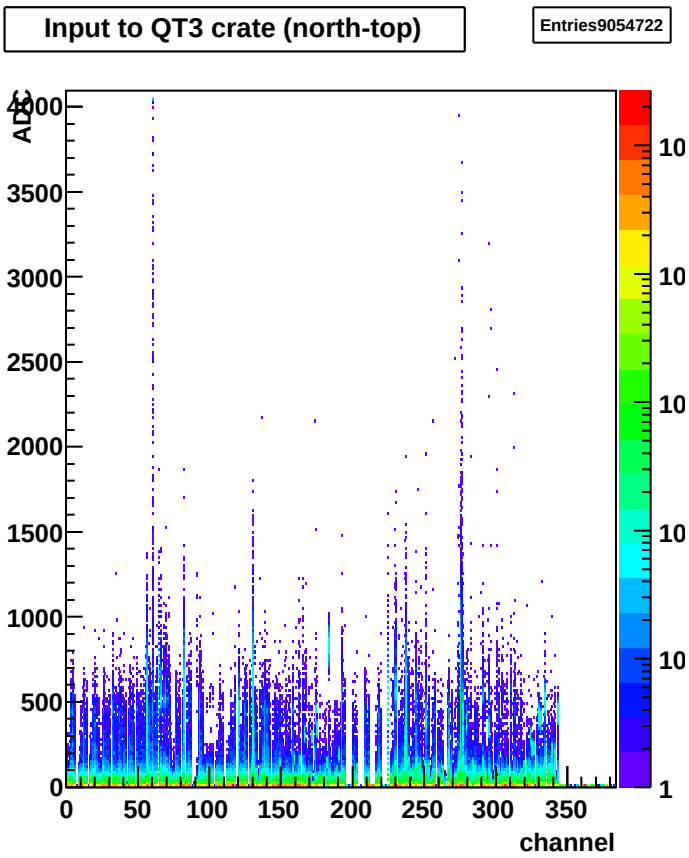

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 833526
Mean 4083
RMS 424.8

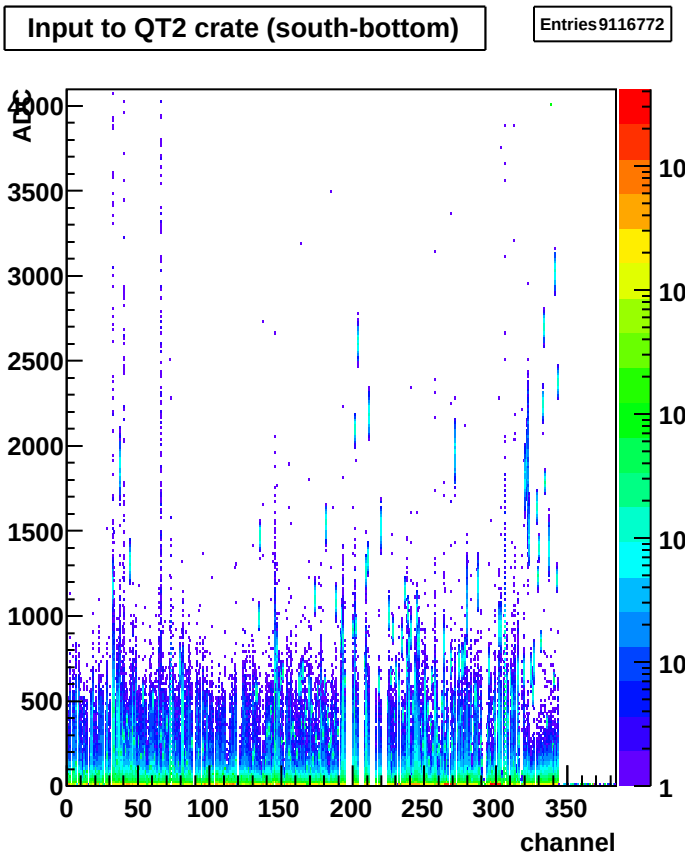

VPD-TAC-E
Entries 575700
Mean 1331
RMS 149

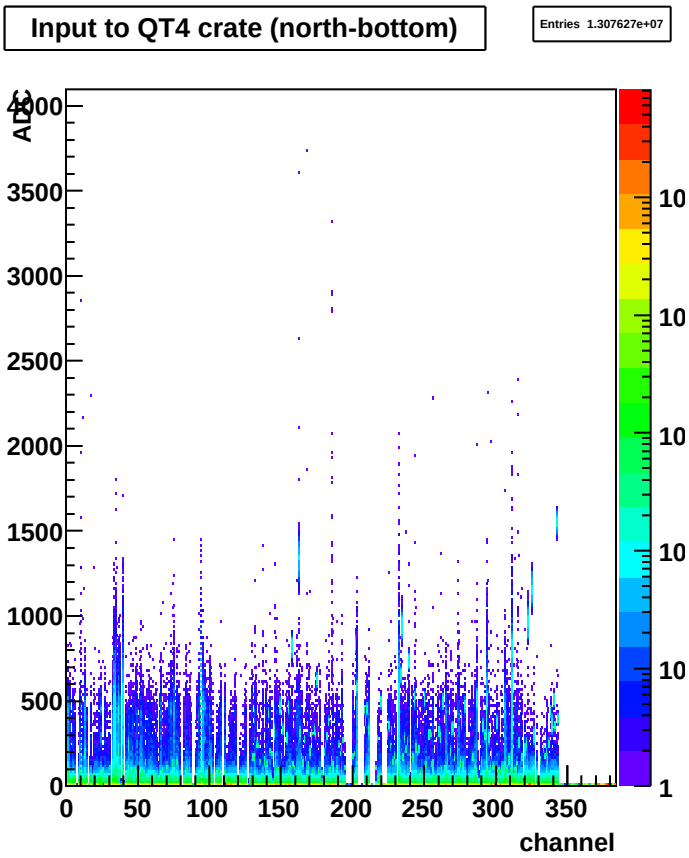

VPD-TAC-W
Entries 548719
Mean 1442
RMS 156.2

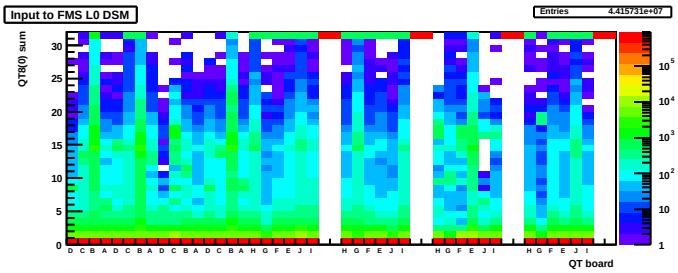

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 414281
Mean 3984
RMS 147.1

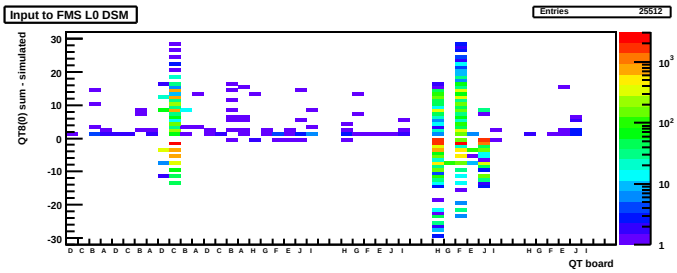

Input to QT1 crate (south-top)
Entries 1.508075e+07
ADC
channel

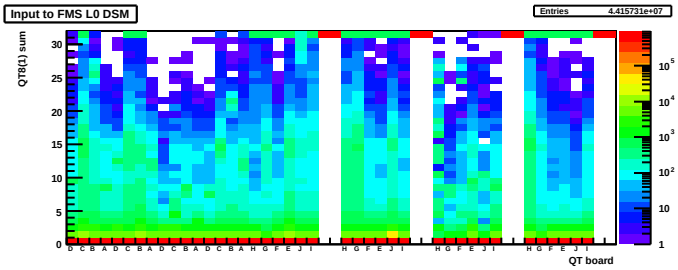

Input to QT3 crate (north-top)
Entries9054722
ADC
channel

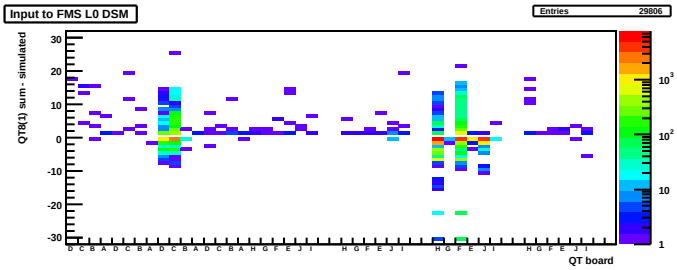

Input to QT2 crate (south-bottom)
Entries9116772
ADC
channel

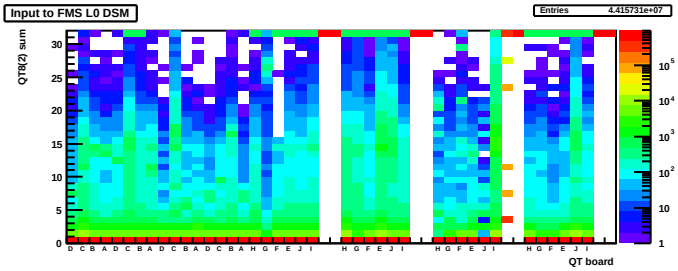

Input to QT4 crate (north-bottom)
Entries 1.307627e+07
ADC
channel

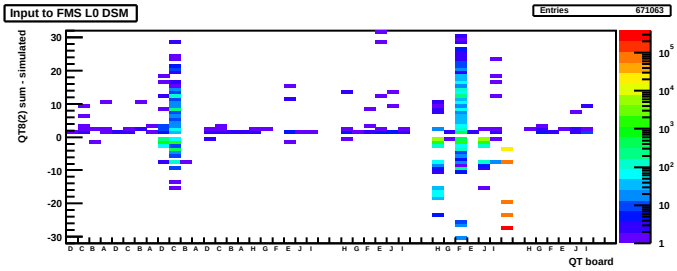

Input to FMS L0 DSM
Entries 4.415731e+07
QT8(0) sum
QT board

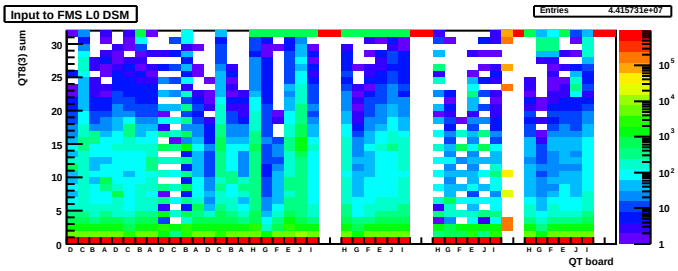

Input to FMS L0 DSM
Entries 25512
QT8(0) sum - simulated
QT board

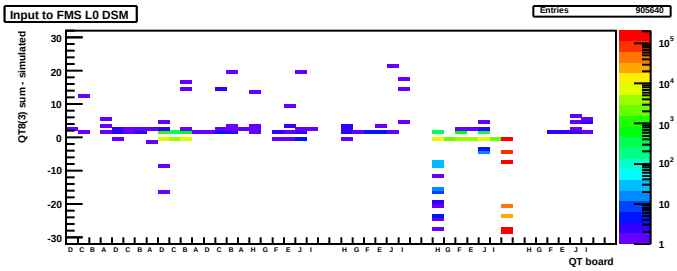

Input to FMS L0 DSM
Entries 4.415731e+07
QT8(1) sum
QT board

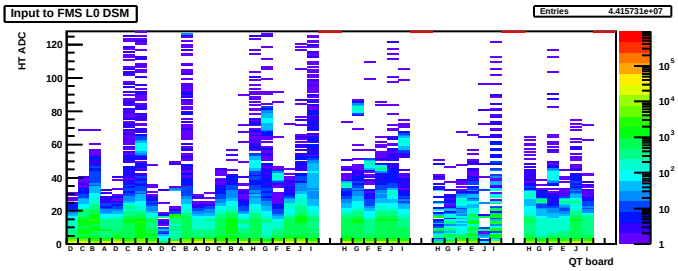

Input to FMS L0 DSM
Entries 29806
QT8(1) sum - simulated
QT board

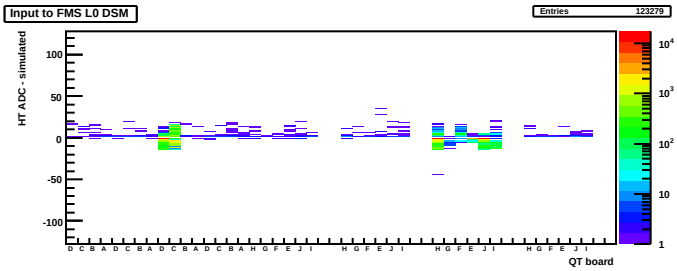

Input to FMS L0 DSM
Entries 4.415731e+07
QT8(2) sum
QT board

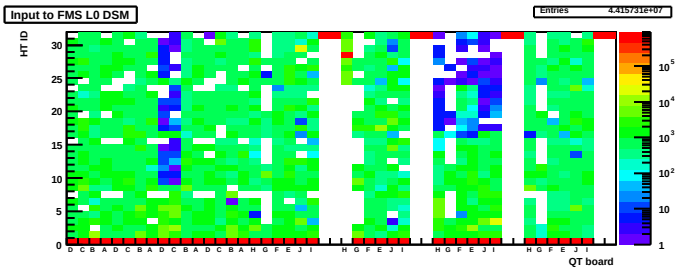

Input to FMS L0 DSM
Entries 671063
QT8(2) sum - simulated
QT board

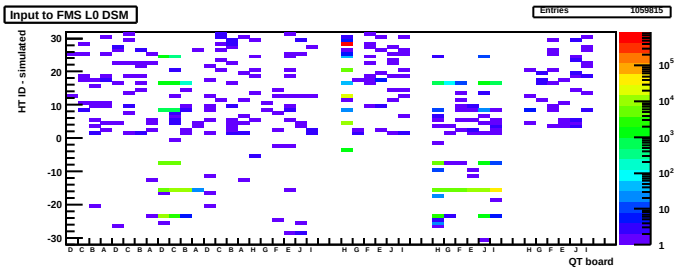

Input to FMS L0 DSM
Entries 4.415731e+07
QT8(3) sum
QT board

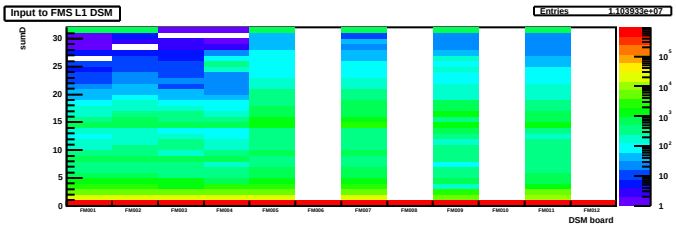

Input to FMS L0 DSM
Entries 905640
QT8(3) sum - simulated
QT board

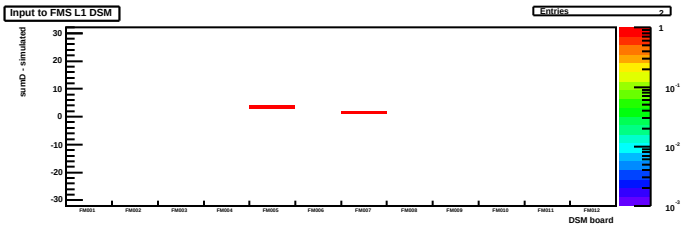

Input to FMS L0 DSM
Entries 4.415731e+07
HT ADC
QT board

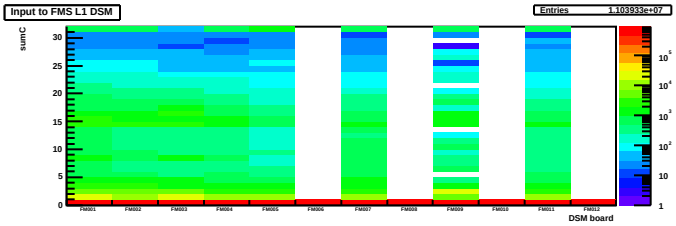

Input to FMS L0 DSM
Entries 123279
HT ADC - simulated
QT board

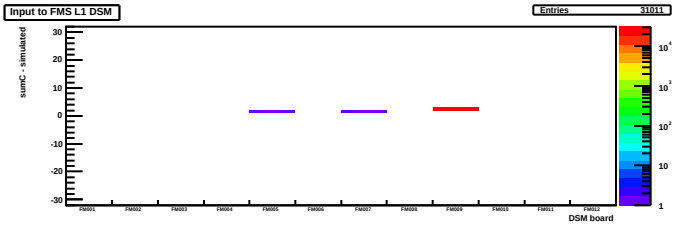

Input to FMS L0 DSM
Entries 4.415731e+07
HT ID
QT board

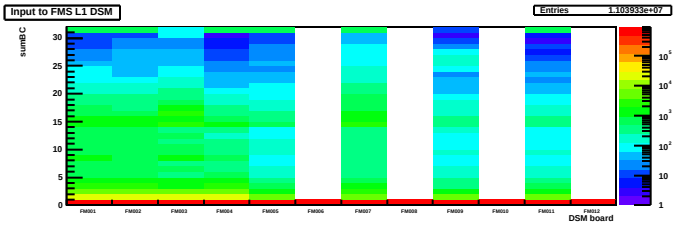

Input to FMS L0 DSM
Entries 1059815
HT ID - simulated
QT board

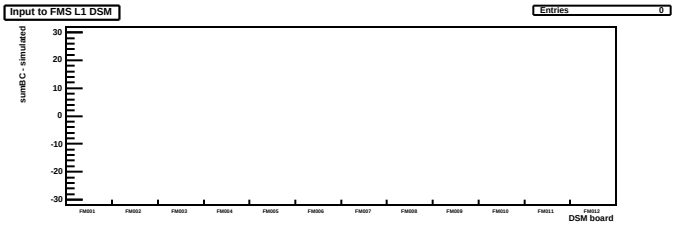

Input to FMS L1 DSM
Entries 1.103933e+07
sumD
DSM board

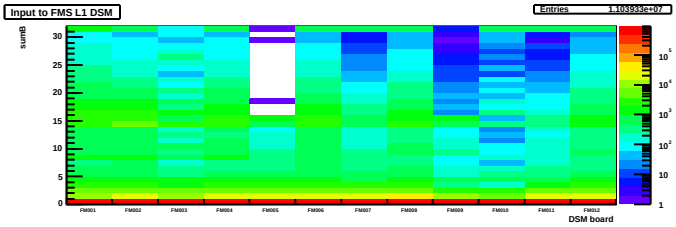

Input to FMS L1 DSM
Entries 2
sumD - simulated
DSM board

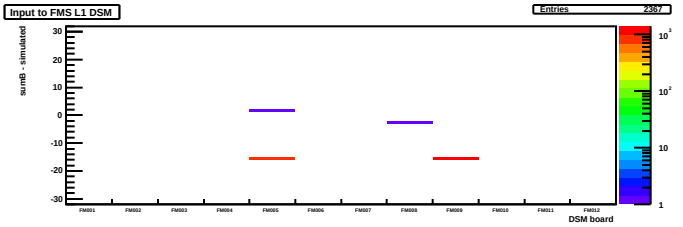

Input to FMS L1 DSM
Entries 1.103933e+07
sumC
DSM board

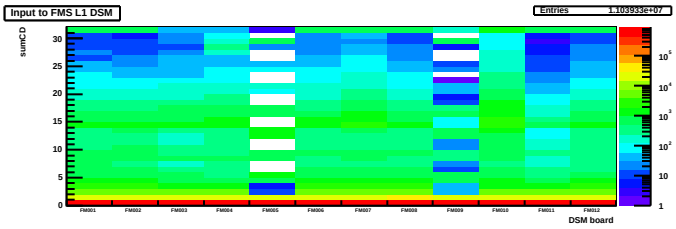

Input to FMS L1 DSM
Entries 31011
sumC - simulated
DSM board

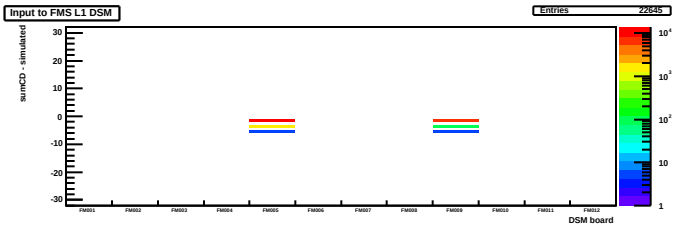

Input to FMS L1 DSM
Entries 1.103933e+07
sumBC
DSM board

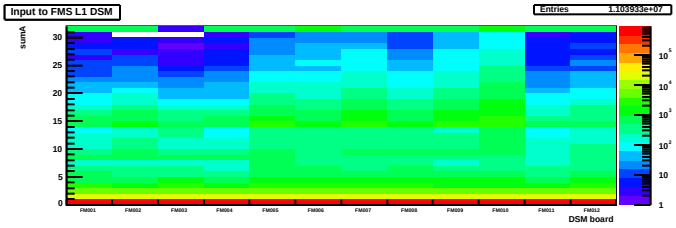

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

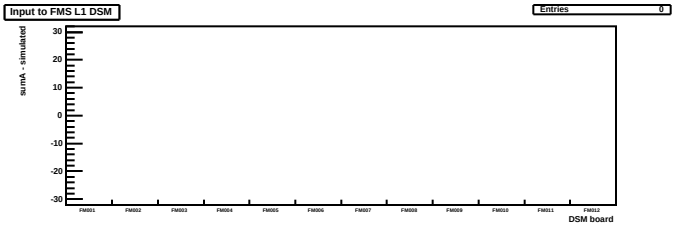

Input to FMS L1 DSM
Entries 1.103933e+07
sumB
DSM board

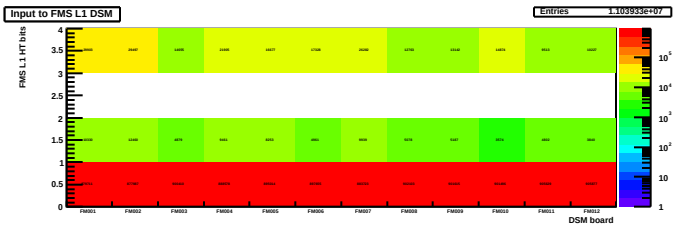

Input to FMS L1 DSM
Entries 2367
sumB - simulated
DSM board

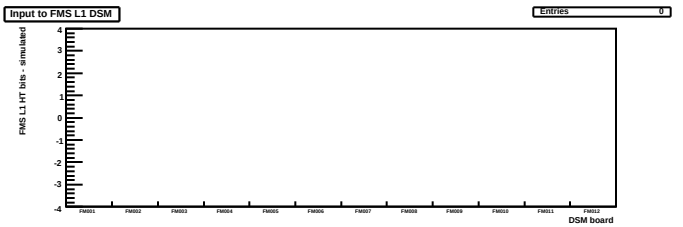

Input to FMS L1 DSM
Entries 1.103933e+07
sumCD
DSM board

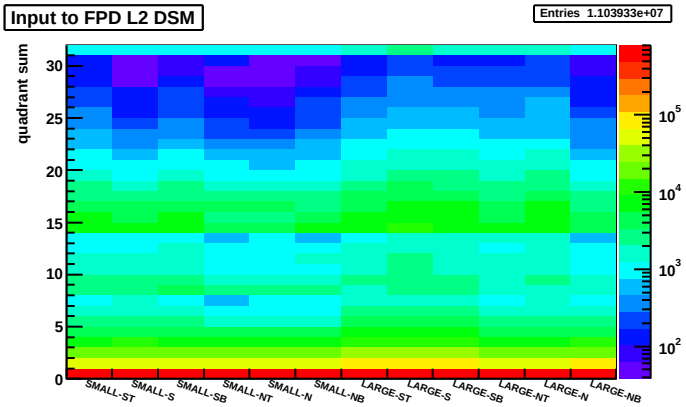

Input to FMS L1 DSM
Entries 22645
sumCD - simulated
DSM board

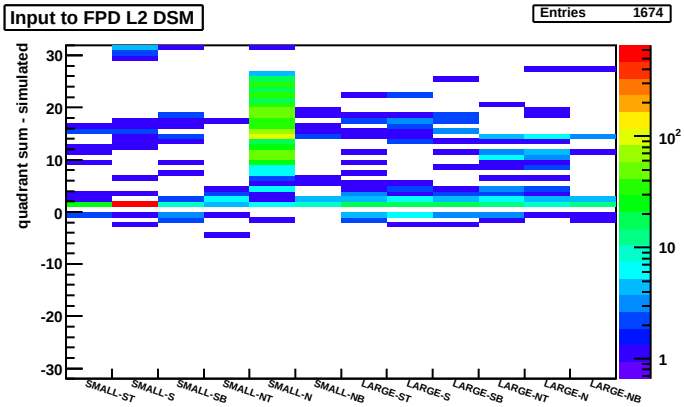

Input to FMS L1 DSM
Entries 1.103933e+07
sumA
DSM board

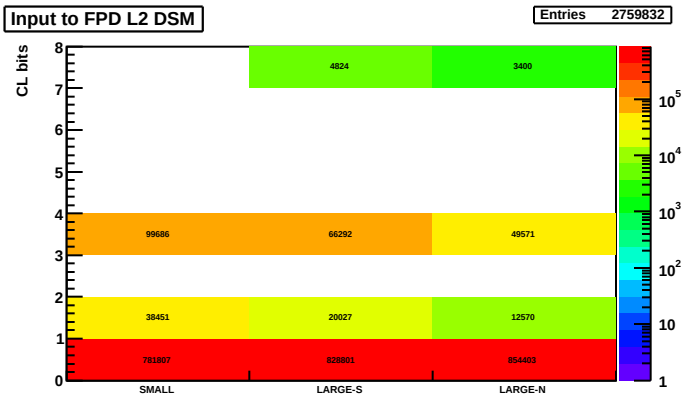

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

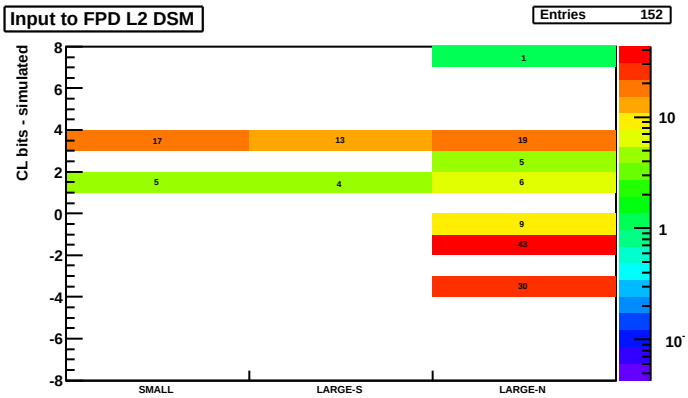

Input to FMS L1 DSM
Entries 1.103933e+07
FMS L1 HT bits
DSM board

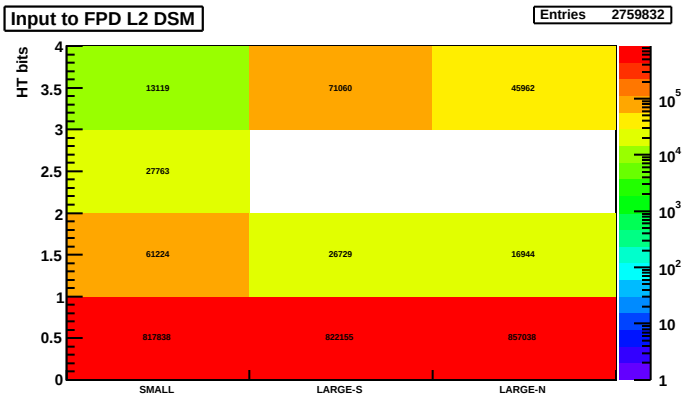

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

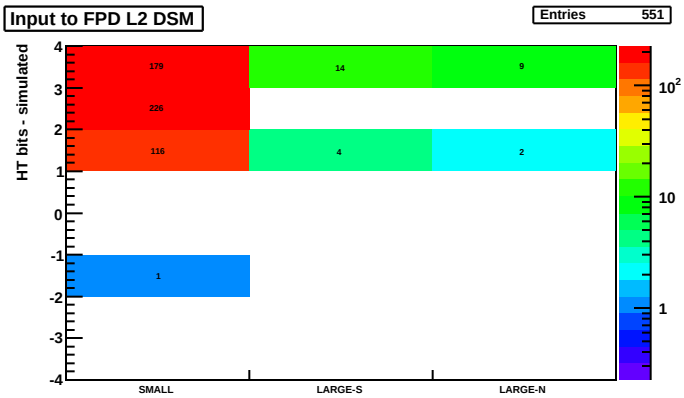

Input to FPD L2 DSM
Entries 1.103933e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

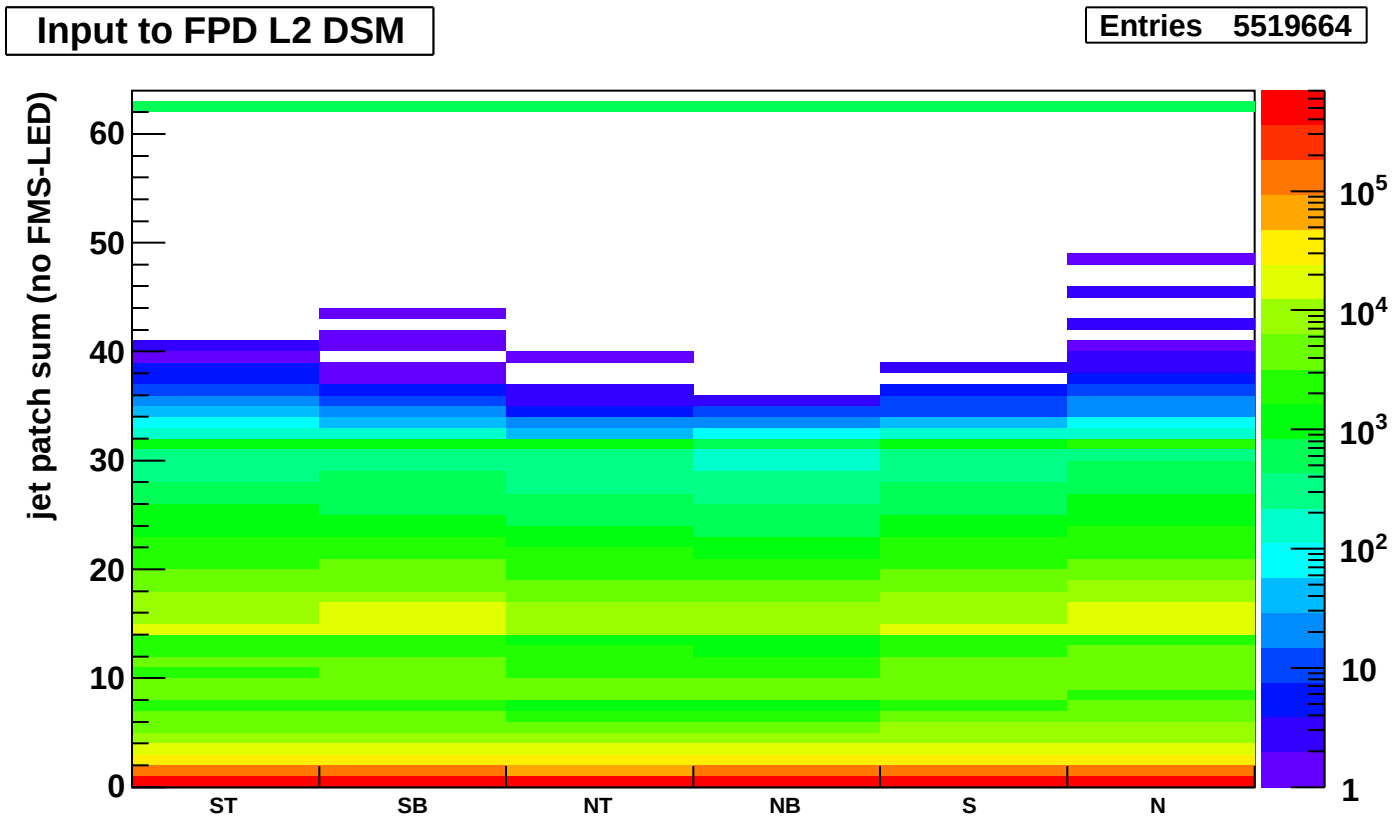

Input to FPD L2 DSM
Entries 1674
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

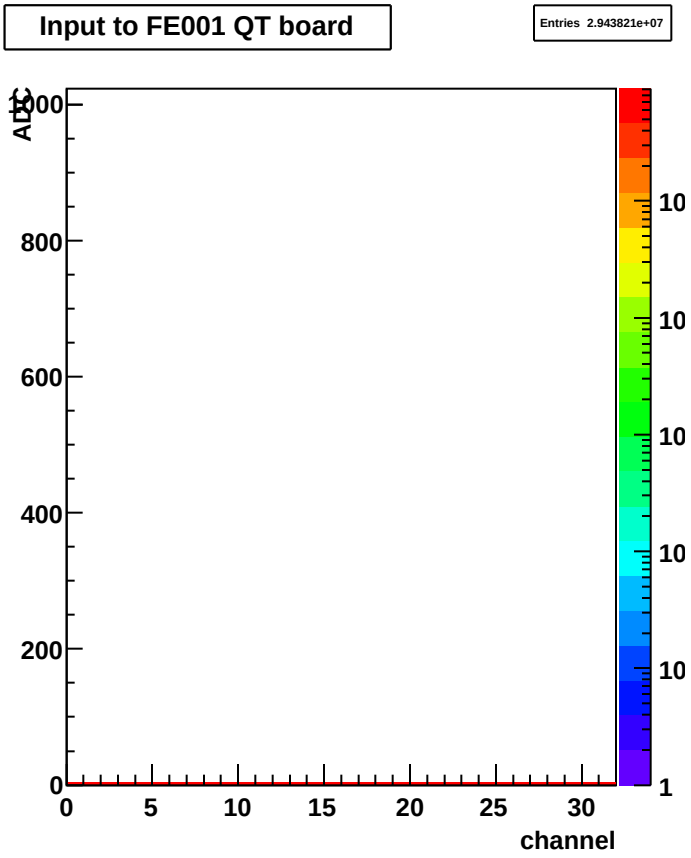

Input to FPD L2 DSM
Entries 2759832
CL bits
4824
3400
99686
66292
49571
38451
20027
12570
781807
828801
854403
SMALL
LARGE-S
LARGE-N

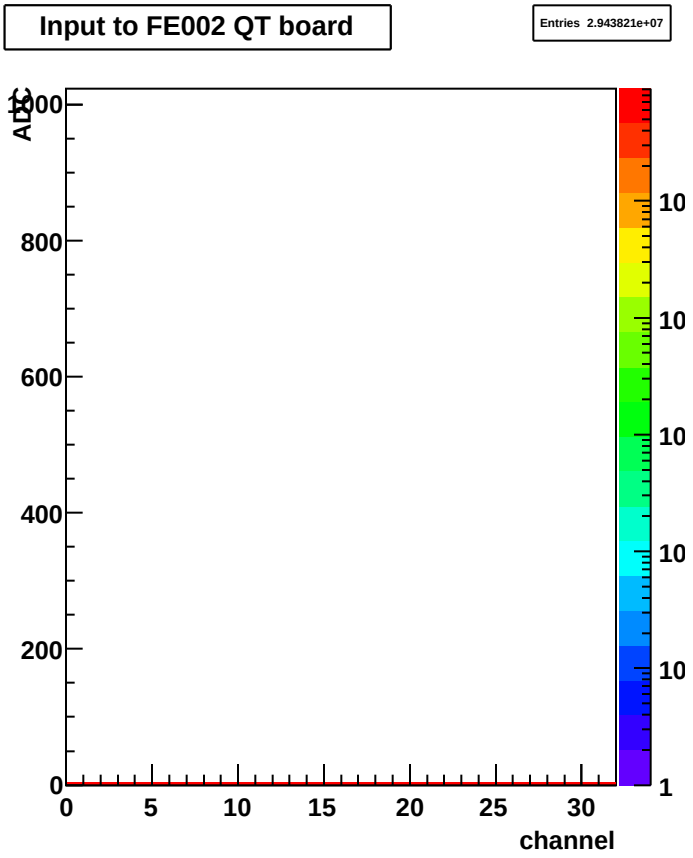

Input to FPD L2 DSM
Entries 152
CL bits - simulated
1
17
13
19
5
5
4
6
9
43
30
SMALL
LARGE-S
LARGE-N

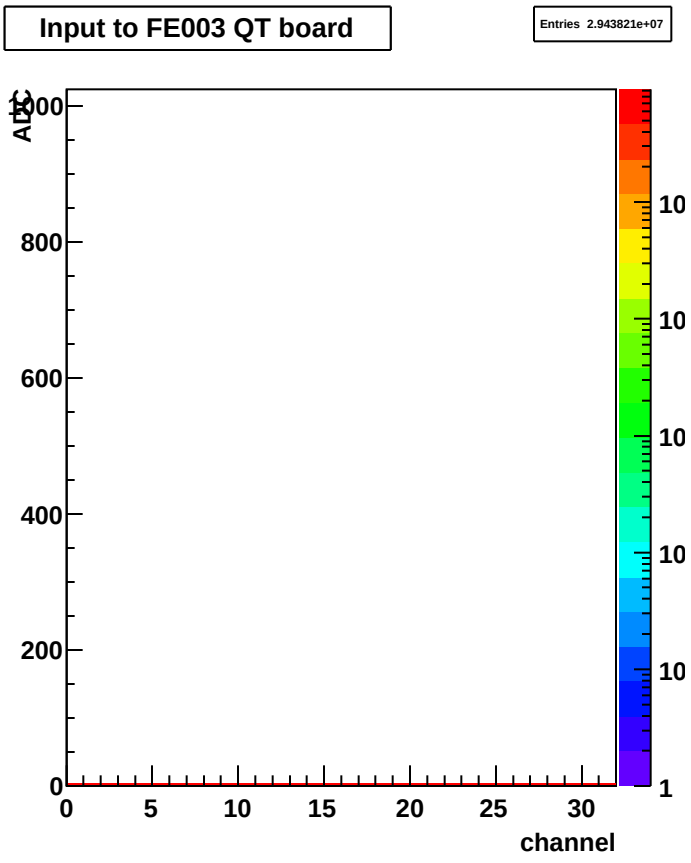

Input to FPD L2 DSM
Entries 2759832
HT bits
13119
71060
45962
27763
61224
26729
16944
817838
822155
857038
SMALL
LARGE-S
LARGE-N

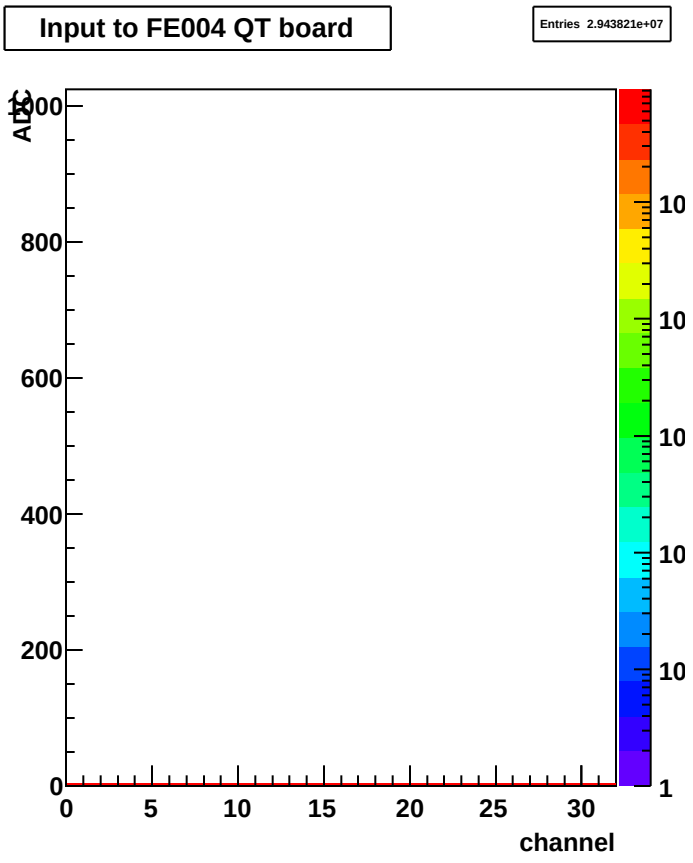

Input to FPD L2 DSM
Entries 551
HT bits - simulated
179
14
9
226
116
4
2
1
SMALL
LARGE-S
LARGE-N

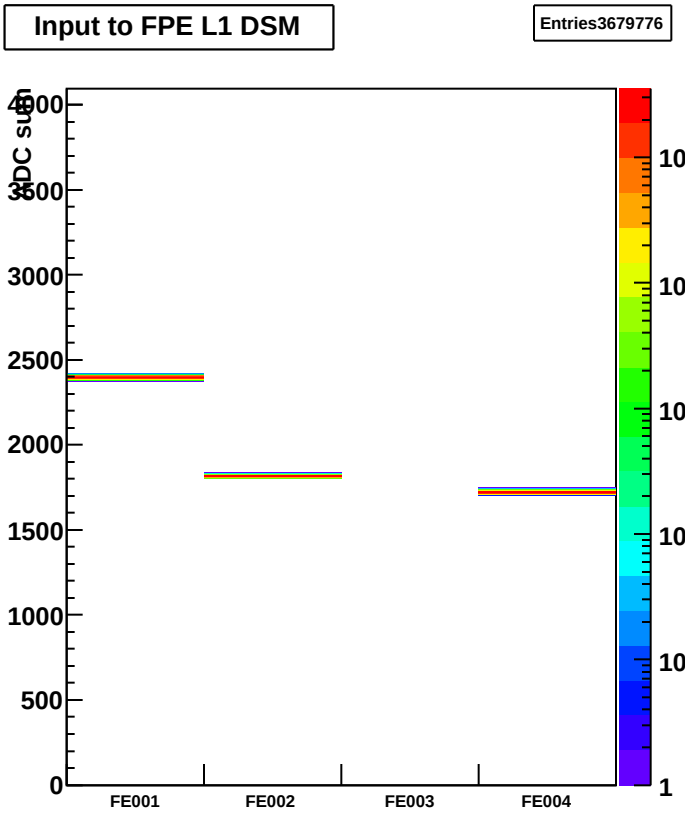

Input to FPD L2 DSM
Entries 5519664
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

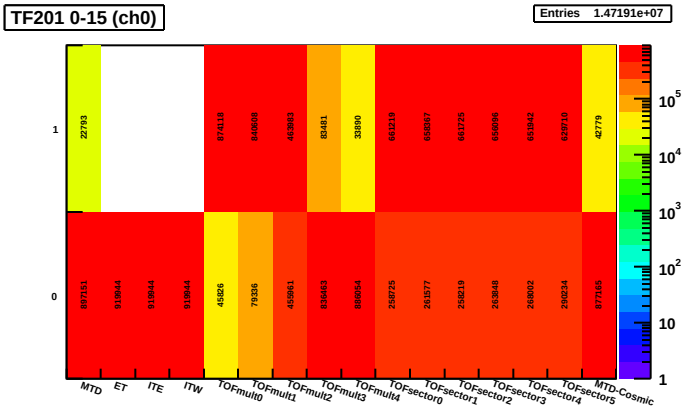

Input to FE001 QT board
Entries 2.943821e+07
ADC
channel

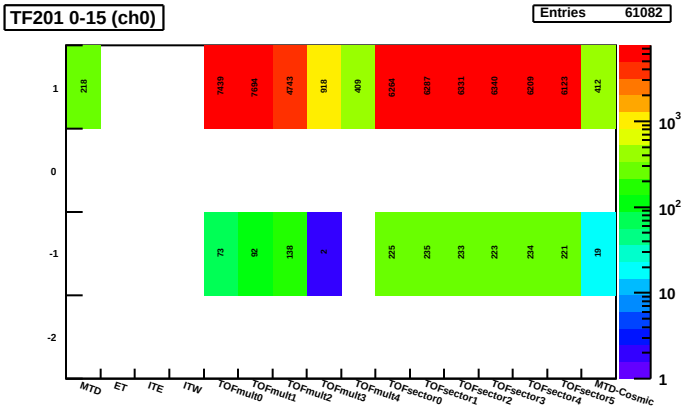

Input to FE002 QT board
Entries 2.943821e+07
ADC
channel

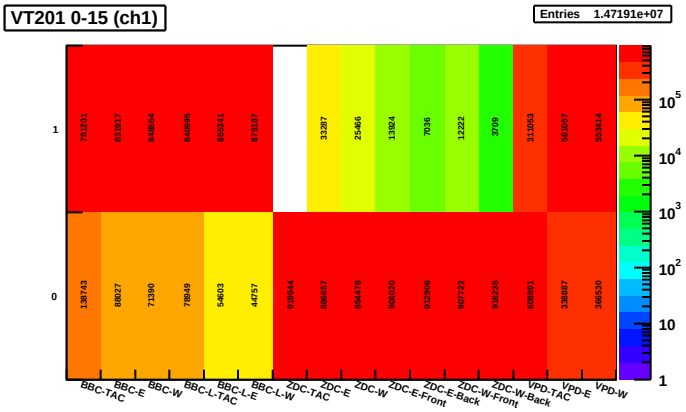

Input to FE003 QT board
Entries 2.943821e+07
ADC
channel

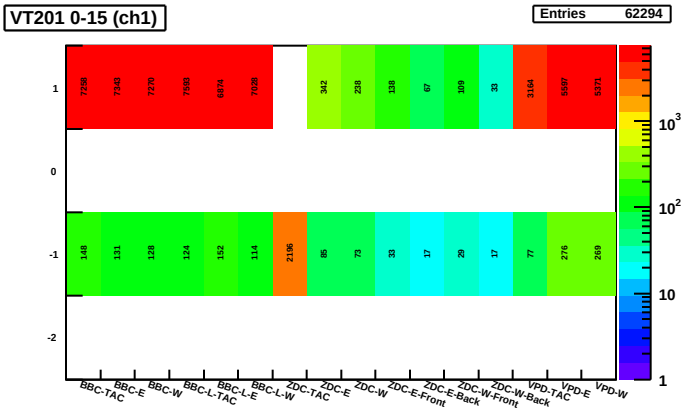

Input to FE004 QT board
Entries 2.943821e+07
ADC
channel

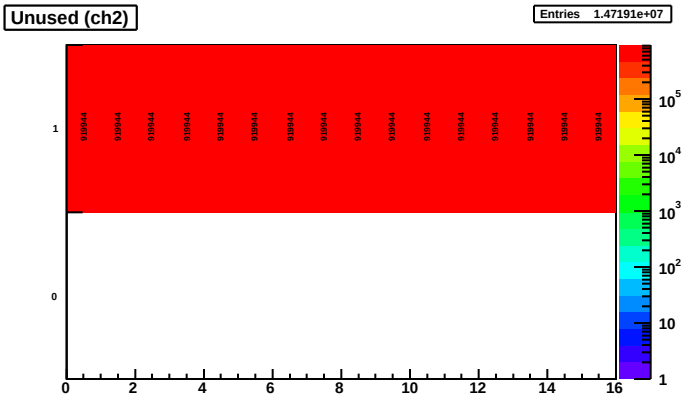

Input to FPE L1 DSM
Entries3679776
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 1.47191e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 61082
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1.47191e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 62294
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
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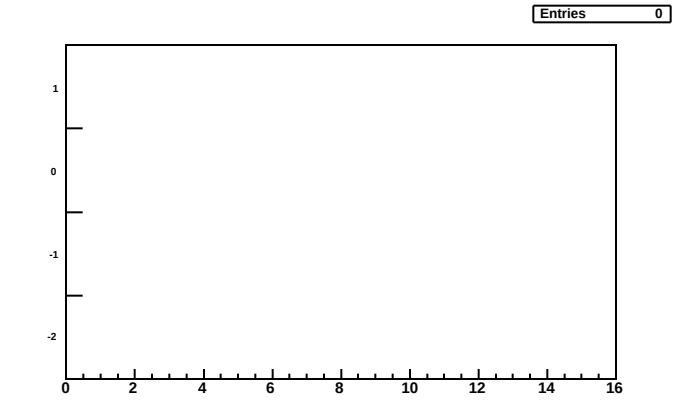
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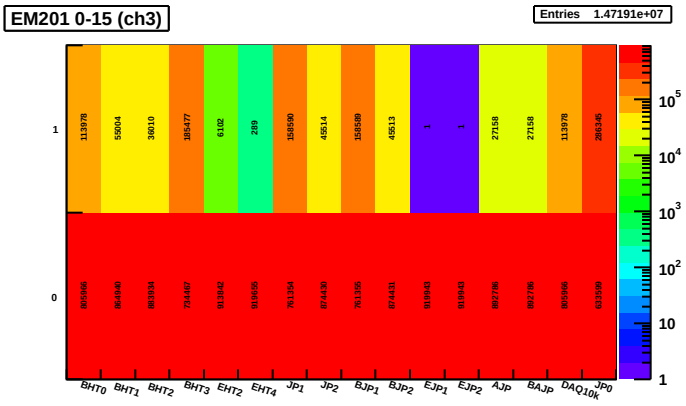
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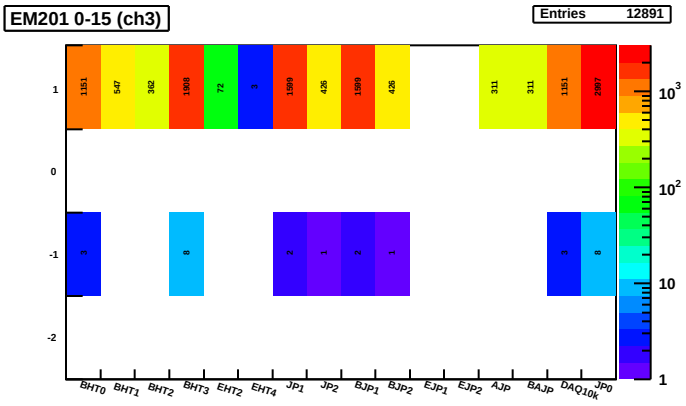
EM201 0-15 (ch3)
Entries 12891
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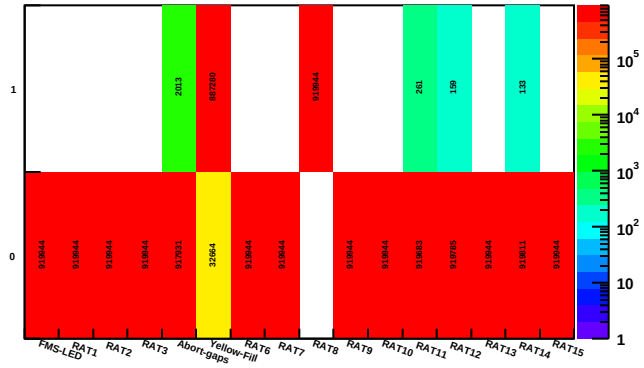
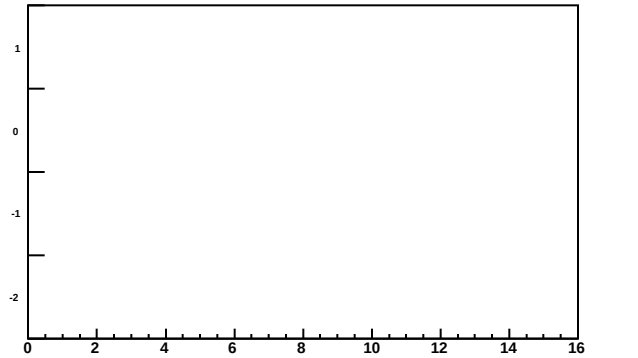
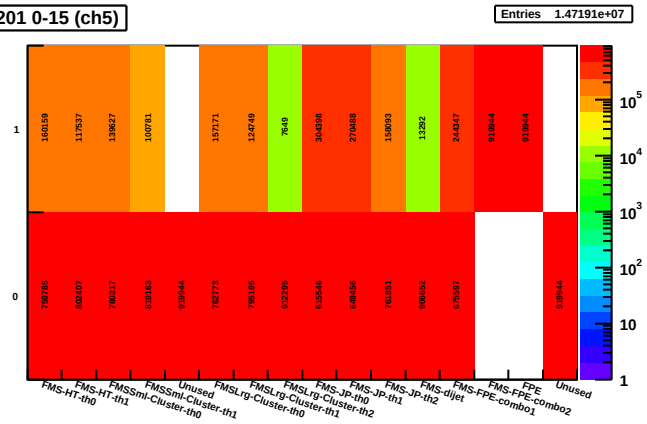
RAT board (ch4)





FP201 0-15 (ch5)



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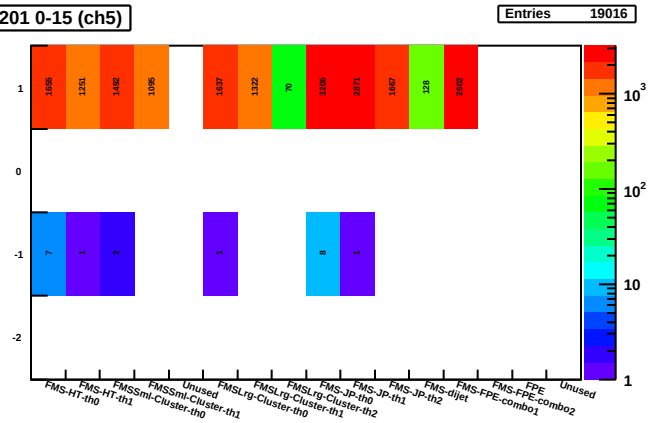
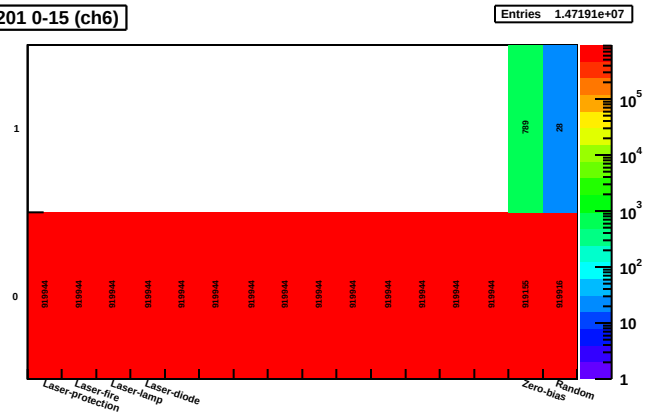
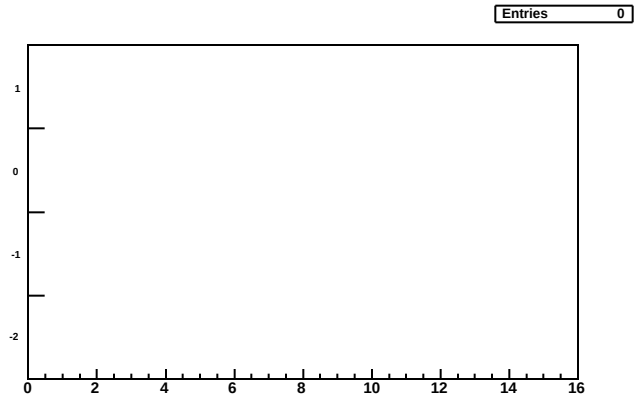
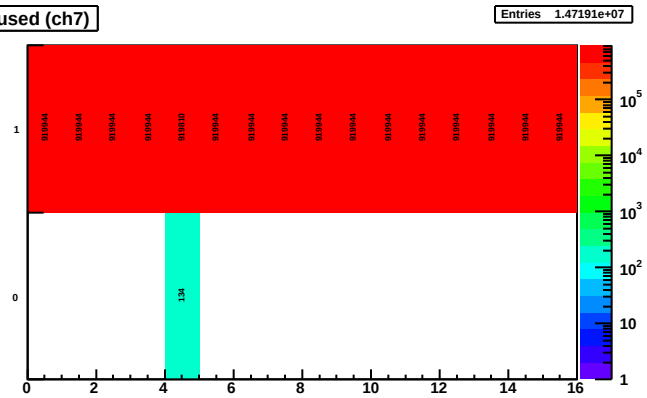
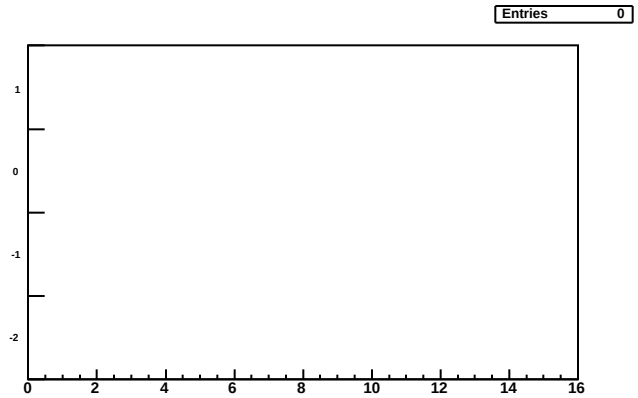


ST201 0-15 (ch6)





Unused (ch7)