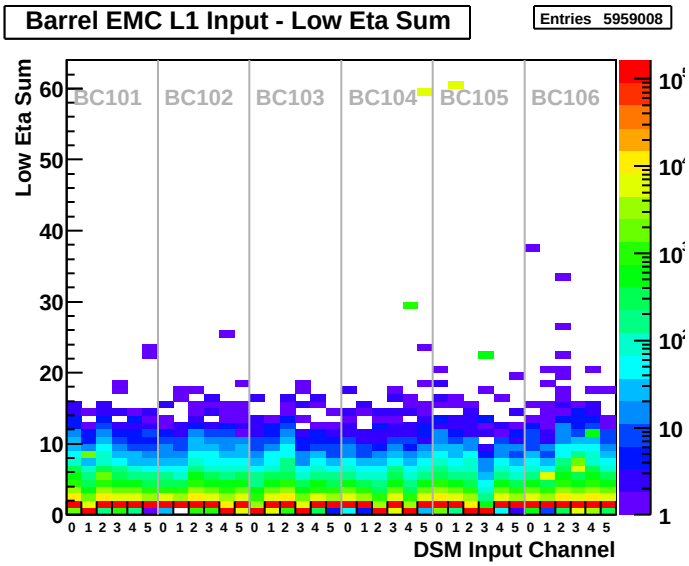
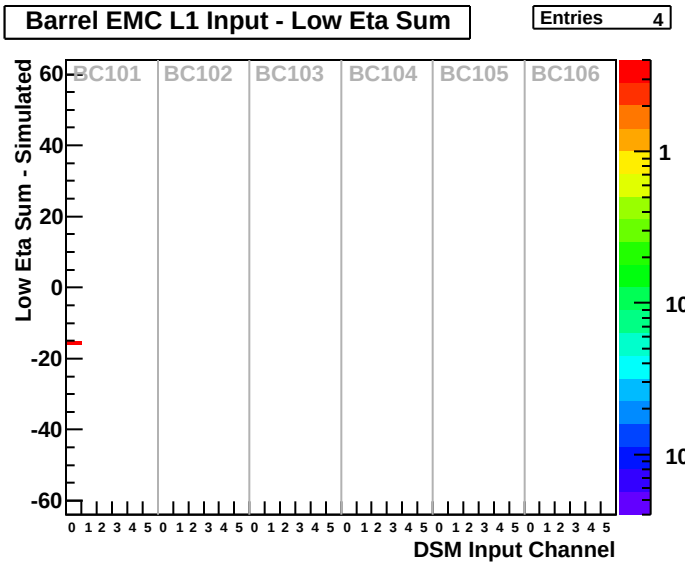
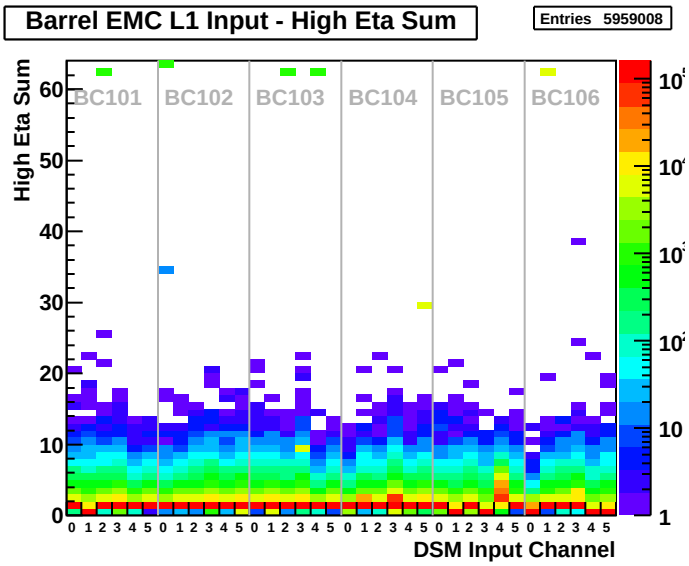
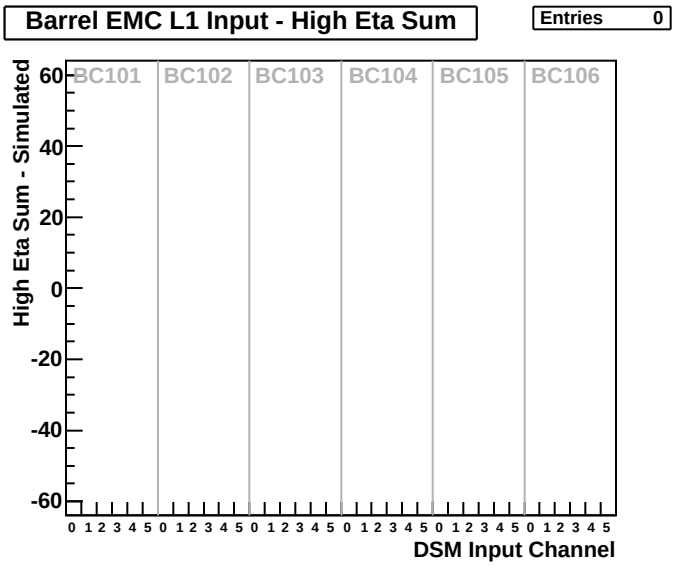
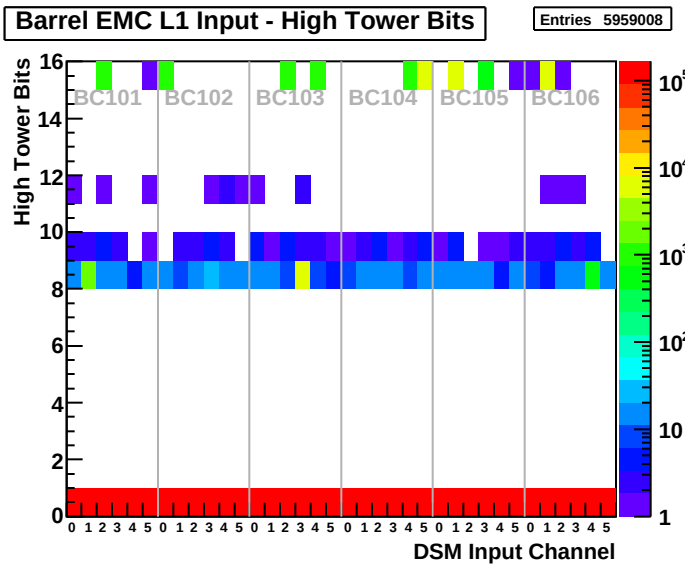
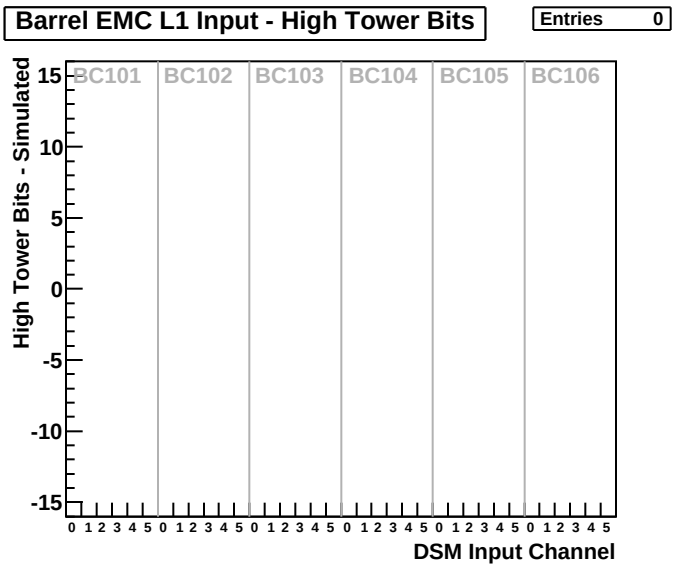

Barrel EMC L1 Input - Low Eta Sum
Entries 5959008
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

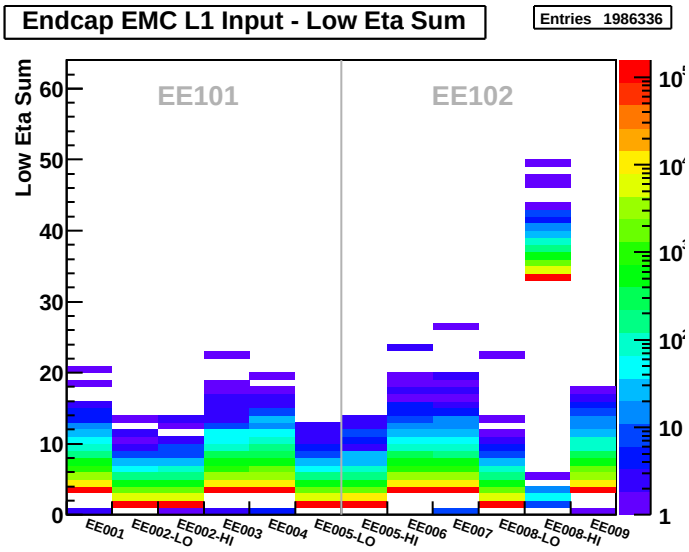

Barrel EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

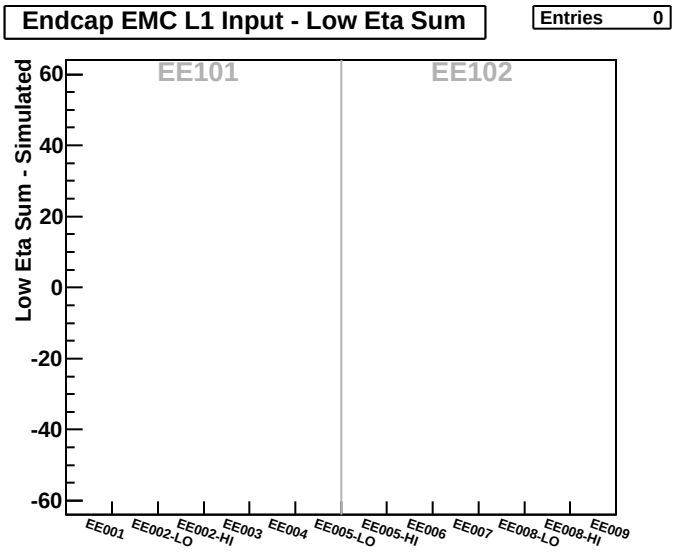

Barrel EMC L1 Input - High Eta Sum
Entries 5959008
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

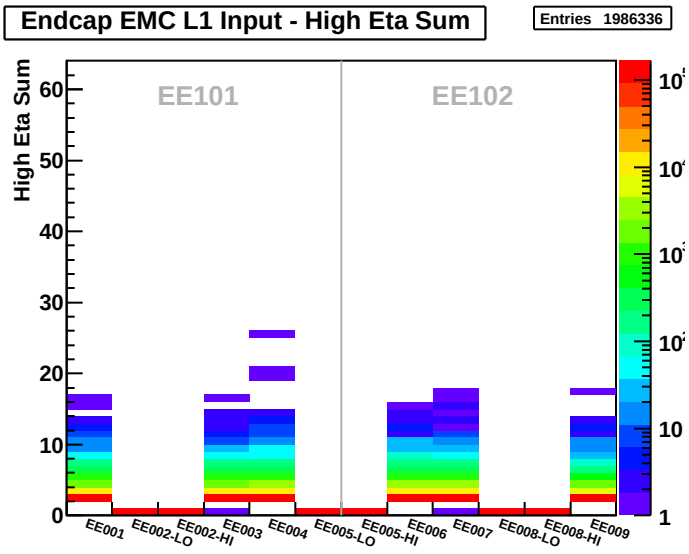

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

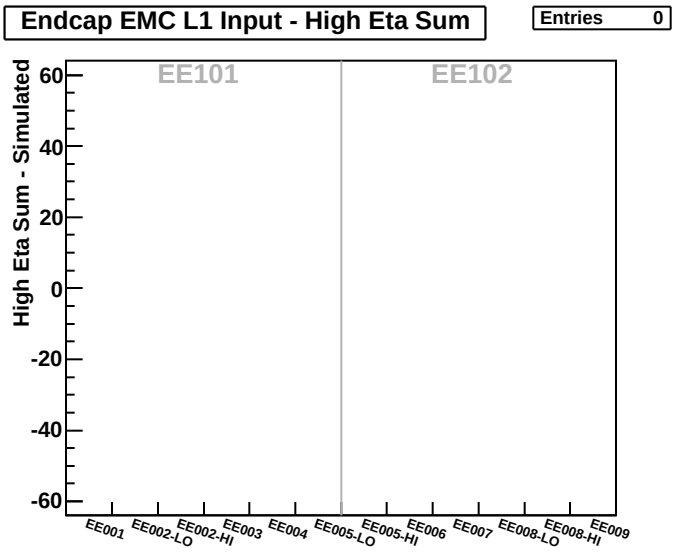

Barrel EMC L1 Input - High Tower Bits
Entries 5959008
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

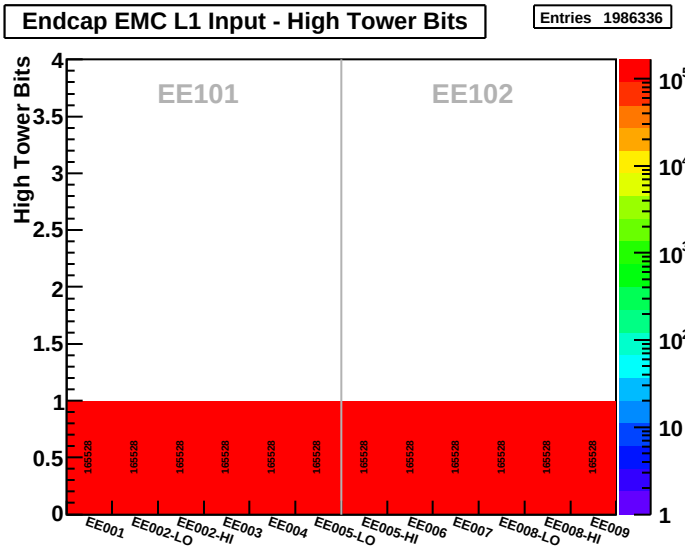

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

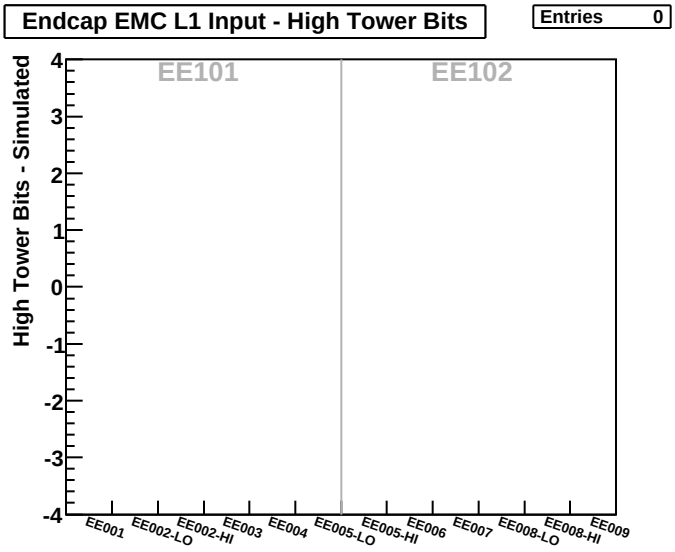

Endcap EMC L1 Input - Low Eta Sum
Entries 1986336
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

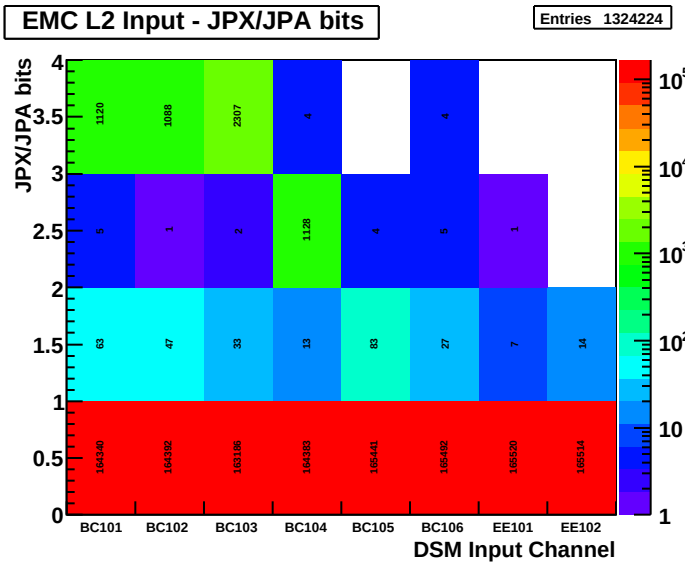

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

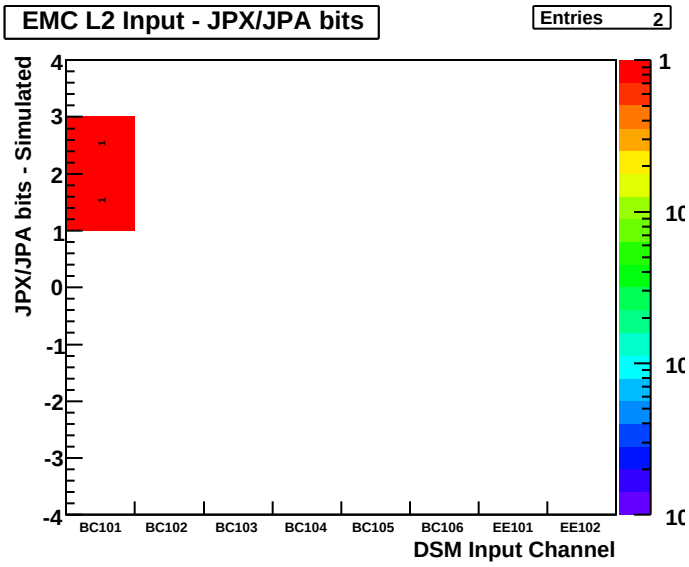

Endcap EMC L1 Input - High Eta Sum
Entries 1986336
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

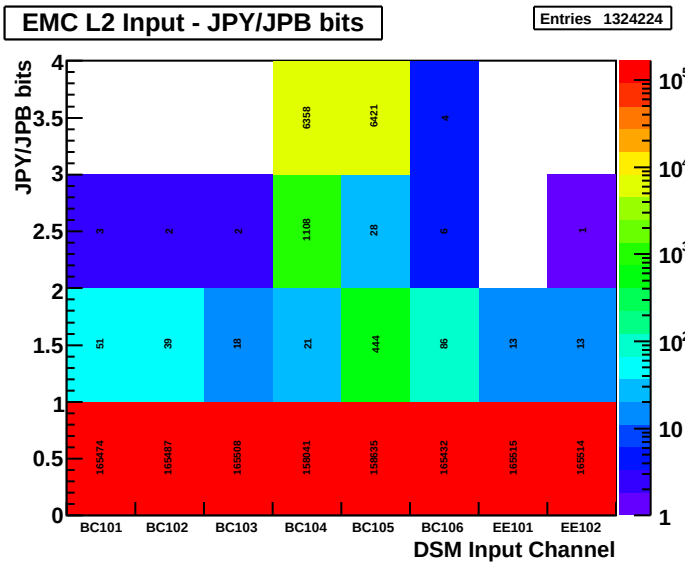

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

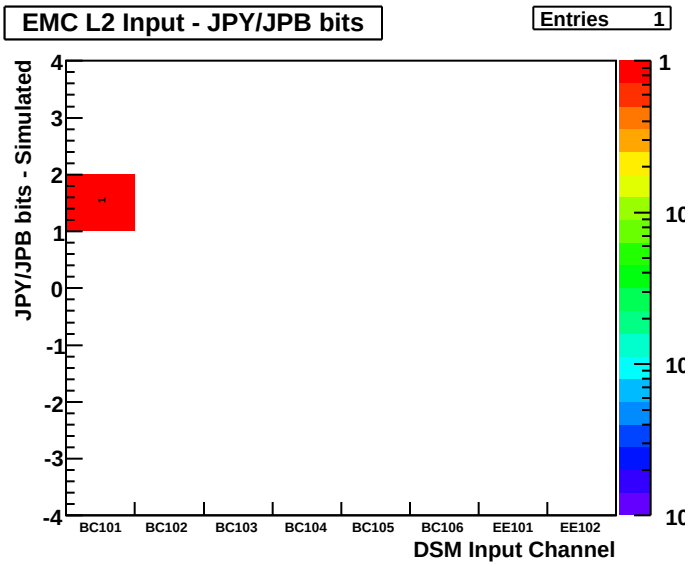

Endcap EMC L1 Input - High Tower Bits
Entries 1986336
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

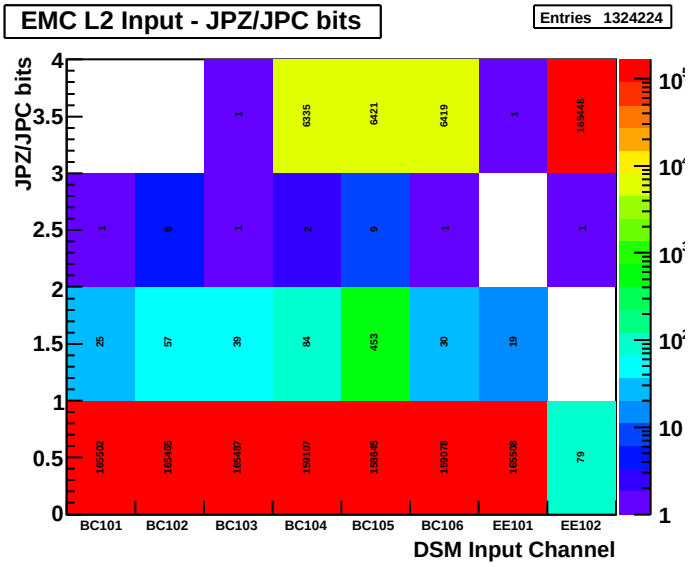

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

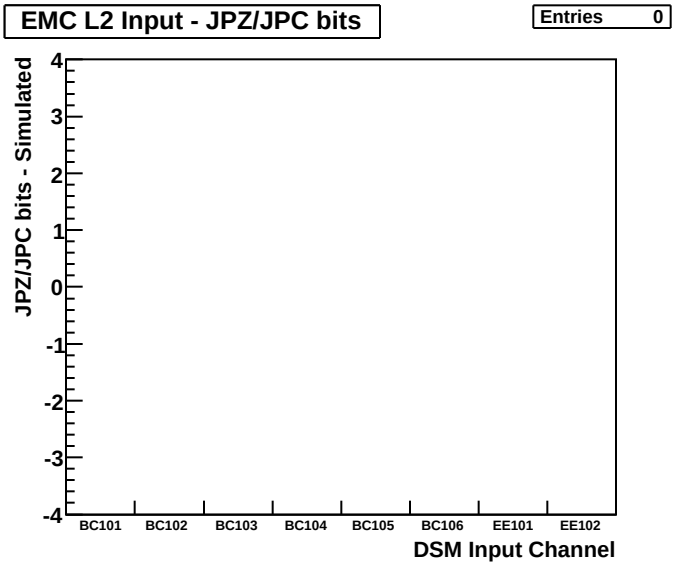

EMC L2 Input - JPX/JPA bits
Entries 1324224
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

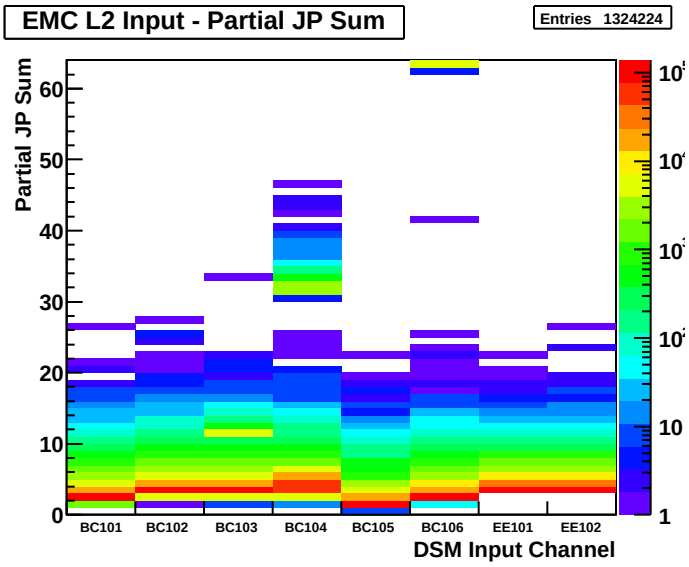

EMC L2 Input - JPX/JPA bits
Entries 2
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

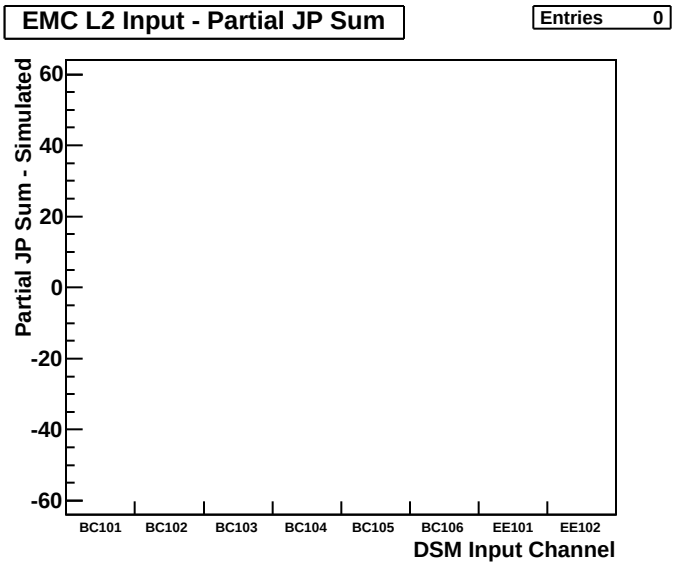

EMC L2 Input - JPY/JPB bits
Entries 1324224
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

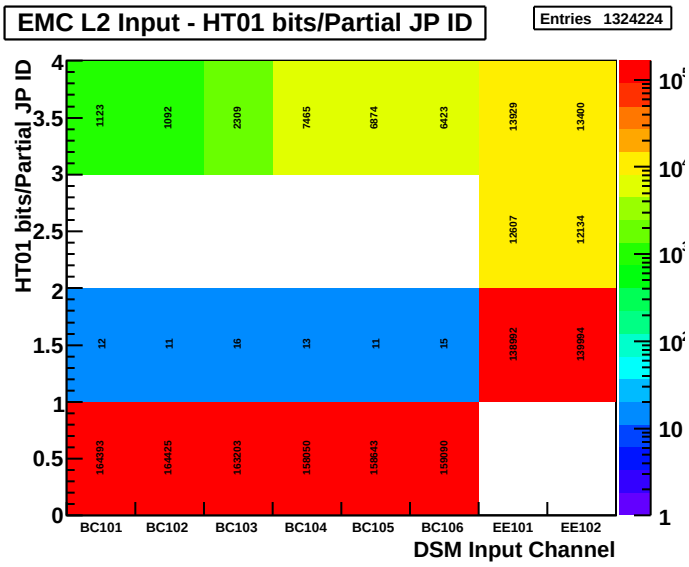

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

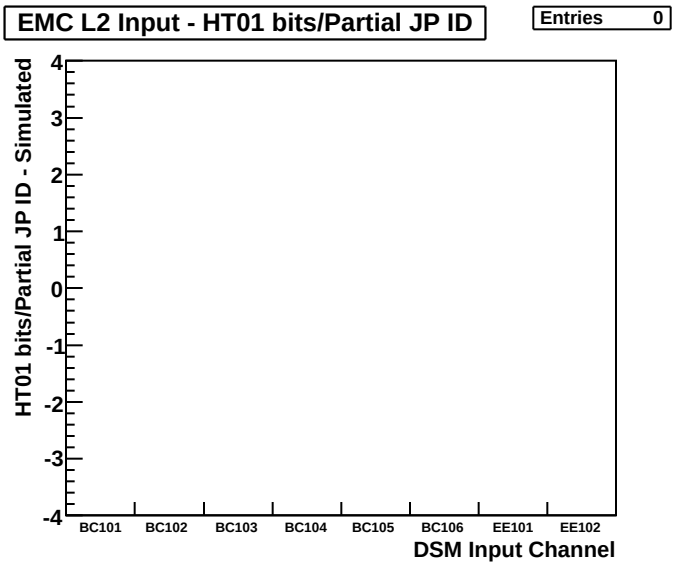

EMC L2 Input - JPZ/JPC bits
Entries 1324224
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

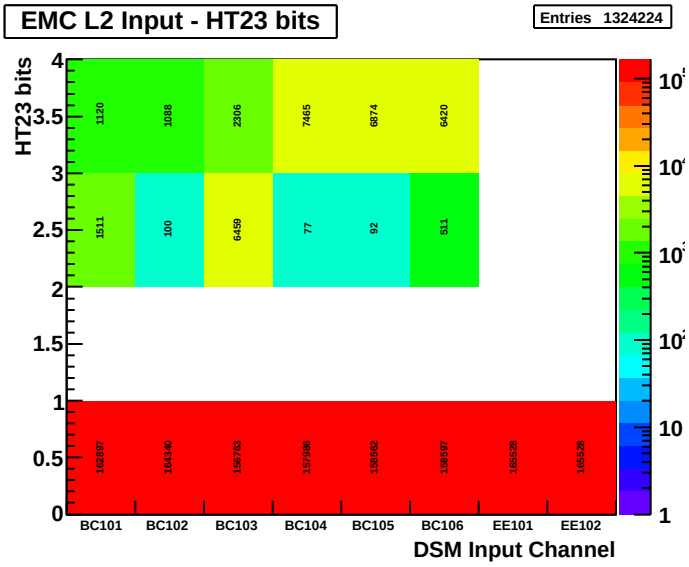

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

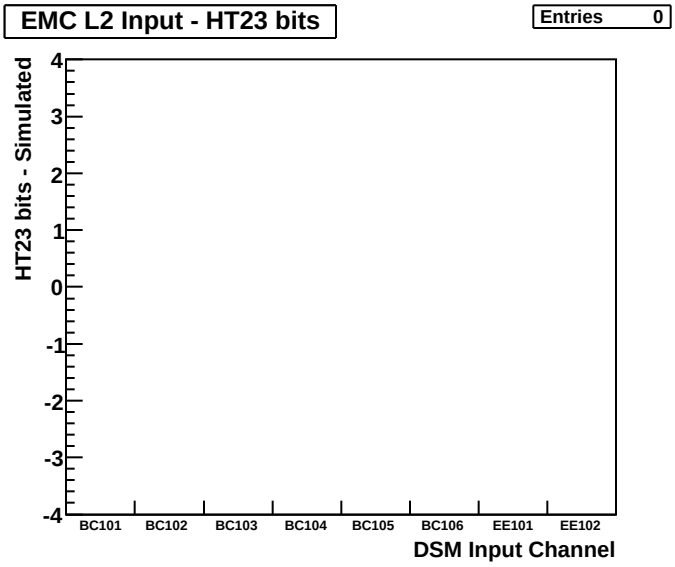

EMC L2 Input - Partial JP Sum
Entries 1324224
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

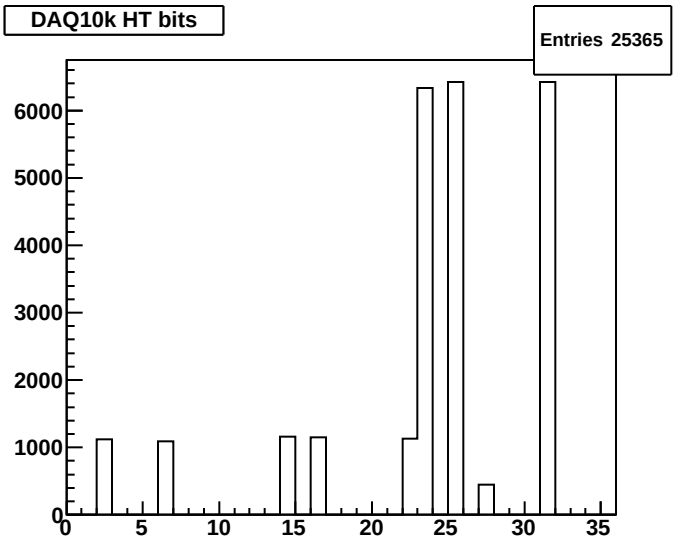

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

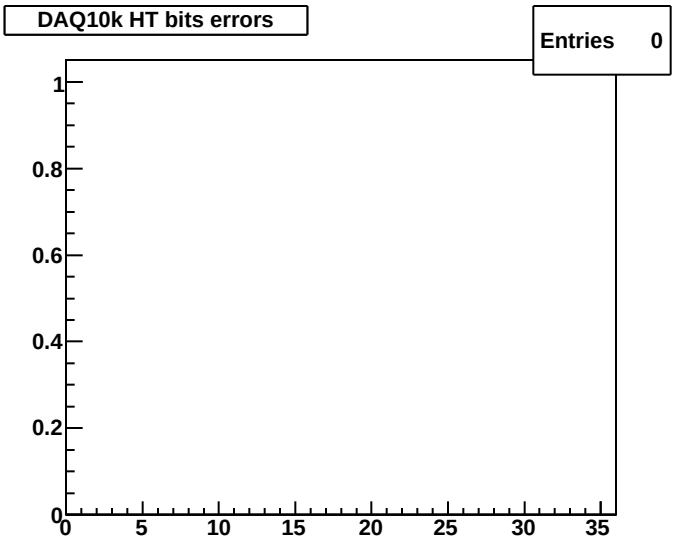

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1324224
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

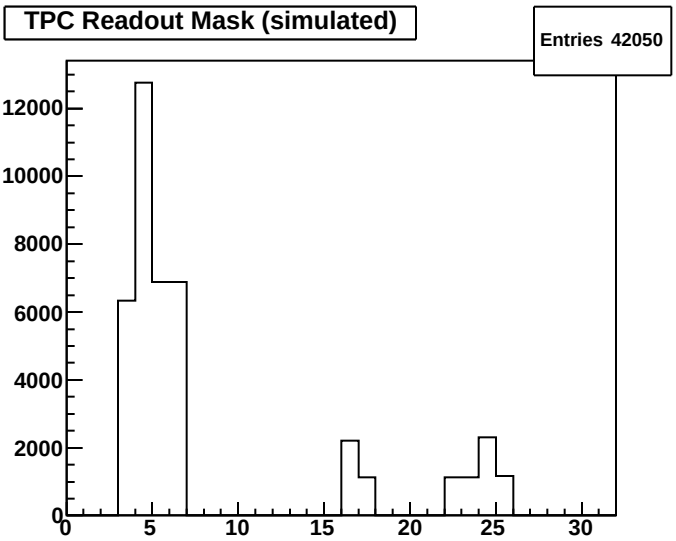

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

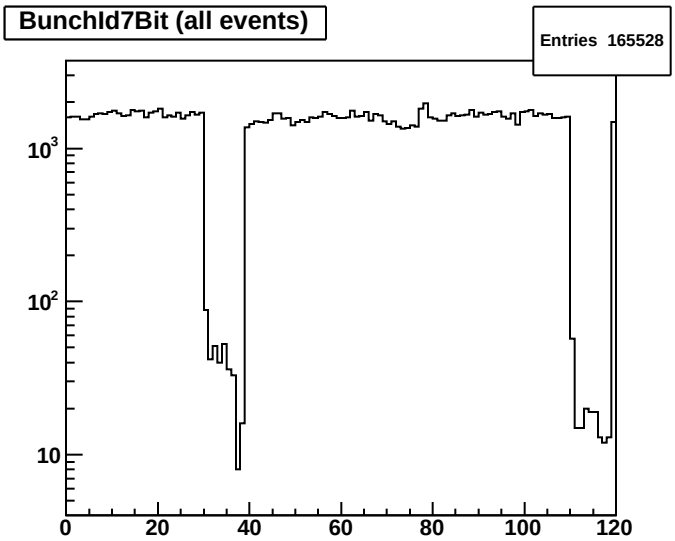

EMC L2 Input - HT23 bits
Entries 1324224
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

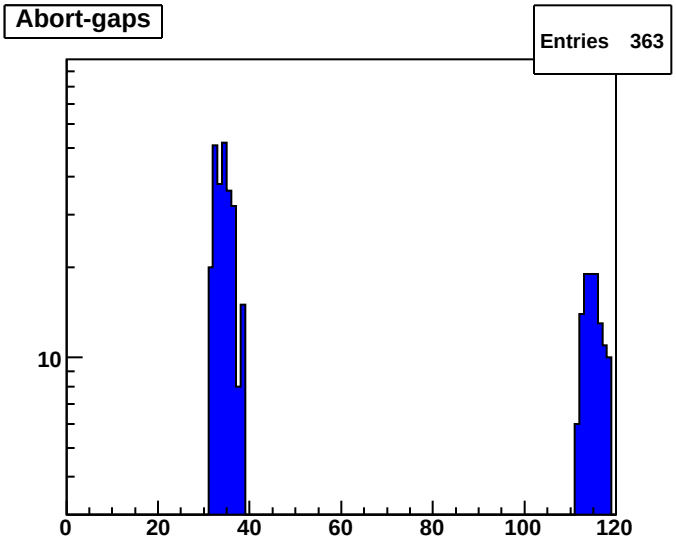

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

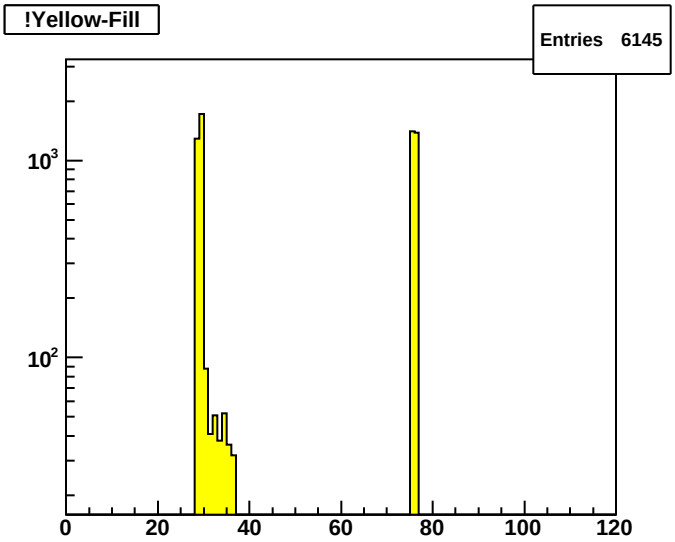

DAQ10k HT bits
Entries 25365

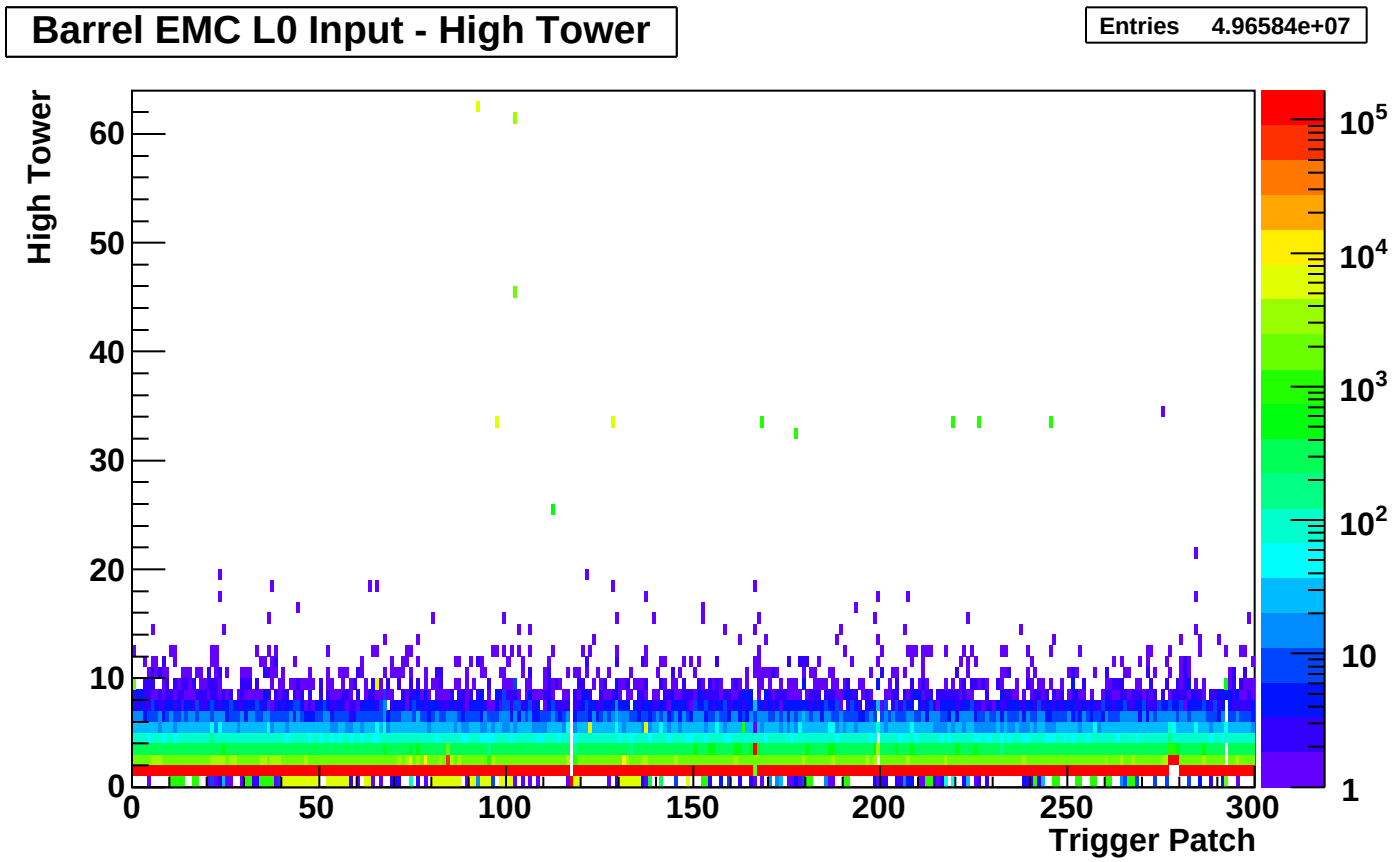

DAQ10k HT bits errors
Entries 0

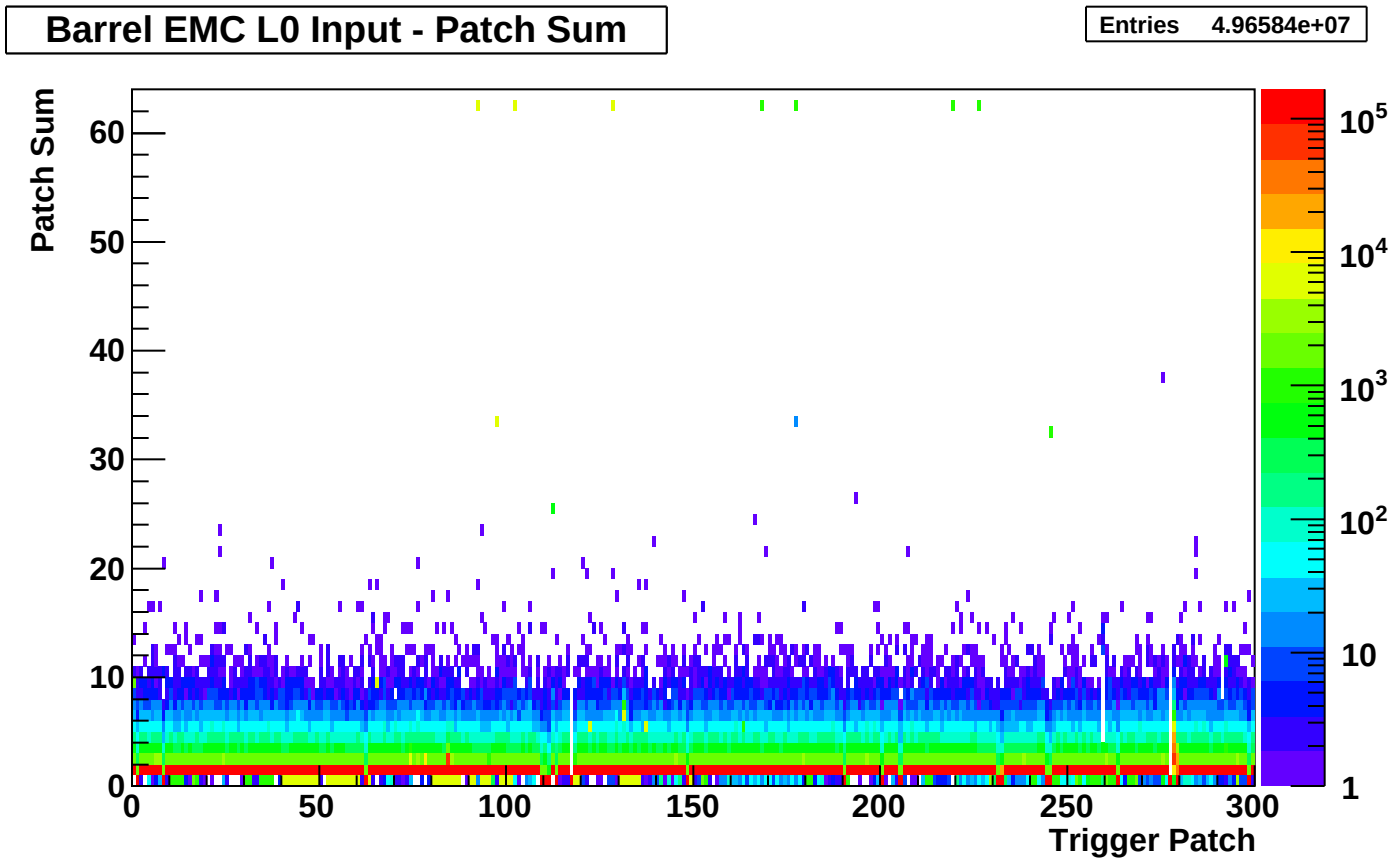

TPC Readout Mask (simulated)
Entries 42050

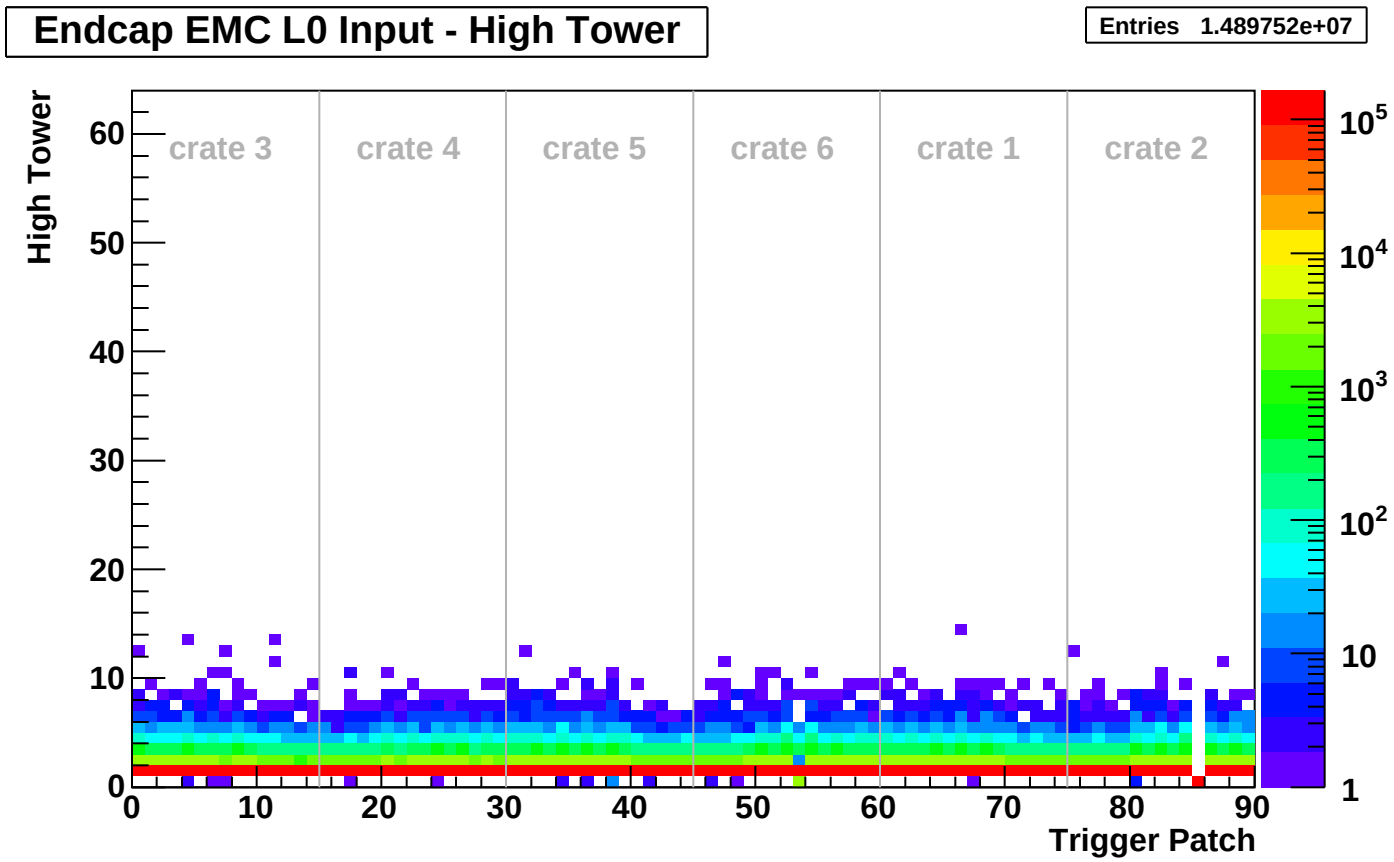

BunchId7Bit (all events)
Entries 165528

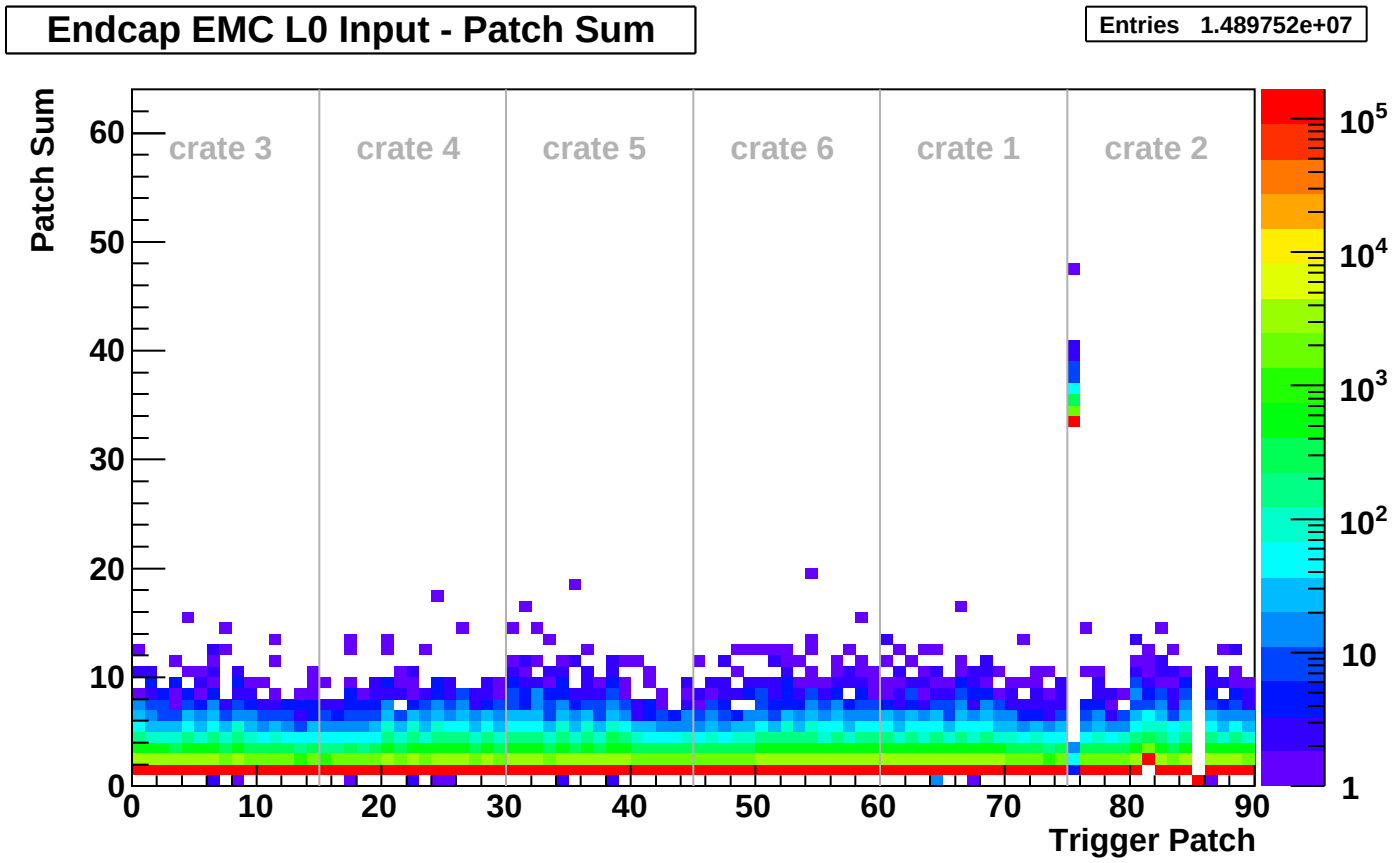

Abort-gaps
Entries 363

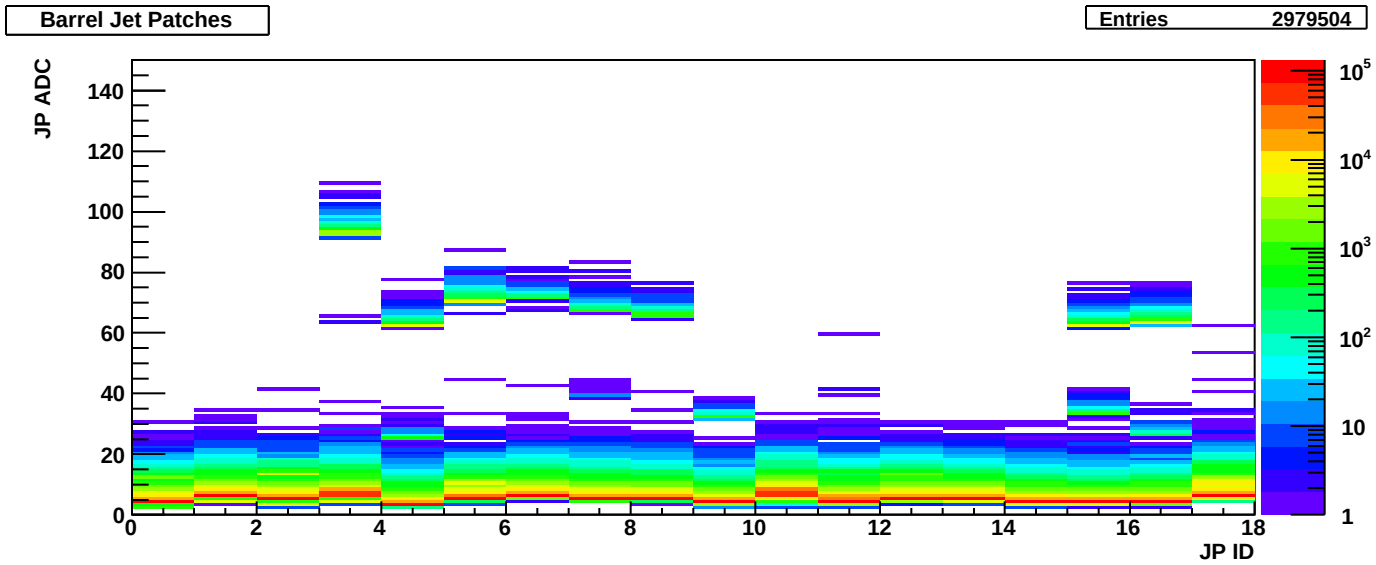

!Yellow-Fill
Entries 6145

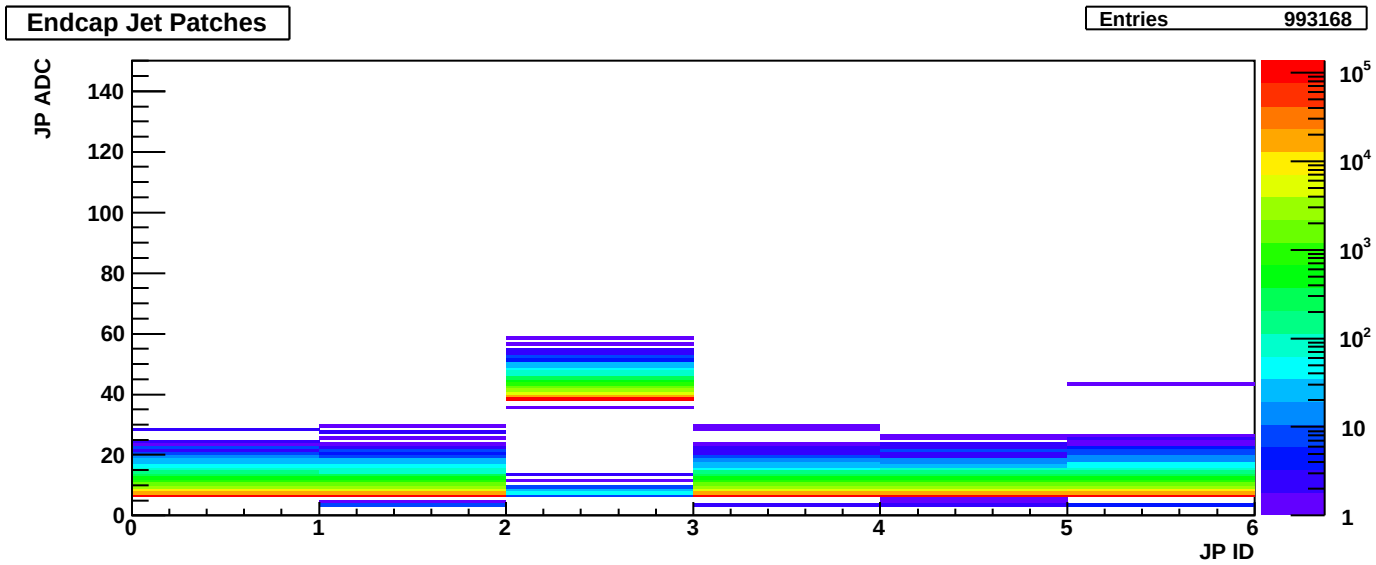

Barrel EMC L0 Input - High Tower
Entries 4.96584e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

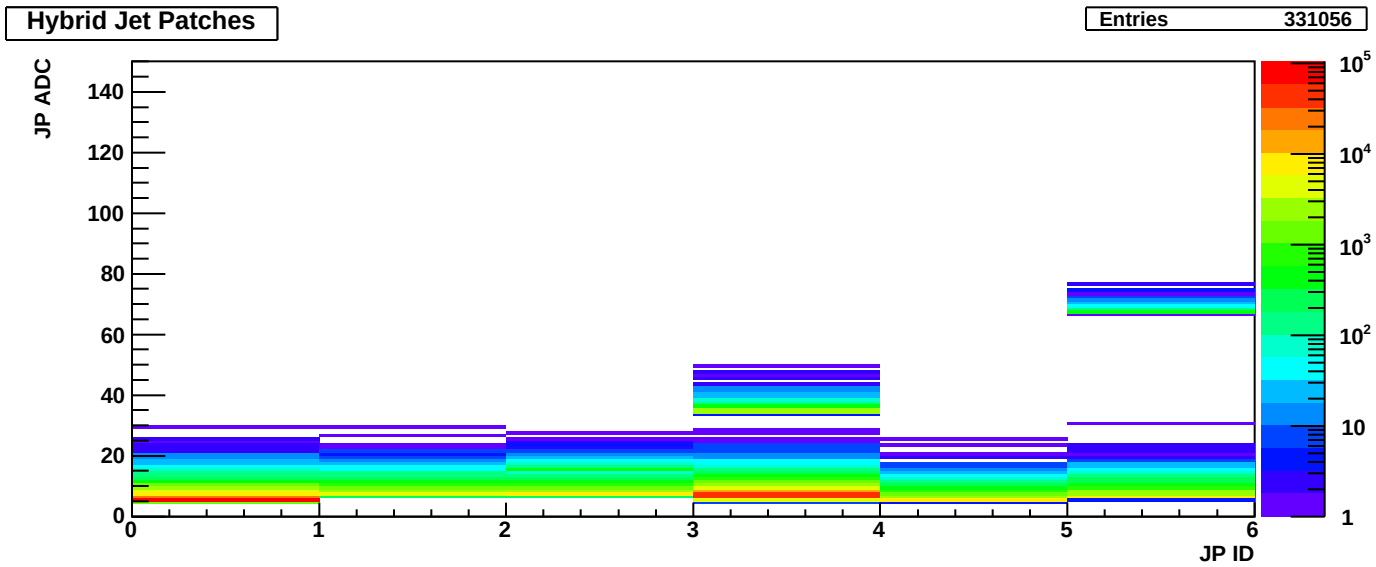

Barrel EMC L0 Input - Patch Sum
Entries 4.96584e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

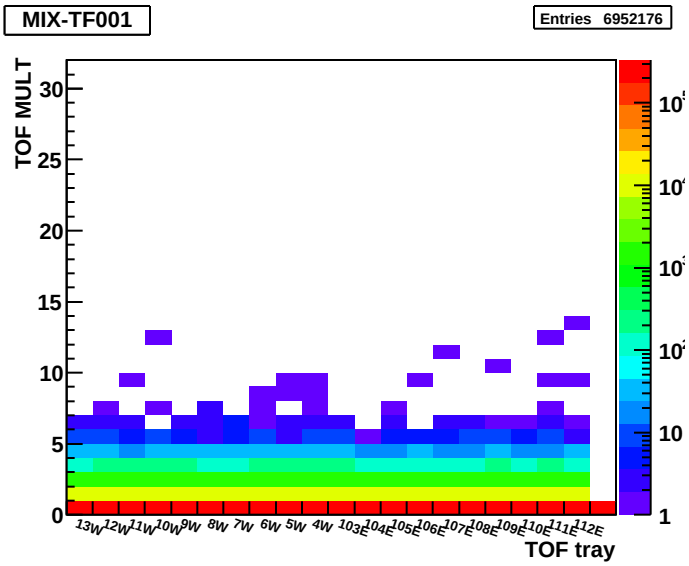

Endcap EMC L0 Input - High Tower
Entries 1.489752e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

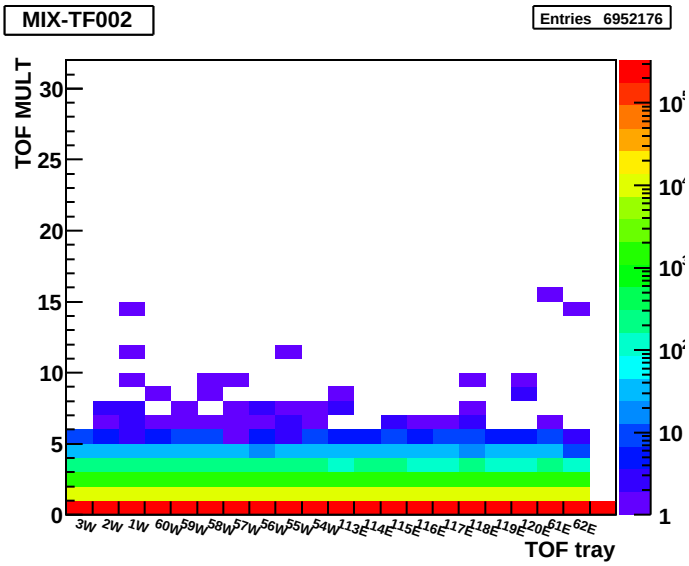

Endcap EMC L0 Input - Patch Sum
Entries 1.489752e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

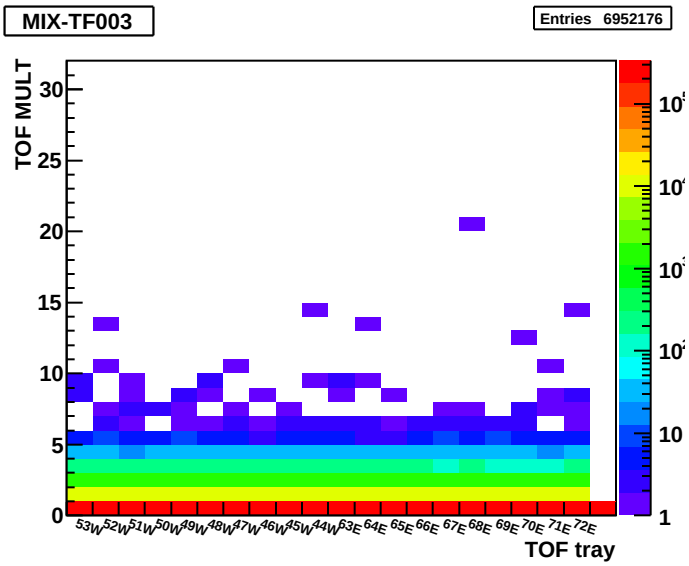

Barrel Jet Patches
Entries 2979504
JP ADC
JP ID

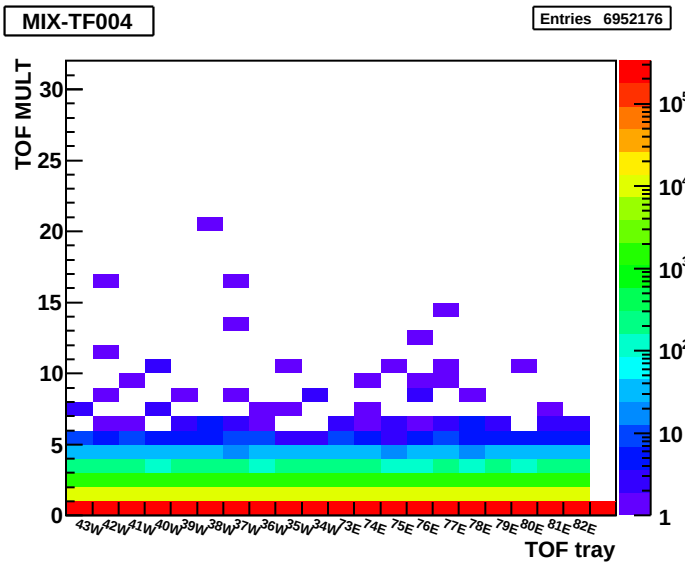

Endcap Jet Patches
Entries 993168
JP ADC
JP ID

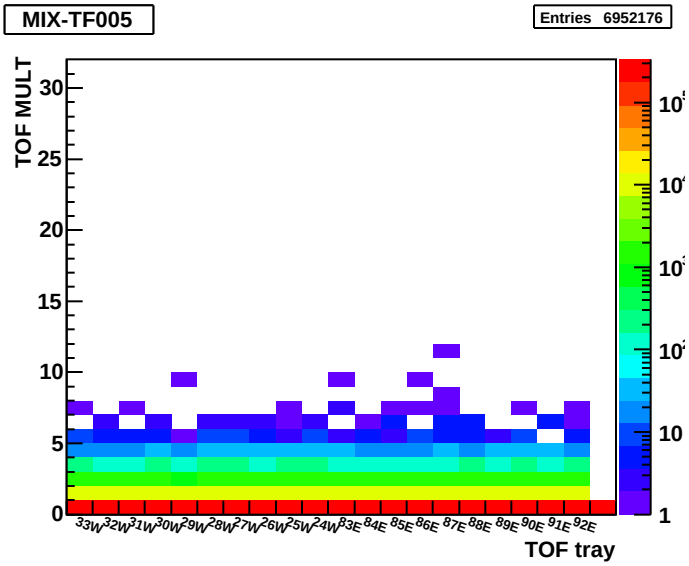

Hybrid Jet Patches
Entries 331056
JP ADC
JP ID

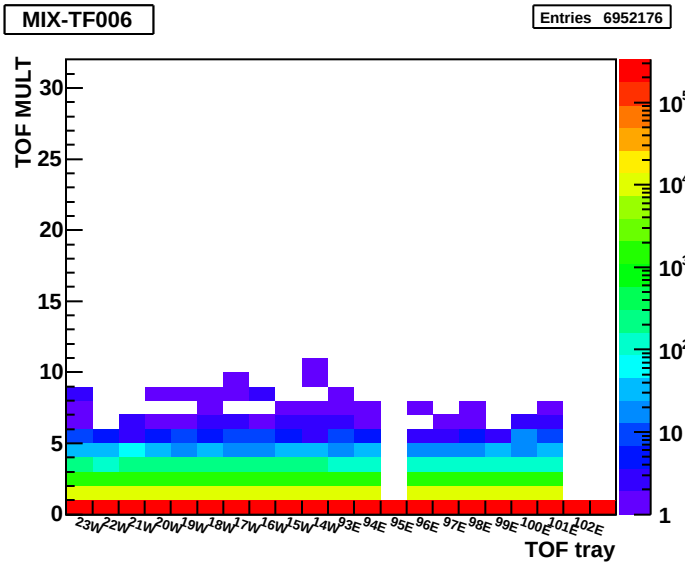

MIX-TF001
Entries 6952176
TOF MULT
TOF tray

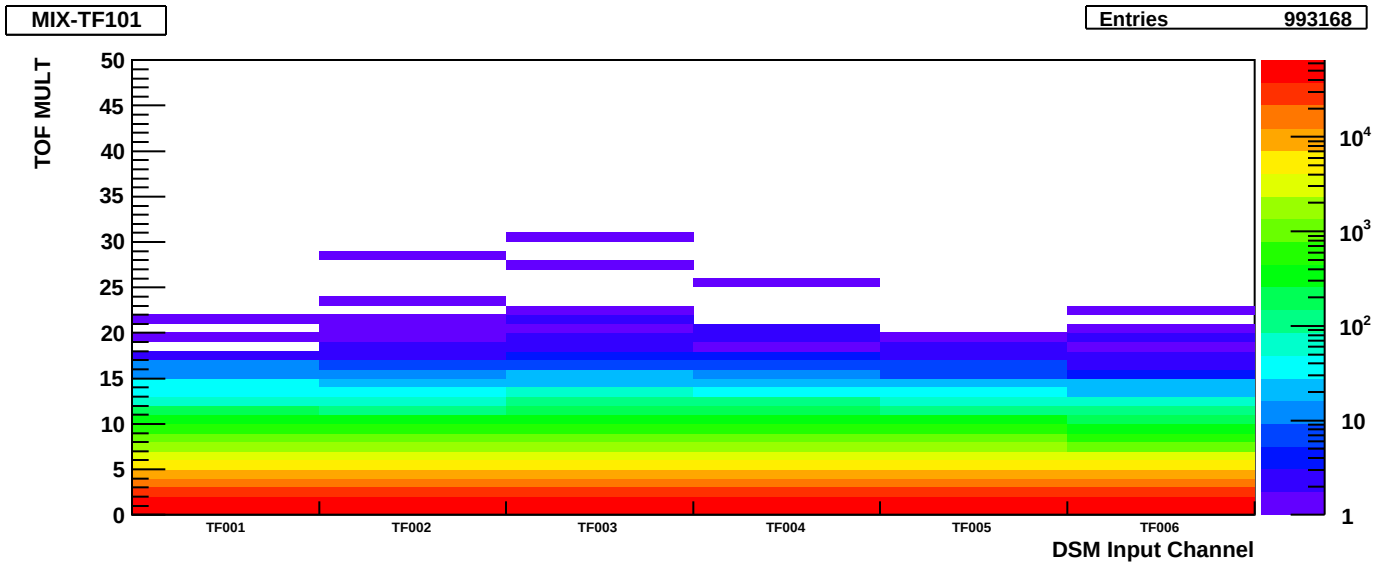

MIX-TF002
Entries 6952176
TOF MULT
TOF tray

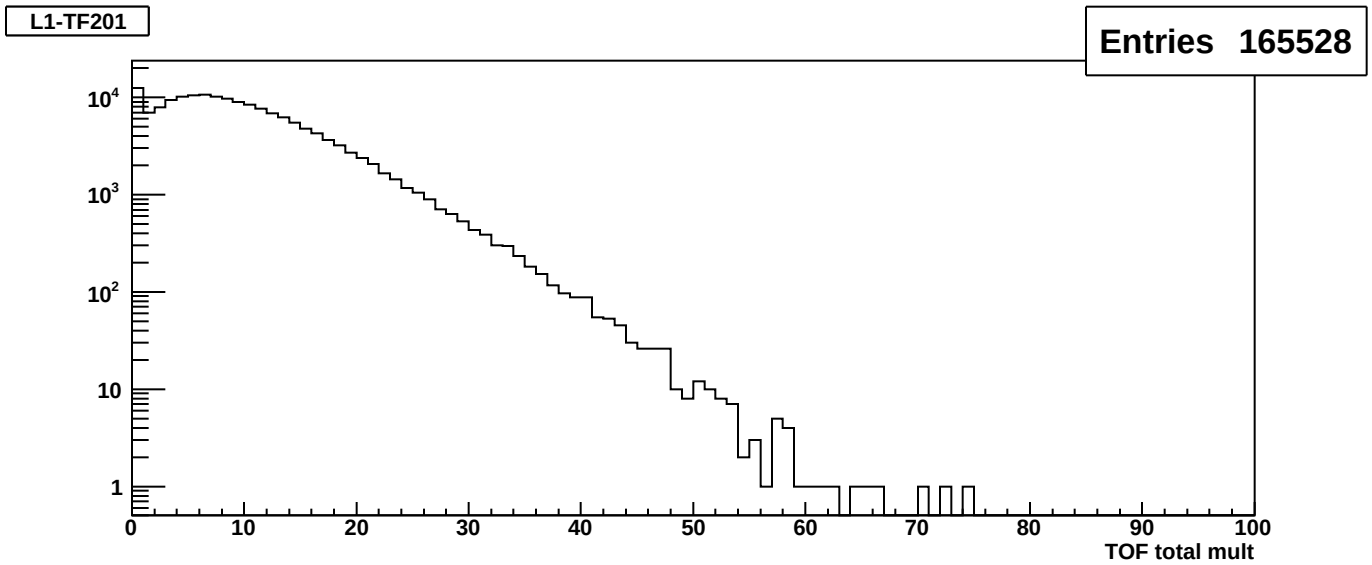

MIX-TF003
Entries 6952176
TOF MULT
TOF tray

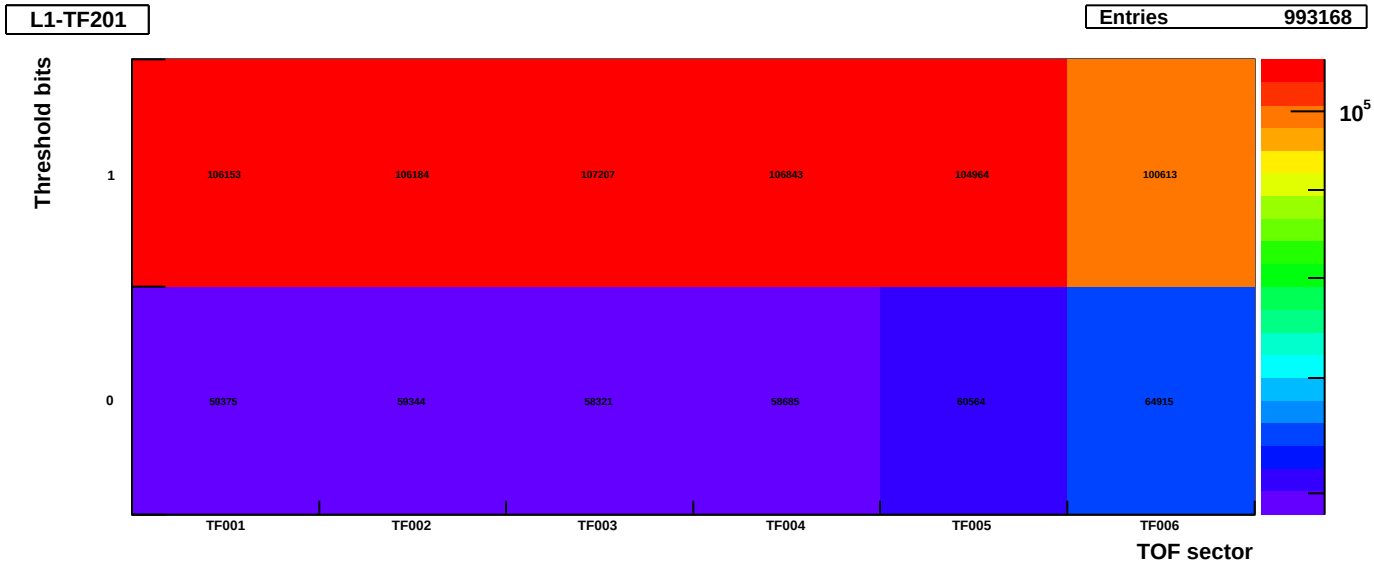

MIX-TF004
Entries 6952176
TOF MULT
TOF tray

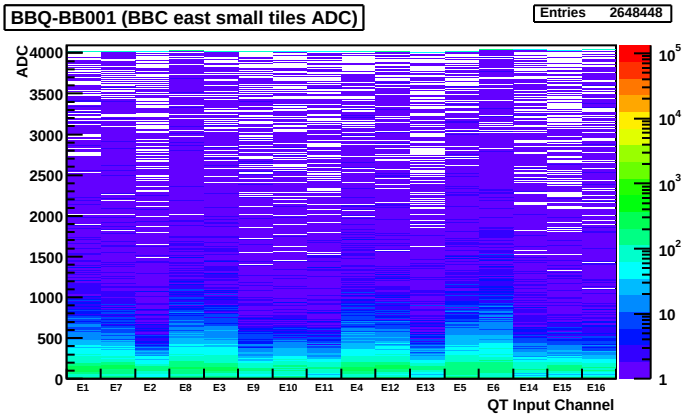

MIX-TF005
Entries 6952176
TOF MULT
TOF tray

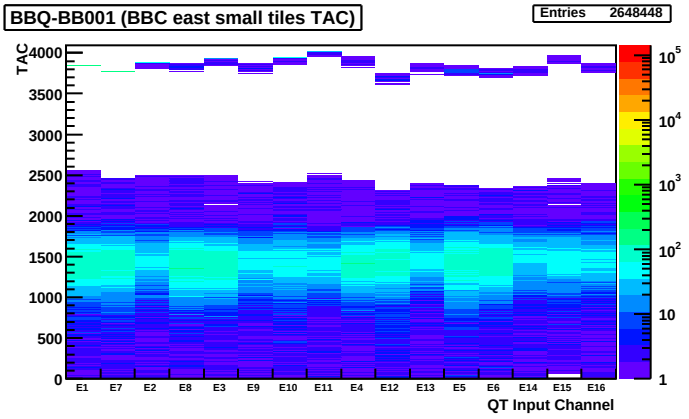

MIX-TF006
Entries 6952176
TOF MULT
TOF tray

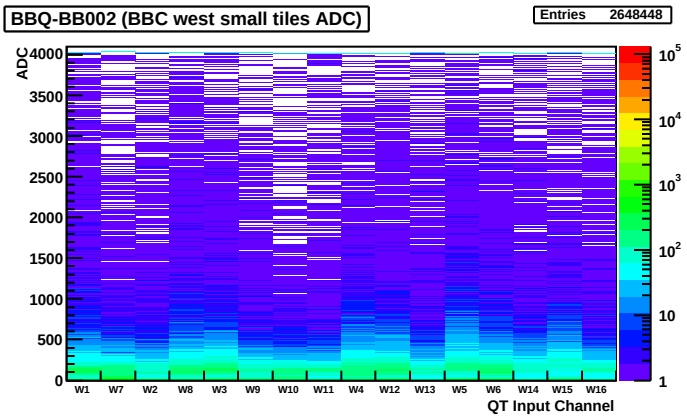

MIX-TF101
Entries 993168
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

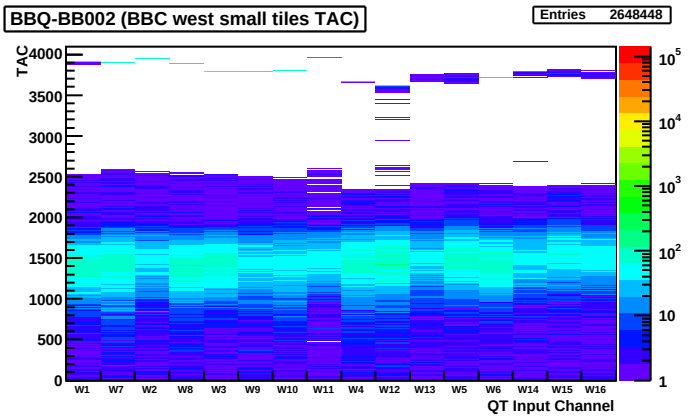

L1-TF201
Entries 165528
TOF total mult

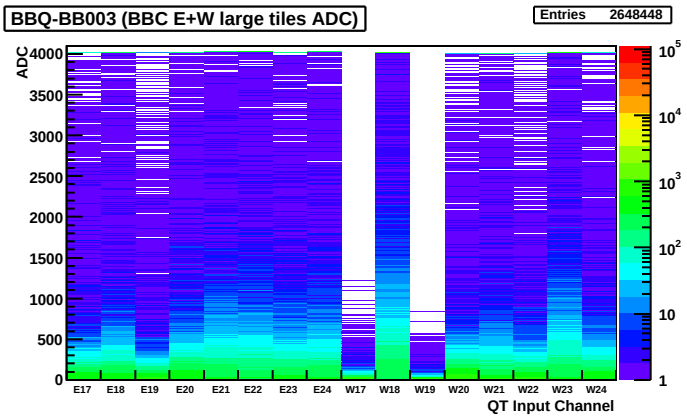

L1-TF201
Entries 993168
Threshold bits
106153
106184
107207
106843
104964
100613
59375
59344
58321
58685
60564
64915
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

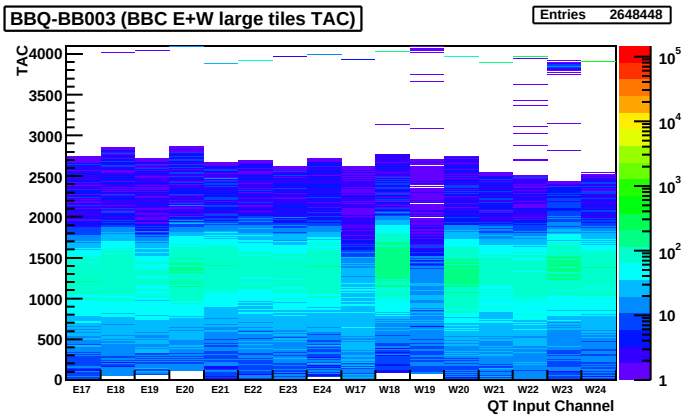

BBQ-BB001 (BBC east small tiles ADC)
Entries 2648448
ADC
QT Input Channel

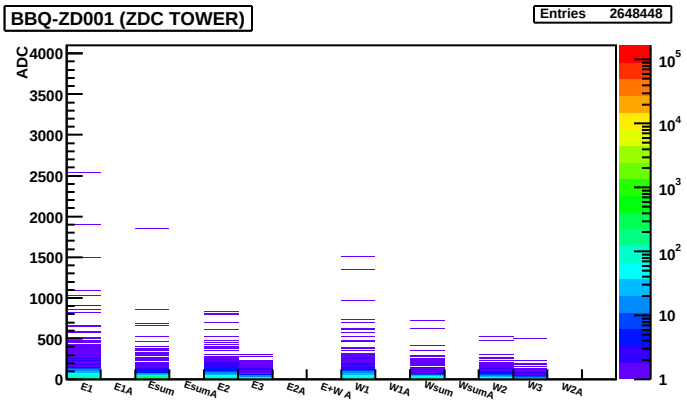

BBQ-BB001 (BBC east small tiles TAC)
Entries 2648448
TAC
QT Input Channel

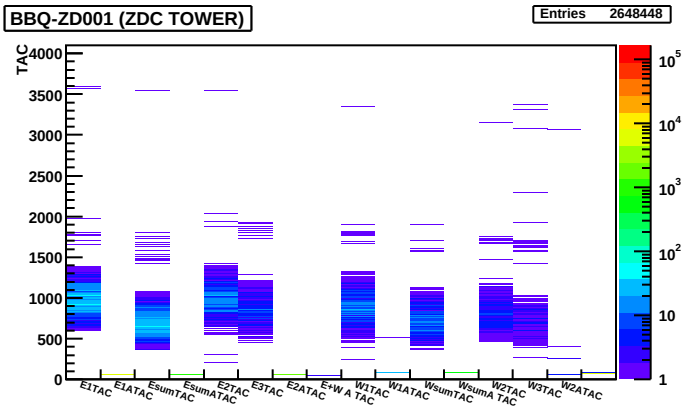

BBQ-BB002 (BBC west small tiles ADC)
Entries 2648448
ADC
QT Input Channel

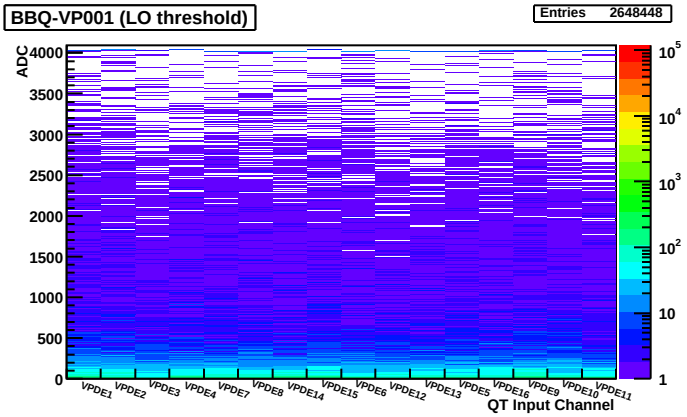

BBQ-BB002 (BBC west small tiles TAC)
Entries 2648448
TAC
QT Input Channel

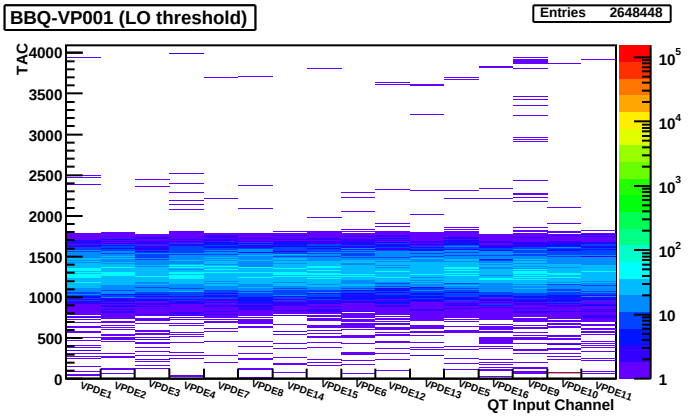

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2648448
ADC
QT Input Channel

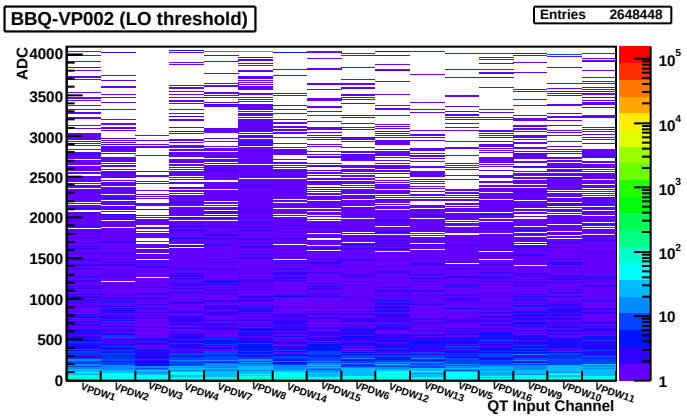

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2648448
TAC
QT Input Channel

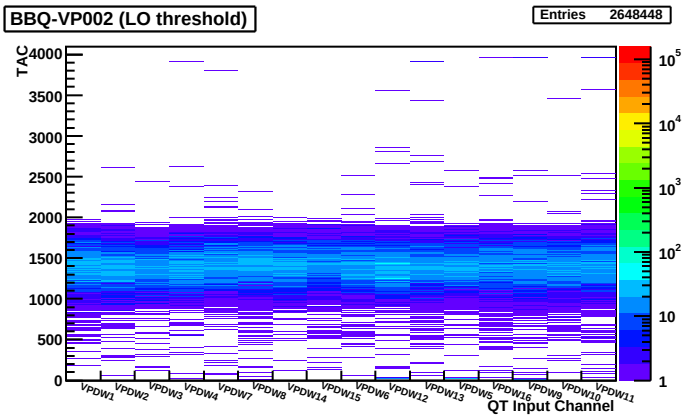

BBQ-ZD001 (ZDC TOWER)
Entries 2648448
ADC

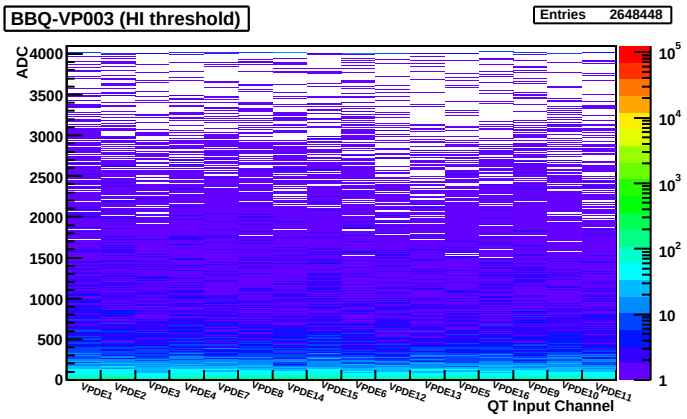

BBQ-ZD001 (ZDC TOWER)
Entries 2648448
TAC

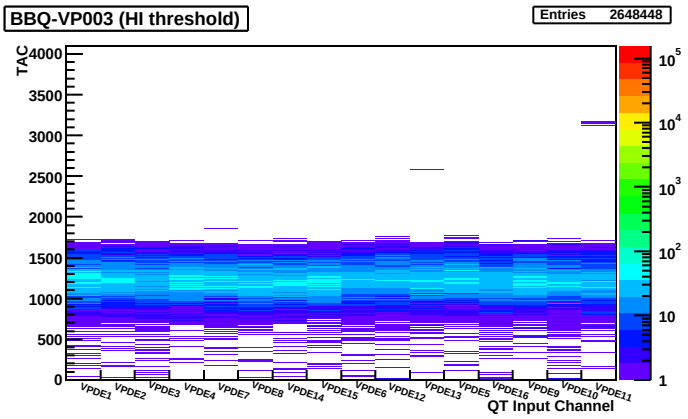

BBQ-VP001 (LO threshold)
Entries 2648448
ADC
QT Input Channel

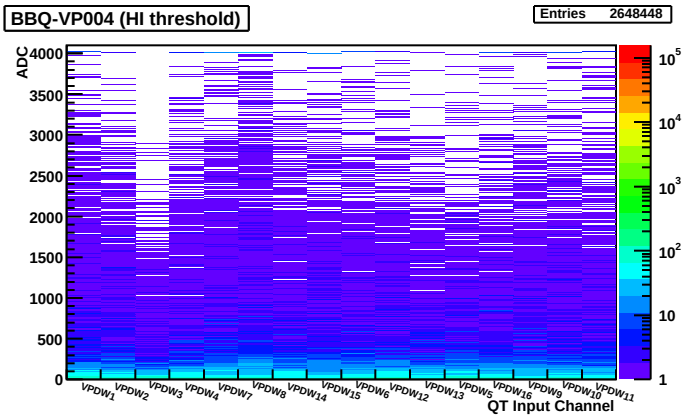

BBQ-VP001 (LO threshold)
Entries 2648448
TAC
QT Input Channel

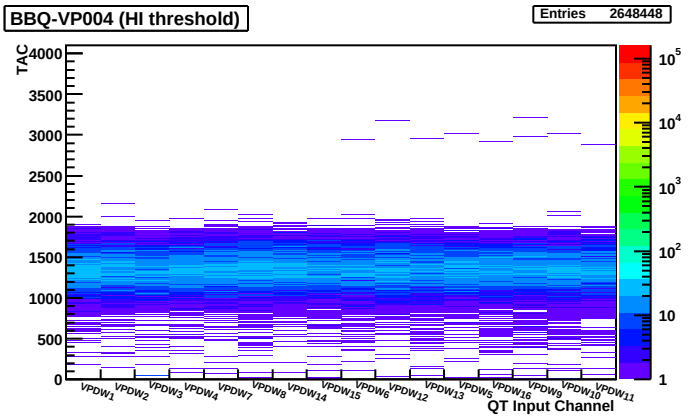

BBQ-VP002 (LO threshold)
Entries 2648448
ADC
QT Input Channel

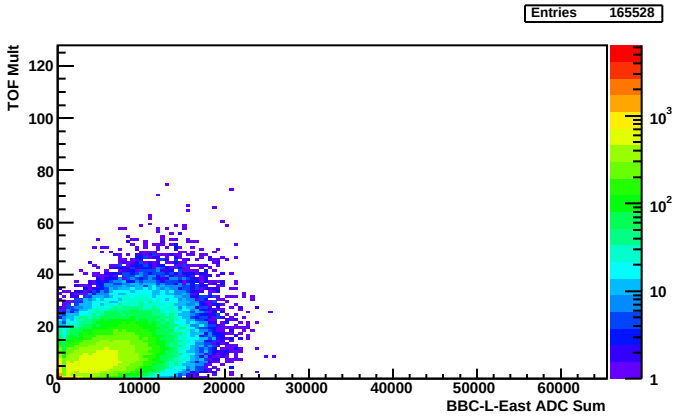

BBQ-VP002 (LO threshold)
Entries 2648448
TAC
QT Input Channel

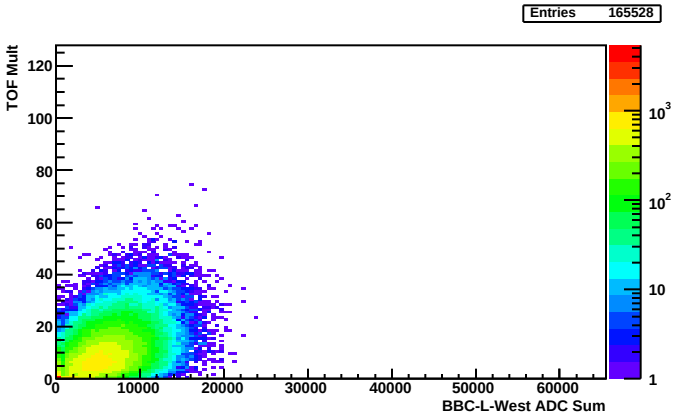

BBQ-VP003 (HI threshold)
Entries 2648448
ADC
QT Input Channel

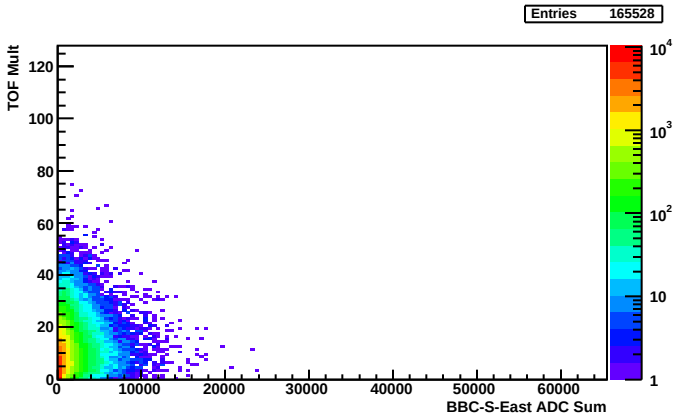

BBQ-VP003 (HI threshold)
Entries 2648448
TAC
QT Input Channel

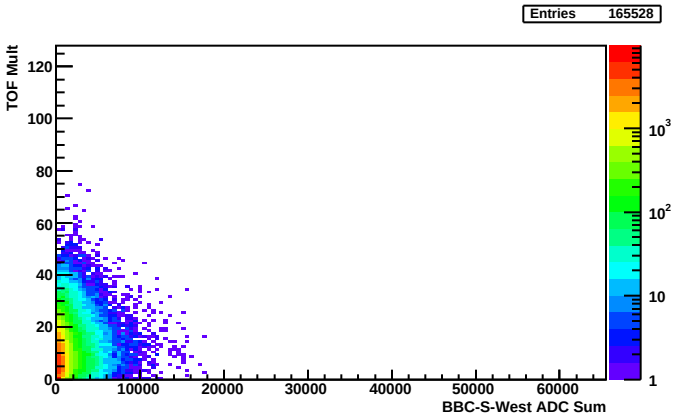

BBQ-VP004 (HI threshold)
Entries 2648448
ADC
QT Input Channel

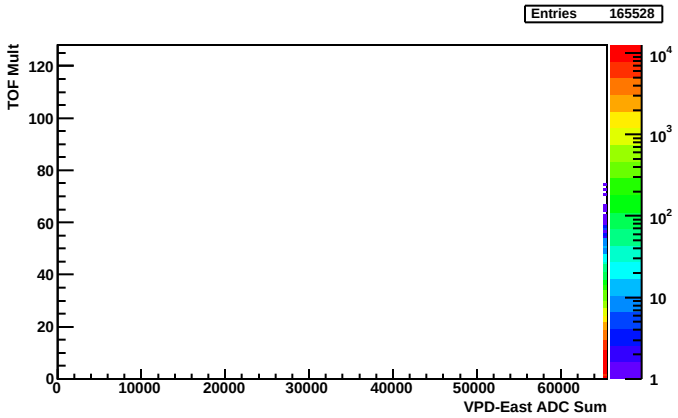

BBQ-VP004 (HI threshold)
Entries 2648448
TAC
QT Input Channel

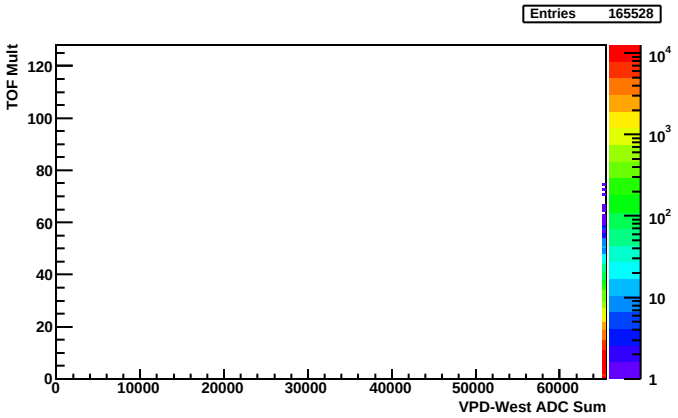

Entries 165528
TOF Mult
BBC-L-East ADC Sum

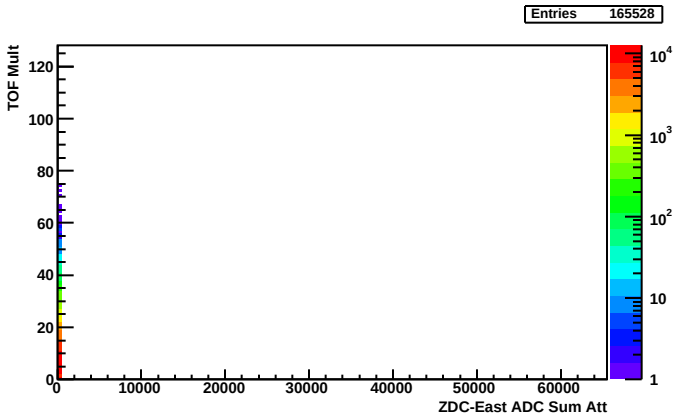

Entries 165528
TOF Mult
BBC-L-West ADC Sum

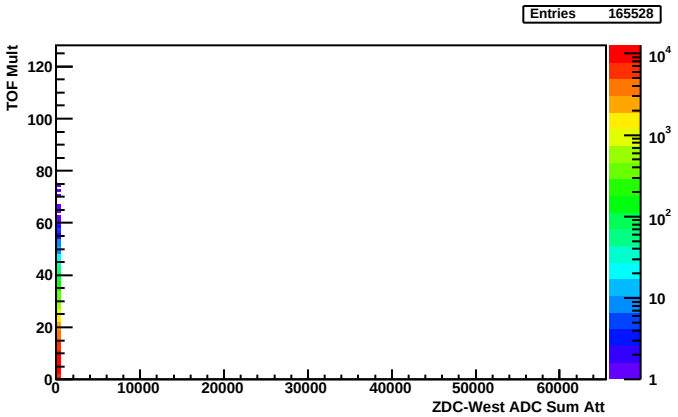

Entries 165528
TOF Mult
BBC-S-East ADC Sum

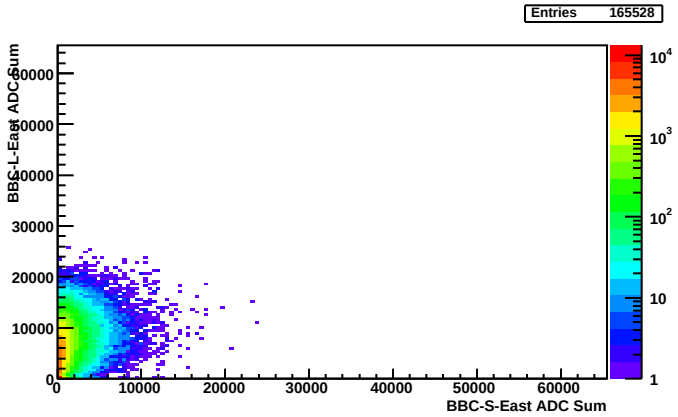

Entries 165528
TOF Mult
BBC-S-West ADC Sum

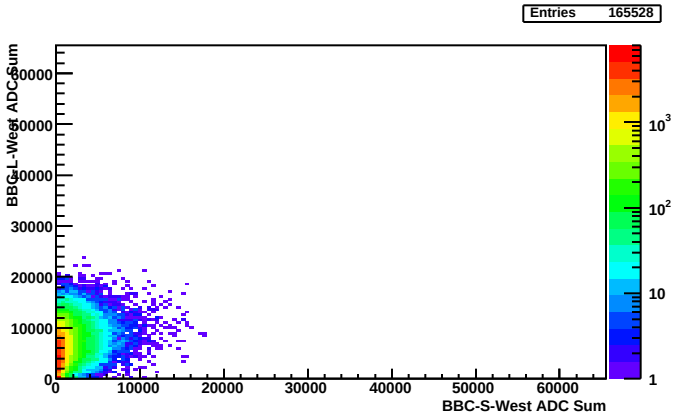

Entries 165528
TOF Mult
VPD-East ADC Sum

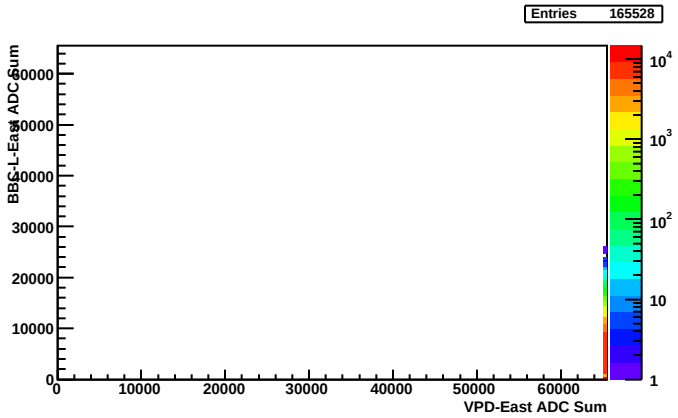

Entries 165528
TOF Mult
VPD-West ADC Sum

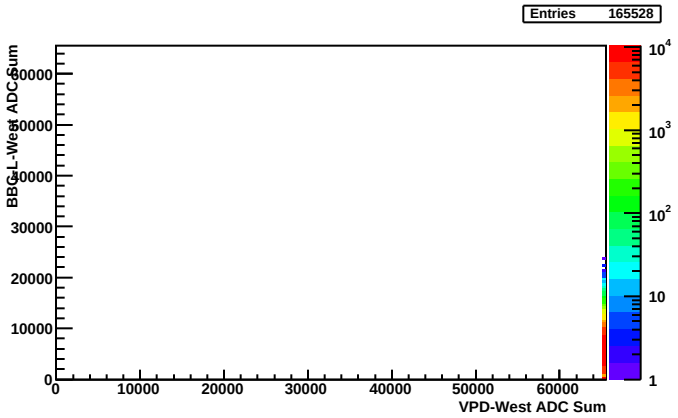

Entries 165528
TOF Mult
ZDC-East ADC Sum Att


Entries 165528
TOF Mult
ZDC-West ADC Sum Att


Entries 165528
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 165528
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 165528
BBC-L-East ADC Sum
VPD-East ADC Sum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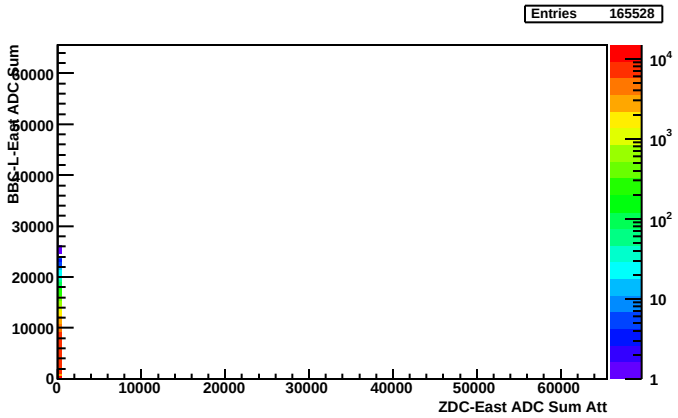
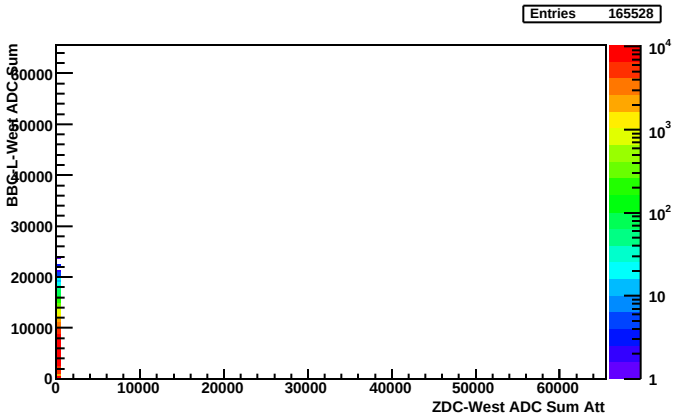
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 165528
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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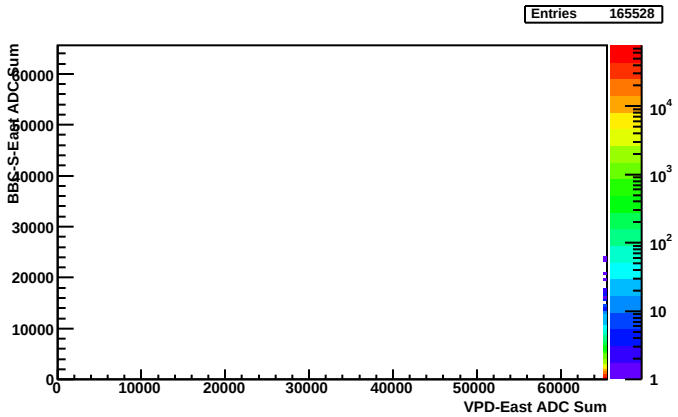
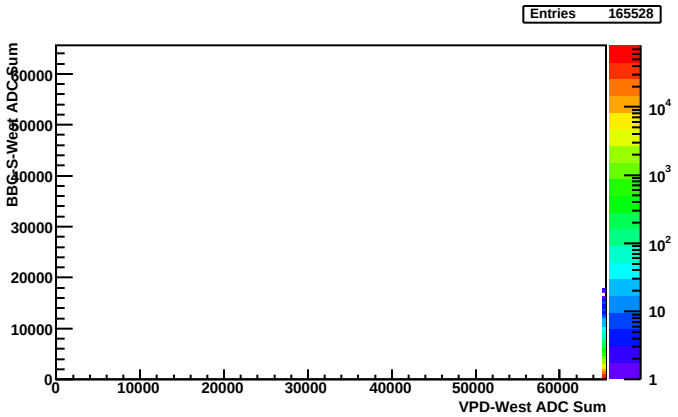
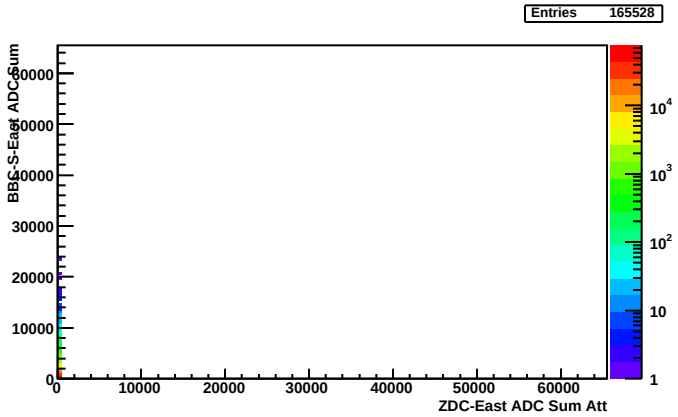
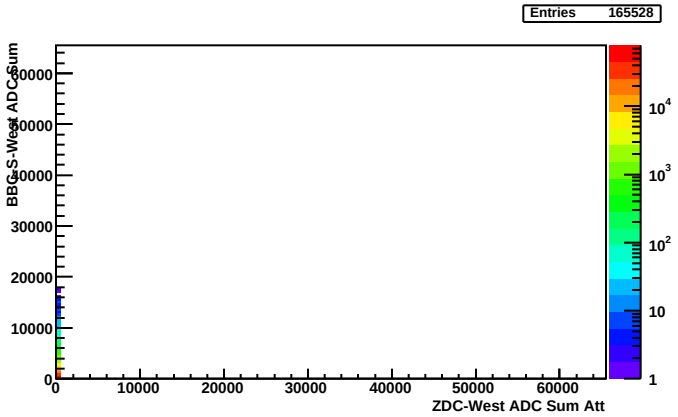
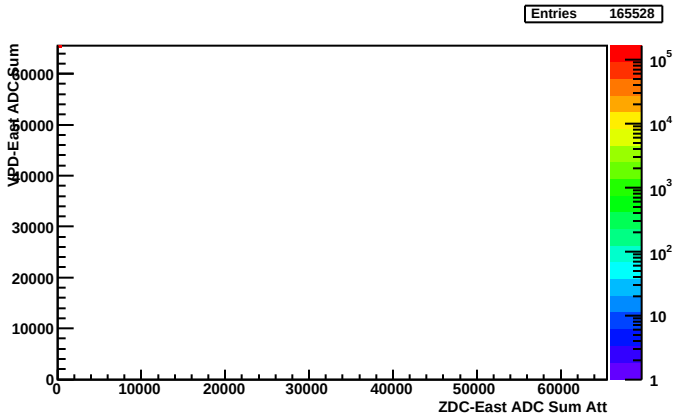
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

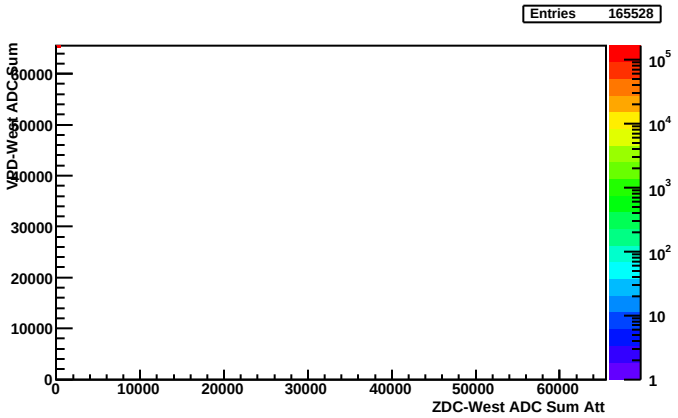

Entries 165528
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 165528
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 165528
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 165528
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 165528
VPD-East ADC Sum
ZDC-East ADC Sum Att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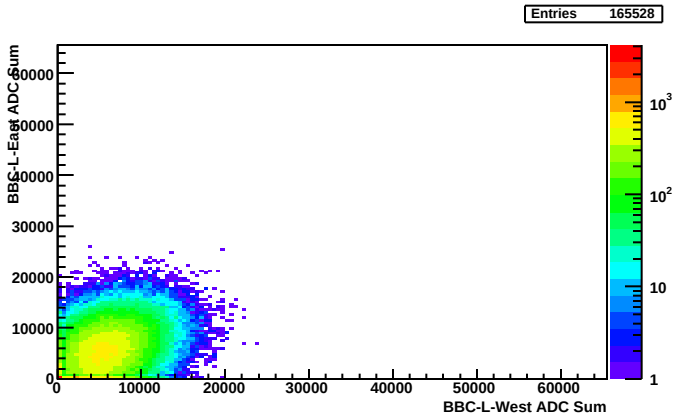
VPD-West ADC Sum
ZDC-West ADC Sum Att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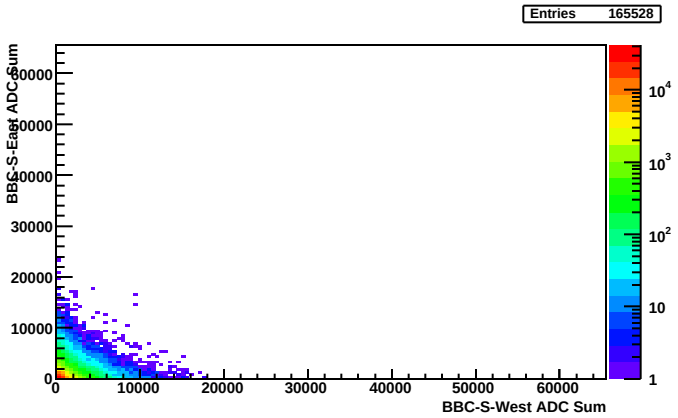
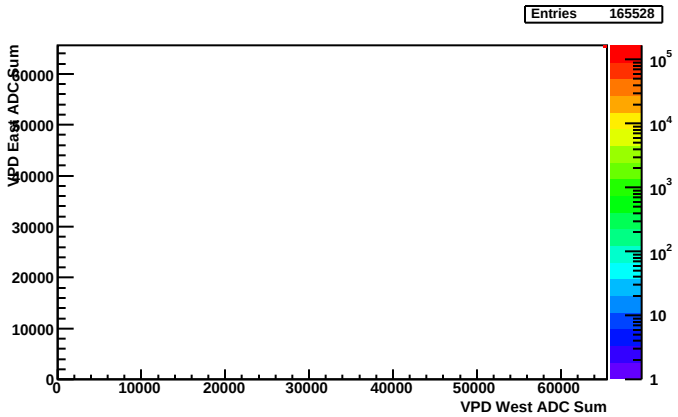
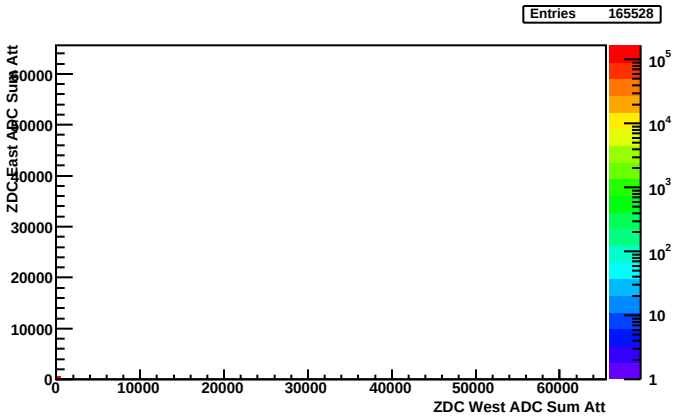
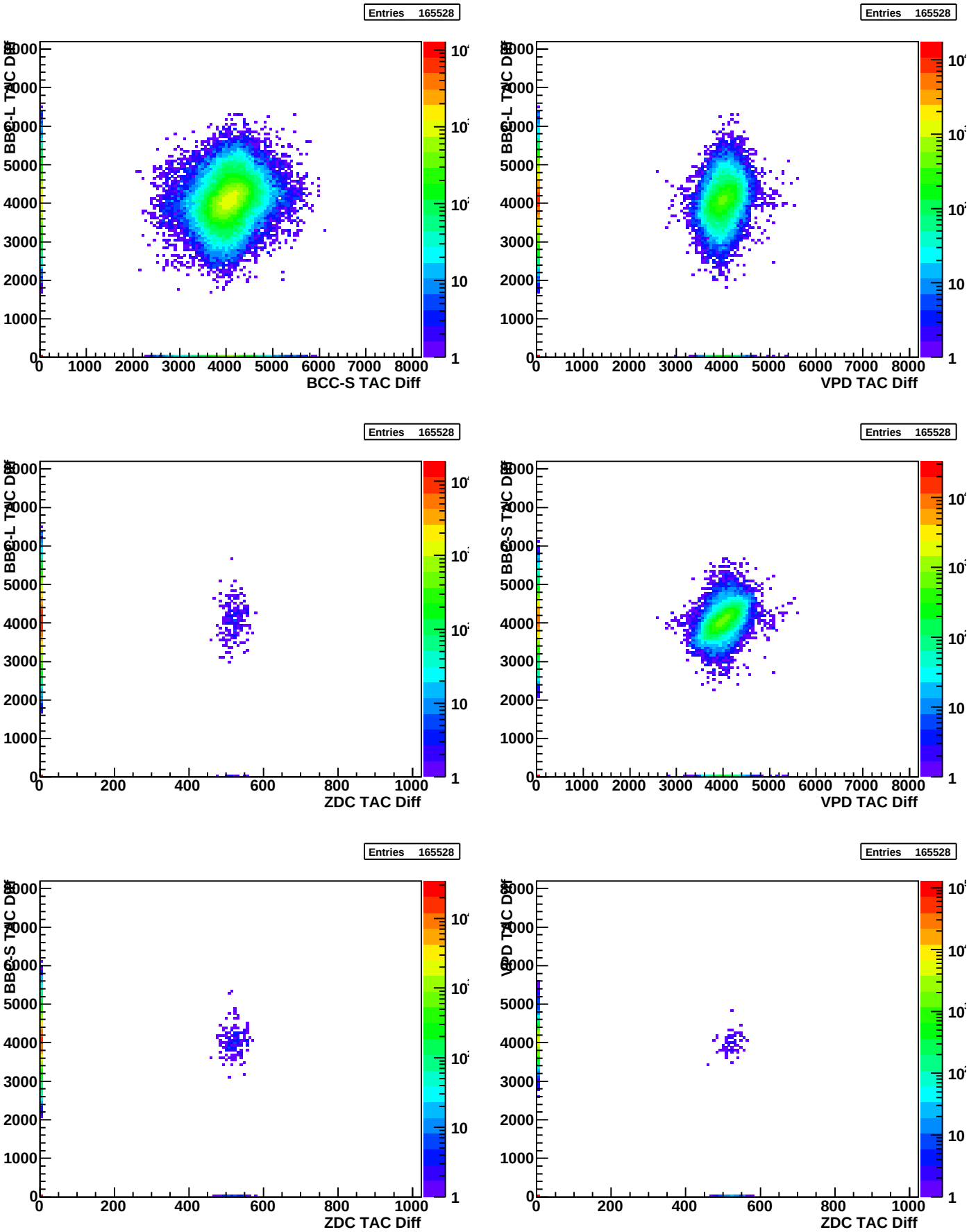
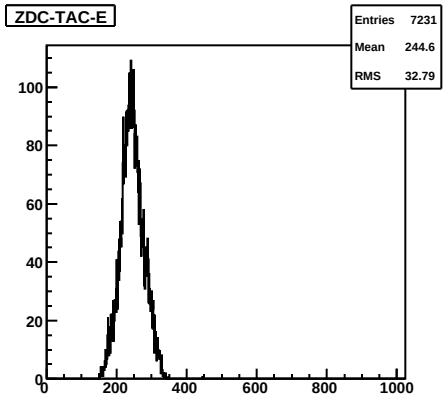
BBC-L-East ADC Sum
BBC-L-West ADC Sum

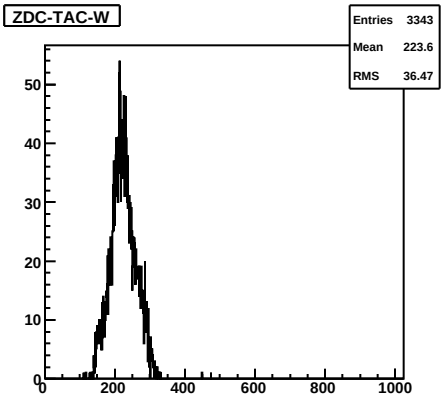

Entries 165528
BBC-S-East ADC Sum
BBC-S-West ADC Sum

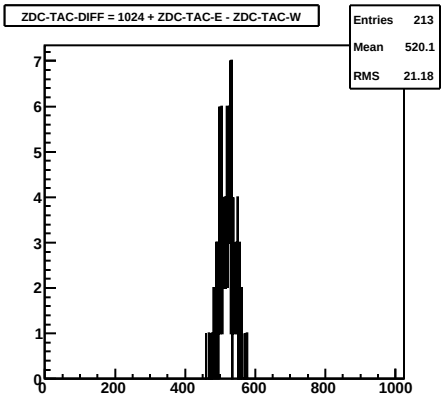

Entries 165528
VPD East ADC Sum
VPD West ADC Sum

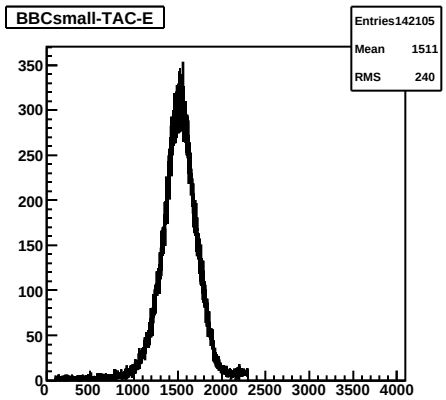

Entries 165528
ZDC East ADC Sum Att
ZDC West ADC Sum Att

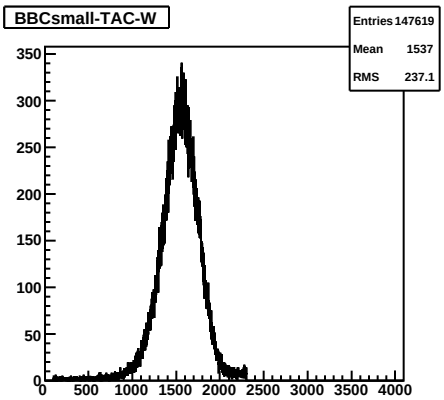

Entries 165528
BBC-L TAC Diff
BCC-S TAC Diff
Entries 165528
BBC-L TAC Diff
VPD TAC Diff
Entries 165528
BBC-L TAC Diff
ZDC TAC Diff
Entries 165528
BBC-S TAC Diff
VPD TAC Diff
Entries 165528
BBC-S TAC Diff
ZDC TAC Diff
Entries 165528
VPD TAC Diff
ZDC TAC Diff

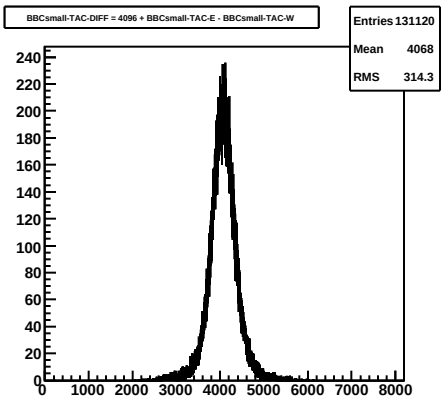

ZDC-TAC-E
Entries 7231
Mean 244.6
RMS 32.79

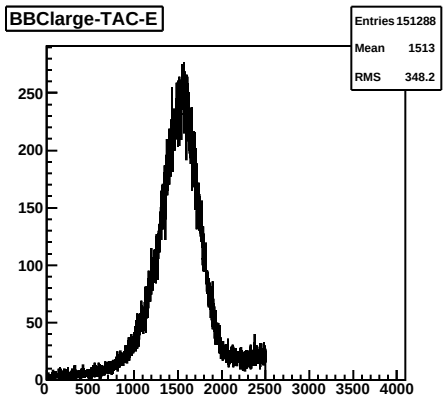

ZDC-TAC-W
Entries 3343
Mean 223.6
RMS 36.47

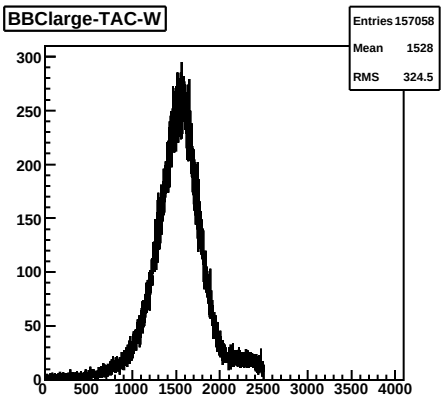

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 213
Mean 520.1
RMS 21.18

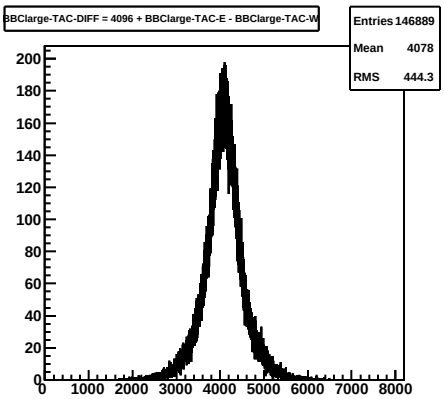

BBCsmall-TAC-E
Entries 142105
Mean 1511
RMS 240

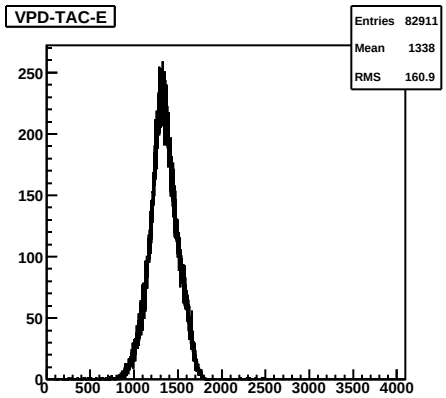

BBCsmall-TAC-W
Entries 147619
Mean 1537
RMS 237.1

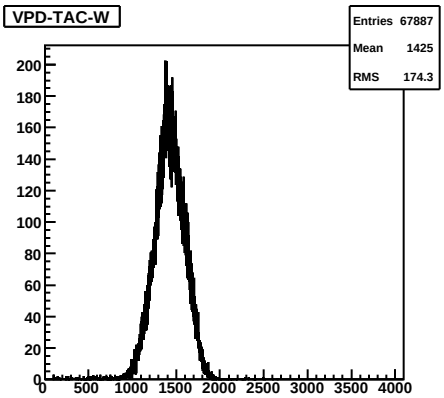

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 131120
Mean 4068
RMS 314.3

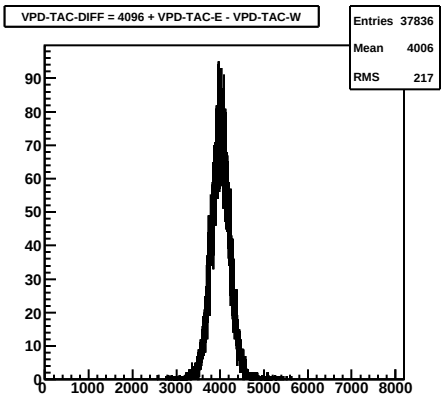

BBClarge-TAC-E
Entries 151288
Mean 1513
RMS 348.2

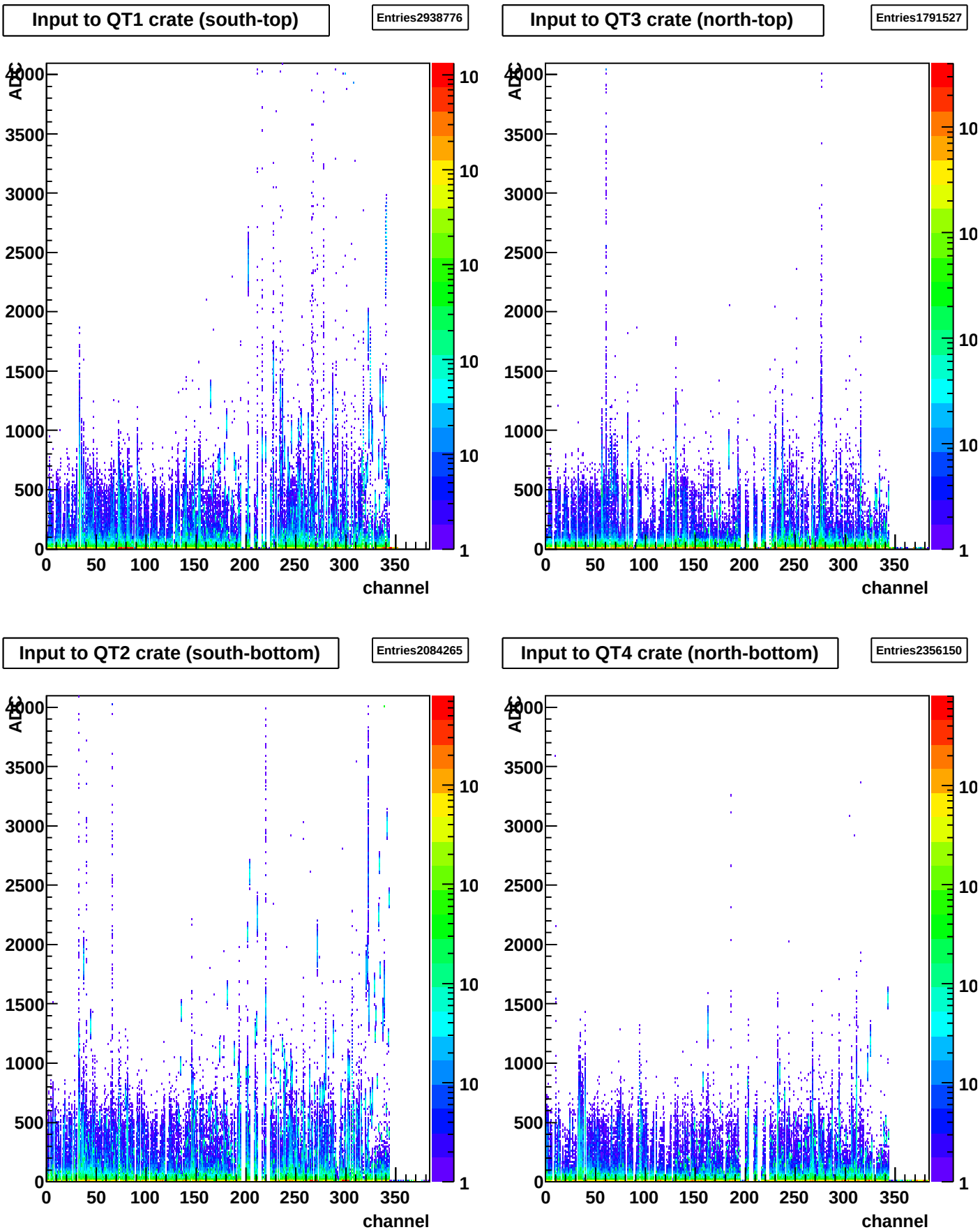

BBClarge-TAC-W
Entries 157058
Mean 1528
RMS 324.5

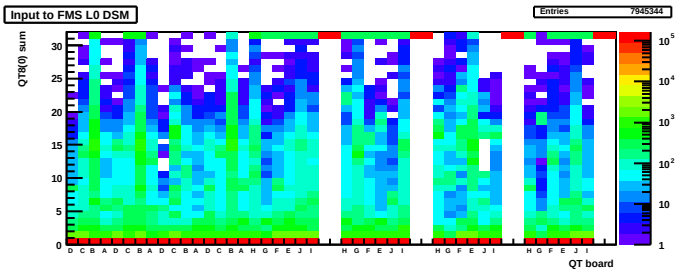

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 146889
Mean 4078
RMS 444.3

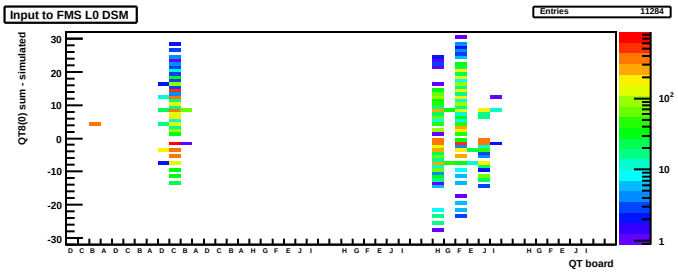

VPD-TAC-E
Entries 82911
Mean 1338
RMS 160.9

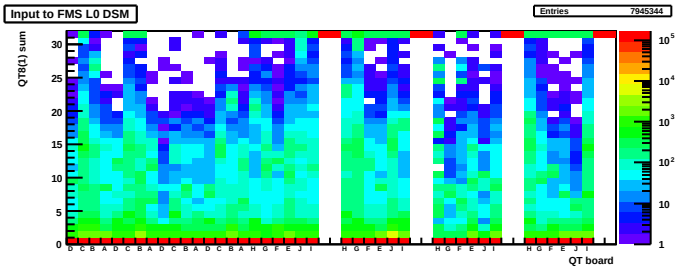

VPD-TAC-W
Entries 67887
Mean 1425
RMS 174.3

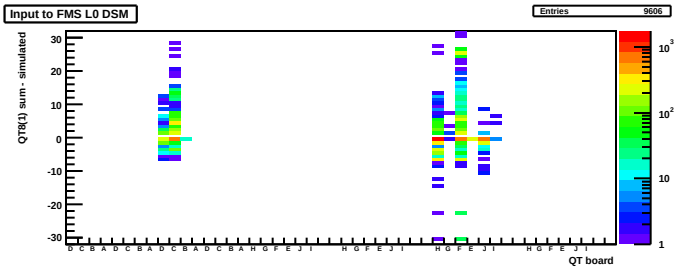

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 37836
Mean 4006
RMS 217

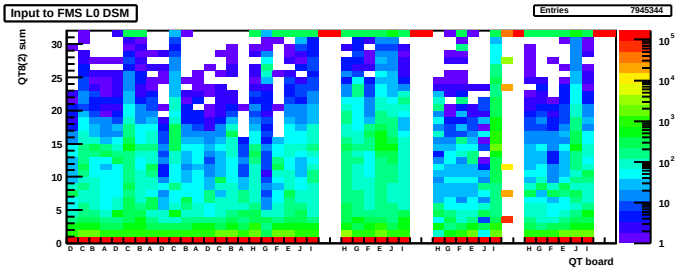

Input to QT1 crate (south-top)
Entries2938776
Input to QT3 crate (north-top)
Entries1791527
Input to QT2 crate (south-bottom)
Entries2084265
Input to QT4 crate (north-bottom)
Entries2356150
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

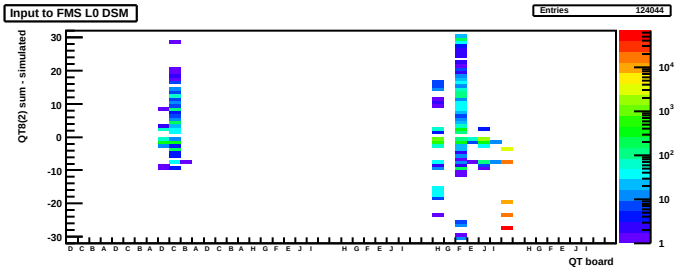

Input to FMS L0 DSM
Entries 7945344
QT8(0) sum
QT board

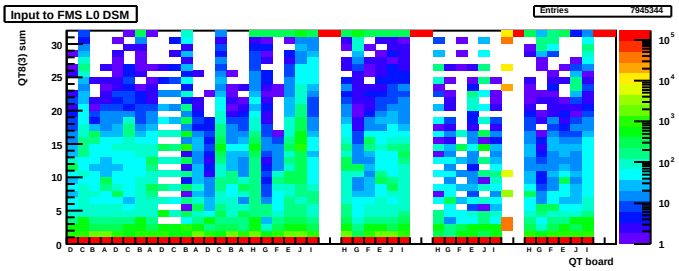

Input to FMS L0 DSM
Entries 11284
QT8(0) sum - simulated
QT board

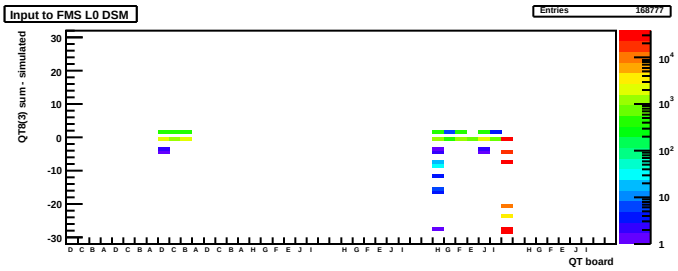

Input to FMS L0 DSM
Entries 7945344
QT8(1) sum
QT board

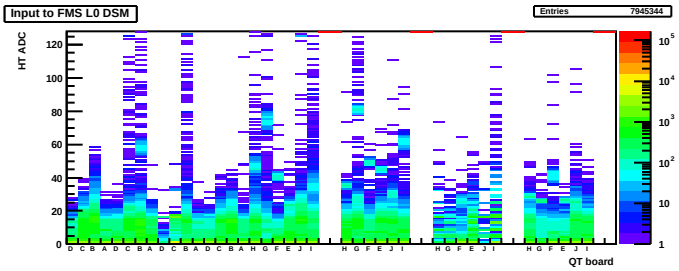

Input to FMS L0 DSM
Entries 9606
QT8(1) sum - simulated
QT board

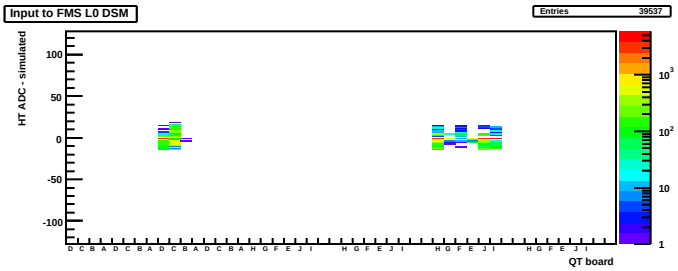

Input to FMS L0 DSM
Entries 7945344
QT8(2) sum
QT board

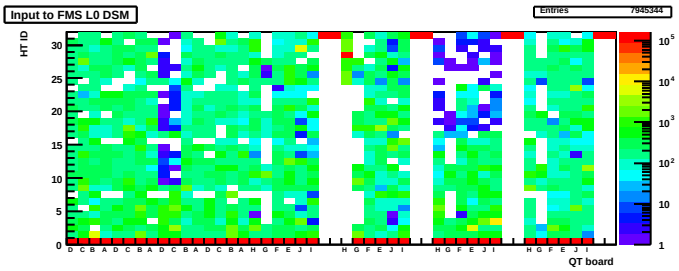

Input to FMS L0 DSM
Entries 124044
QT8(2) sum - simulated
QT board

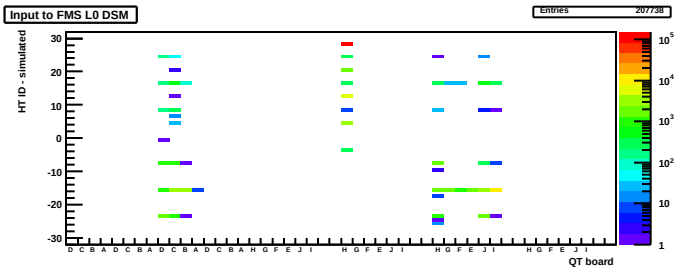

Input to FMS L0 DSM
Entries 7945344
QT8(3) sum
QT board

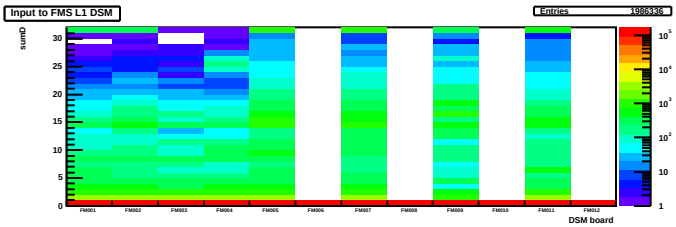

Input to FMS L0 DSM
Entries 168777
QT8(3) sum - simulated
QT board

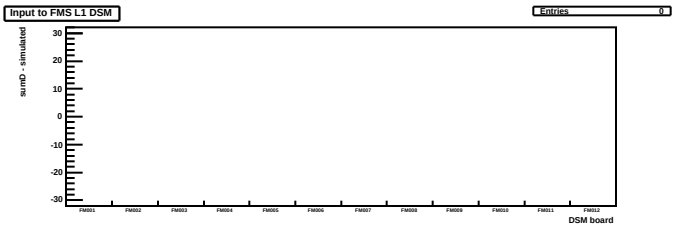

Input to FMS L0 DSM
Entries 7945344
HT ADC
QT board

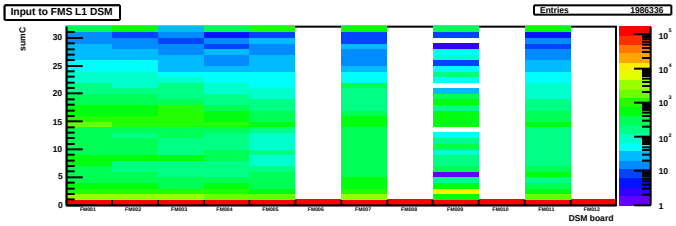

Input to FMS L0 DSM
Entries 39537
HT ADC - simulated
QT board

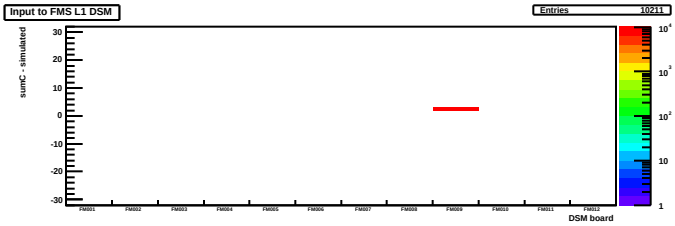

Input to FMS L0 DSM
Entries 7945344
HT ID
QT board

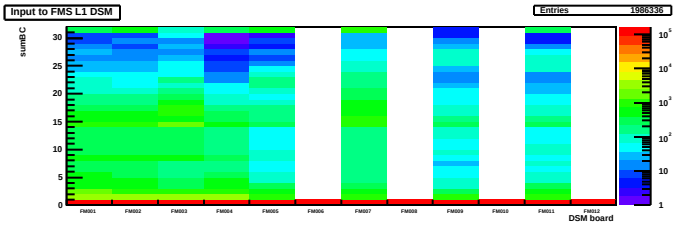

Input to FMS L0 DSM
Entries 207738
HT ID - simulated
QT board

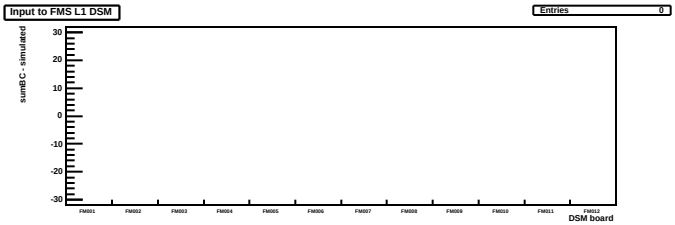

Input to FMS L1 DSM
Entries 1986336
sumD
DSM board

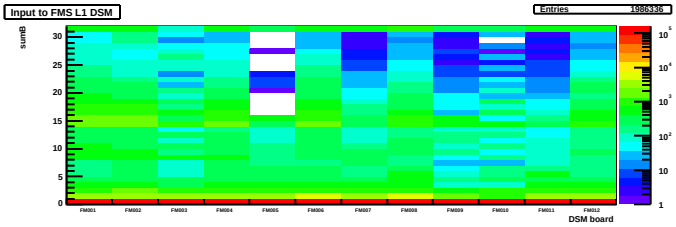

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

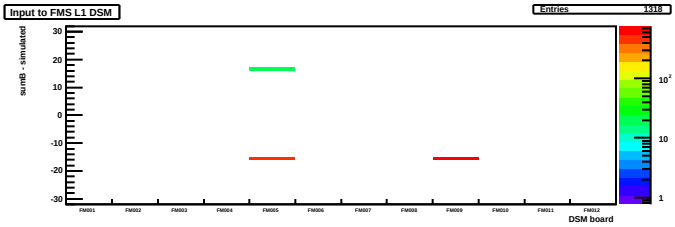

Input to FMS L1 DSM
Entries 1986336
sumC
DSM board

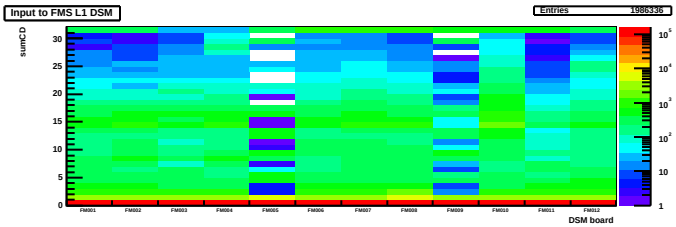

Input to FMS L1 DSM
Entries 10211
sumC - simulated
DSM board

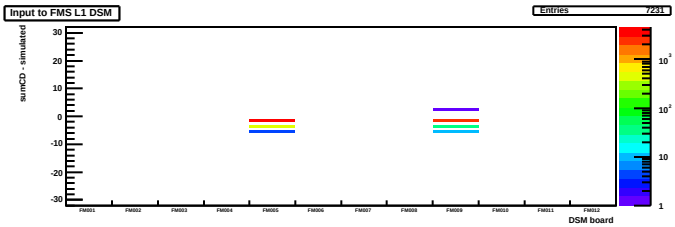

Input to FMS L1 DSM
Entries 1986336
sumBC
DSM board

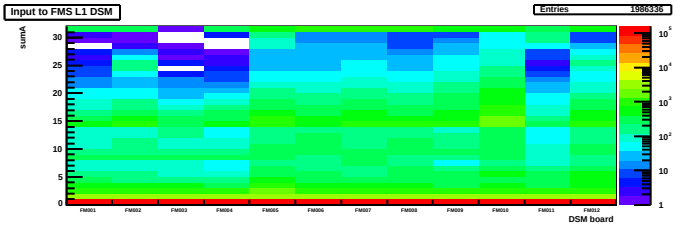

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

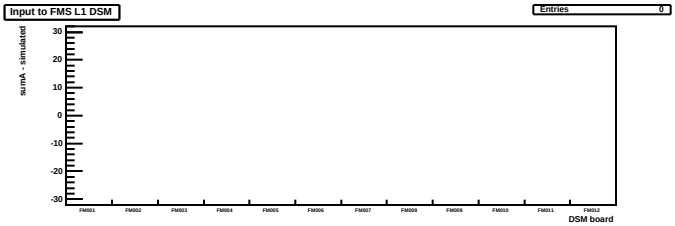

Input to FMS L1 DSM
Entries 1986336
sumB
DSM board

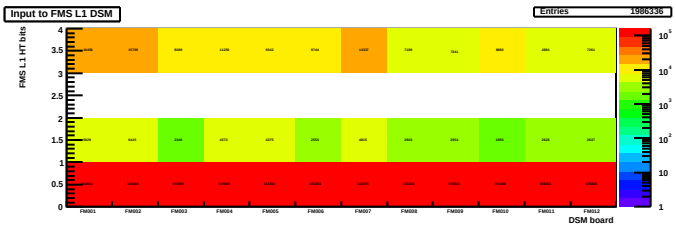

Input to FMS L1 DSM
Entries 1318
sumB - simulated
DSM board

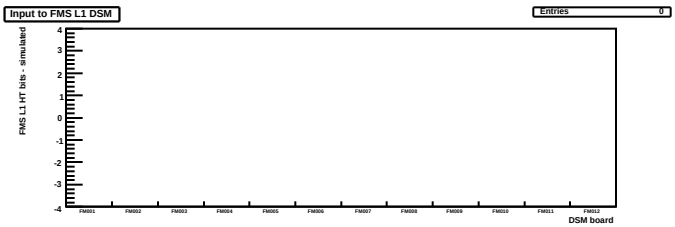

Input to FMS L1 DSM
Entries 1986336
sumCD
DSM board

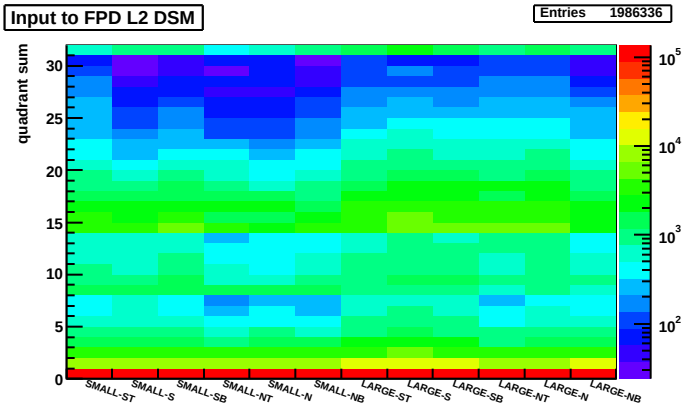

Input to FMS L1 DSM
Entries 7231
sumCD - simulated
DSM board

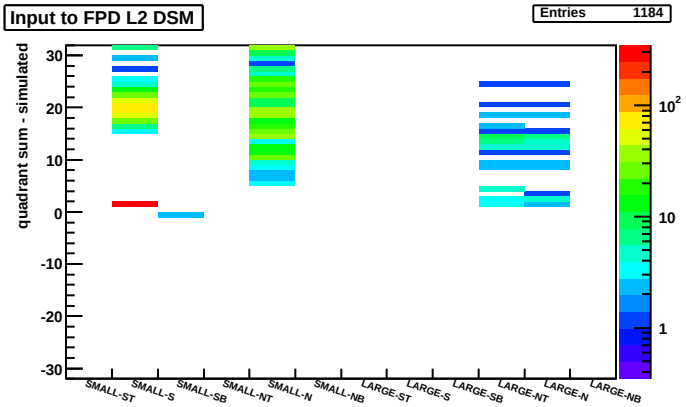

Input to FMS L1 DSM
Entries 1986336
sumA
DSM board

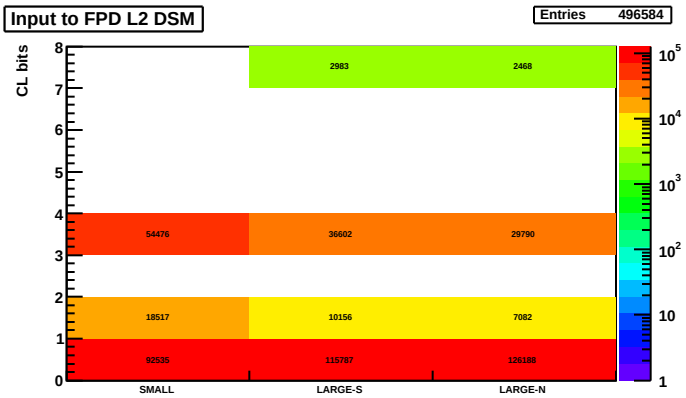

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

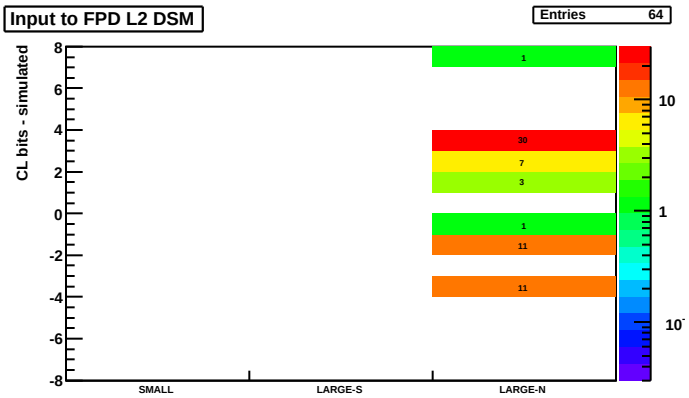

Input to FMS L1 DSM
Entries 1986336
FMS L1 HT bits
DSM board

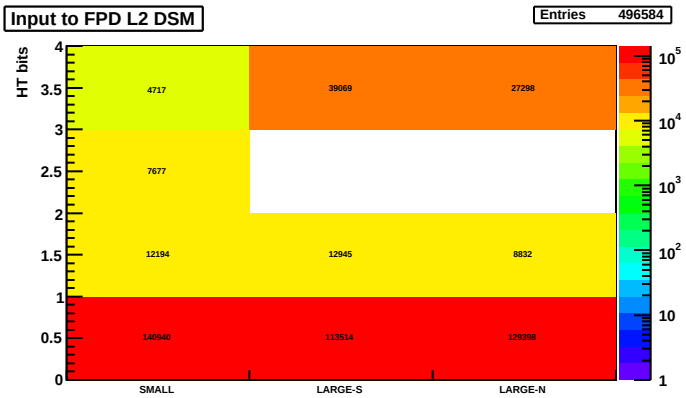

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

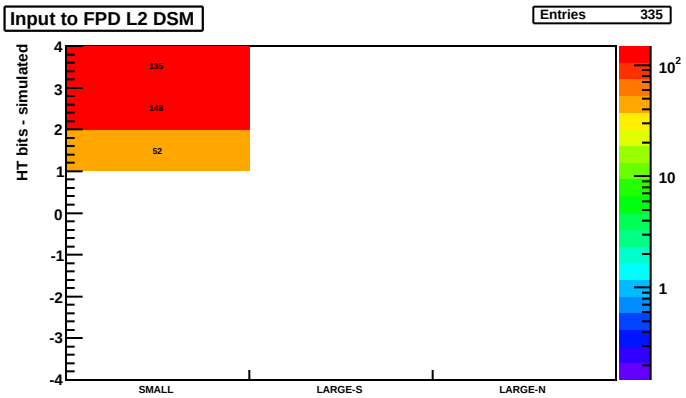

Input to FPD L2 DSM
Entries 1986336
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

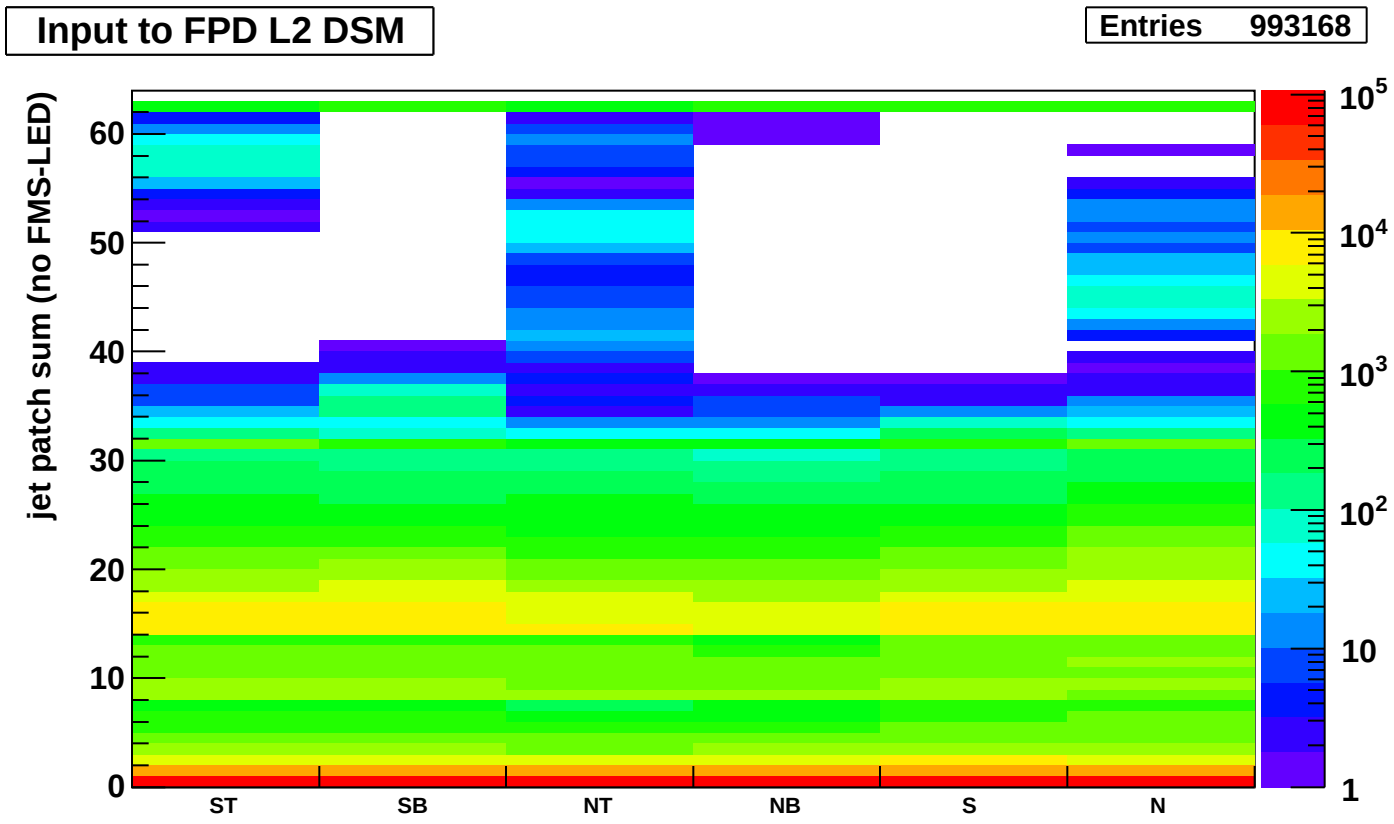

Input to FPD L2 DSM
Entries 1184
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

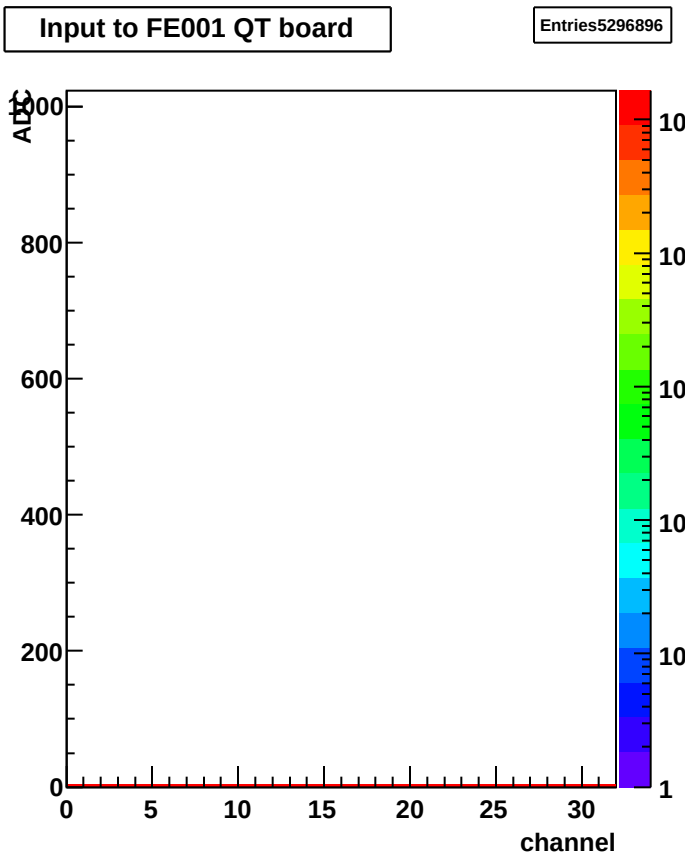

Input to FPD L2 DSM
Entries 496584
CL bits
2983
2468
54476
36602
29790
18517
10156
7082
92535
115787
126188
SMALL
LARGE-S
LARGE-N

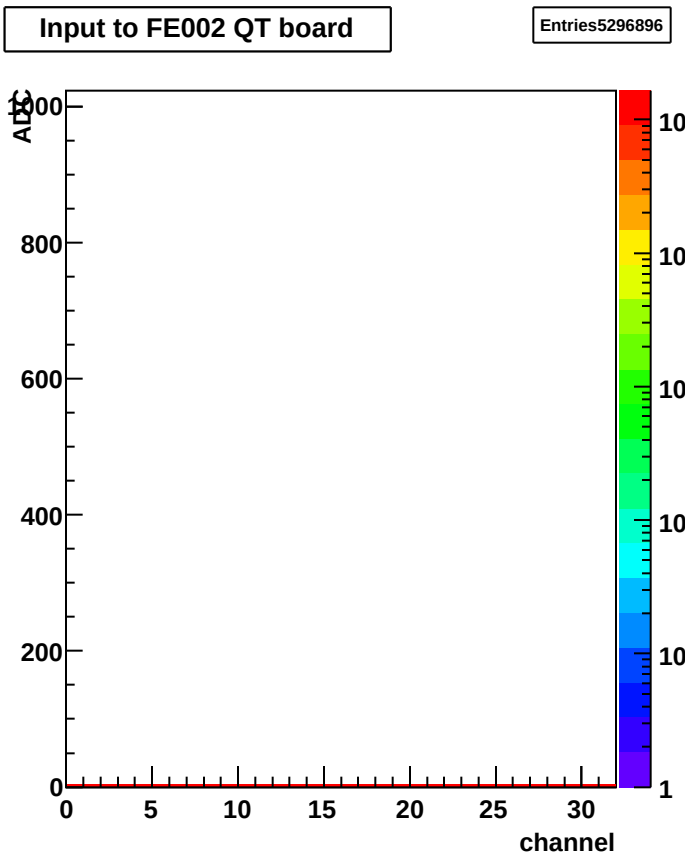

Input to FPD L2 DSM
Entries 64
CL bits - simulated
1
30
7
3
1
11
11
SMALL
LARGE-S
LARGE-N

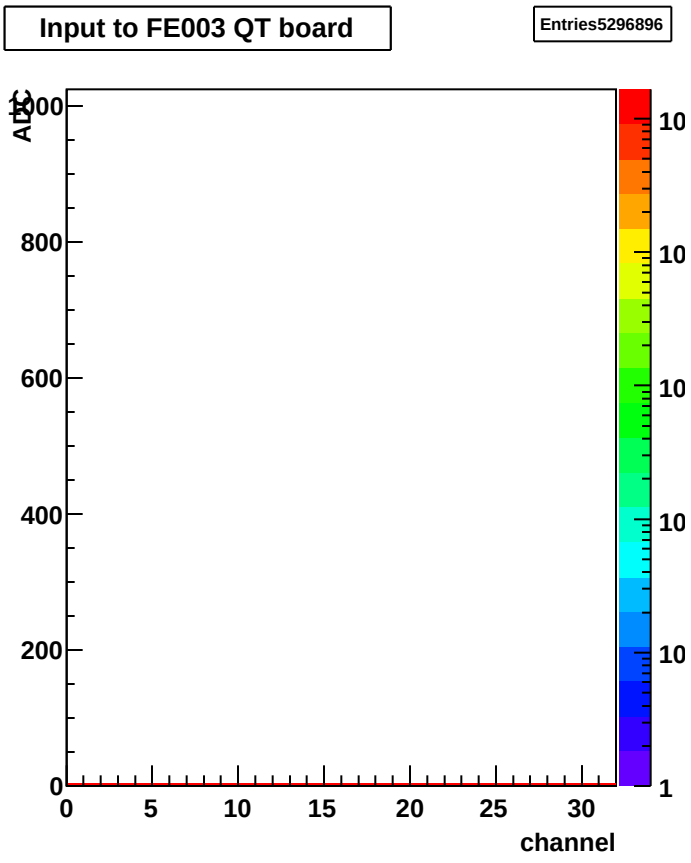

Input to FPD L2 DSM
Entries 496584
HT bits
4717
39069
27298
7677
12194
12945
8832
140940
113514
129398
SMALL
LARGE-S
LARGE-N

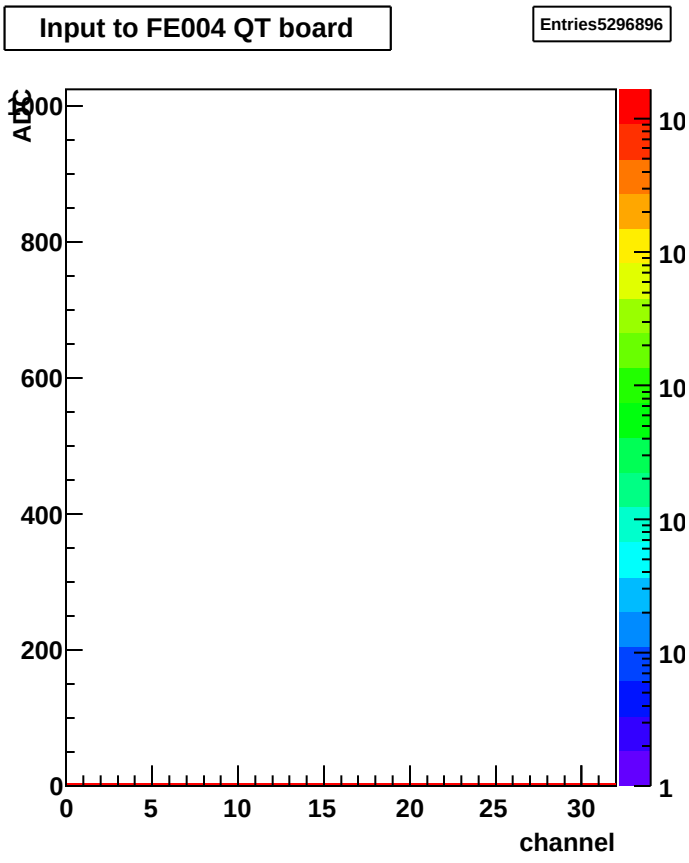

Input to FPD L2 DSM
Entries 335
HT bits - simulated
135
148
52
SMALL
LARGE-S
LARGE-N

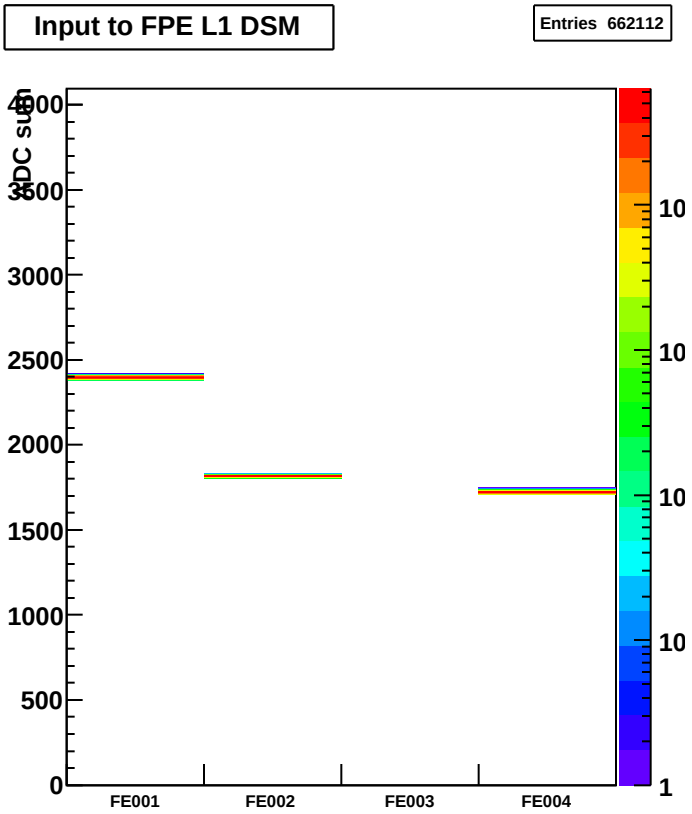

Input to FPD L2 DSM
Entries 993168
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

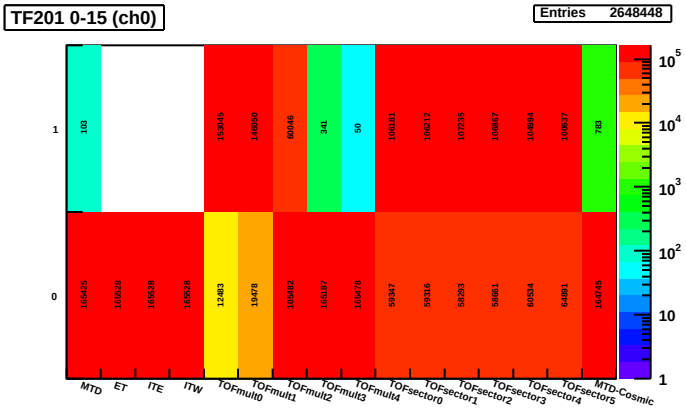

Input to FE001 QT board
Entries5296896
ADC
channel

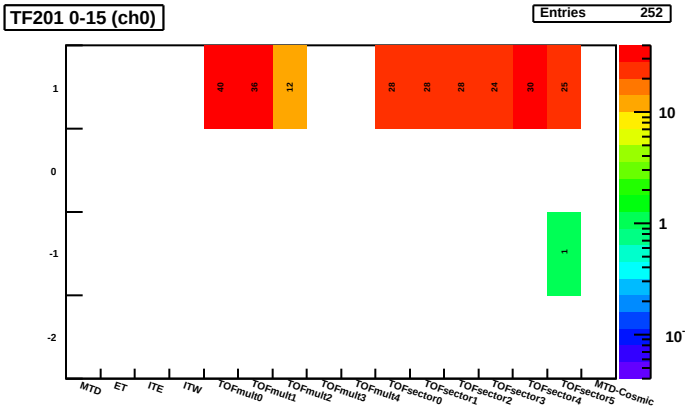

Input to FE002 QT board
Entries5296896
ADC
channel

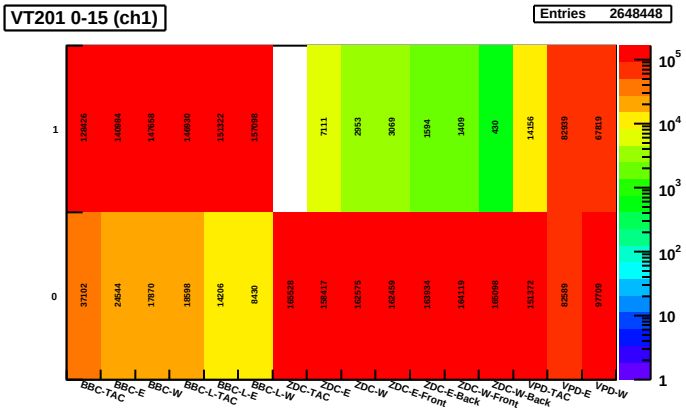

Input to FE003 QT board
Entries5296896
ADC
channel

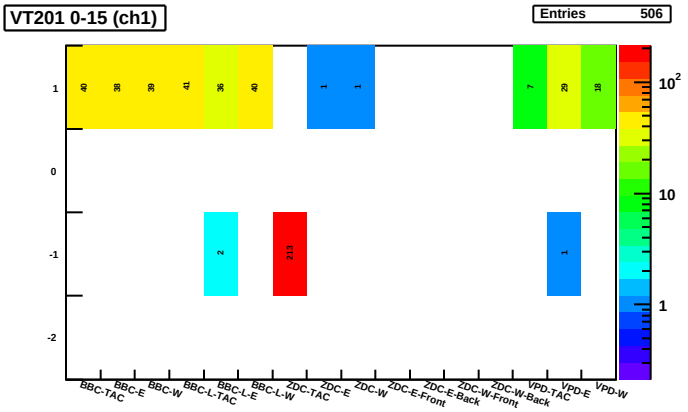

Input to FE004 QT board
Entries5296896
ADC
channel

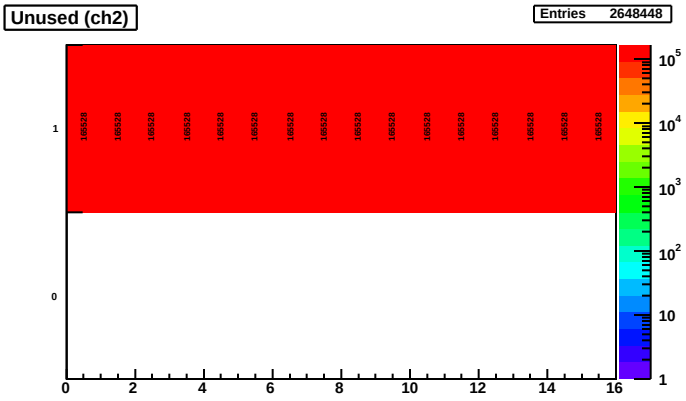

Input to FPE L1 DSM
Entries 662112
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

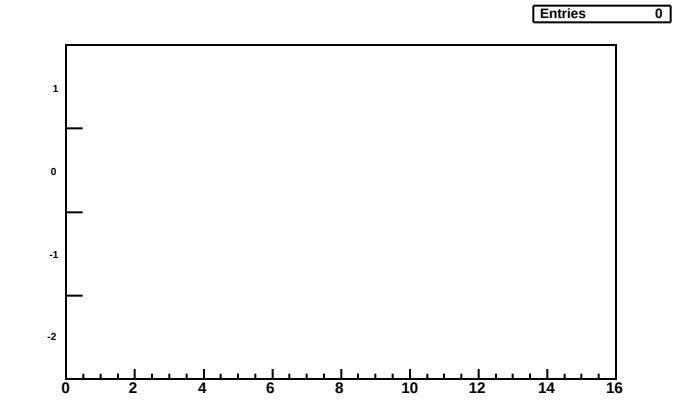

TF201 0-15 (ch0)
Entries 2648448
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

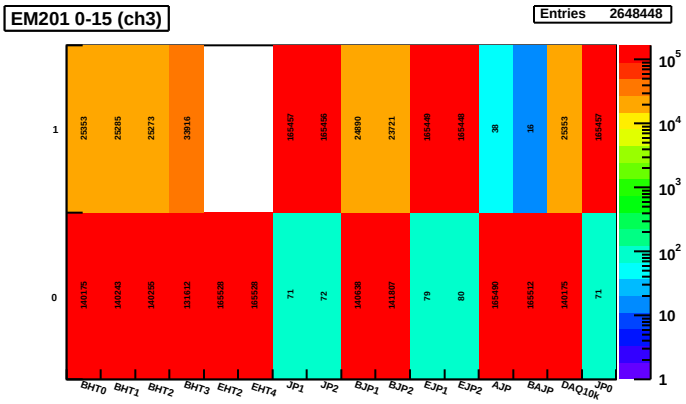

TF201 0-15 (ch0)
Entries 252
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

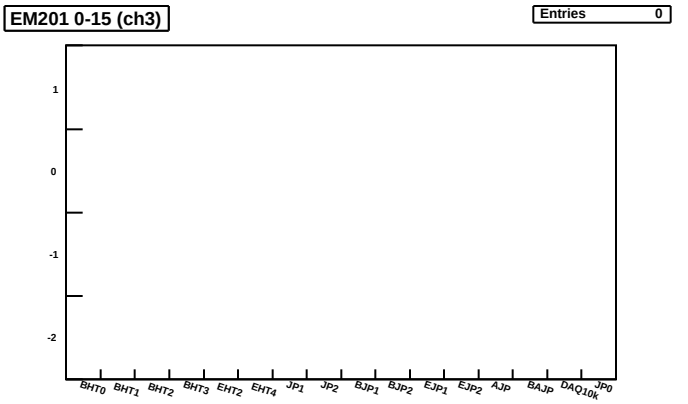

VT201 0-15 (ch1)
Entries 2648448
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 506
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2648448


Entries 0


EM201 0-15 (ch3)
Entries 2648448
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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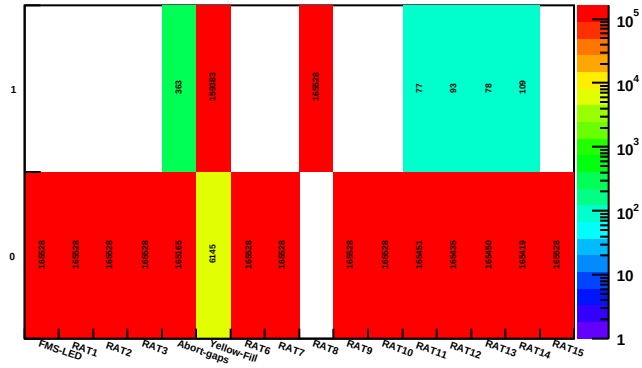

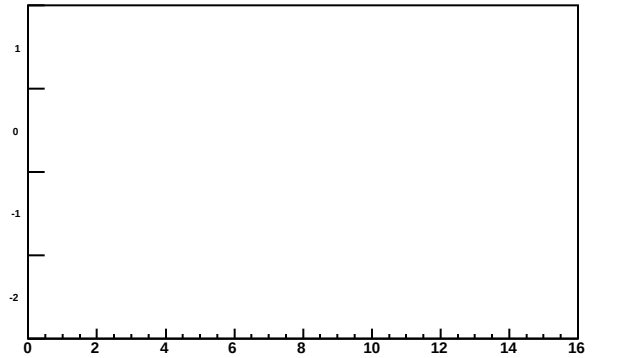

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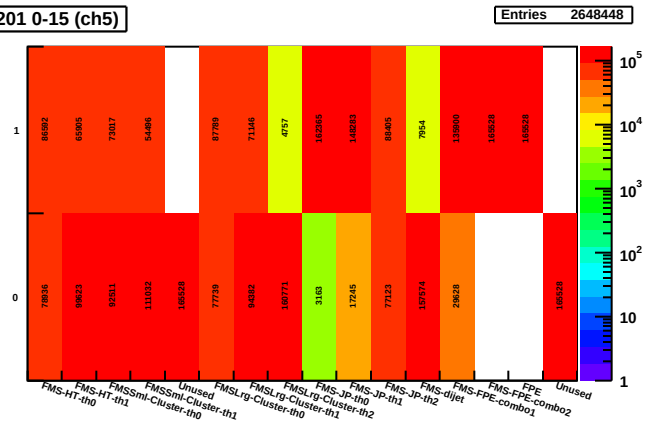

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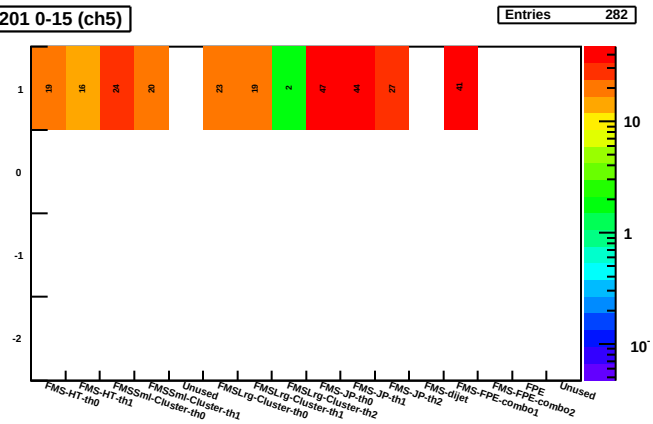

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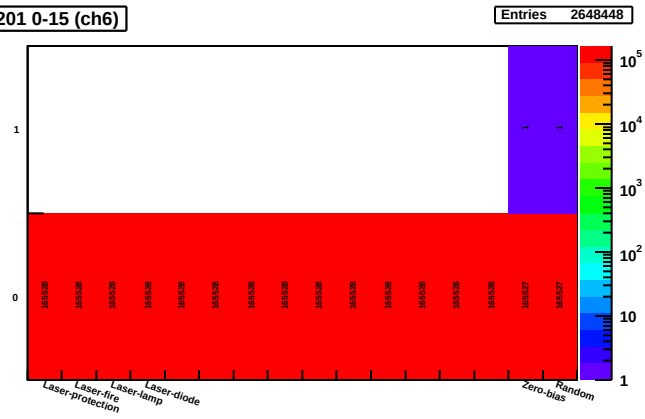

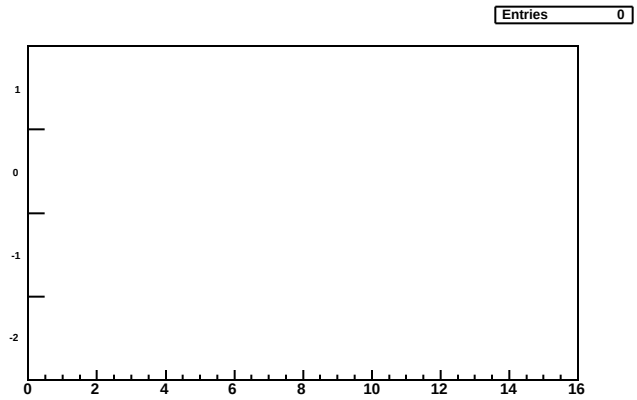
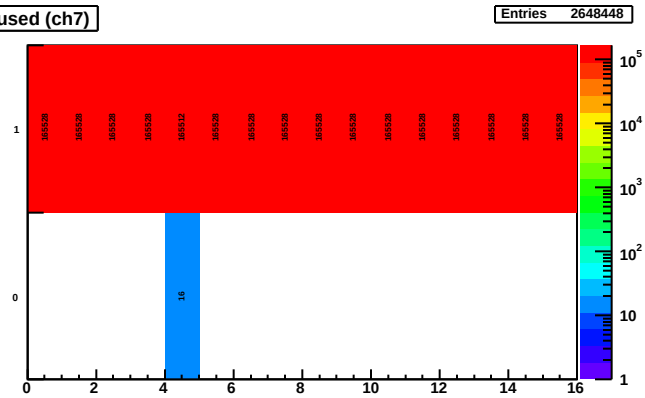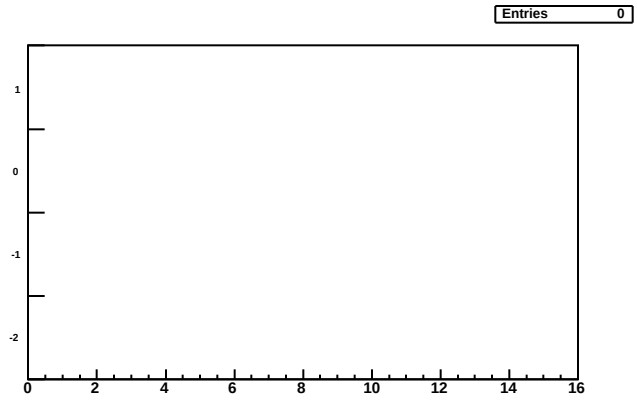
Unused (ch7)