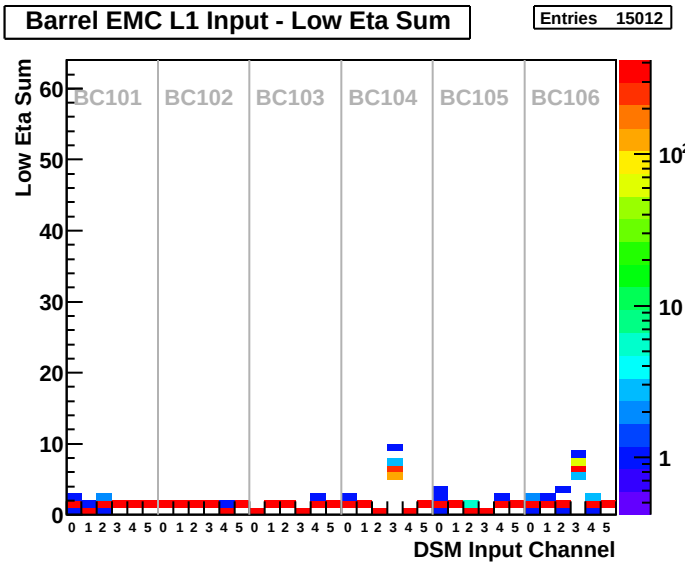
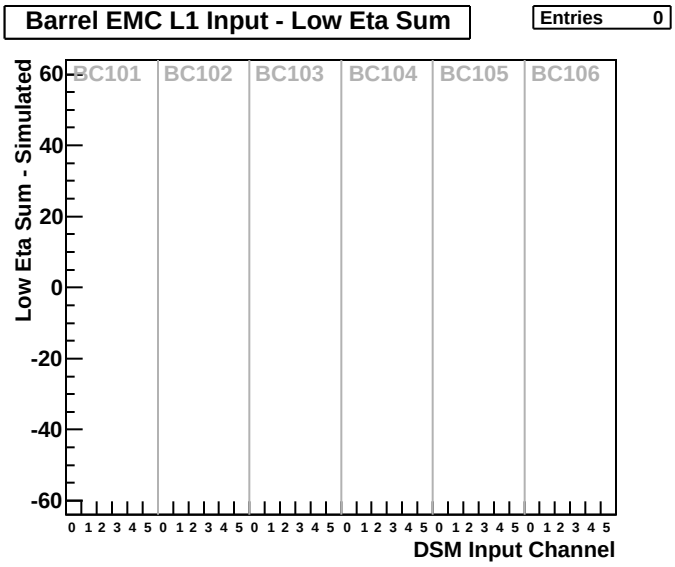
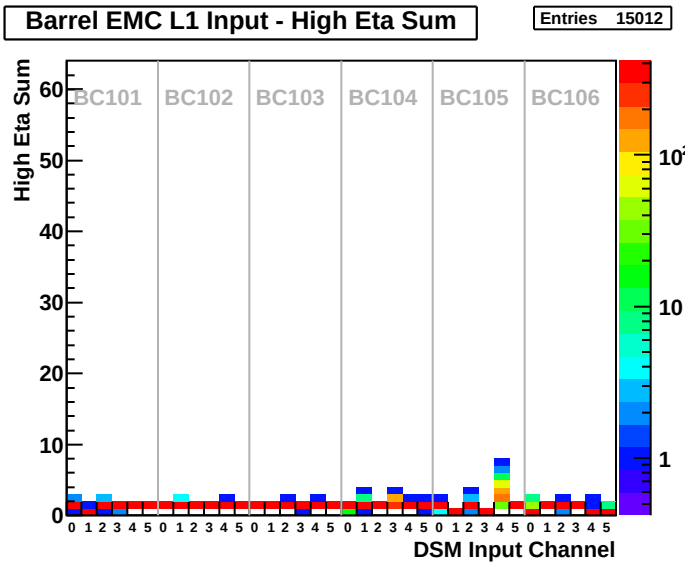
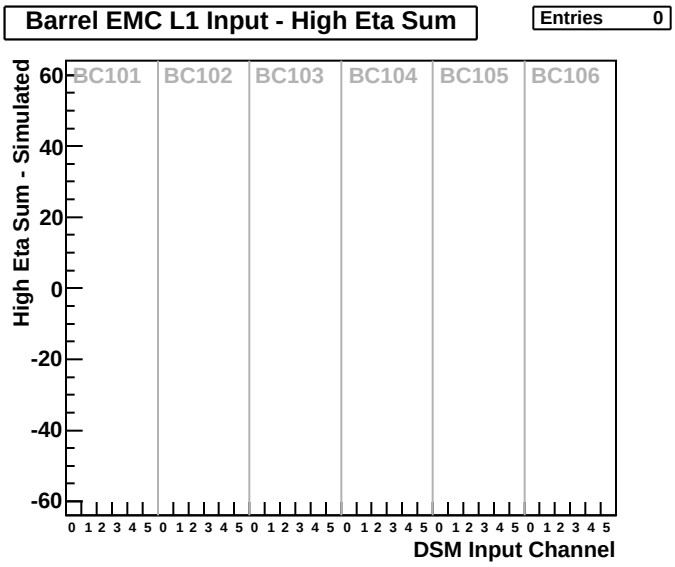
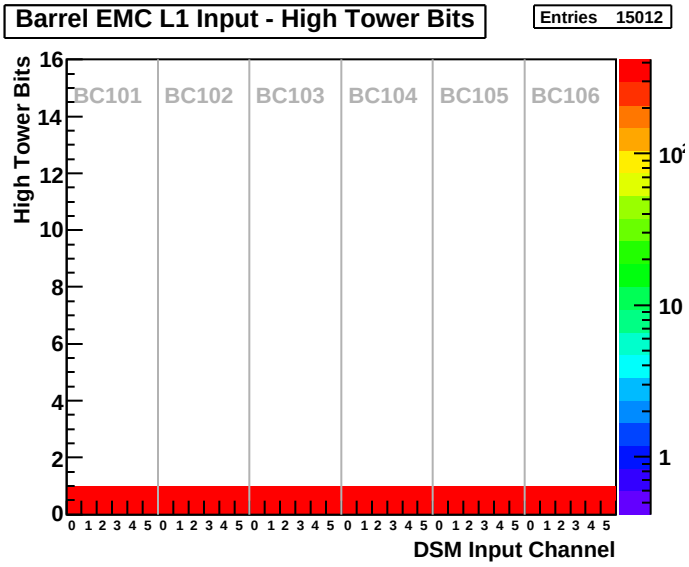
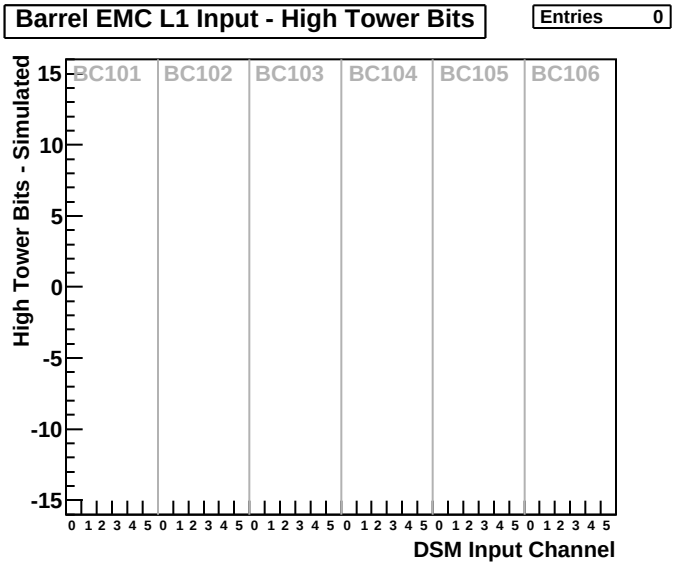

Barrel EMC L1 Input - Low Eta Sum
Entries 15012
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

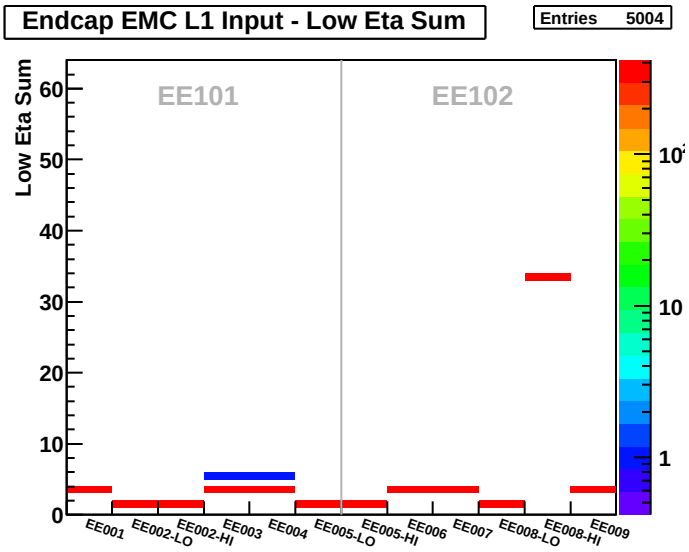

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

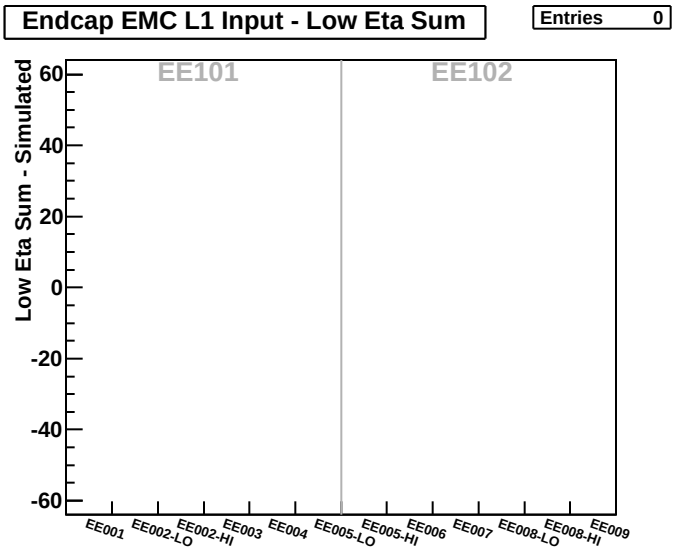

Barrel EMC L1 Input - High Eta Sum
Entries 15012
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

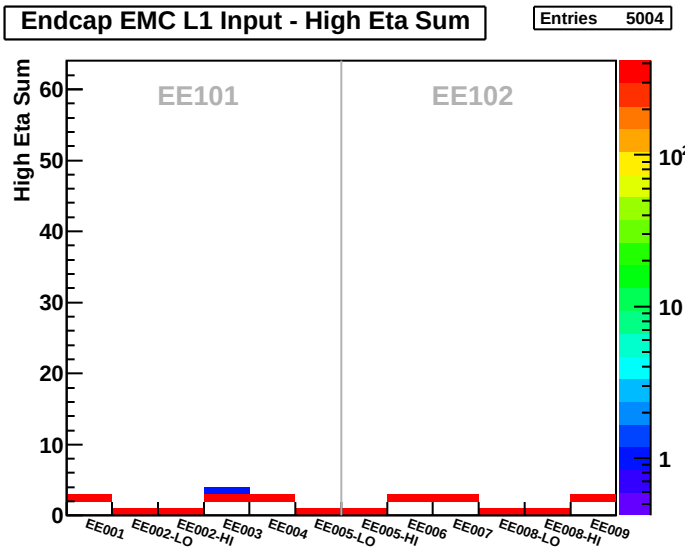

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

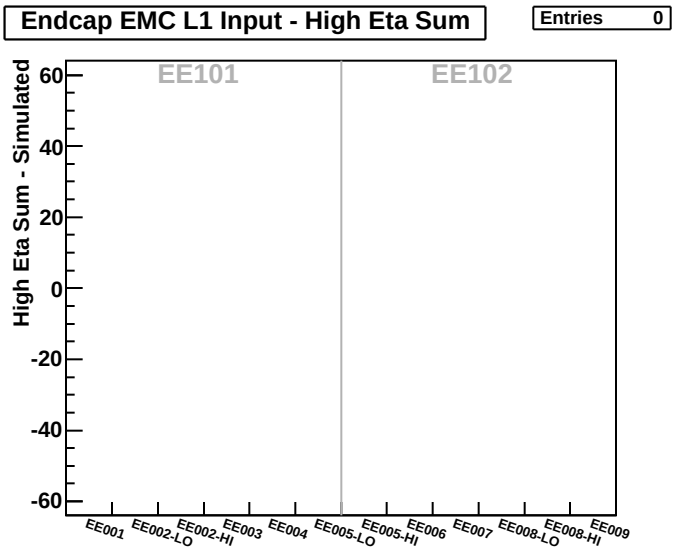

Barrel EMC L1 Input - High Tower Bits
Entries 15012
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

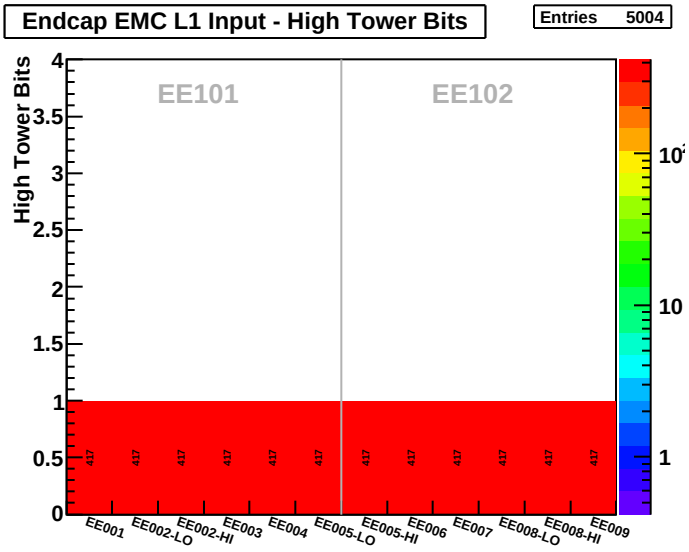

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

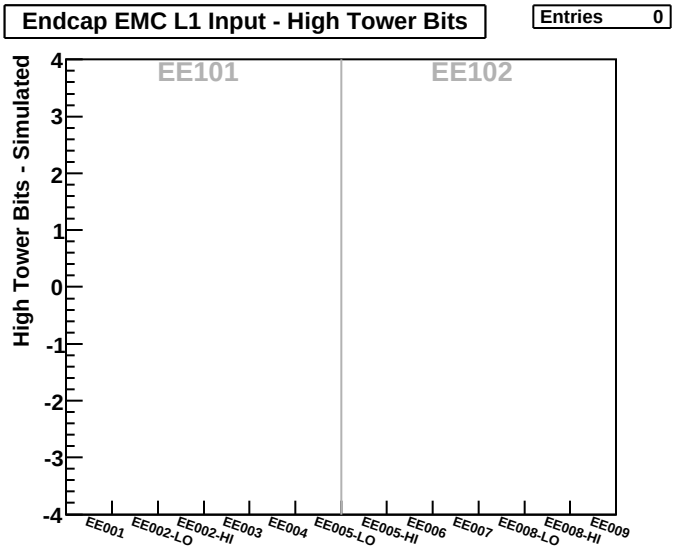

Endcap EMC L1 Input - Low Eta Sum
Entries 5004
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

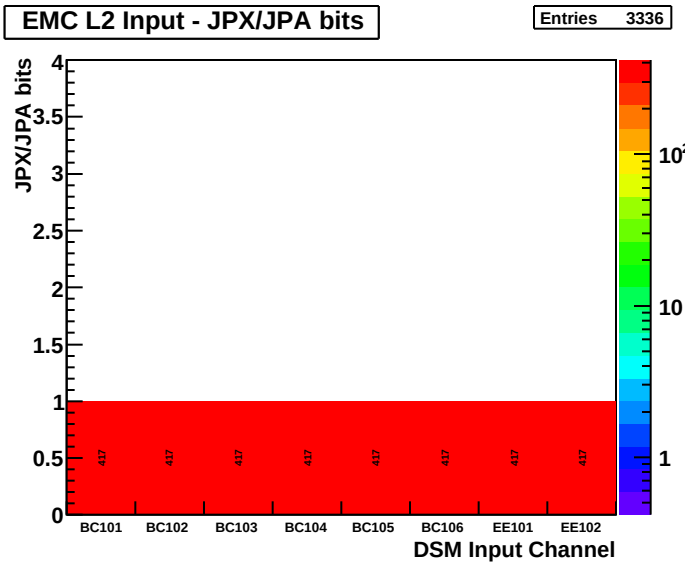

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

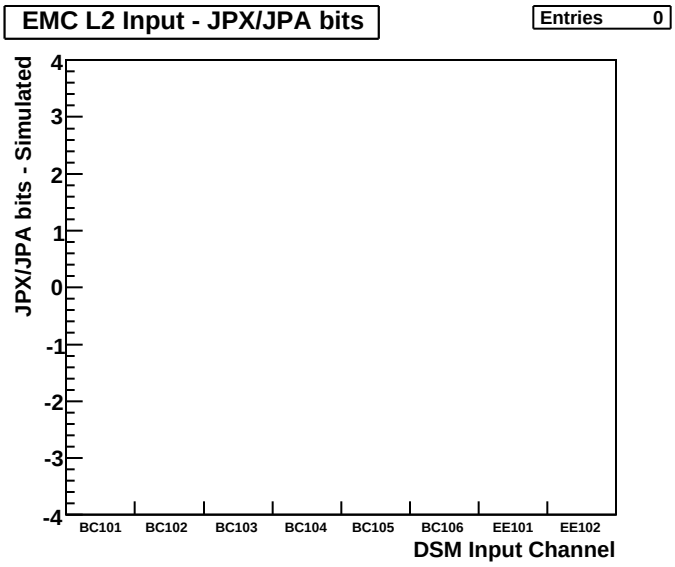

Endcap EMC L1 Input - High Eta Sum
Entries 5004
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

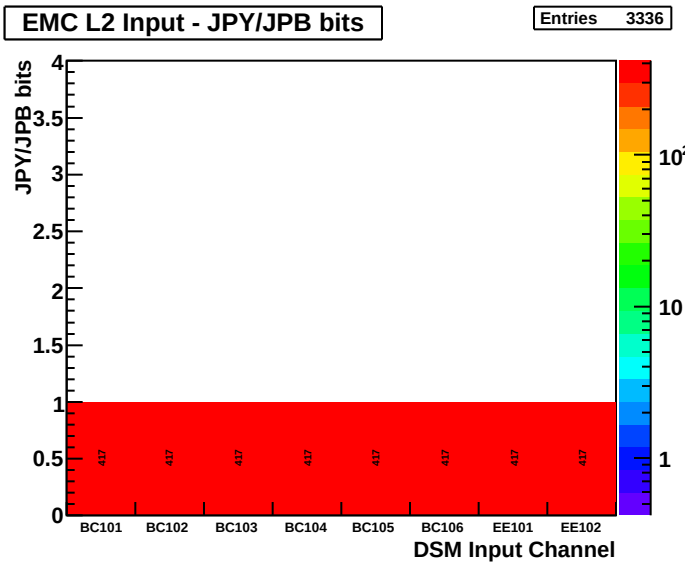

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

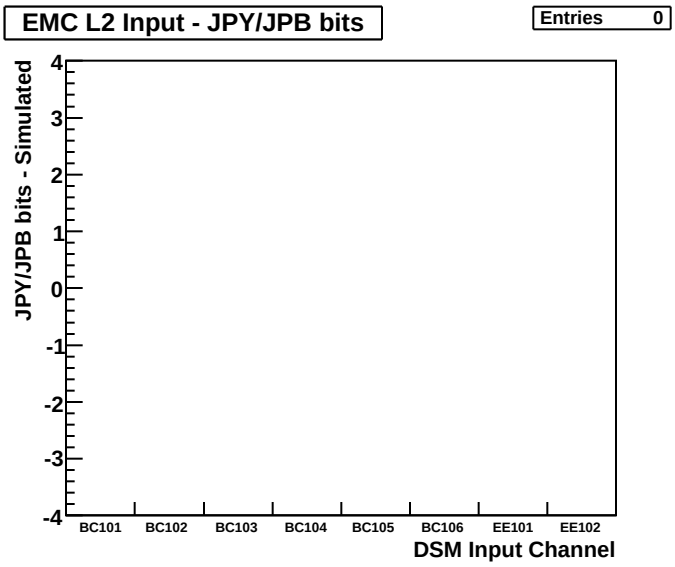

Endcap EMC L1 Input - High Tower Bits
Entries 5004
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

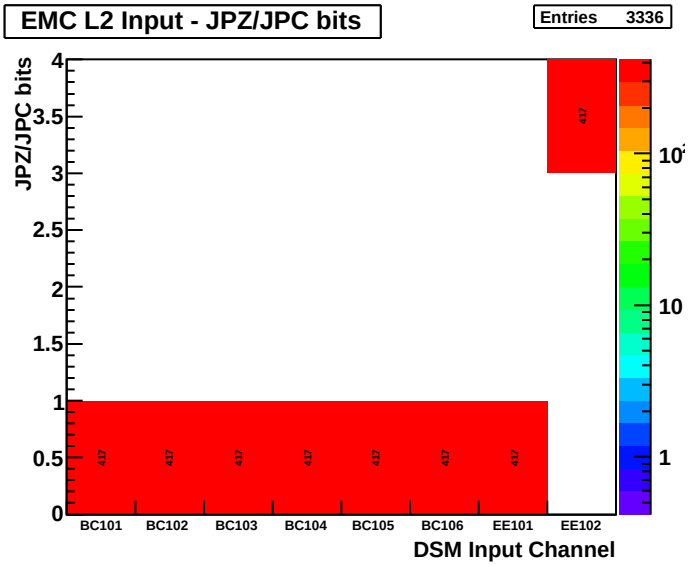

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

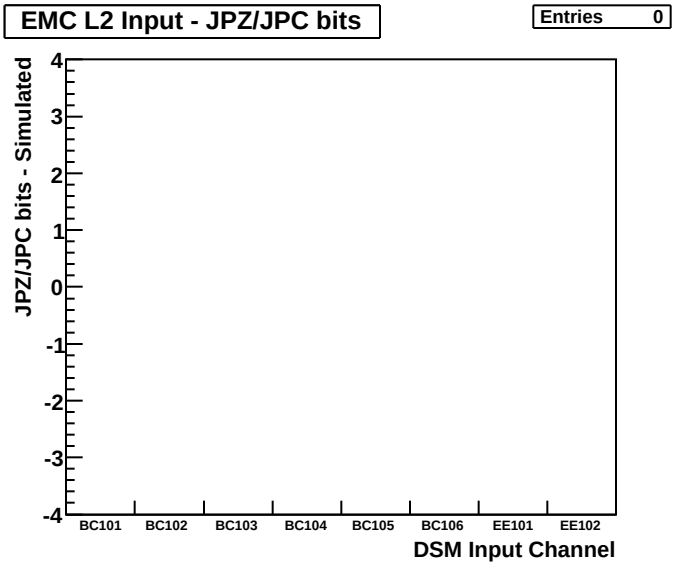

EMC L2 Input - JPX/JPA bits
Entries 3336
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
417

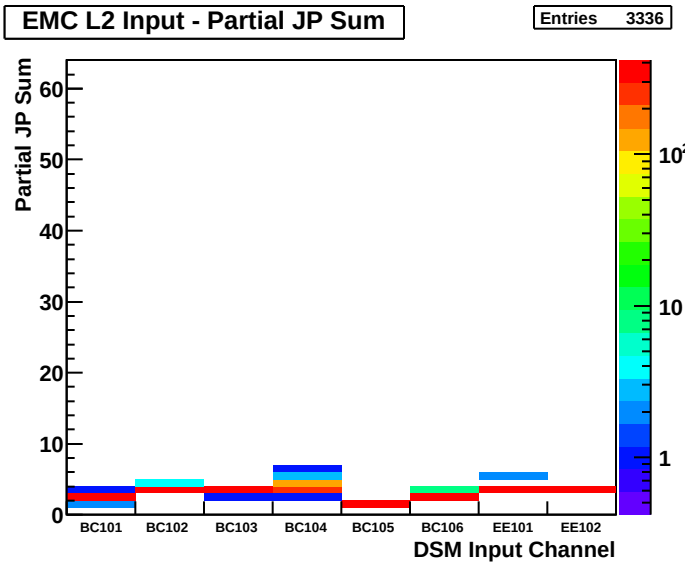

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

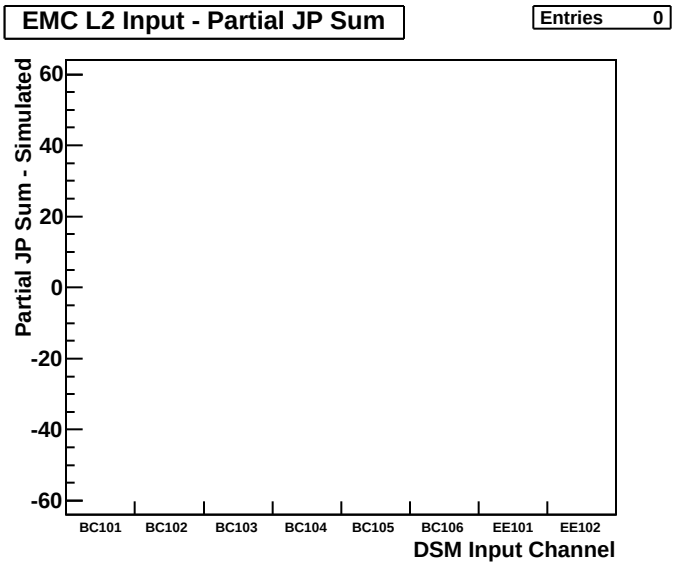

EMC L2 Input - JPY/JPB bits
Entries 3336
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
417

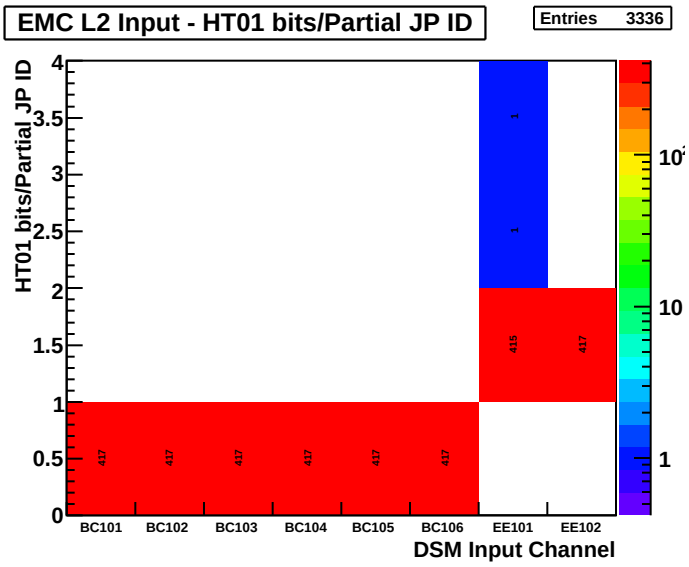

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

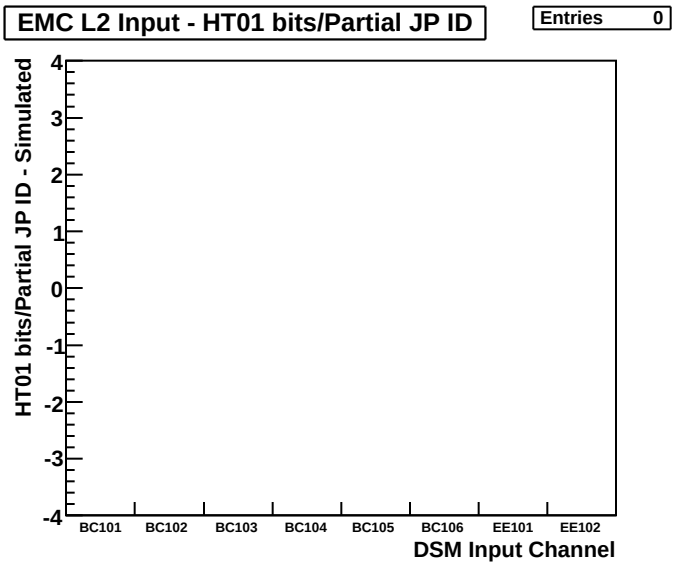

EMC L2 Input - JPZ/JPC bits
Entries 3336
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
417

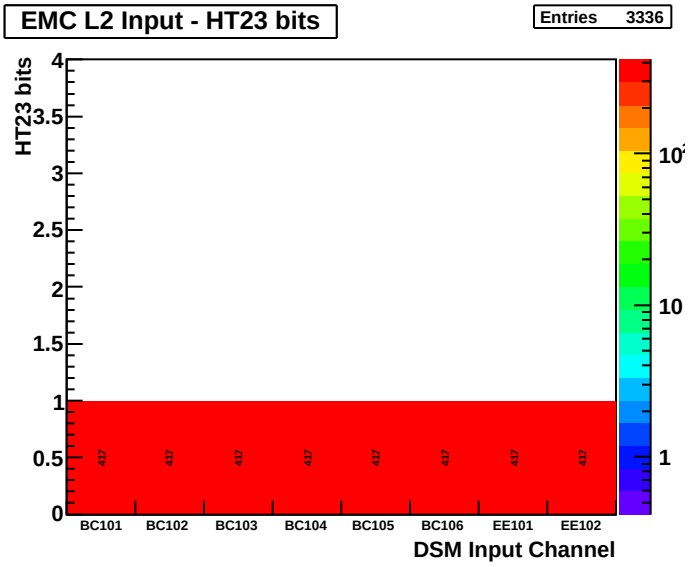

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

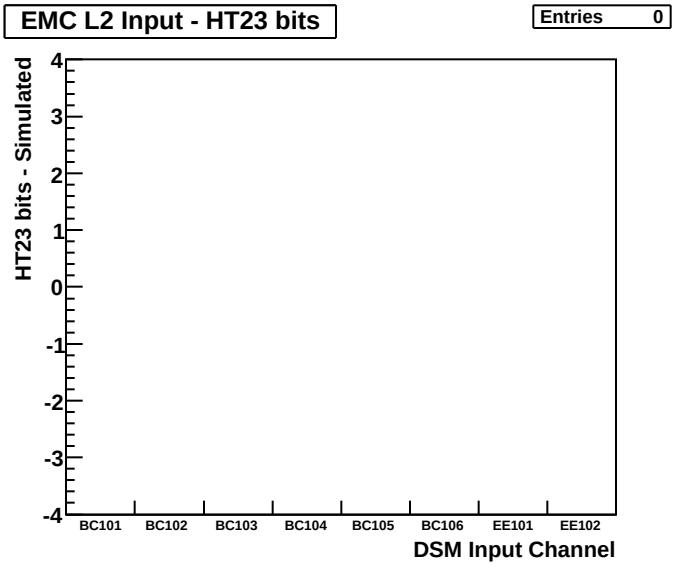

EMC L2 Input - Partial JP Sum
Entries 3336
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

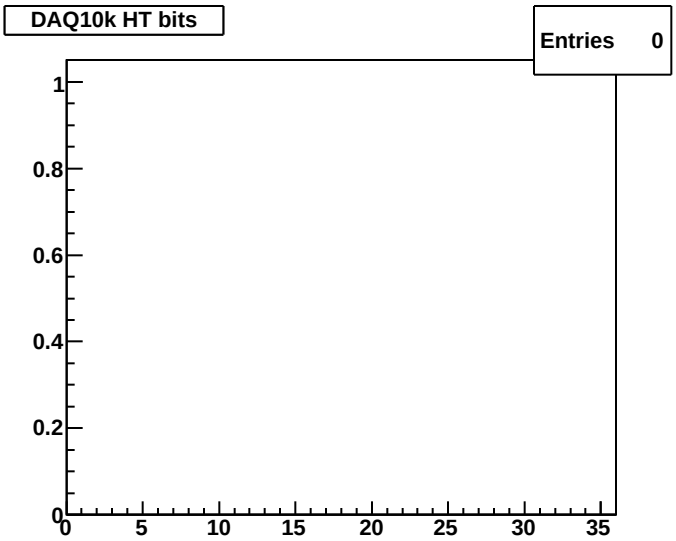

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

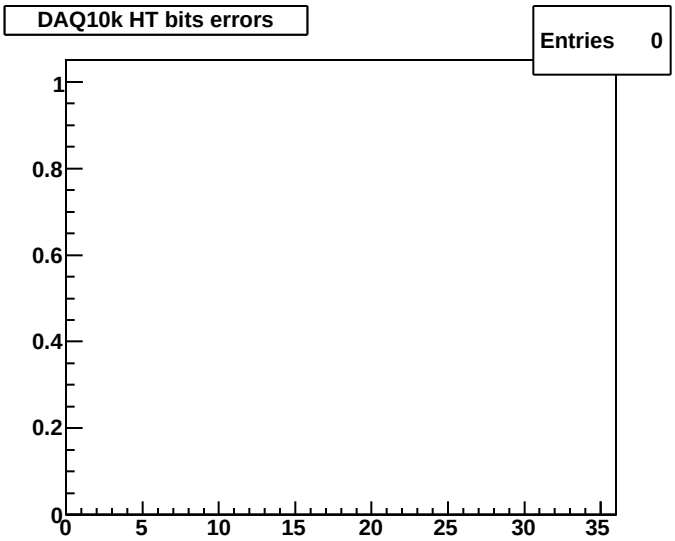

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3336
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

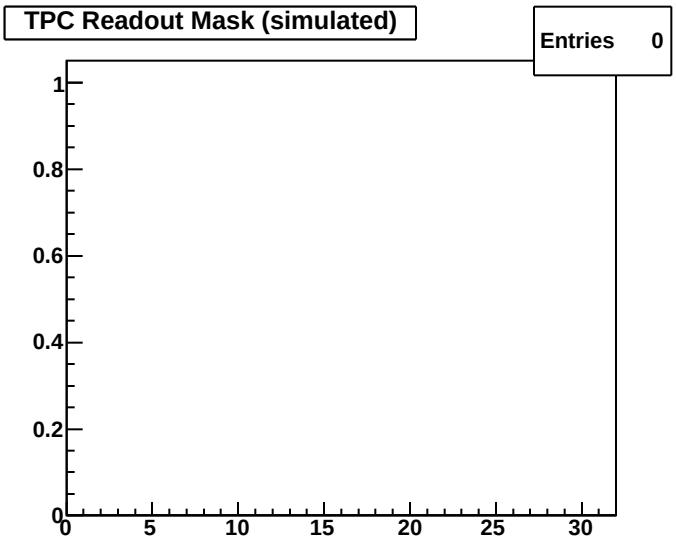

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

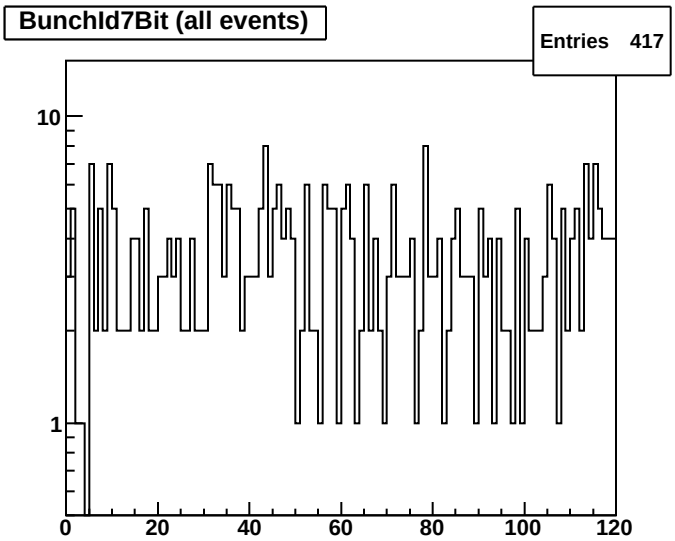

EMC L2 Input - HT23 bits
Entries 3336
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

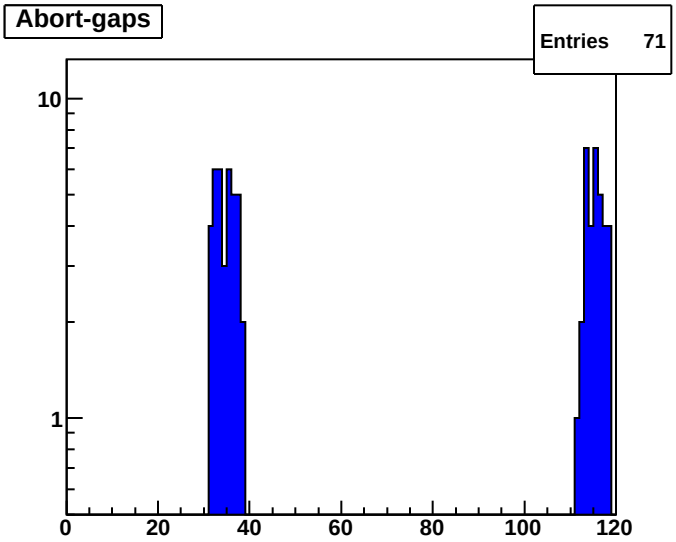

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

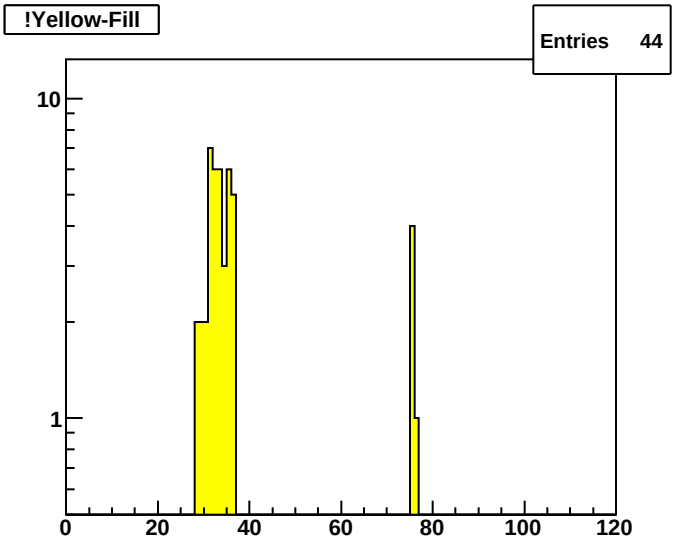

DAQ10k HT bits
Entries 0

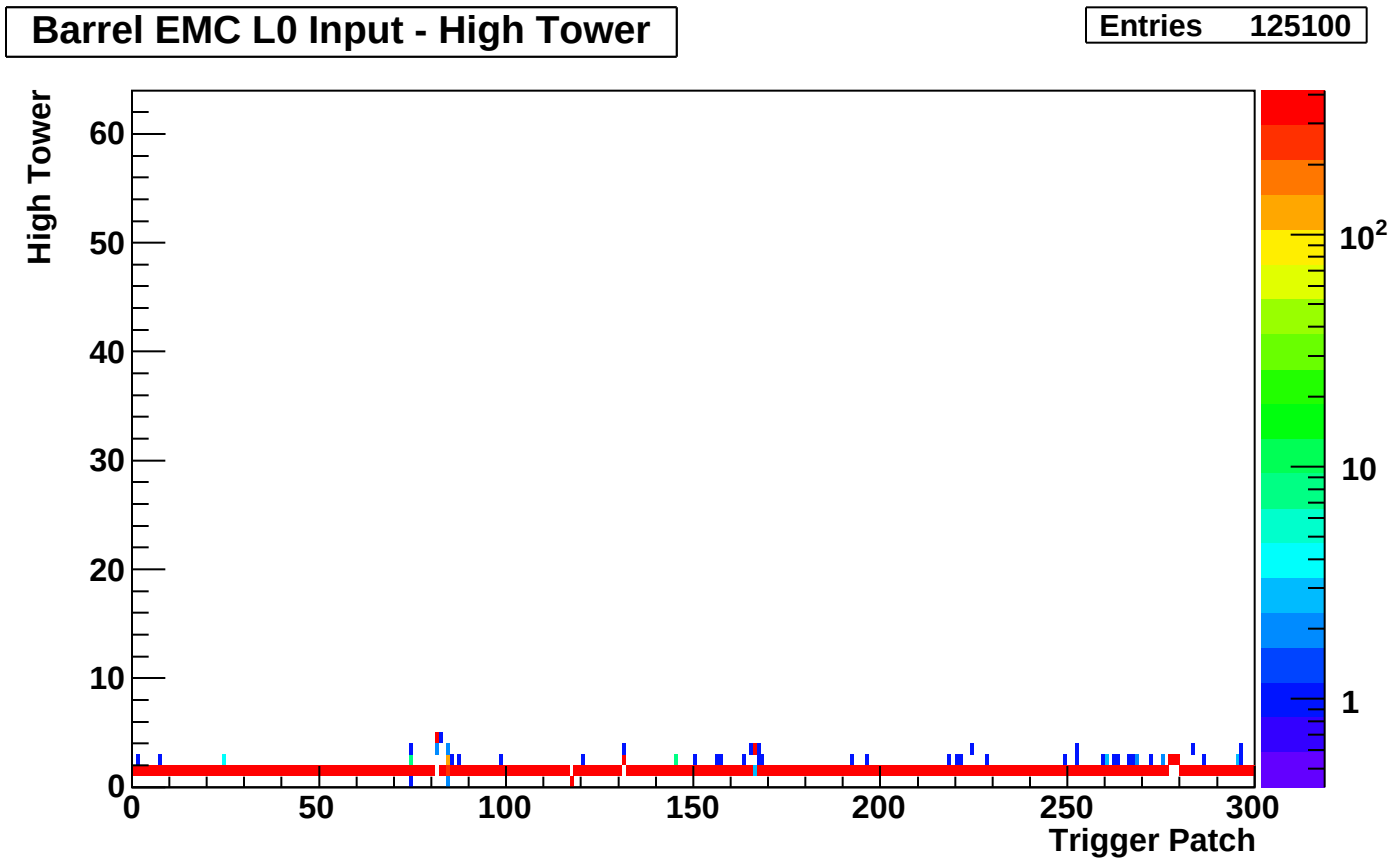

DAQ10k HT bits errors
Entries 0

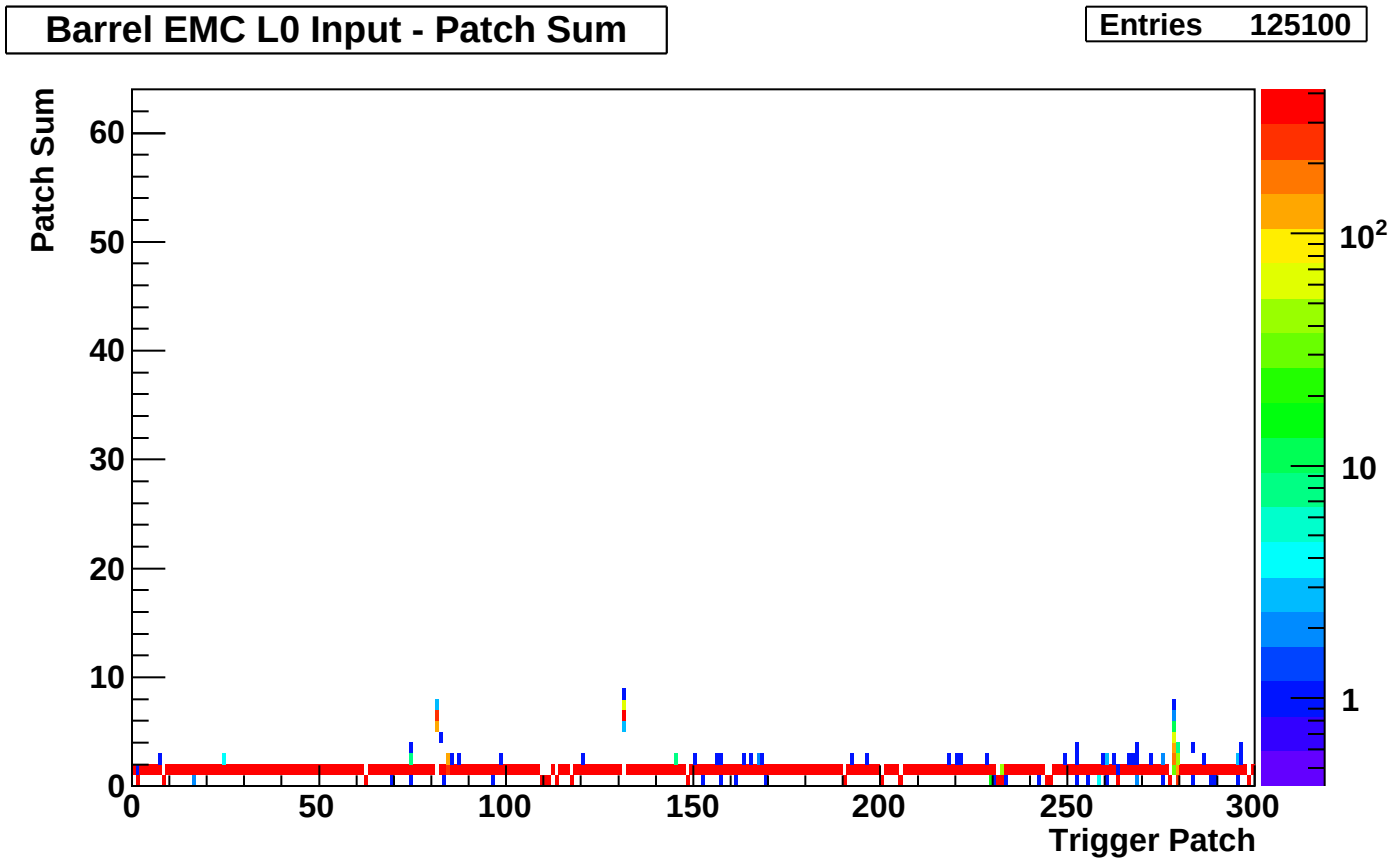

TPC Readout Mask (simulated)
Entries 0

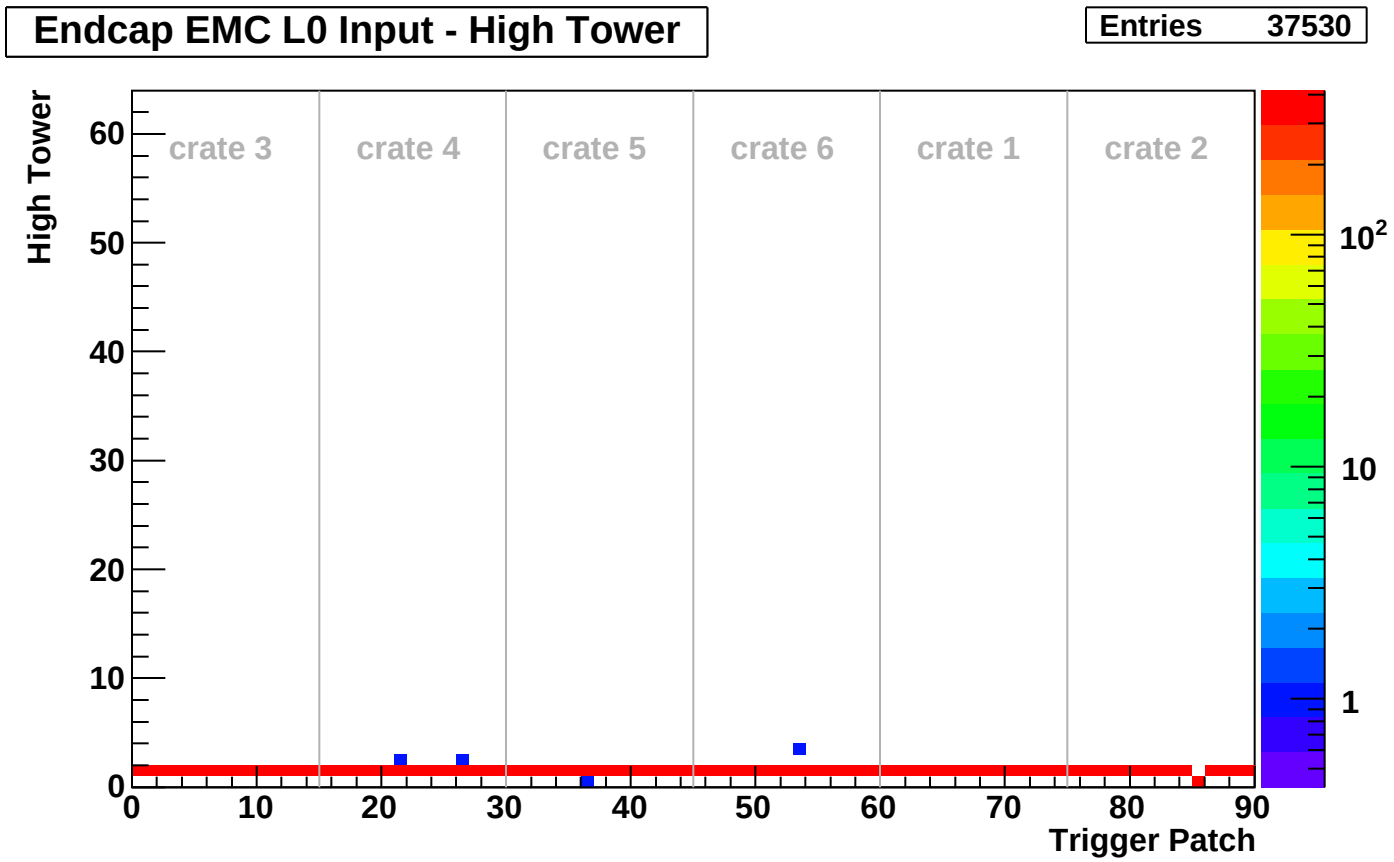

BunchId7Bit (all events)
Entries 417

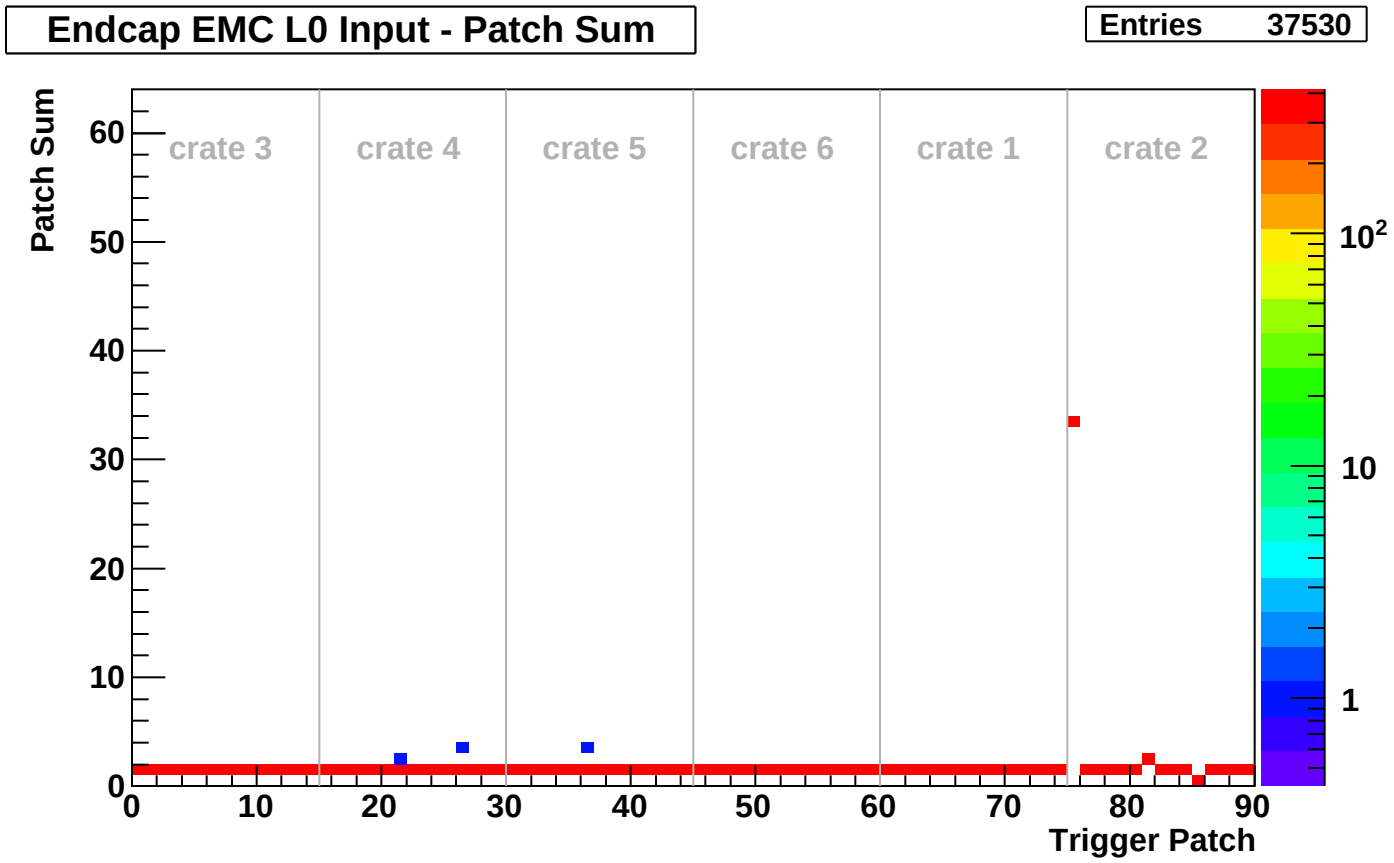

Abort-gaps
Entries 71

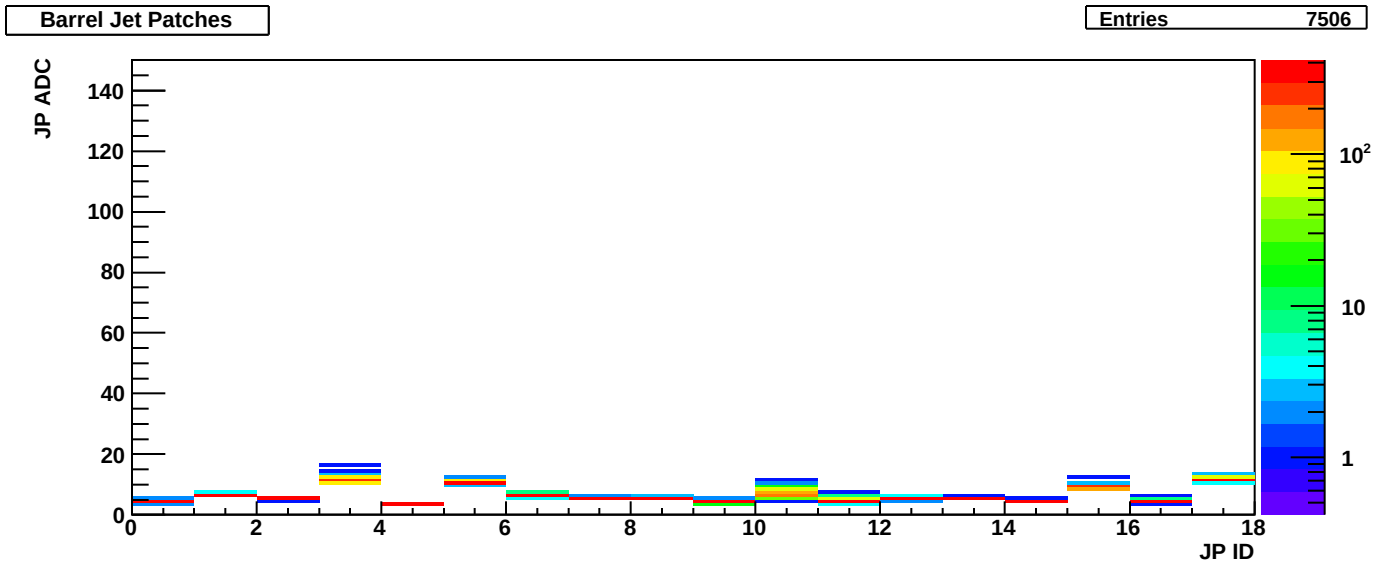

!Yellow-Fill
Entries 44

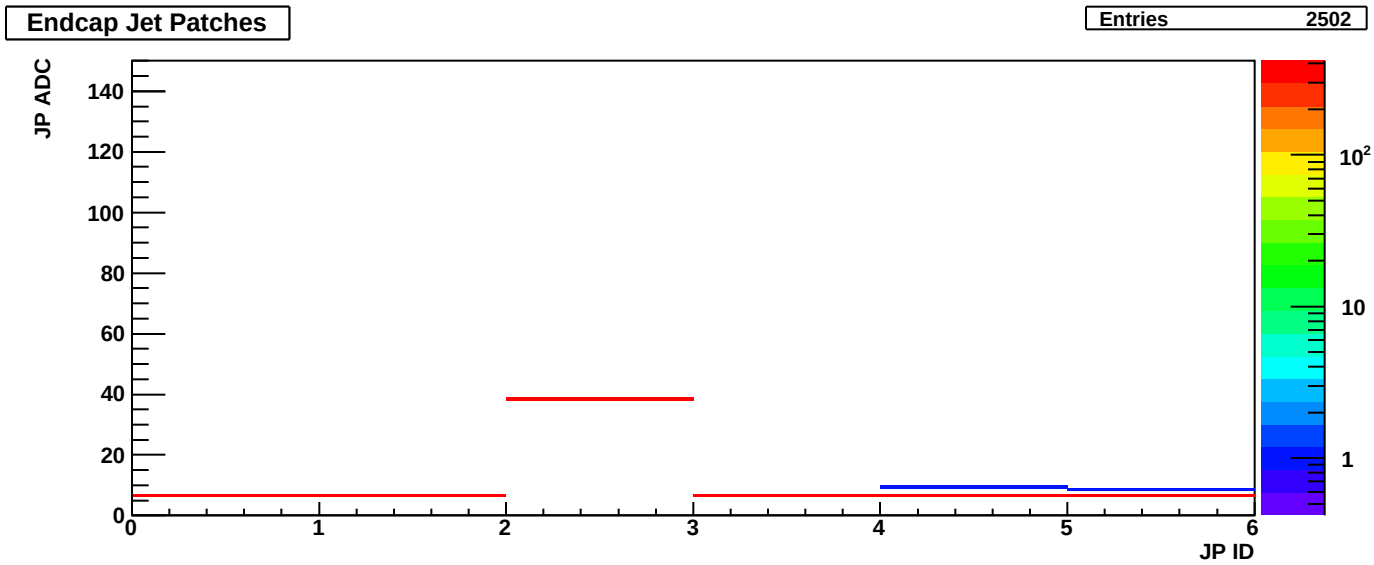

Barrel EMC L0 Input - High Tower
Entries 125100
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2

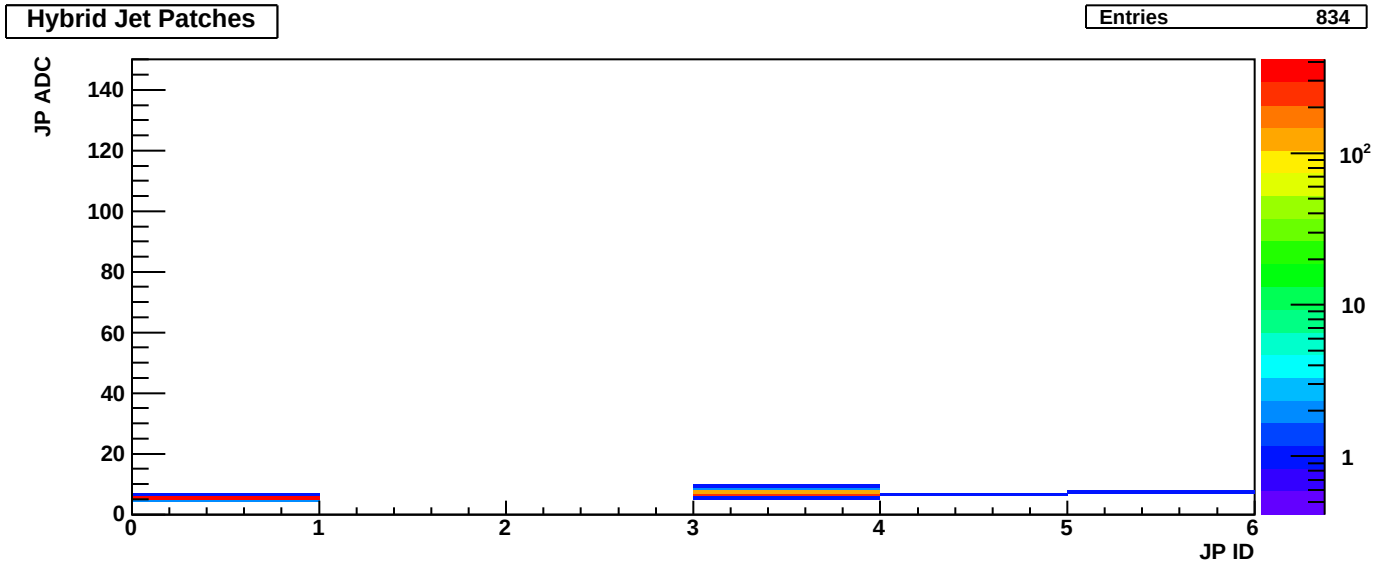

Barrel EMC L0 Input - Patch Sum
Entries 125100
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2

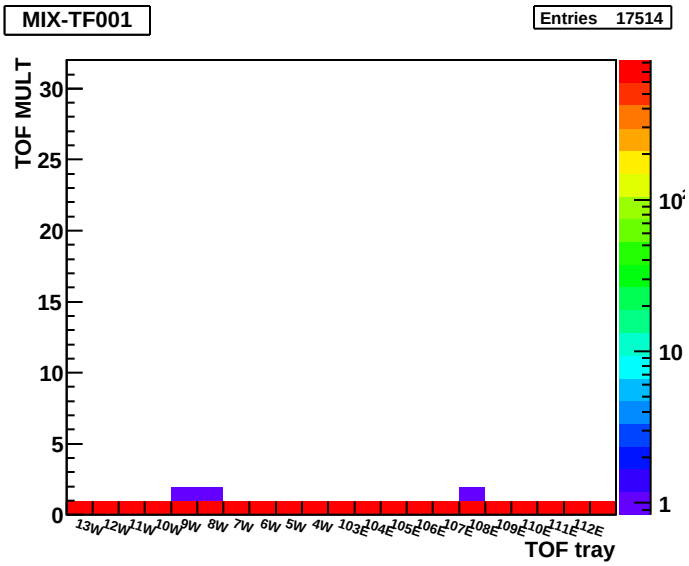

Endcap EMC L0 Input - High Tower
Entries 37530
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

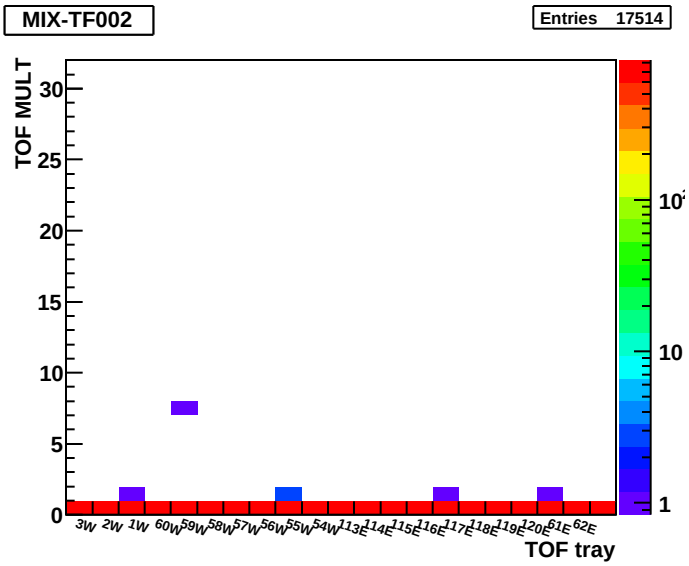

Endcap EMC L0 Input - Patch Sum
Entries 37530
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

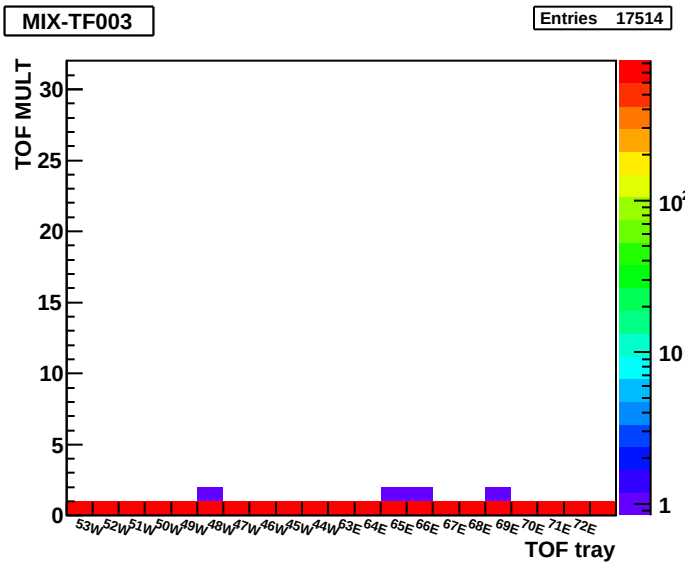

Barrel Jet Patches
Entries 7506
JP ADC
JP ID

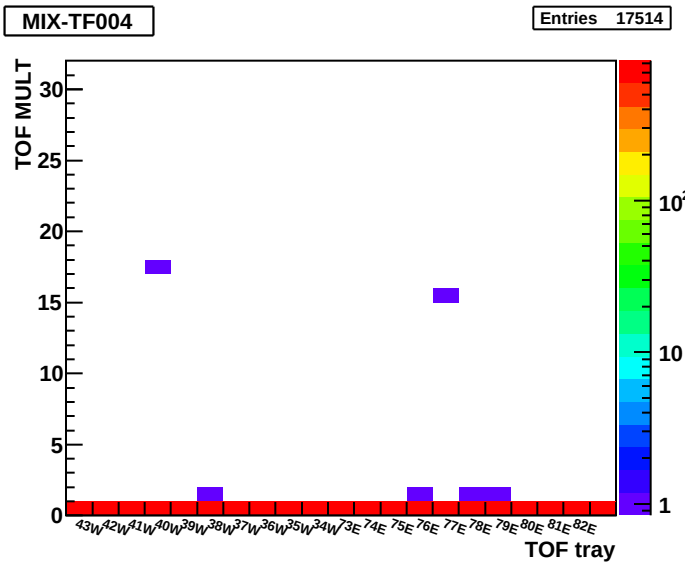

Endcap Jet Patches
Entries 2502
JP ADC
JP ID

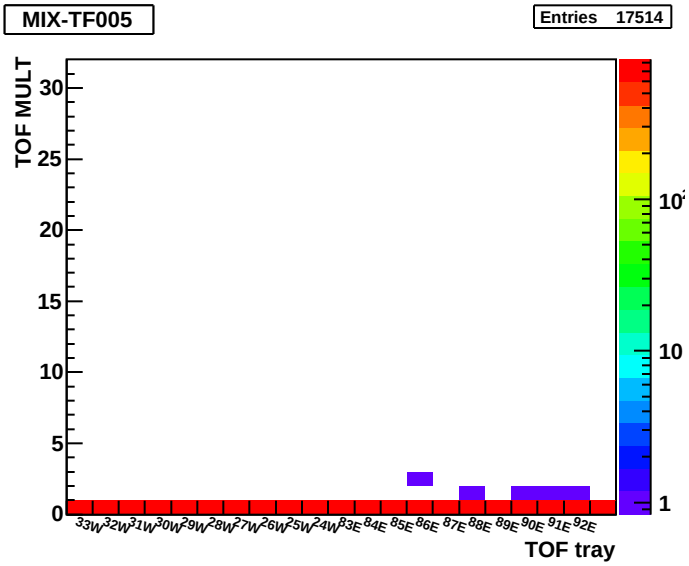

Hybrid Jet Patches
Entries 834
JP ADC
JP ID

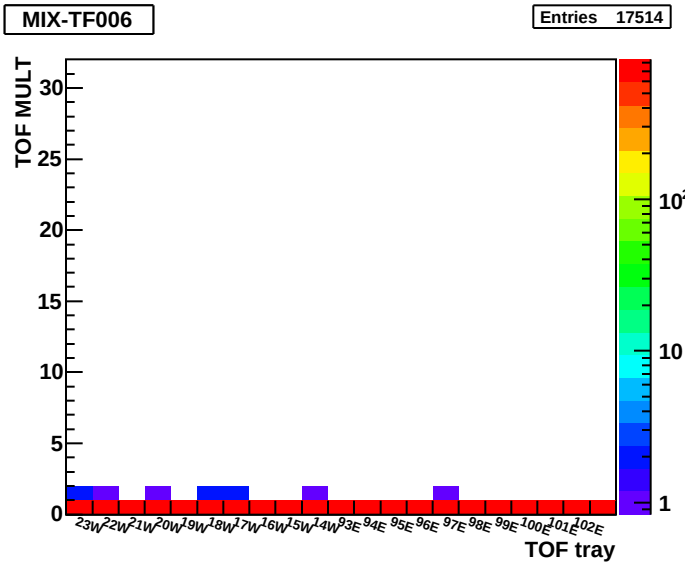

MIX-TF001
Entries 17514
TOF MULT
TOF tray

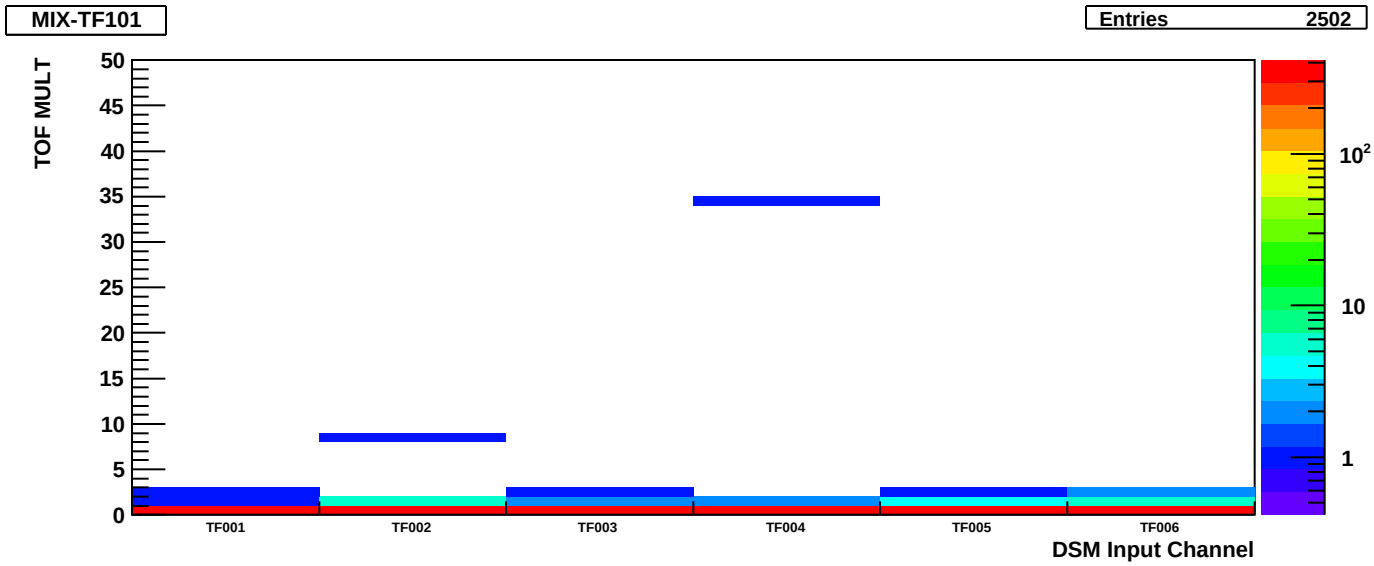

MIX-TF002
Entries 17514
TOF MULT
TOF tray

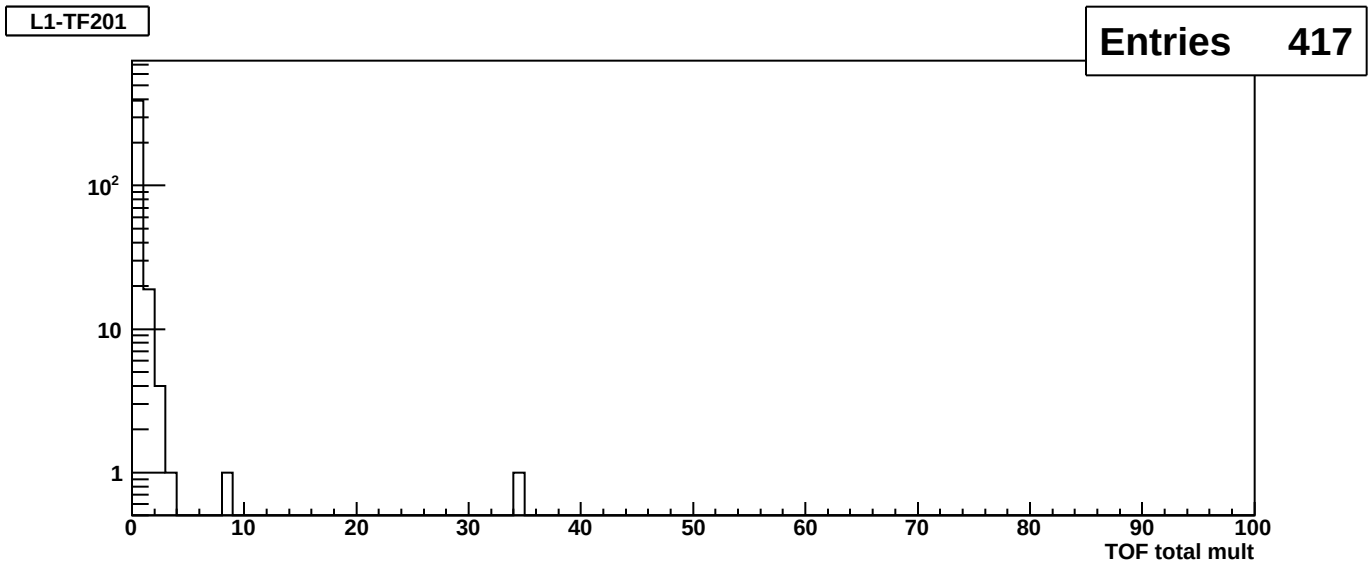

MIX-TF003
Entries 17514
TOF MULT
TOF tray

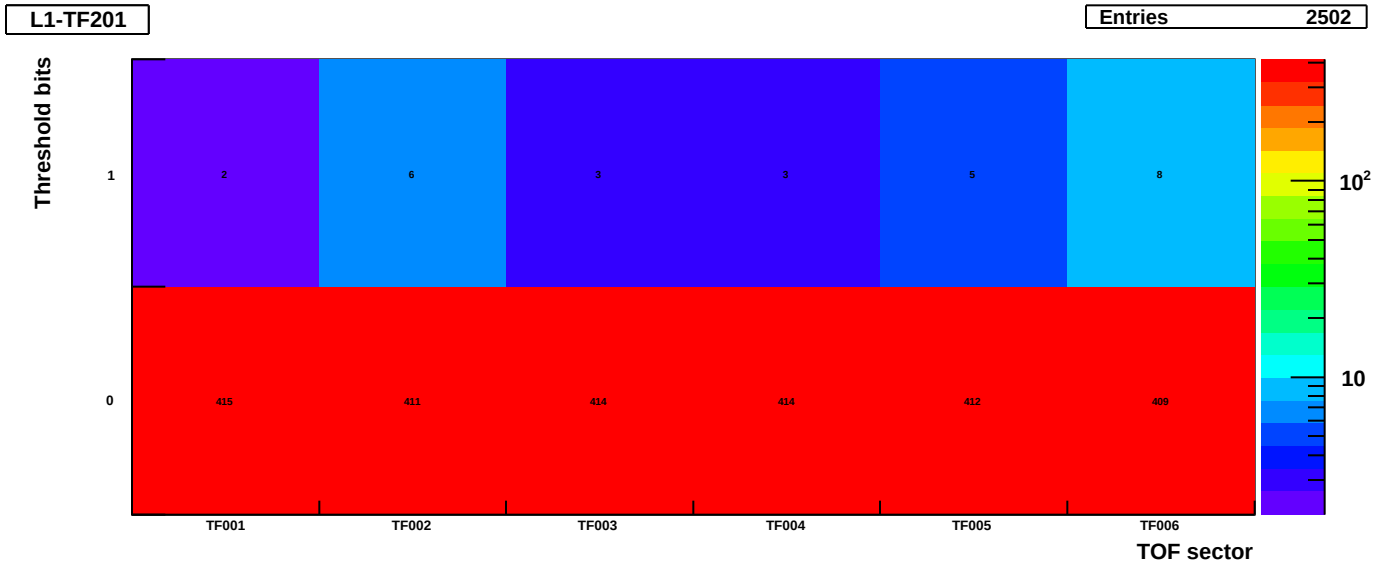

MIX-TF004
Entries 17514
TOF MULT
TOF tray

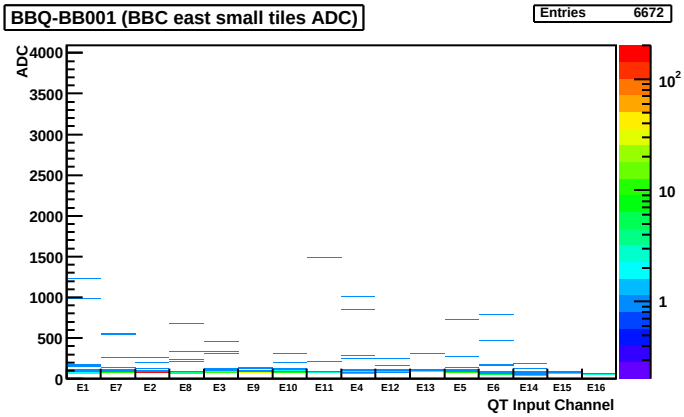

MIX-TF005
Entries 17514
TOF MULT
TOF tray

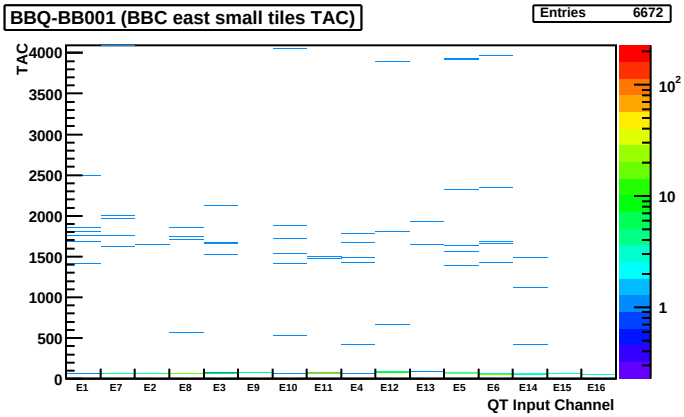

MIX-TF006
Entries 17514
TOF MULT
TOF tray

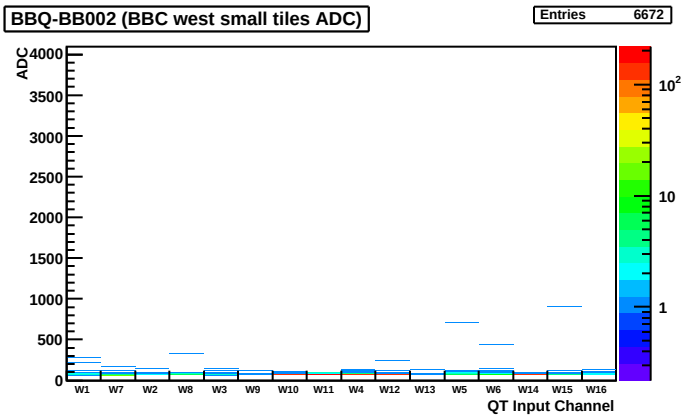

MIX-TF101
Entries 2502
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

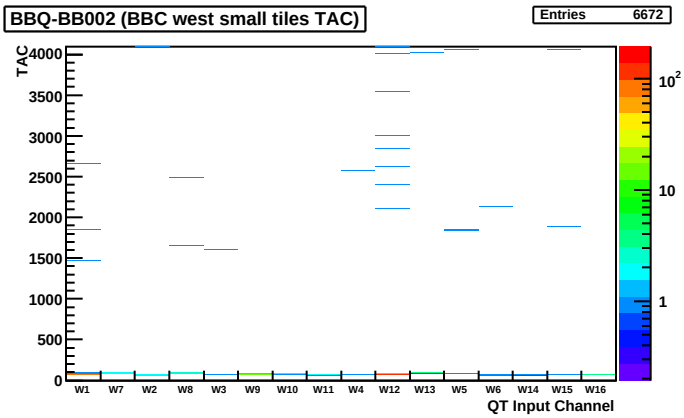

L1-TF201
Entries 417
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

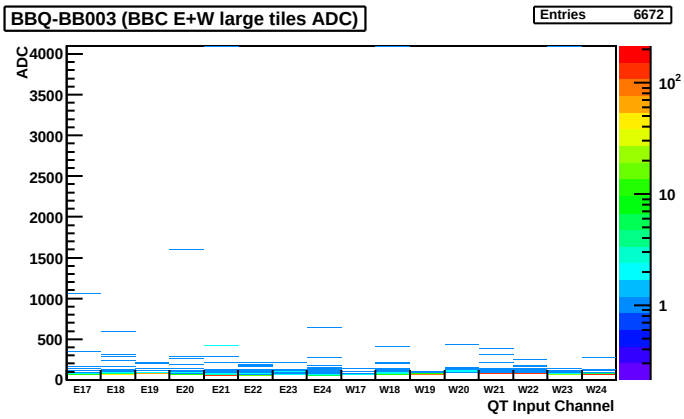

L1-TF201
Entries 2502
Threshold bits
1
0
2
6
3
3
5
8
415
411
414
414
412
409
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^2
10

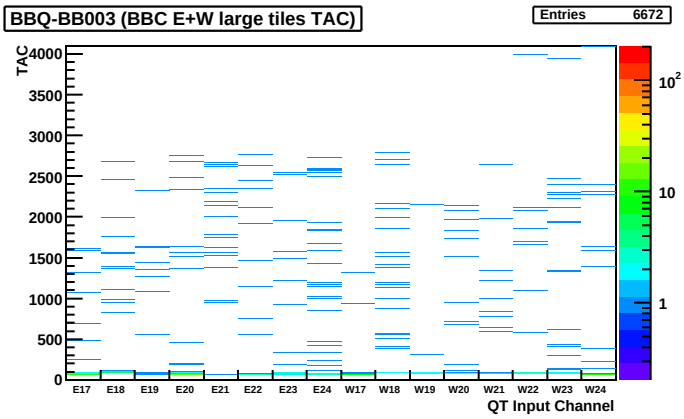

BBQ-BB001 (BBC east small tiles ADC)
Entries 6672
ADC
QT Input Channel

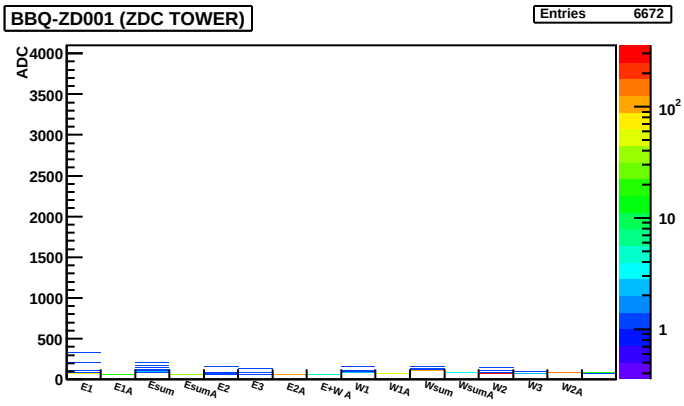

BBQ-BB001 (BBC east small tiles TAC)
Entries 6672
TAC
QT Input Channel

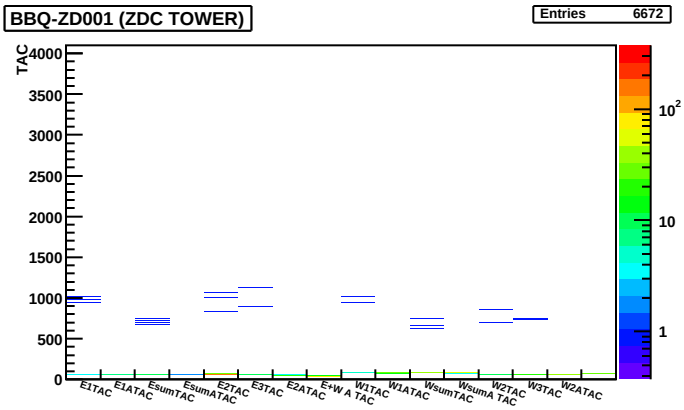

BBQ-BB002 (BBC west small tiles ADC)
Entries 6672
ADC
QT Input Channel

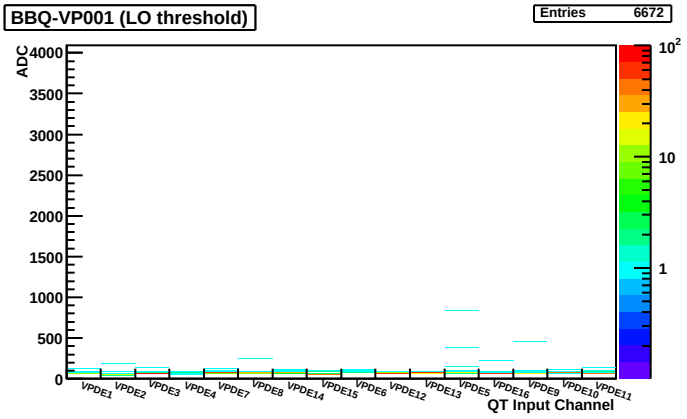

BBQ-BB002 (BBC west small tiles TAC)
Entries 6672
TAC
QT Input Channel

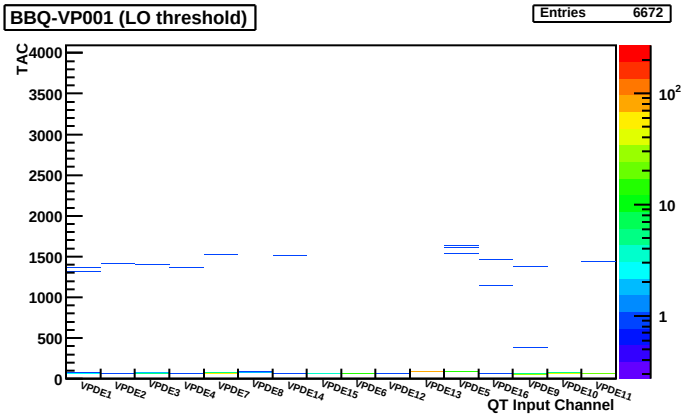

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6672
ADC
QT Input Channel

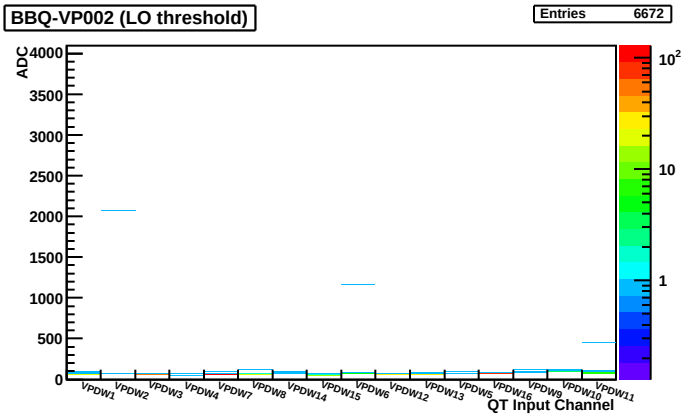

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6672
TAC
QT Input Channel

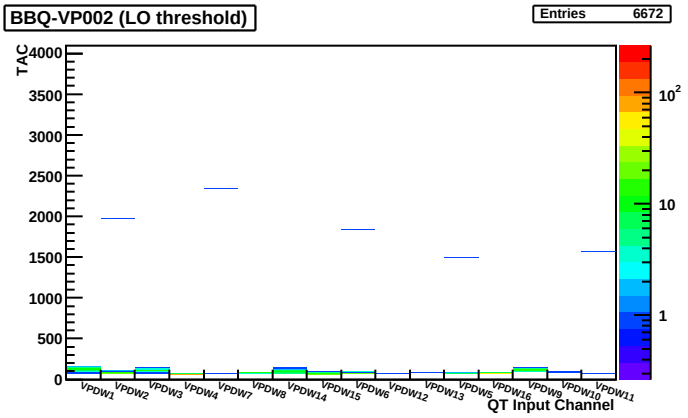

BBQ-ZD001 (ZDC TOWER)
Entries 6672
ADC

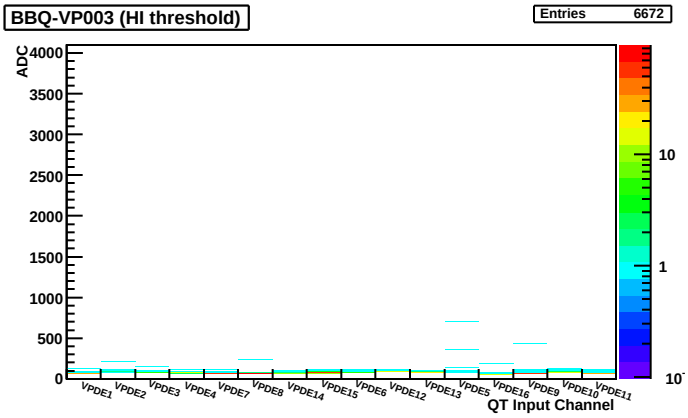

BBQ-ZD001 (ZDC TOWER)
Entries 6672
TAC

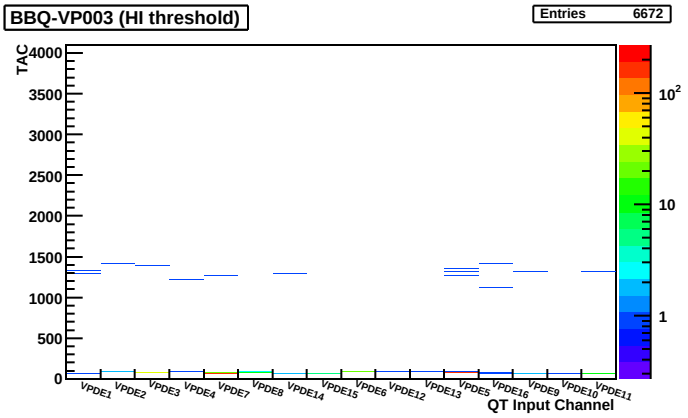

BBQ-VP001 (LO threshold)
Entries 6672
ADC
QT Input Channel

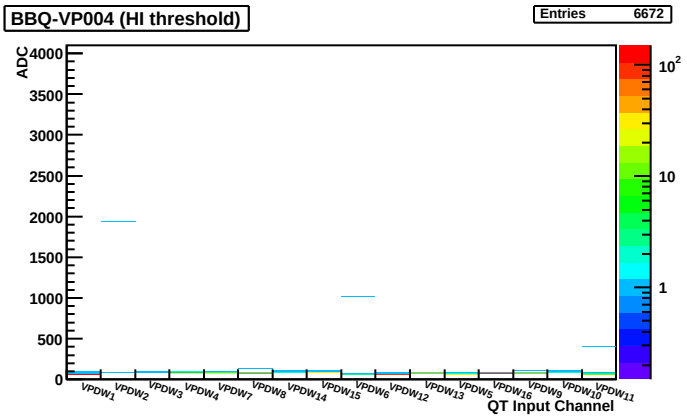

BBQ-VP001 (LO threshold)
Entries 6672
TAC
QT Input Channel

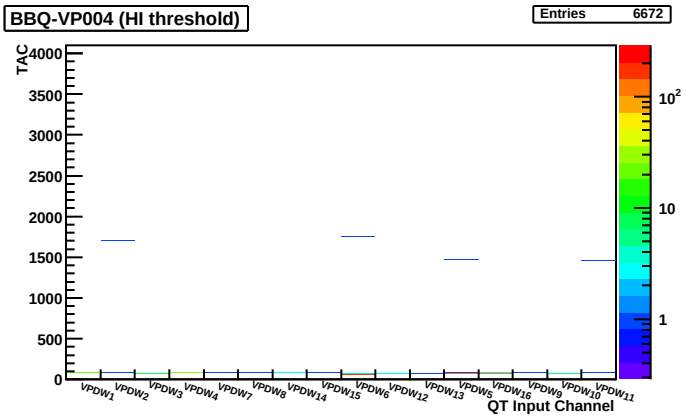

BBQ-VP002 (LO threshold)
Entries 6672
ADC
QT Input Channel

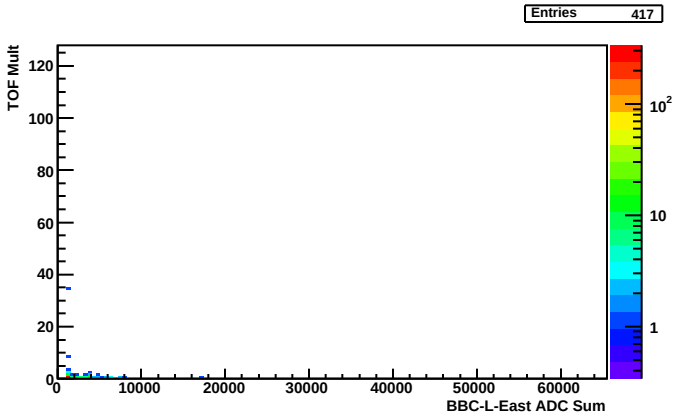

BBQ-VP002 (LO threshold)
Entries 6672
TAC
QT Input Channel

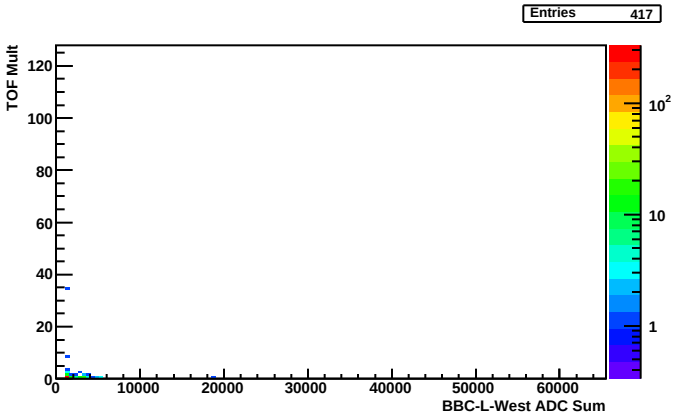

BBQ-VP003 (HI threshold)
Entries 6672
ADC
QT Input Channel

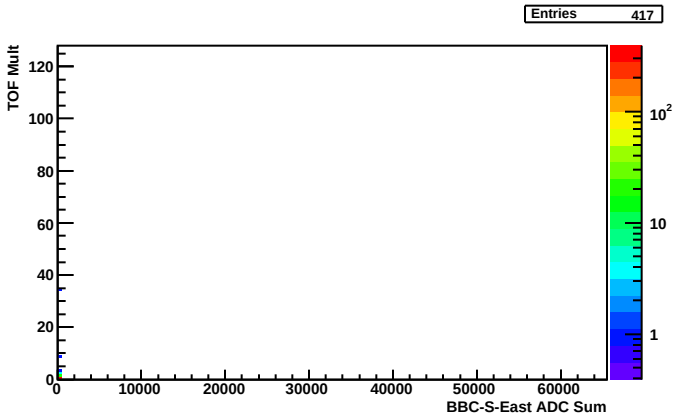

BBQ-VP003 (HI threshold)
Entries 6672
TAC
QT Input Channel

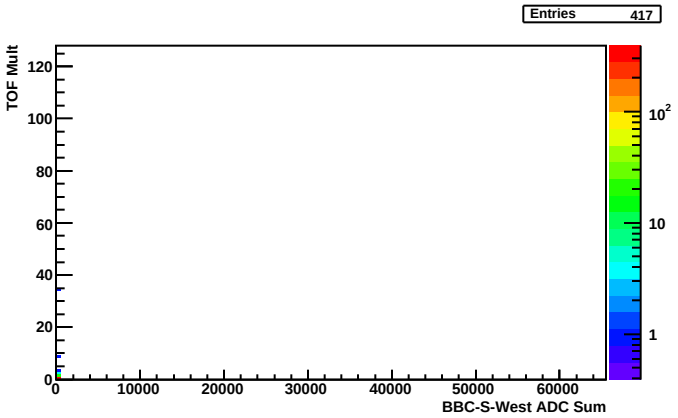

BBQ-VP004 (HI threshold)
Entries 6672
ADC
QT Input Channel

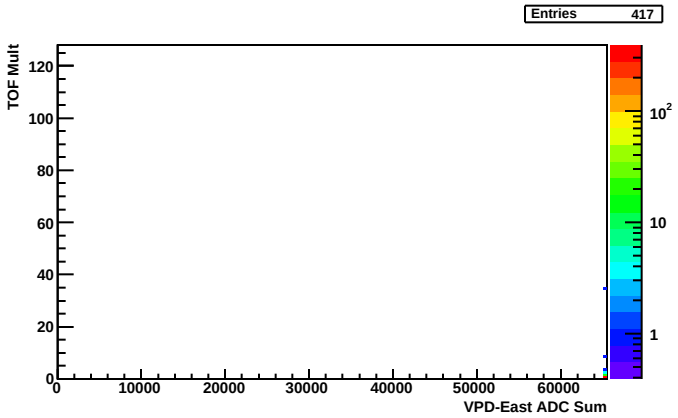

BBQ-VP004 (HI threshold)
Entries 6672
TAC
QT Input Channel

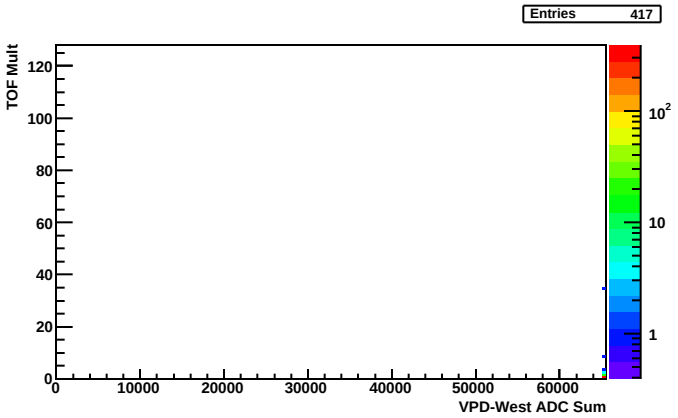

Entries 417
TOF Mult
BBC-L-East ADC Sum

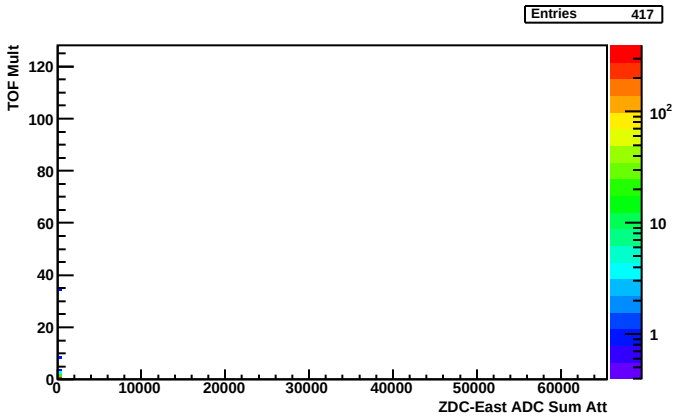

Entries 417
TOF Mult
BBC-L-West ADC Sum

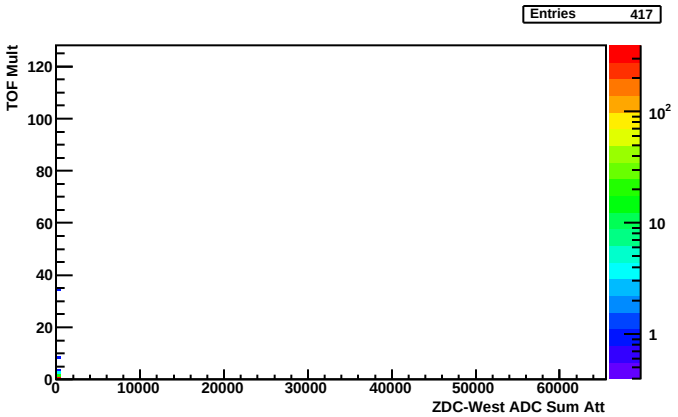

Entries 417
TOF Mult
BBC-S-East ADC Sum

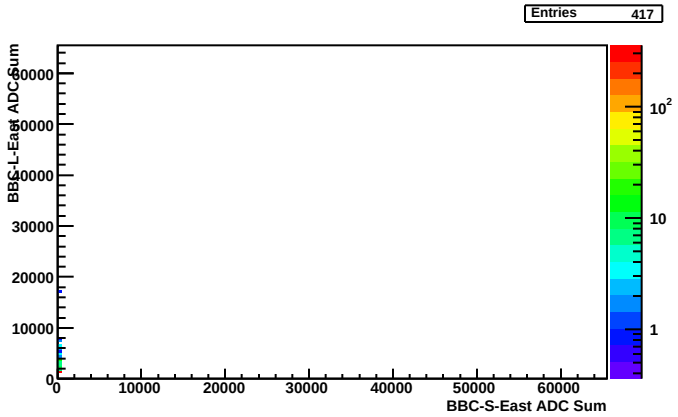

Entries 417
TOF Mult
BBC-S-West ADC Sum

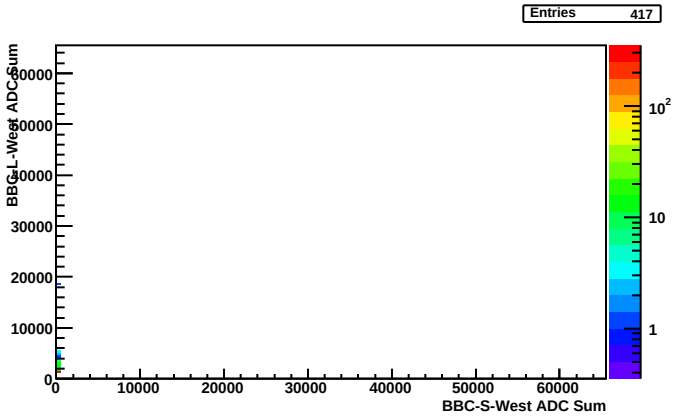

Entries 417
TOF Mult
VPD-East ADC Sum

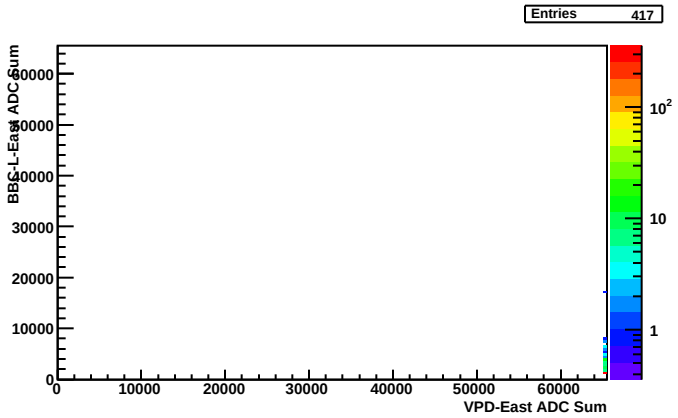

Entries 417
TOF Mult
VPD-West ADC Sum

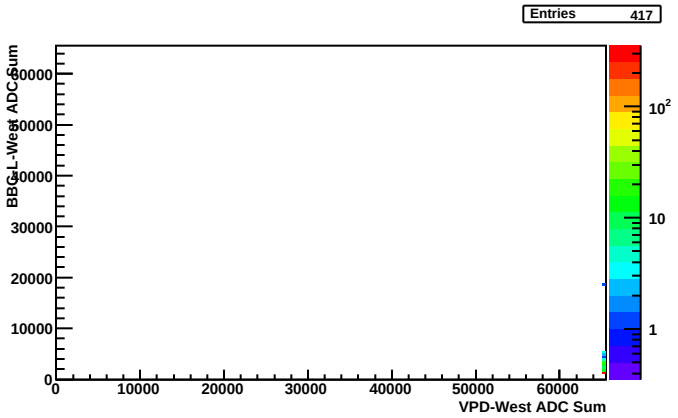

Entries 417
TOF Mult
ZDC-East ADC Sum Att

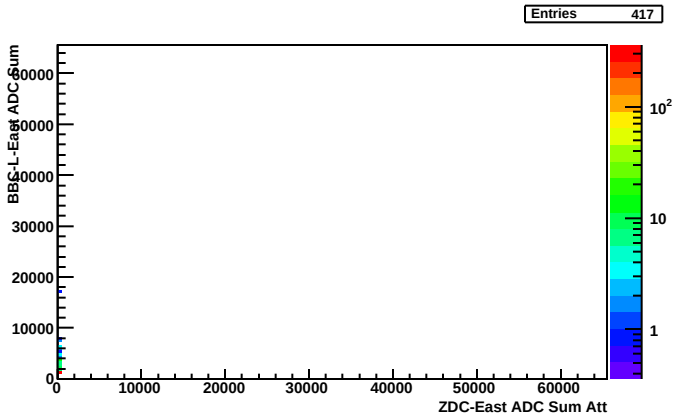

Entries 417
TOF Mult
ZDC-West ADC Sum Att

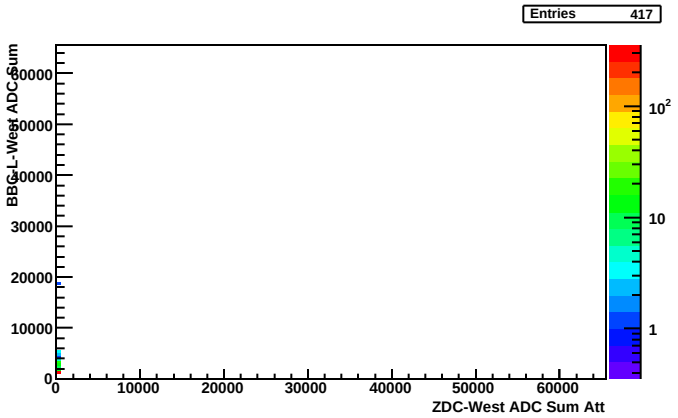

Entries 417
BBC-L-East ADC Sum
BBC-S-East ADC Sum

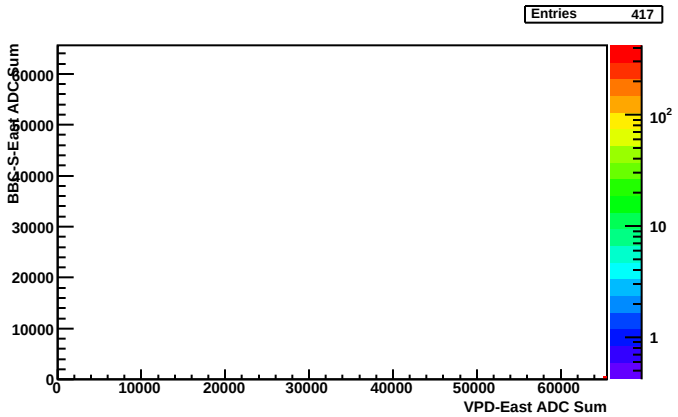

Entries 417
BBC-L-West ADC Sum
BBC-S-West ADC Sum

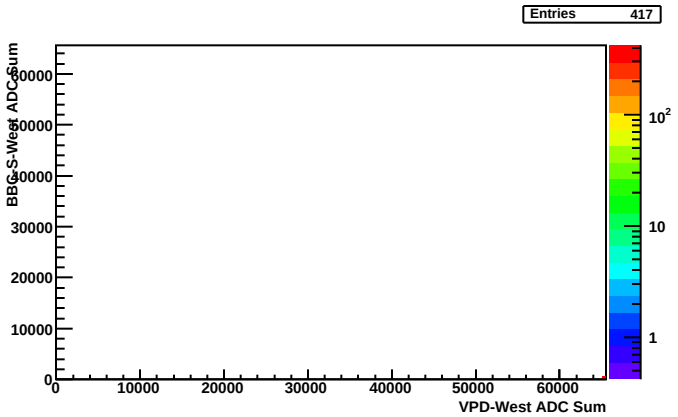

Entries 417
BBC-L-East ADC Sum
VPD-East ADC Sum

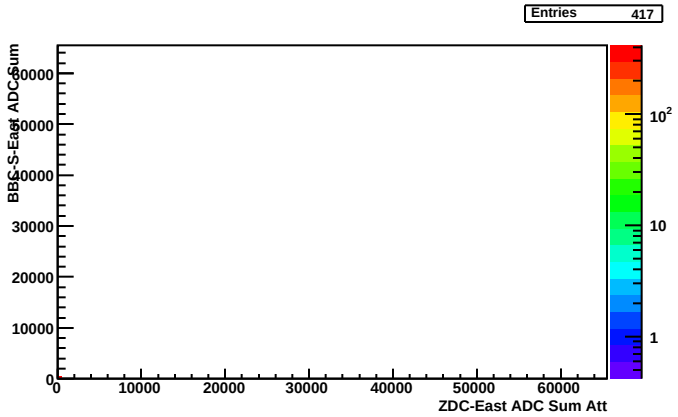

Entries 417
BBC-L-West ADC Sum
VPD-West ADC Sum

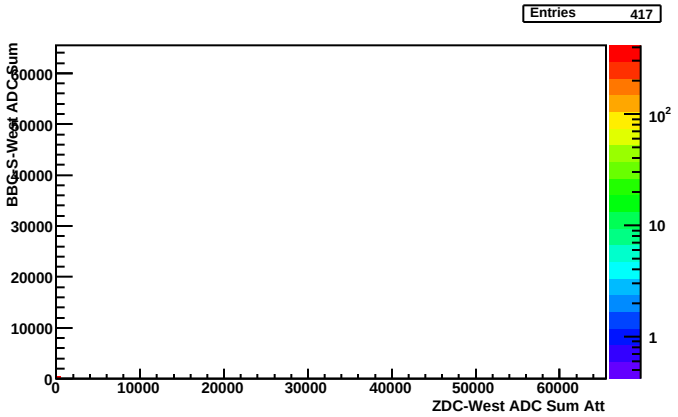

Entries 417
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

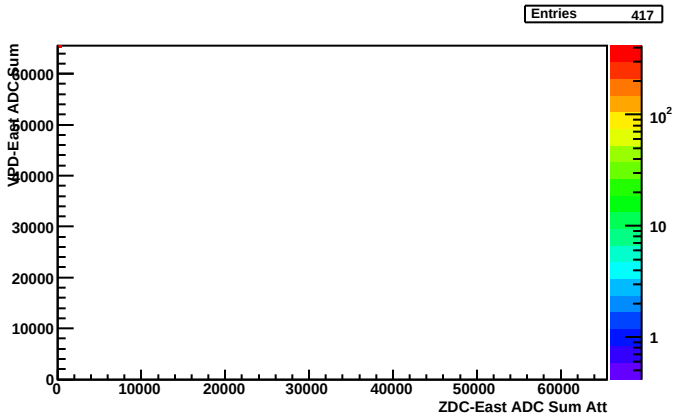

Entries 417
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

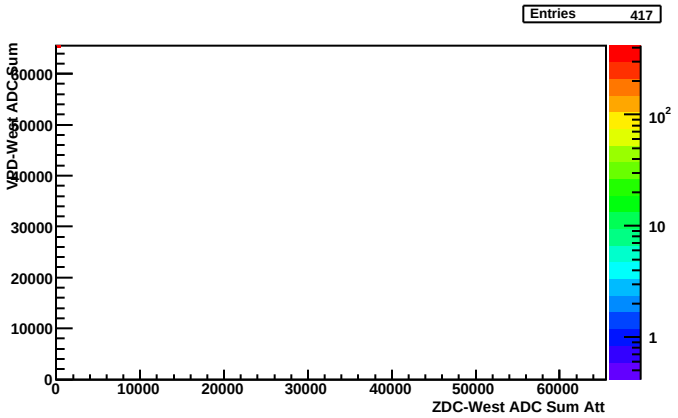

Entries 417
BBC-S-East ADC Sum
VPD-East ADC Sum

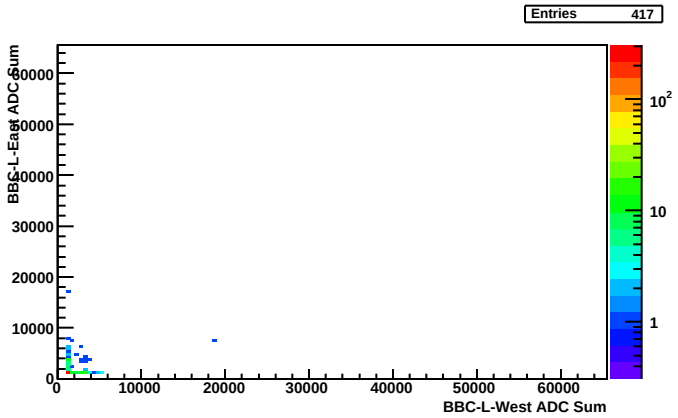

Entries 417
BBC-S-West ADC Sum
VPD-West ADC Sum

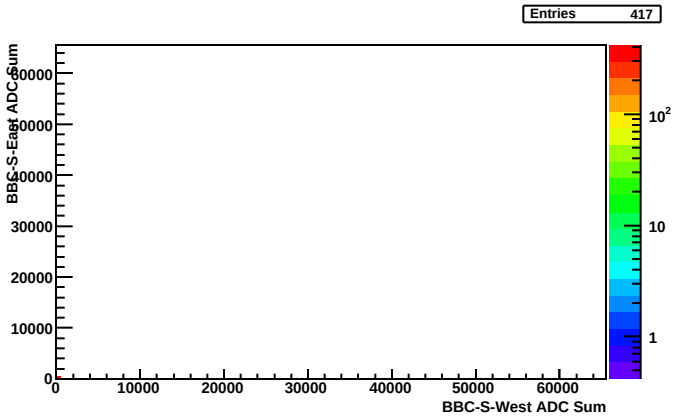

Entries 417
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

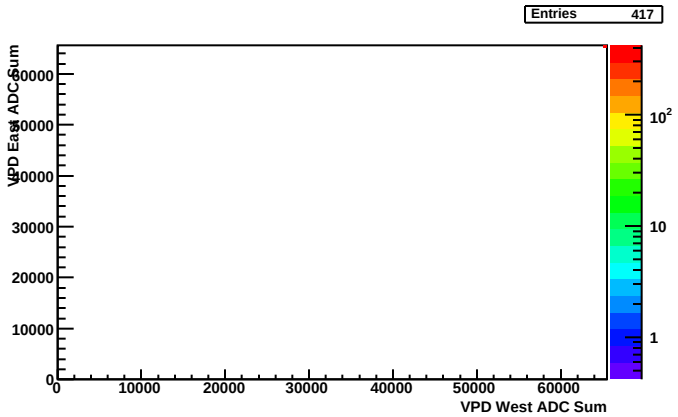

Entries 417
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

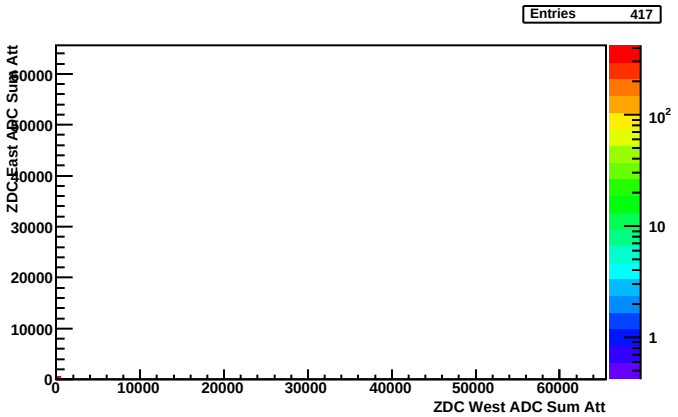

Entries 417
VPD-East ADC Sum
ZDC-East ADC Sum Att

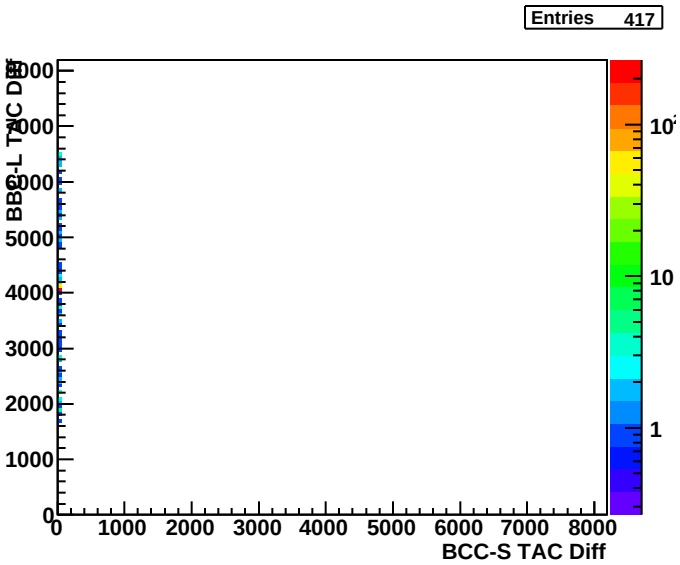

Entries 417
VPD-West ADC Sum
ZDC-West ADC Sum Att

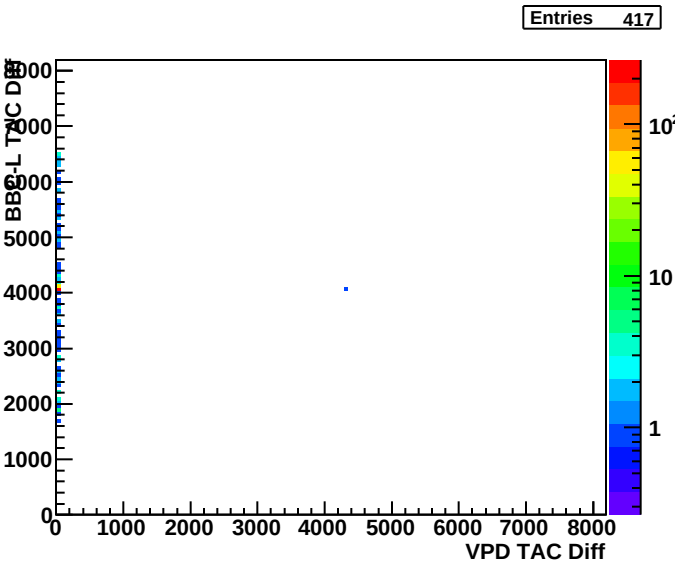

Entries 417
BBC-L-East ADC Sum
BBC-L-West ADC Sum

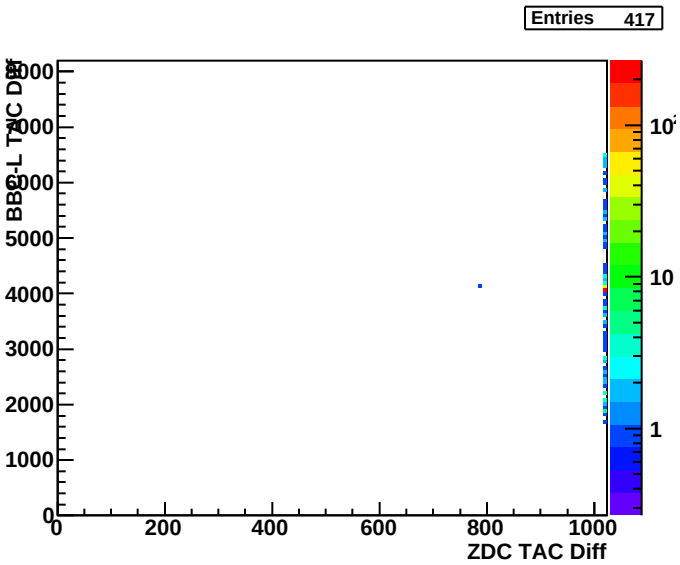

Entries 417
BBC-S-East ADC Sum
BBC-S-West ADC Sum

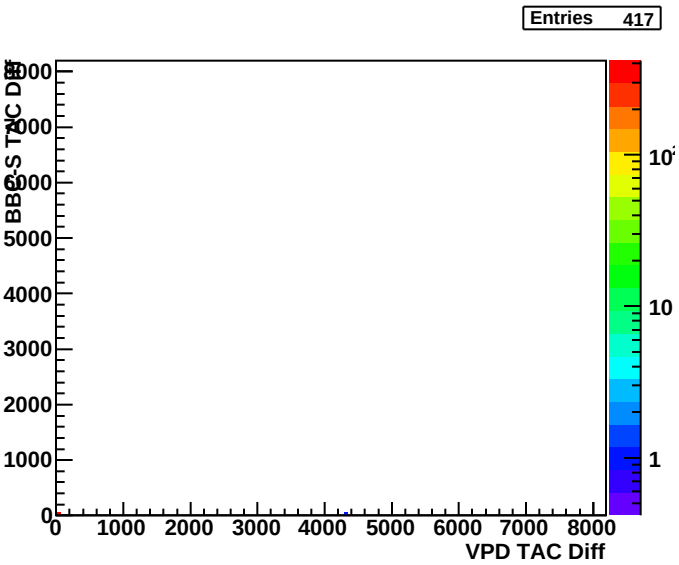

Entries 417
VPD East ADC Sum
VPD West ADC Sum

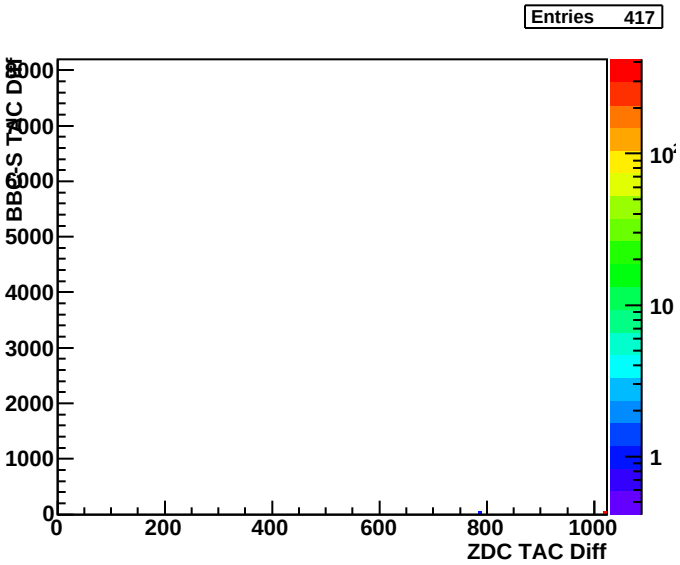

Entries 417
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

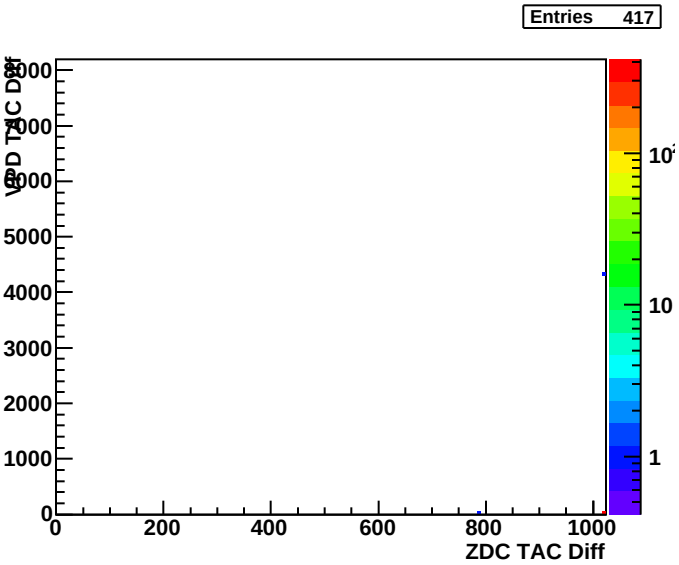

Entries 417
BBC-L TAC Diff
BCC-S TAC Diff

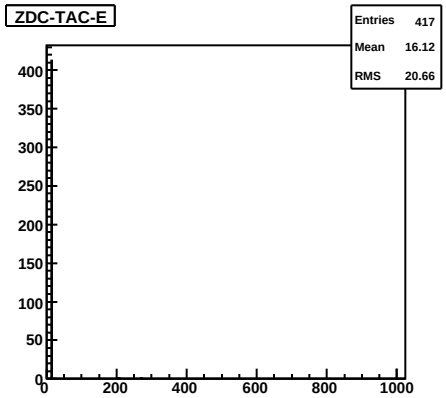

Entries 417
BBC-L TAC Diff
VPD TAC Diff

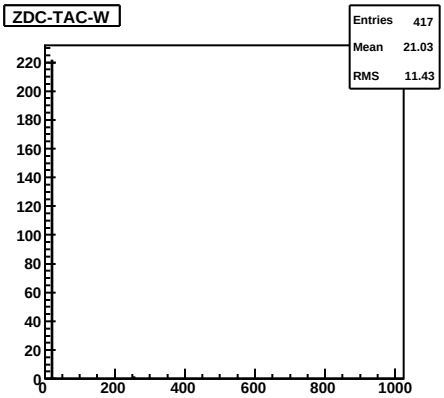

Entries 417
BBC-L TAC Diff
ZDC TAC Diff

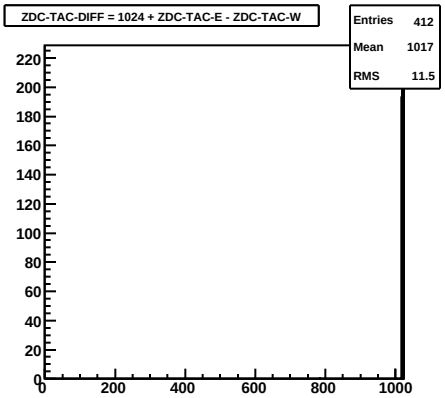

Entries 417
BBC-S TAC Diff
VPD TAC Diff

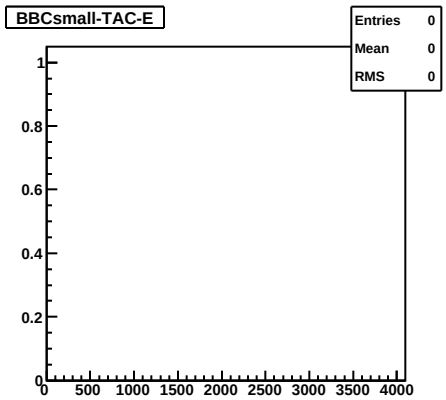

Entries 417
BBC-S TAC Diff
ZDC TAC Diff

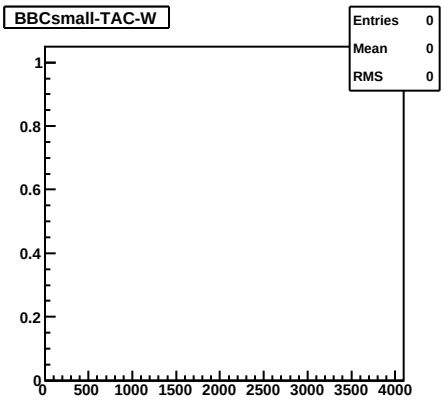

Entries 417
VPD TAC Diff
ZDC TAC Diff

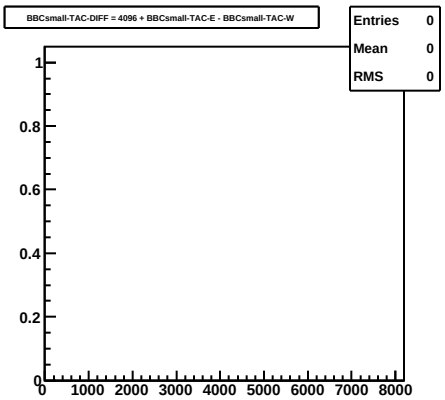

ZDC-TAC-E
Entries 417
Mean 16.12
RMS 20.66

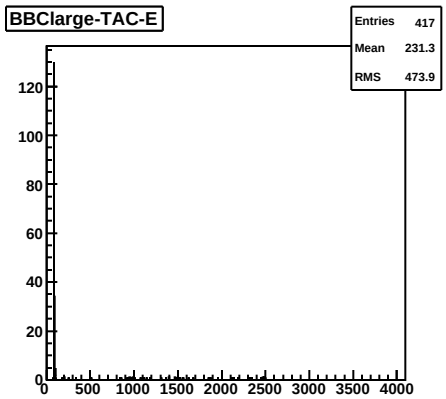

ZDC-TAC-W
Entries 417
Mean 21.03
RMS 11.43

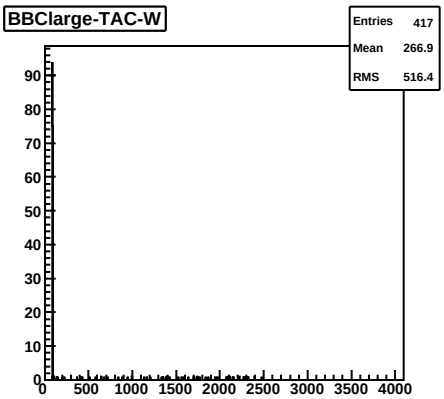

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 412
Mean 1017
RMS 11.5

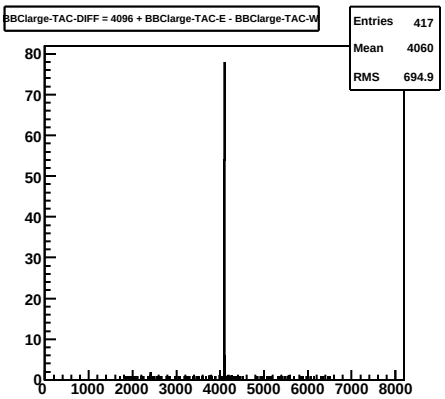

BBCsmall-TAC-E
Entries 0
Mean 0
RMS 0

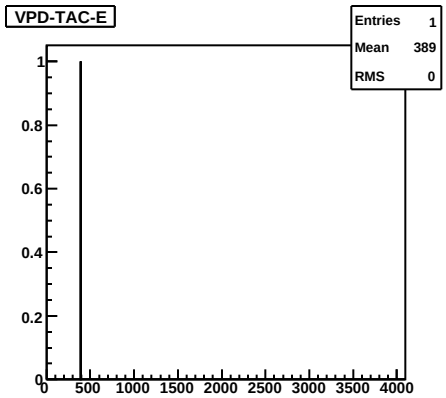

BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

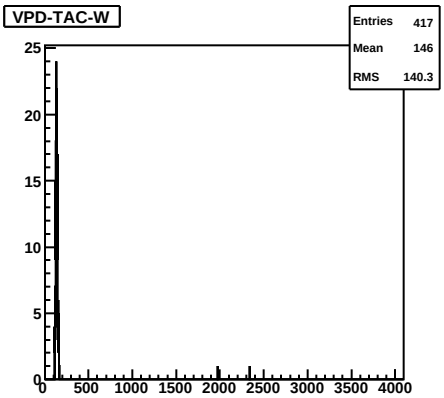

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

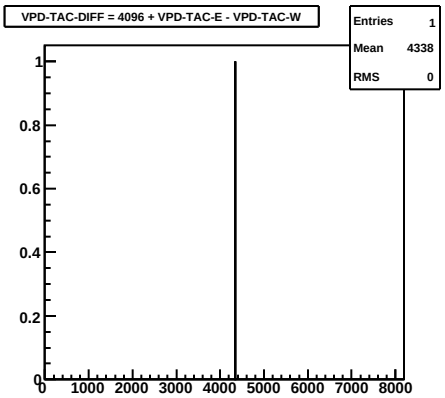

BBClarge-TAC-E
Entries 417
Mean 231.3
RMS 473.9

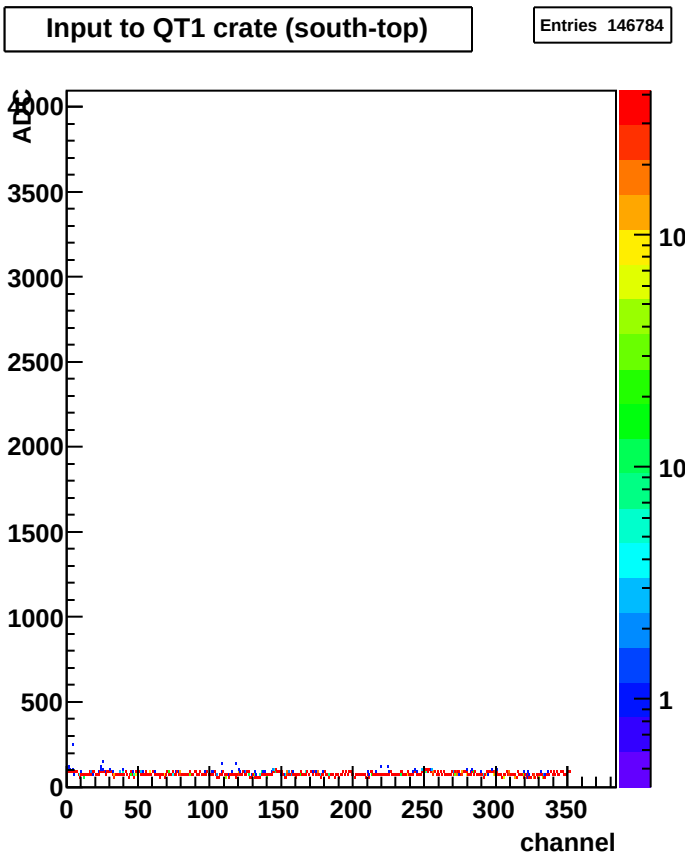

BBClarge-TAC-W
Entries 417
Mean 266.9
RMS 516.4

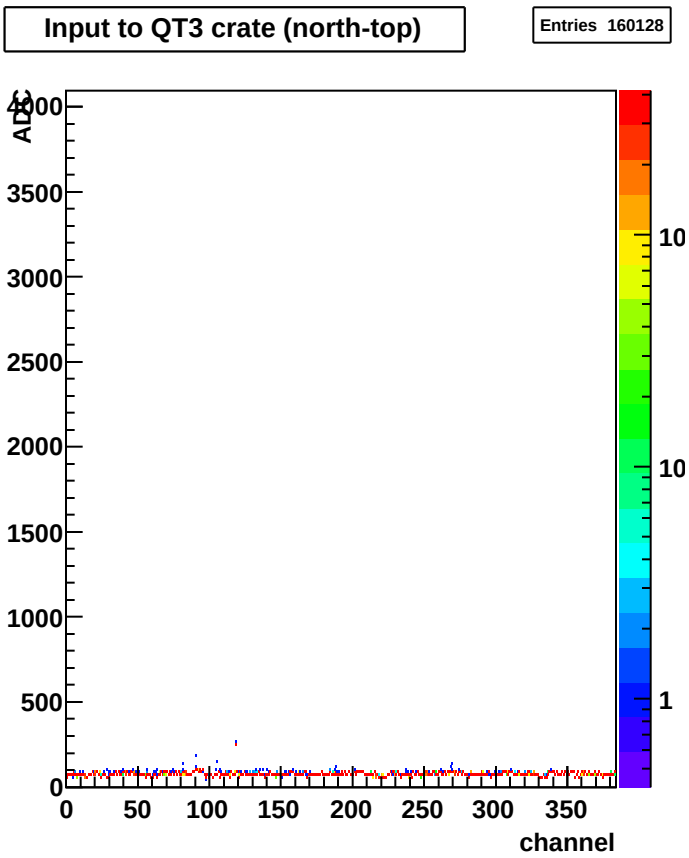

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 417
Mean 4060
RMS 694.9

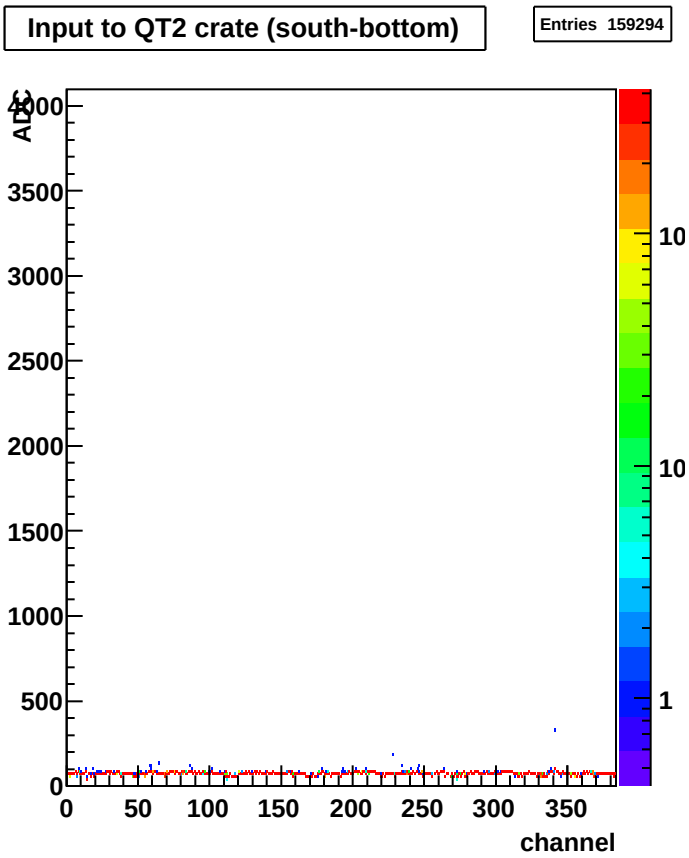

VPD-TAC-E
Entries 1
Mean 389
RMS 0

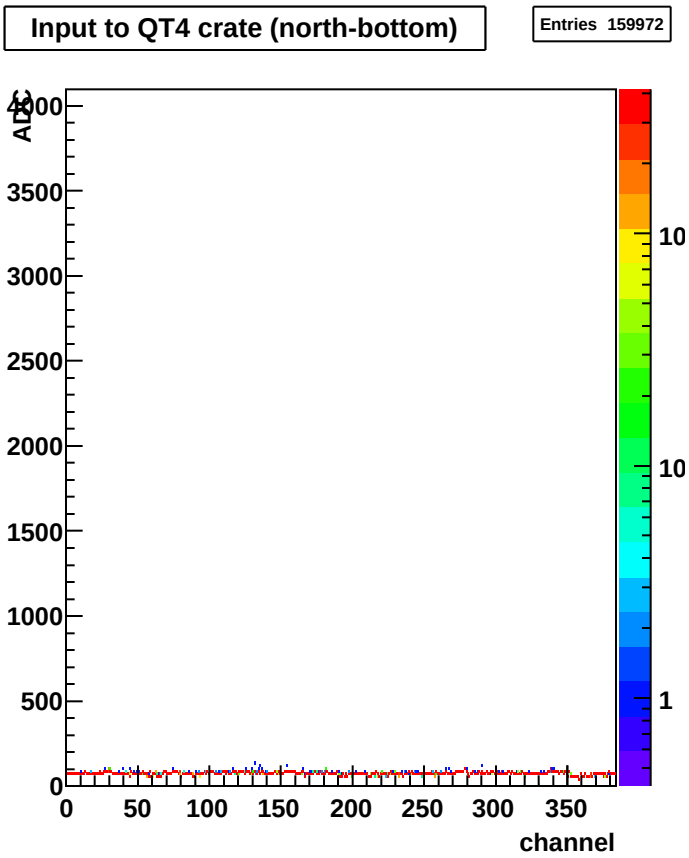

VPD-TAC-W
Entries 417
Mean 146
RMS 140.3

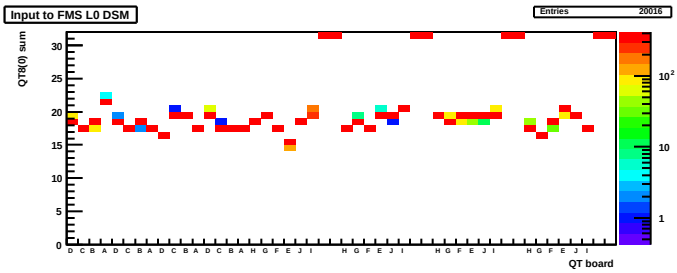

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1
Mean 4338
RMS 0

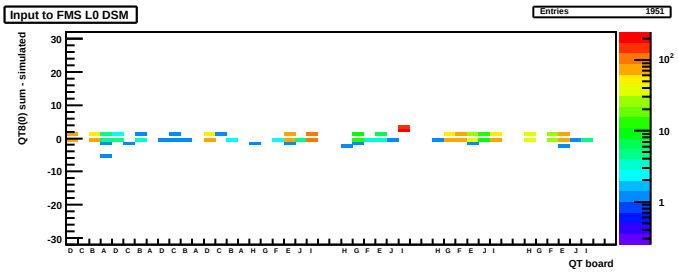

Input to QT1 crate (south-top)
Entries 146784
ADC
channel

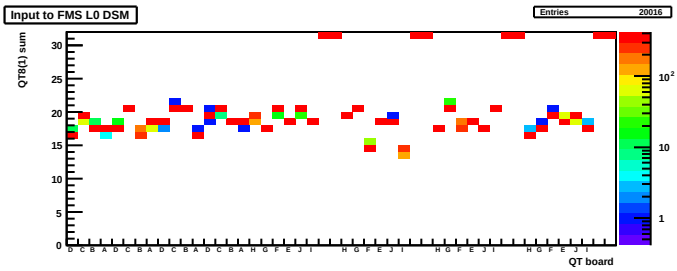

Input to QT3 crate (north-top)
Entries 160128
ADC
channel

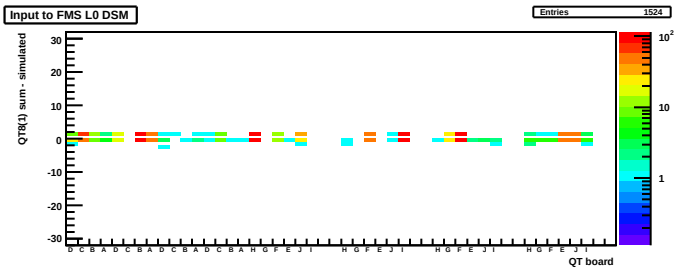

Input to QT2 crate (south-bottom)
Entries 159294
ADC
channel

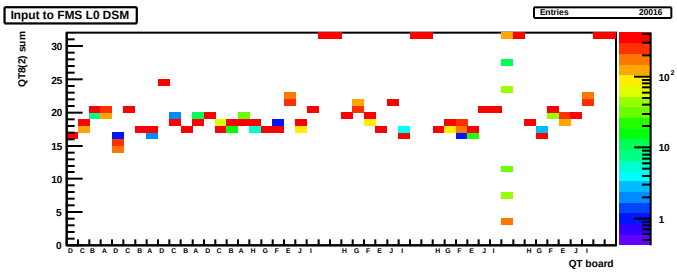

Input to QT4 crate (north-bottom)
Entries 159972
ADC
channel

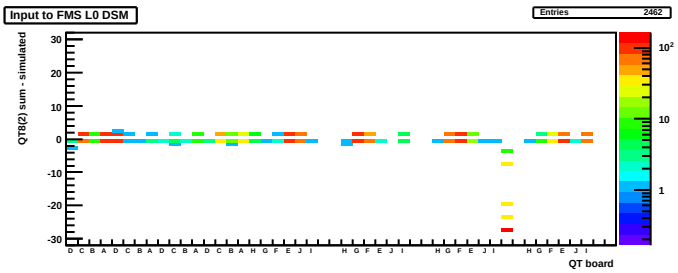

Input to FMS L0 DSM
Entries 20016
QT8(0) sum
QT board

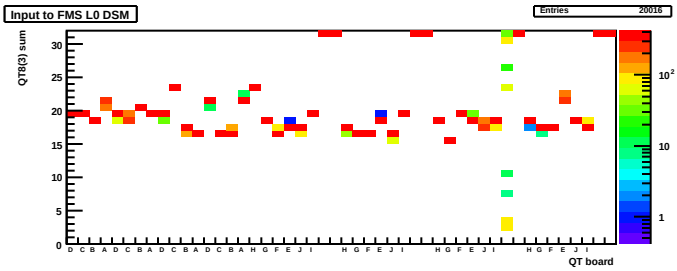

Input to FMS L0 DSM
Entries 1951
QT8(0) sum - simulated
QT board

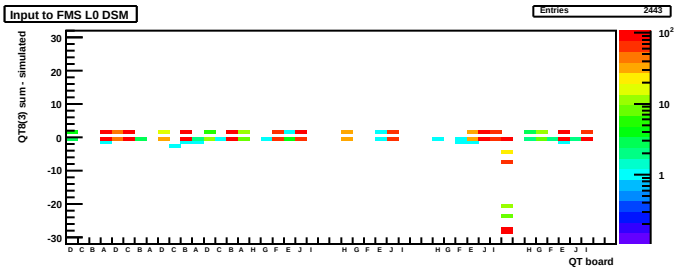

Input to FMS L0 DSM
Entries 20016
QT8(1) sum
QT board

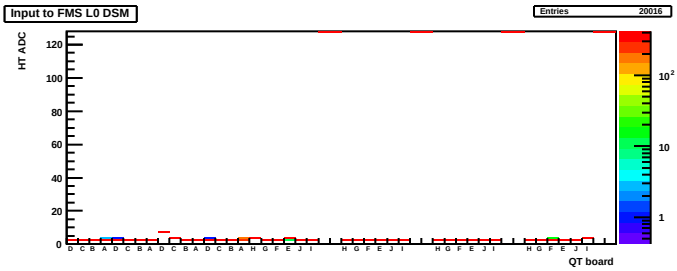

Input to FMS L0 DSM
Entries 1524
QT8(1) sum - simulated
QT board

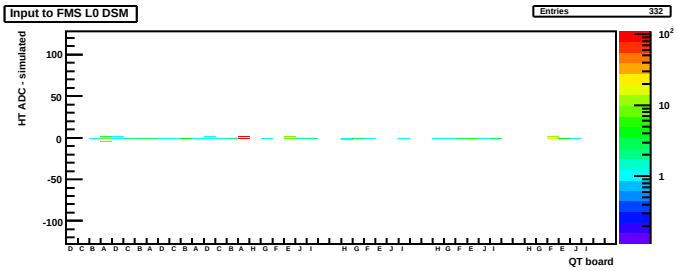

Input to FMS L0 DSM
Entries 20016
QT8(2) sum
QT board

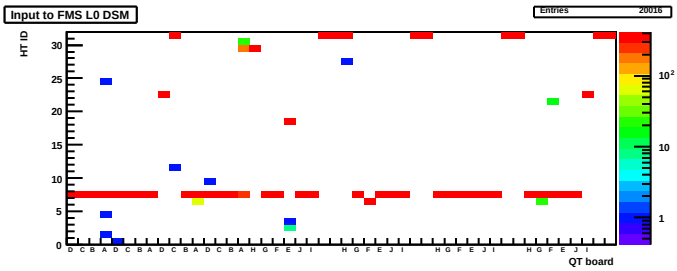

Input to FMS L0 DSM
Entries 2462
QT8(2) sum - simulated
QT board

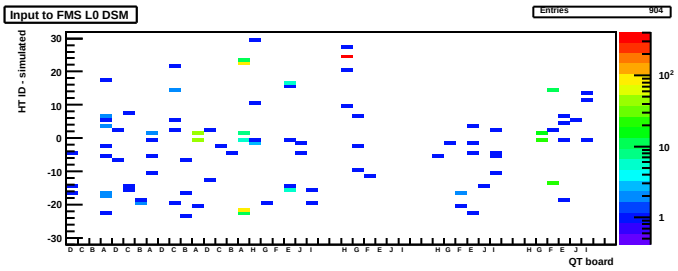

Input to FMS L0 DSM
Entries 20016
QT8(3) sum
QT board

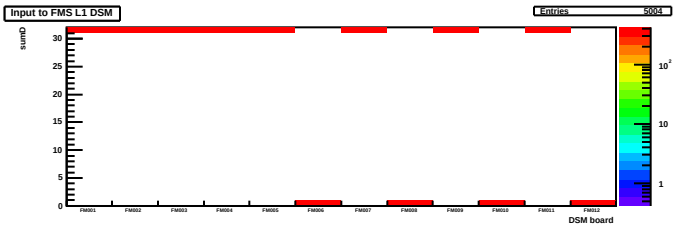

Input to FMS L0 DSM
Entries 2443
QT8(3) sum - simulated
QT board

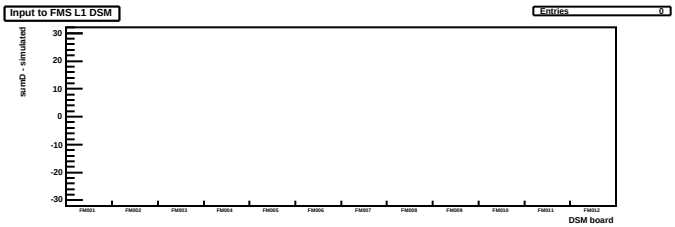

Input to FMS L0 DSM
Entries 20016
HT ADC
QT board

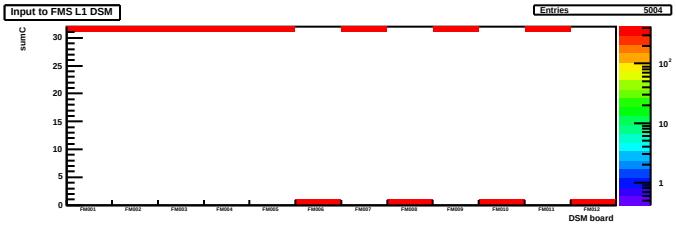

Input to FMS L0 DSM
Entries 332
HT ADC - simulated
QT board

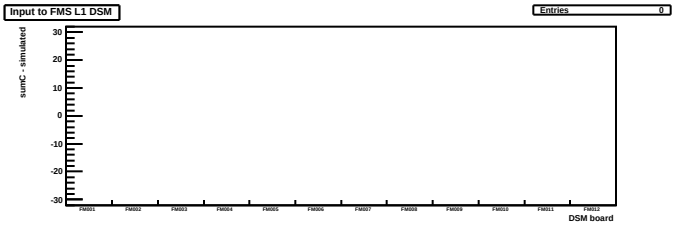

Input to FMS L0 DSM
Entries 20016
HT ID
QT board

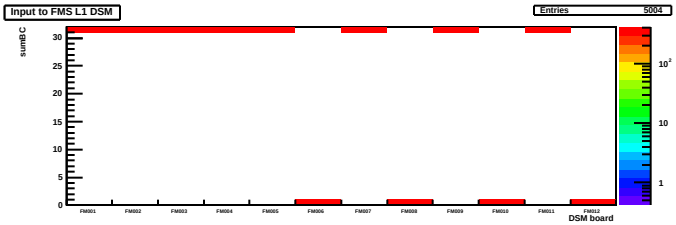

Input to FMS L0 DSM
Entries 904
HT ID - simulated
QT board

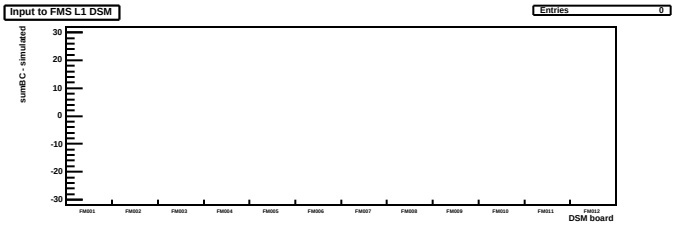

Input to FMS L1 DSM
Entries 5004
sumD
DSM board

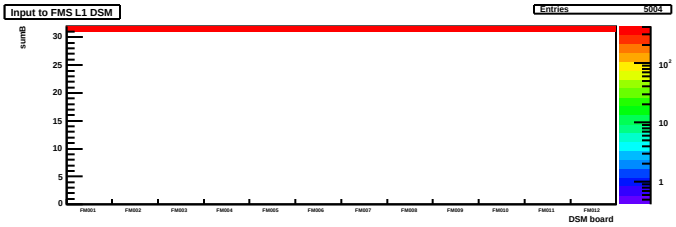

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

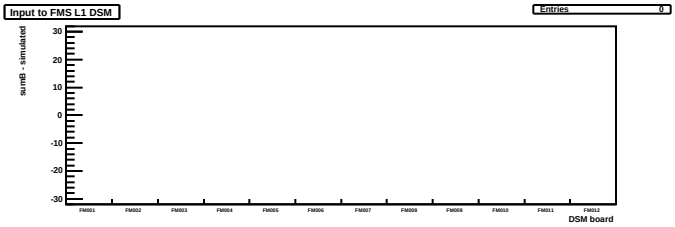

Input to FMS L1 DSM
Entries 5004
sumC
DSM board

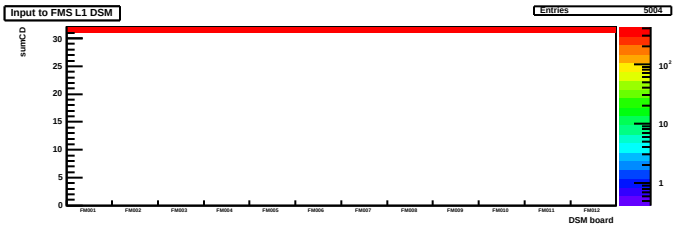

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

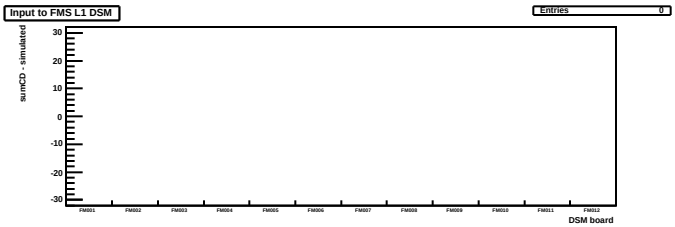

Input to FMS L1 DSM
Entries 5004
sumBC
DSM board

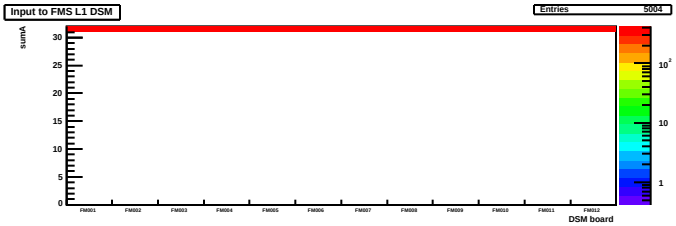

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

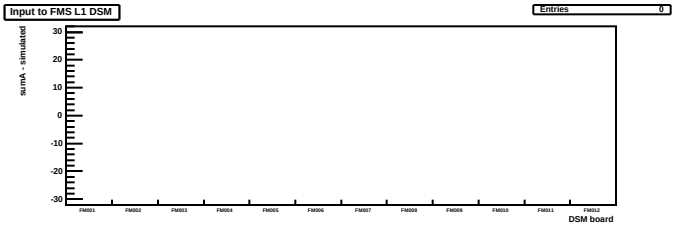

Input to FMS L1 DSM
Entries 5004
sumB
DSM board

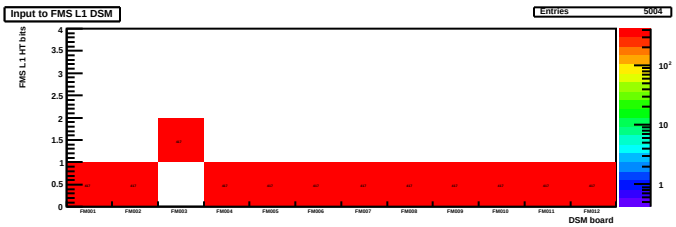

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

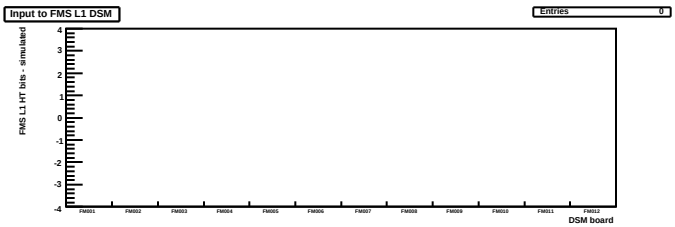

Input to FMS L1 DSM
Entries 5004
sumCD
DSM board

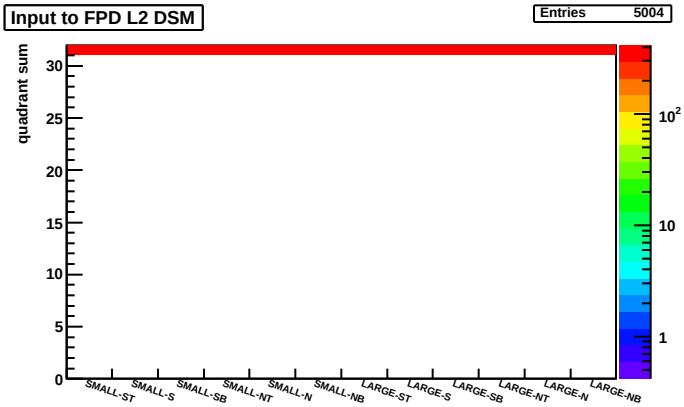

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

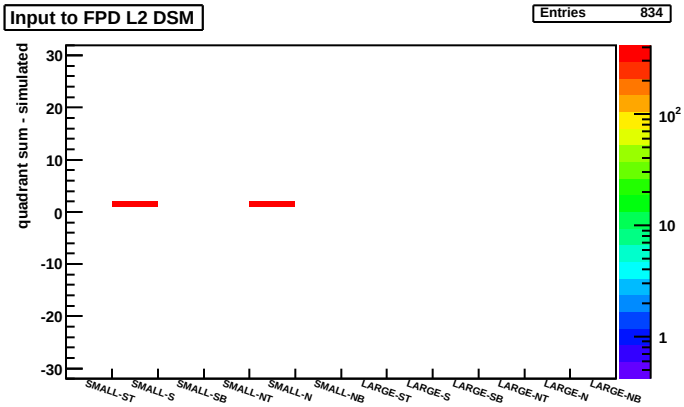

Input to FMS L1 DSM
Entries 5004
sumA
DSM board

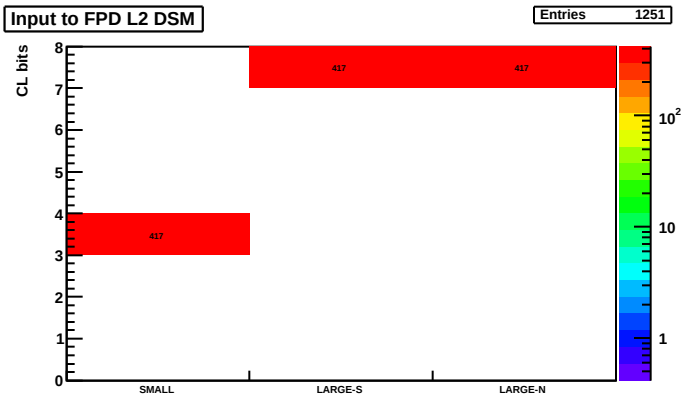

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

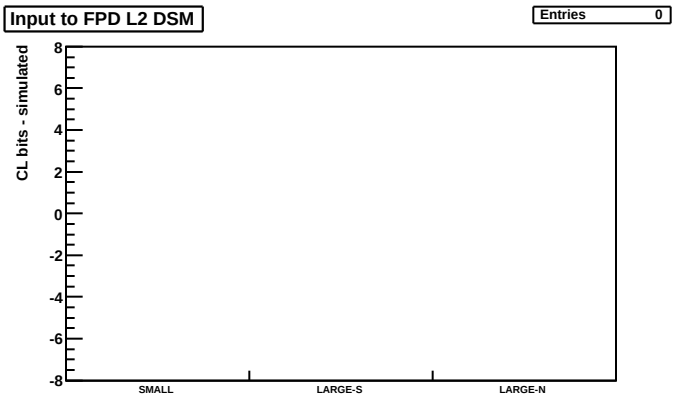

Input to FMS L1 DSM
Entries 5004
FMS L1 HT bits
DSM board

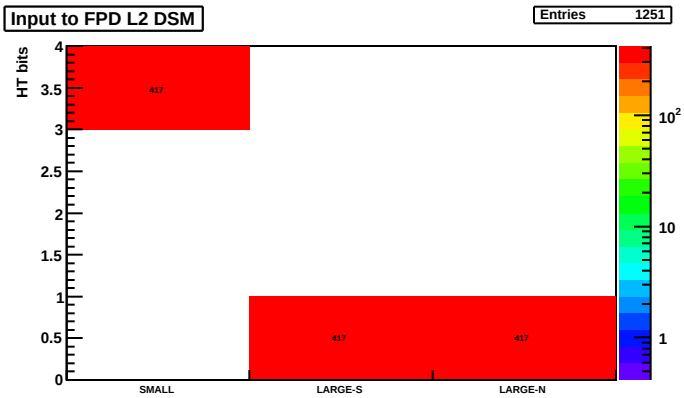

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

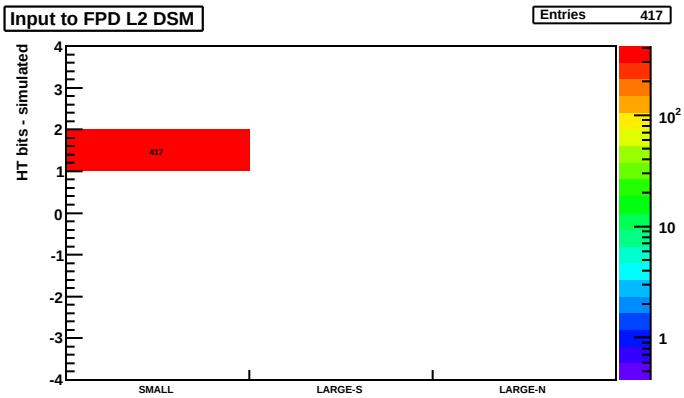

Input to FPD L2 DSM
Entries 5004
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

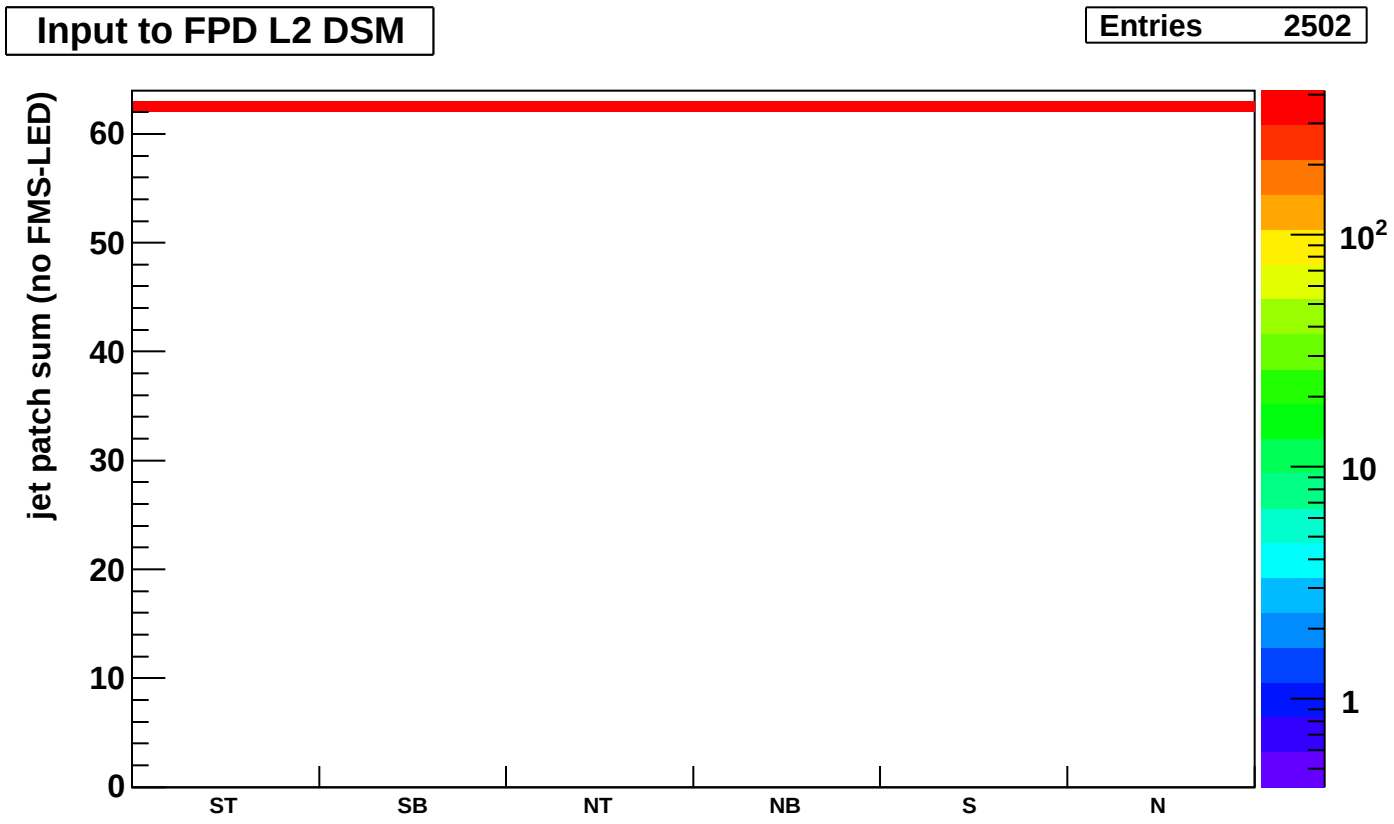

Input to FPD L2 DSM
Entries 834
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

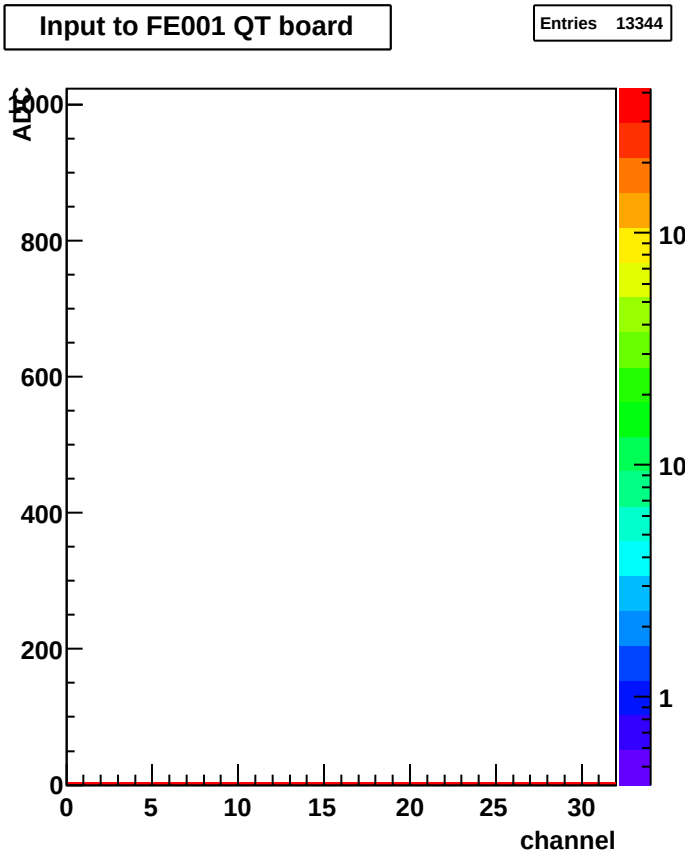

Input to FPD L2 DSM
Entries 1251
CL bits
417
417
417
SMALL
LARGE-S
LARGE-N

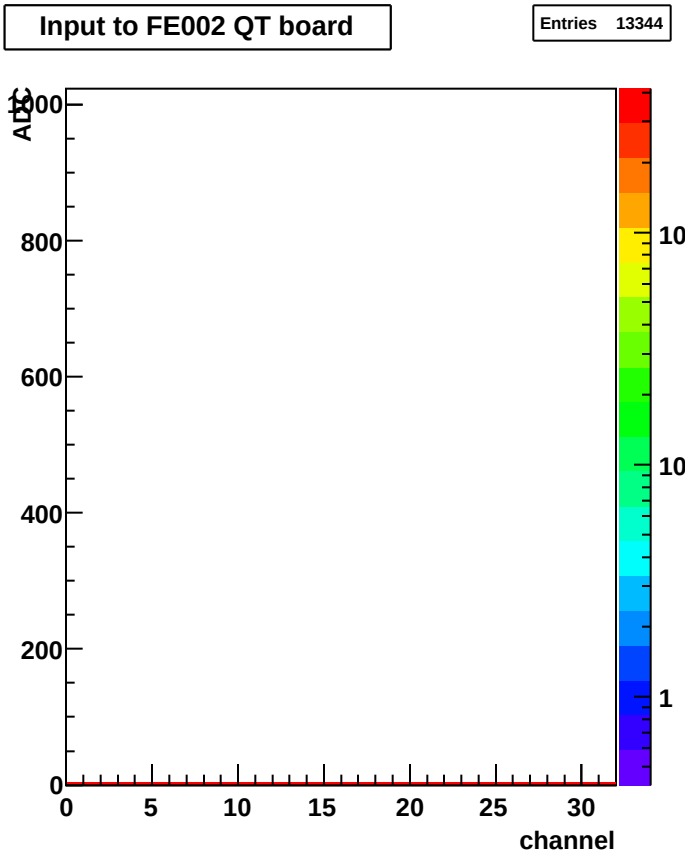

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

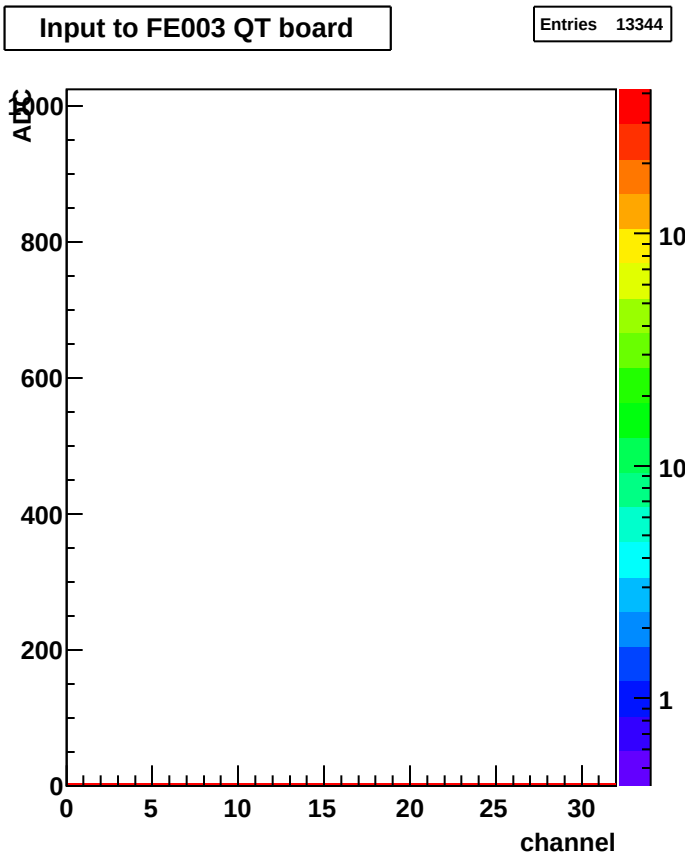

Input to FPD L2 DSM
Entries 1251
HT bits
417
417
417
SMALL
LARGE-S
LARGE-N

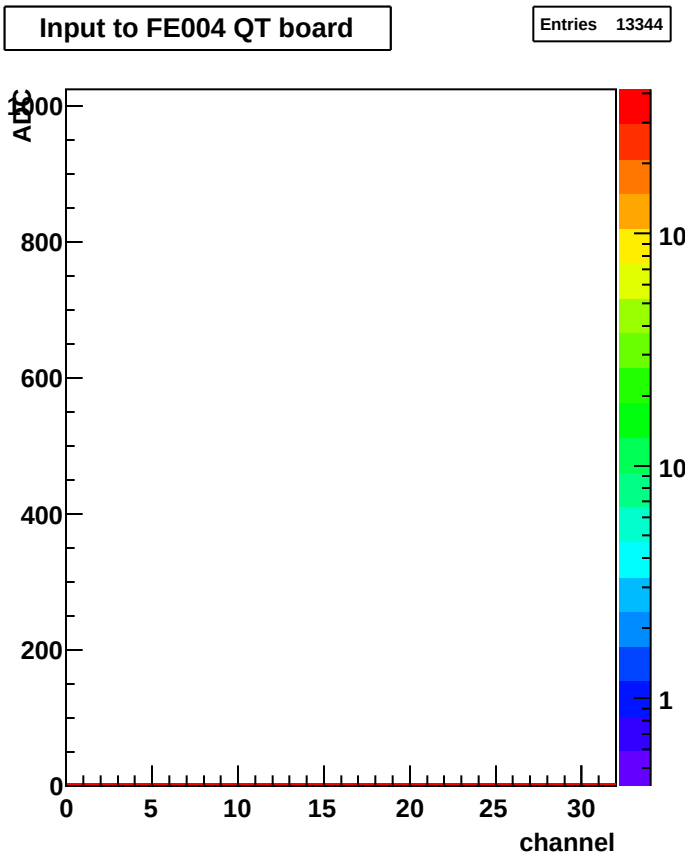

Input to FPD L2 DSM
Entries 417
HT bits - simulated
417
SMALL
LARGE-S
LARGE-N

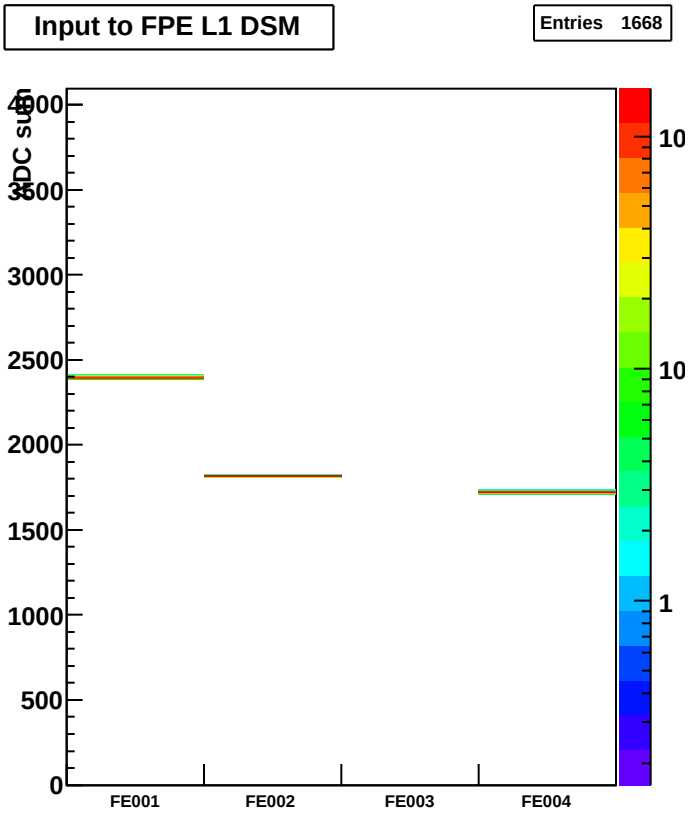

Input to FPD L2 DSM
Entries 2502
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2

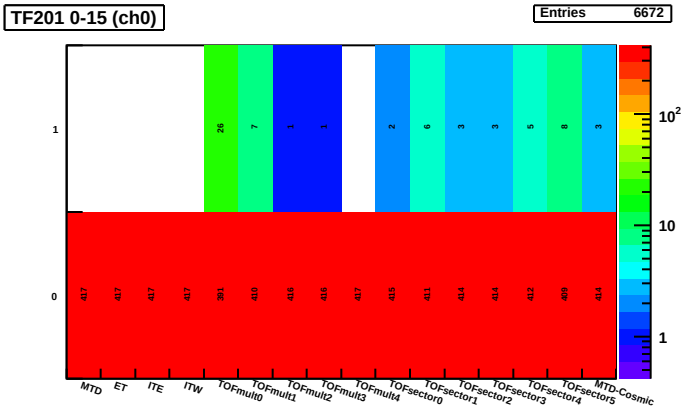

Input to FE001 QT board
Entries 13344
ADC
channel

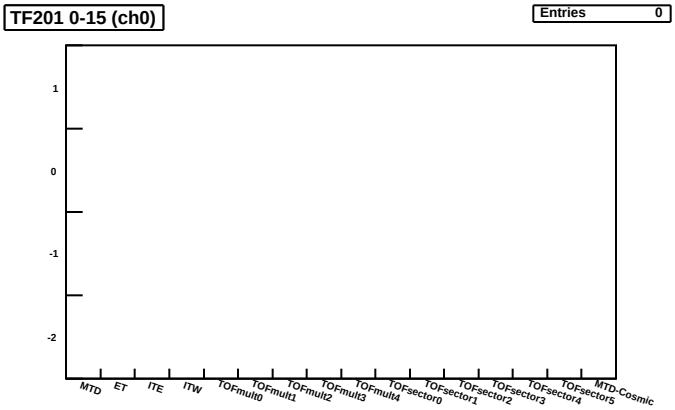

Input to FE002 QT board
Entries 13344
ADC
channel

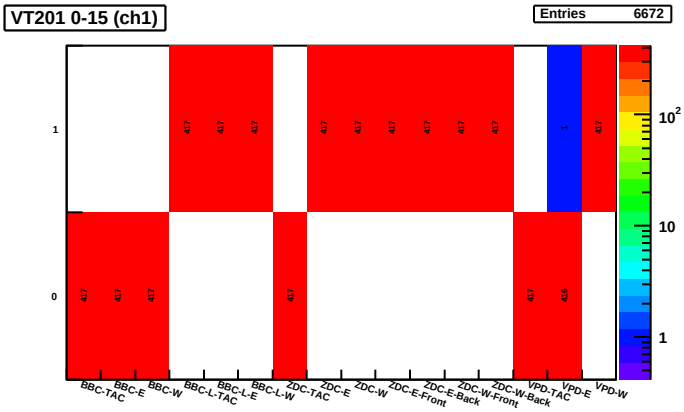

Input to FE003 QT board
Entries 13344
ADC
channel

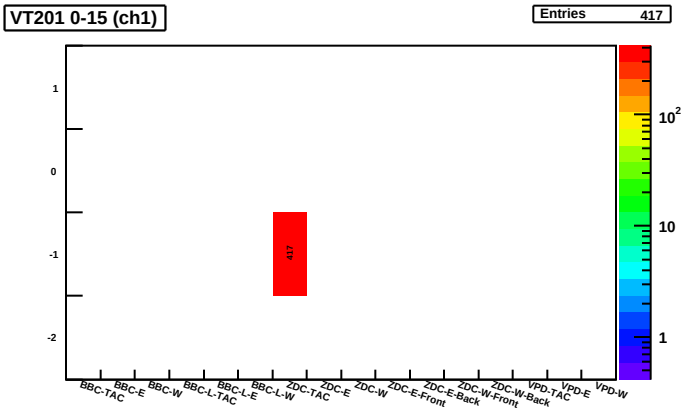

Input to FE004 QT board
Entries 13344
ADC
channel


Input to FPE L1 DSM
Entries 1668
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
1


TF201 0-15 (ch0)
Entries 6672
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 6672
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 417
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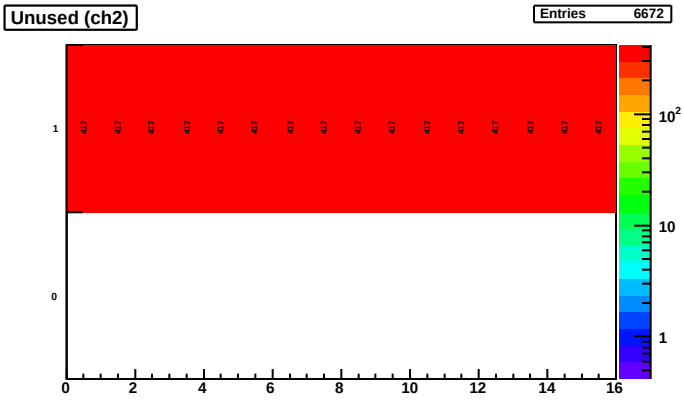
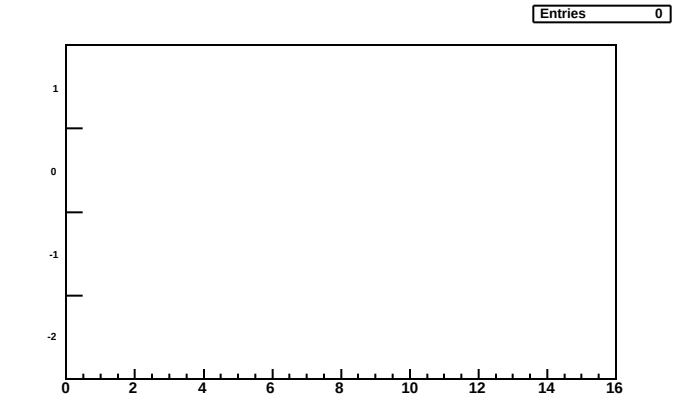

Unused (ch2)
Entries 6672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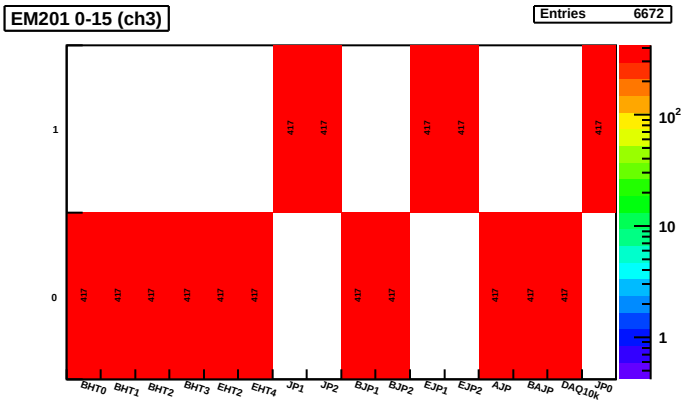
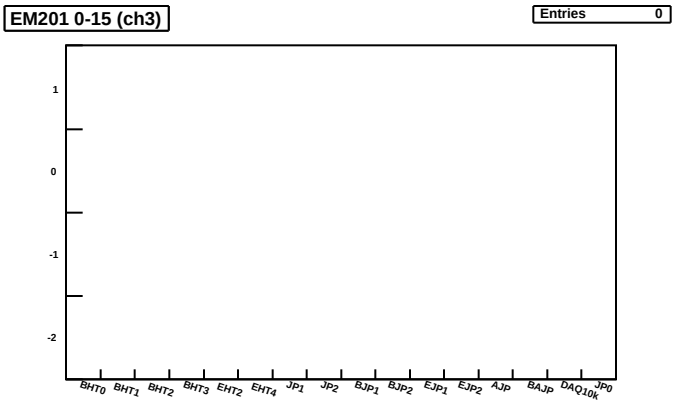

EM201 0-15 (ch3)
Entries 6672
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
Entries 0
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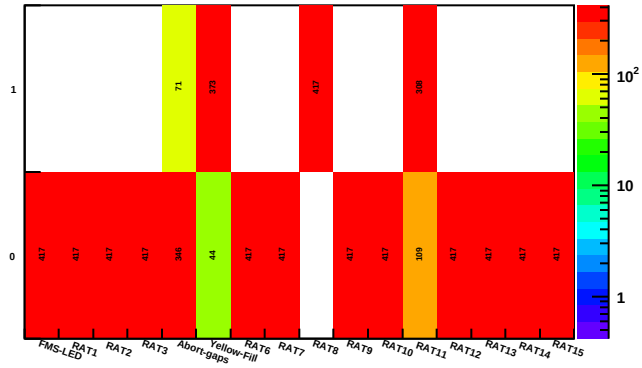
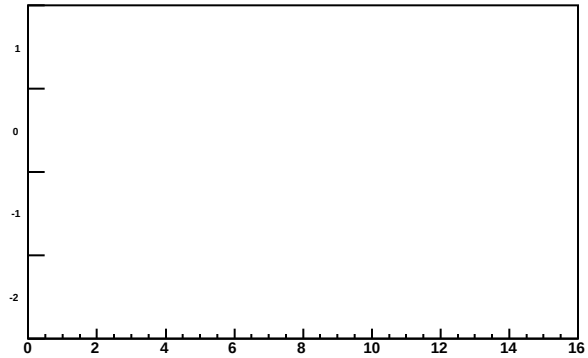
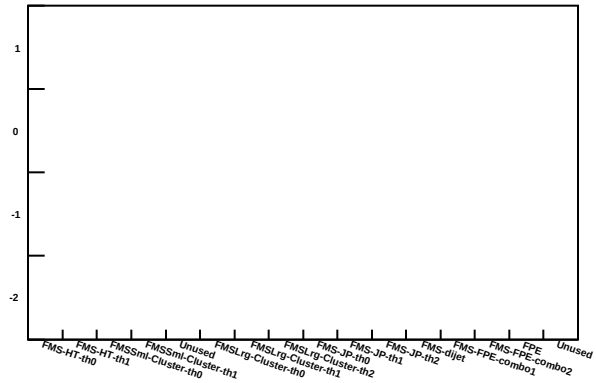
RAT board (ch4)



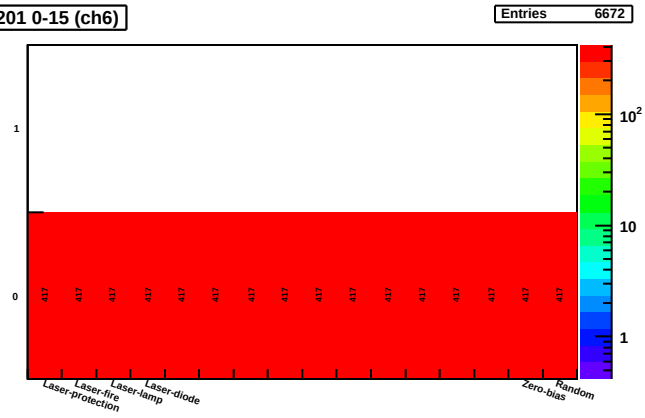


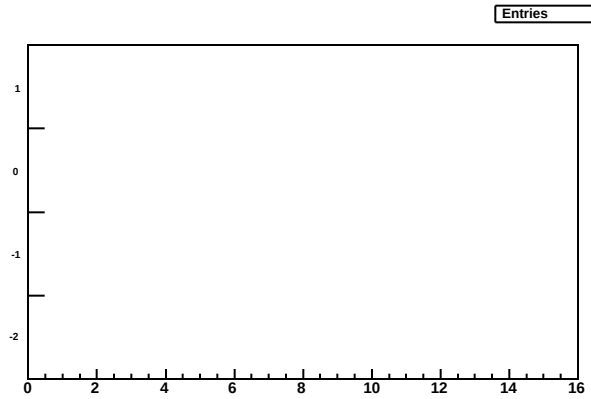
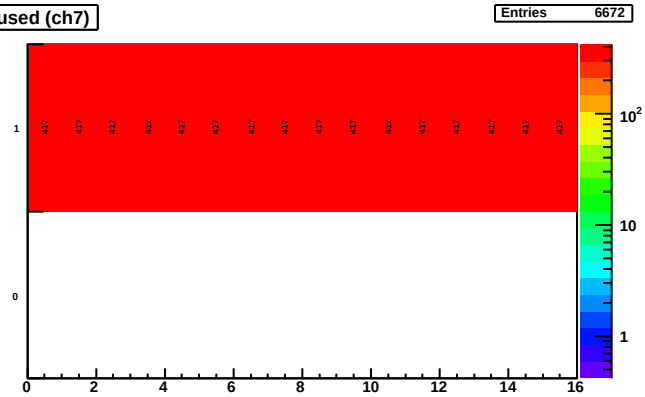
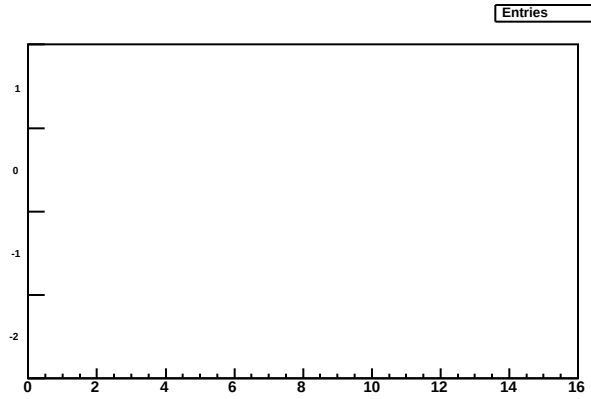
FP201 0-15 (ch5)

FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)