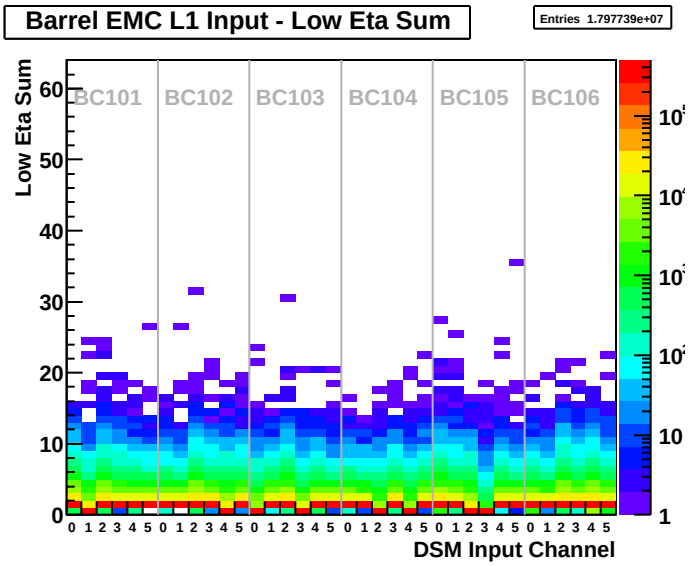
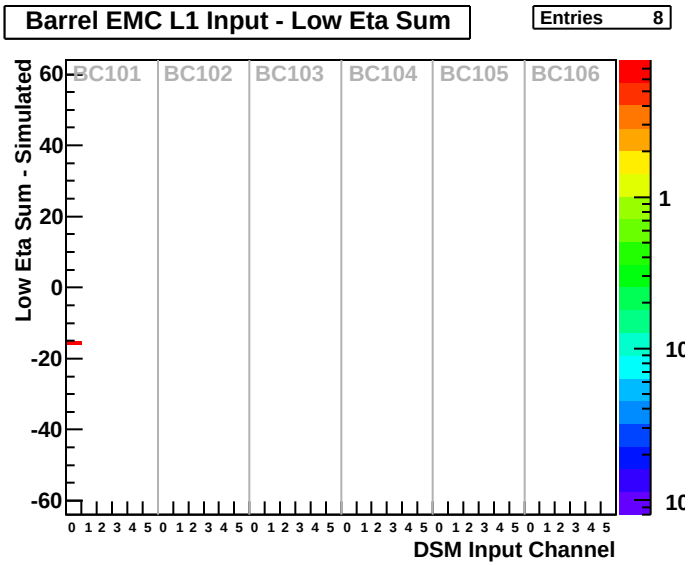
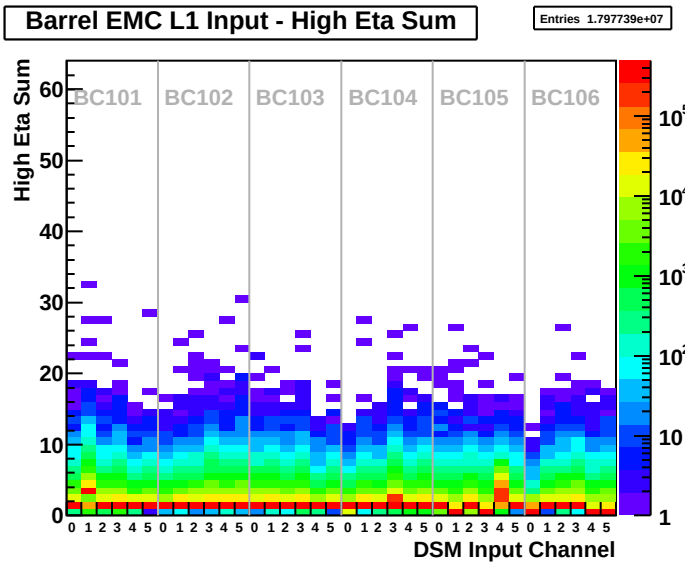
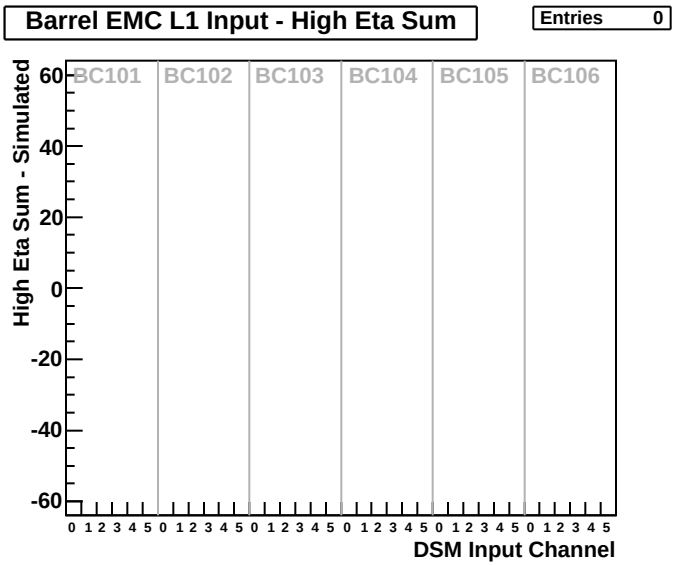
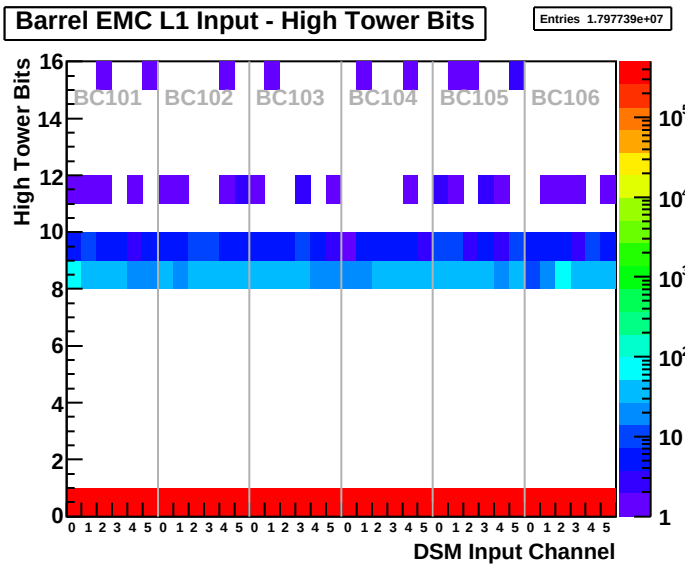
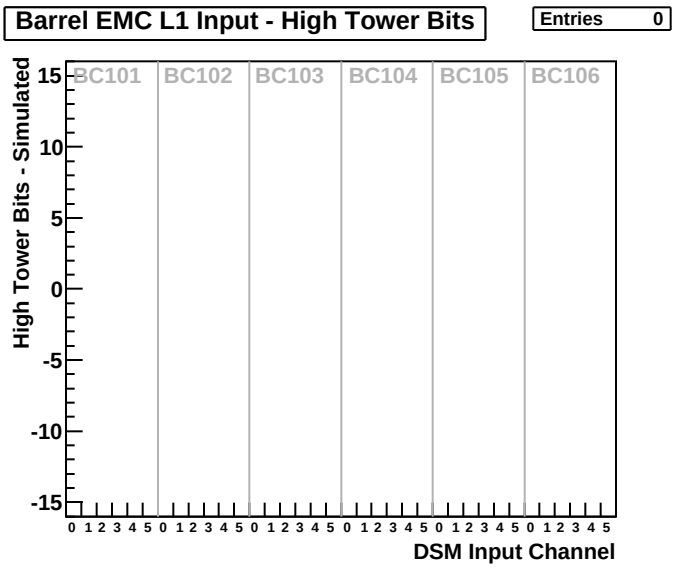

Barrel EMC L1 Input - Low Eta Sum
Entries 1.797739e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

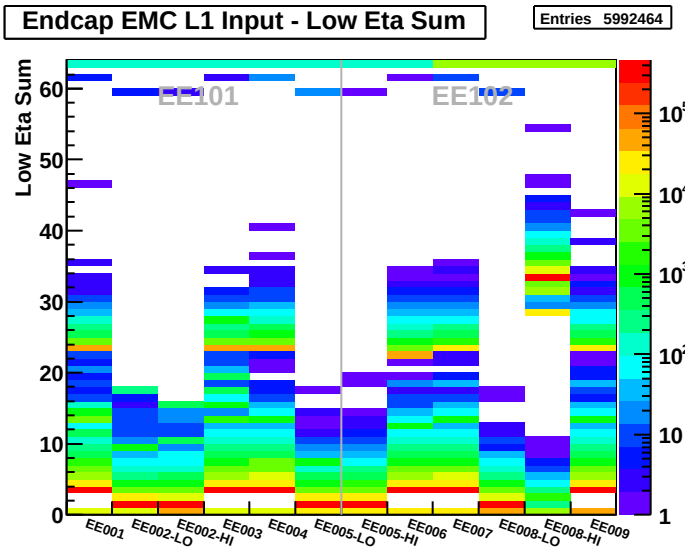

Barrel EMC L1 Input - Low Eta Sum
Entries 8
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

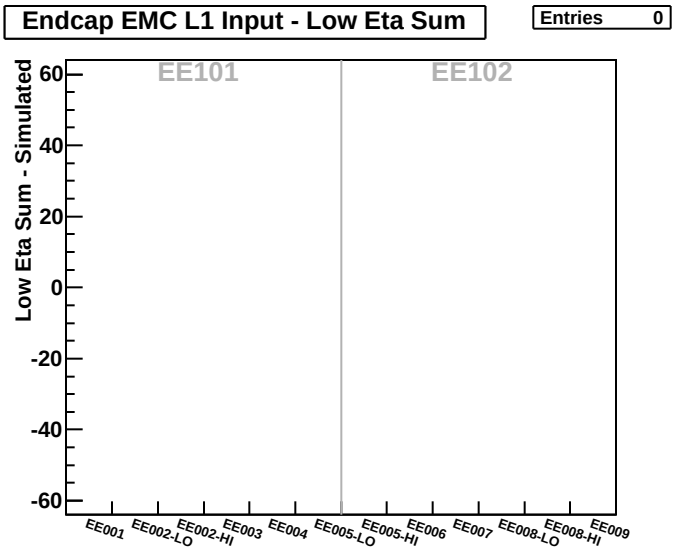

Barrel EMC L1 Input - High Eta Sum
Entries 1.797739e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

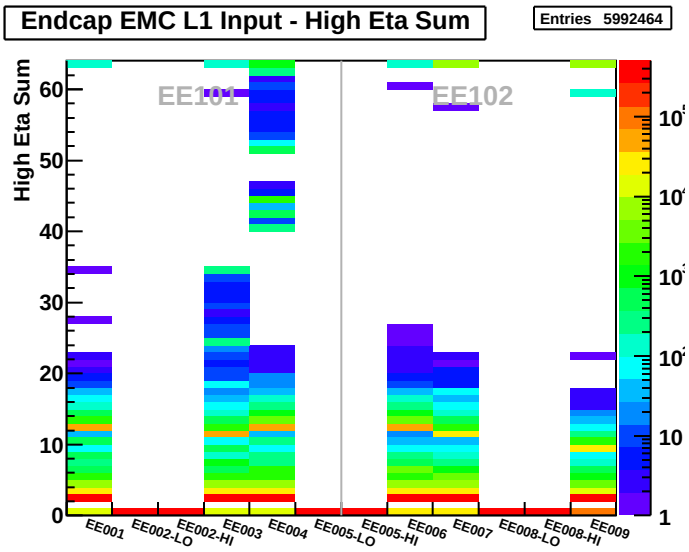

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

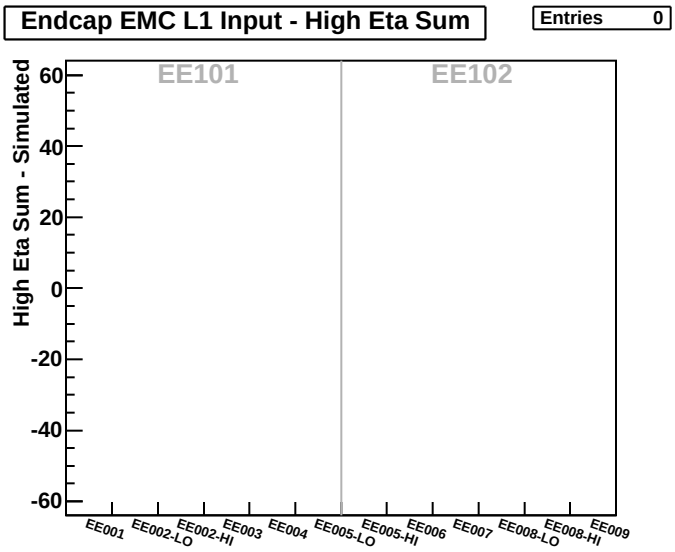

Barrel EMC L1 Input - High Tower Bits
Entries 1.797739e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

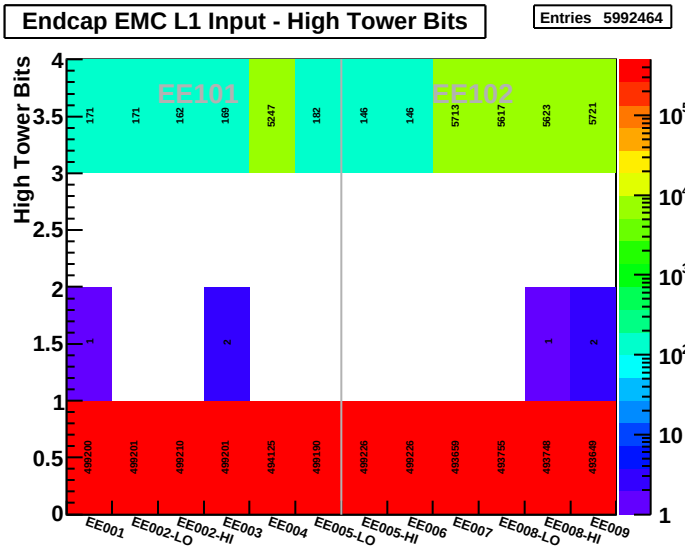

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

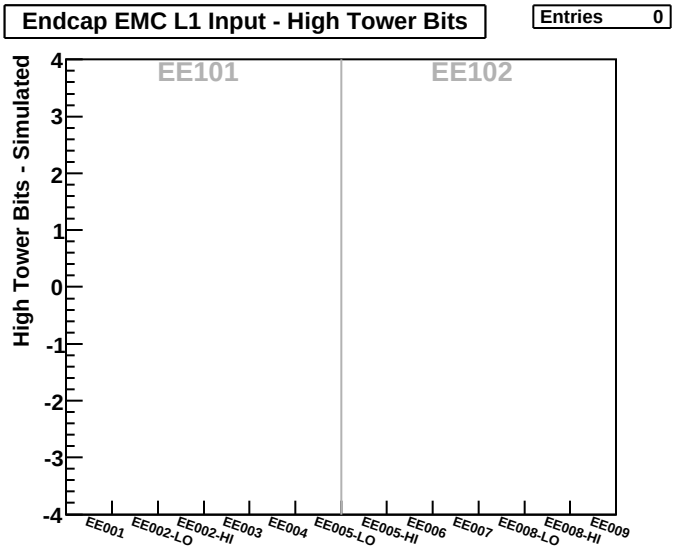

Endcap EMC L1 Input - Low Eta Sum
Entries 5992464
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

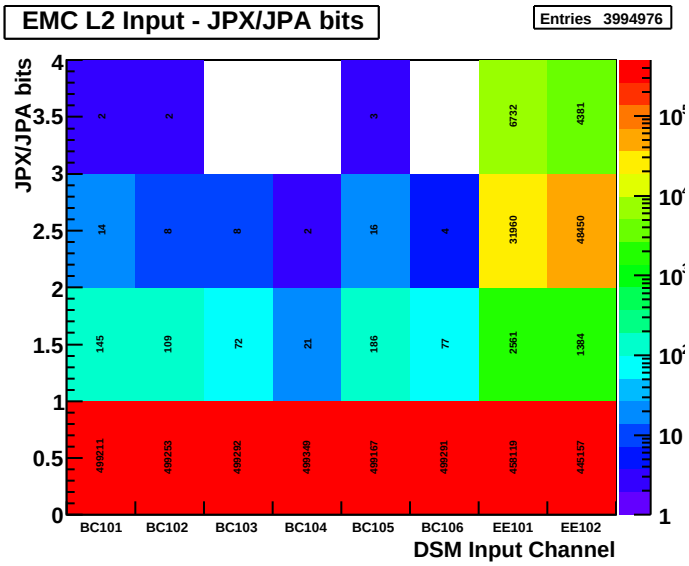

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

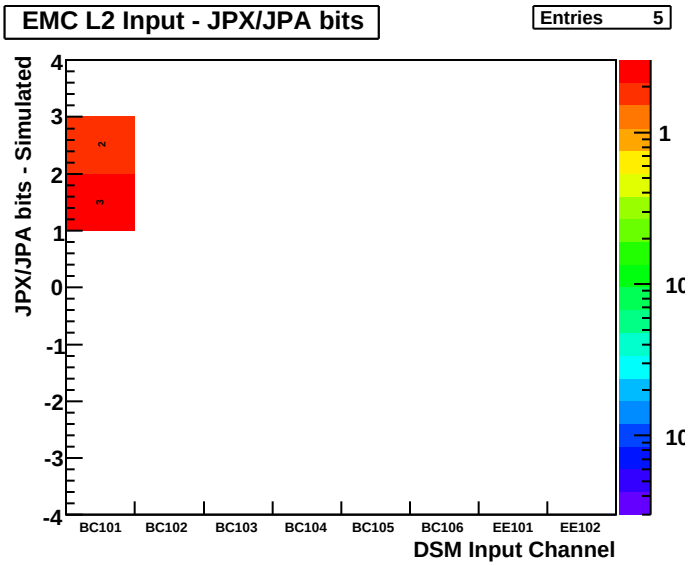

Endcap EMC L1 Input - High Eta Sum
Entries 5992464
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

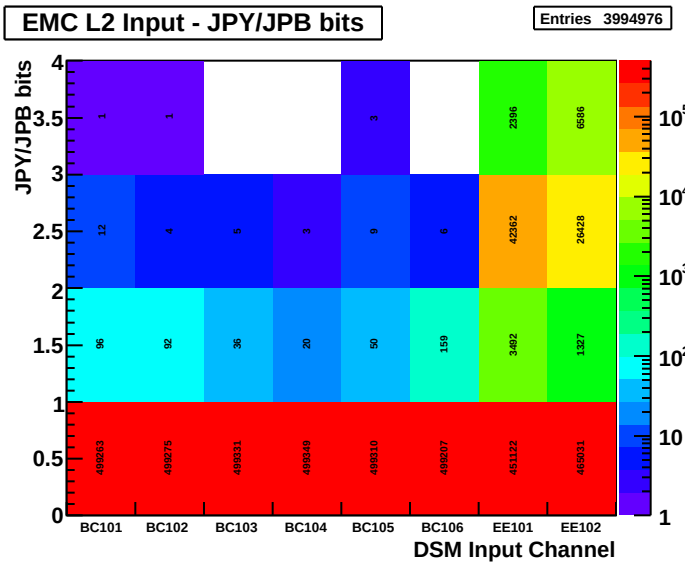

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

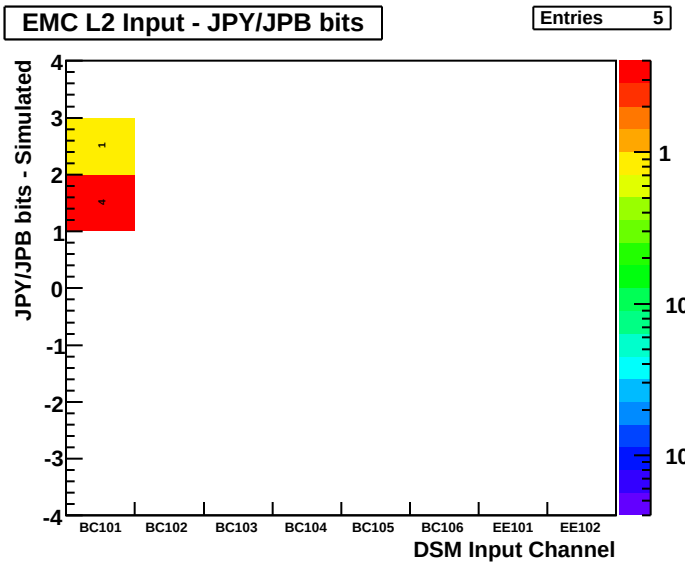

Endcap EMC L1 Input - High Tower Bits
Entries 5992464
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

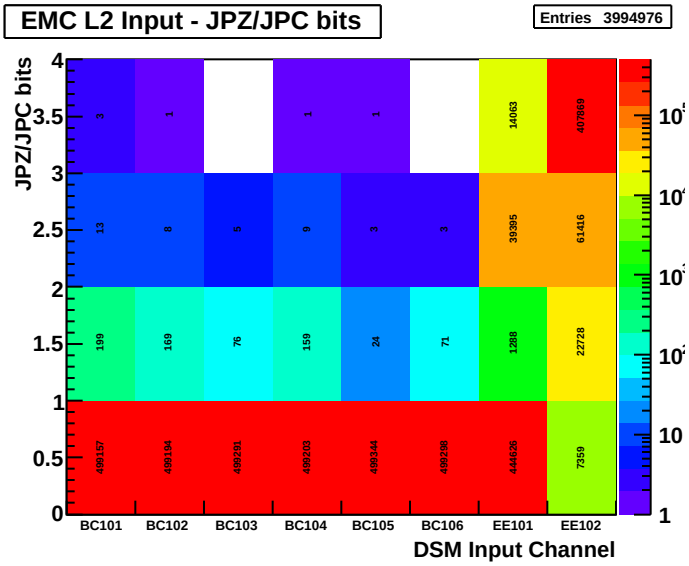

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

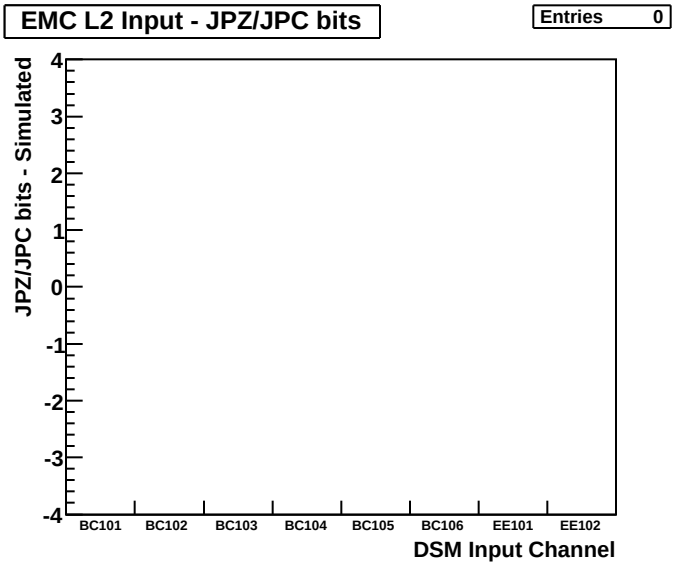

EMC L2 Input - JPX/JPA bits
Entries 3994976
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

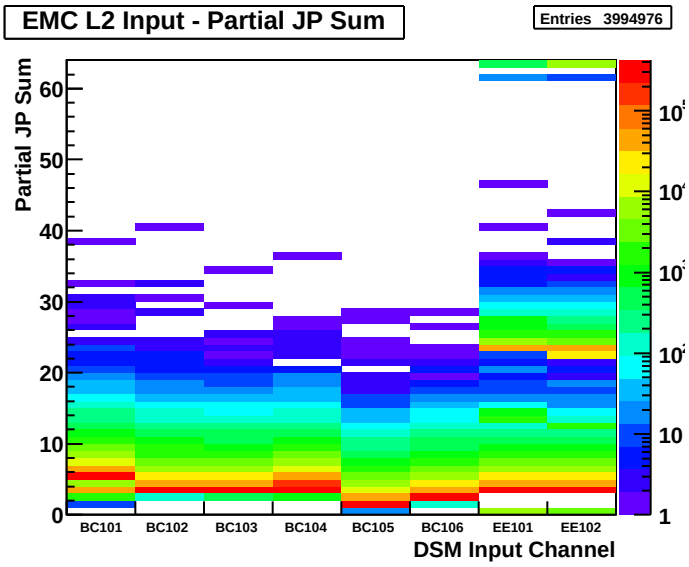

EMC L2 Input - JPX/JPA bits
Entries 5
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

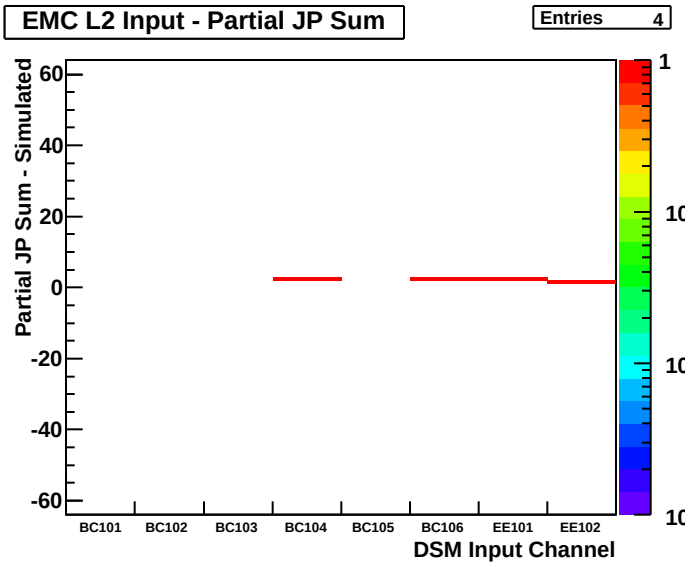

EMC L2 Input - JPY/JPB bits
Entries 3994976
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

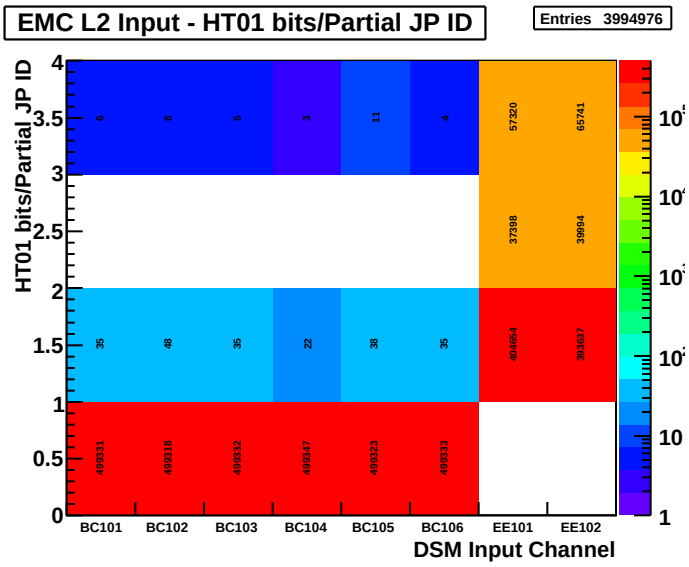

EMC L2 Input - JPY/JPB bits
Entries 5
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

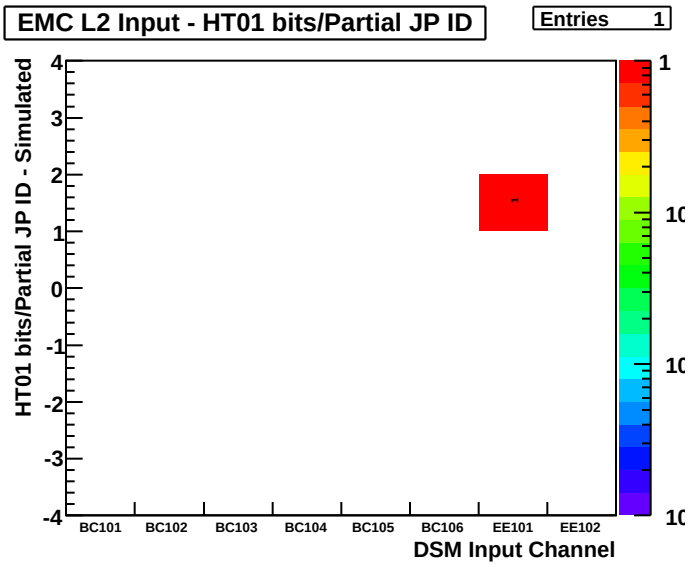

EMC L2 Input - JPZ/JPC bits
Entries 3994976
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

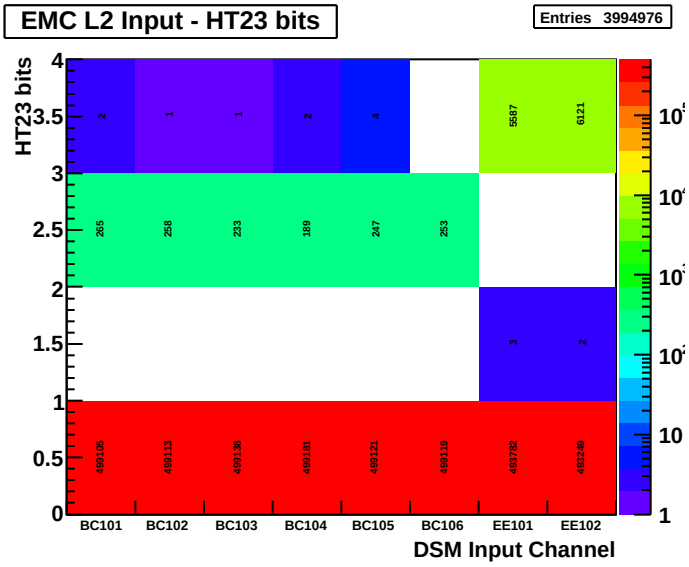

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

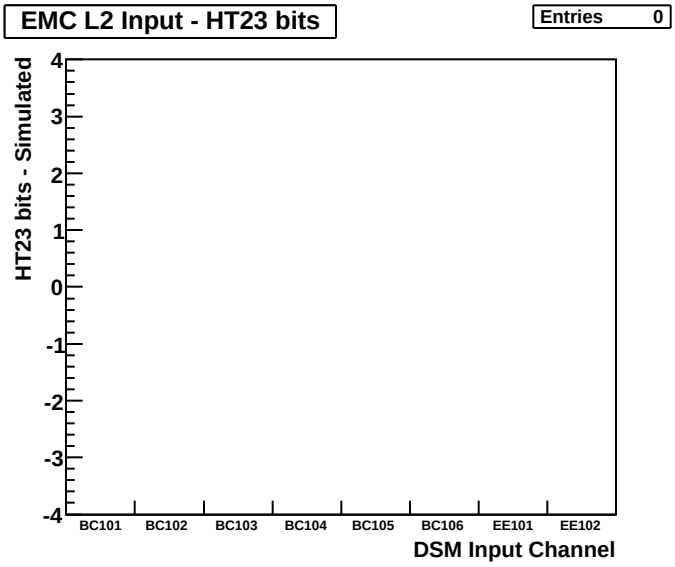

EMC L2 Input - Partial JP Sum
Entries 3994976
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

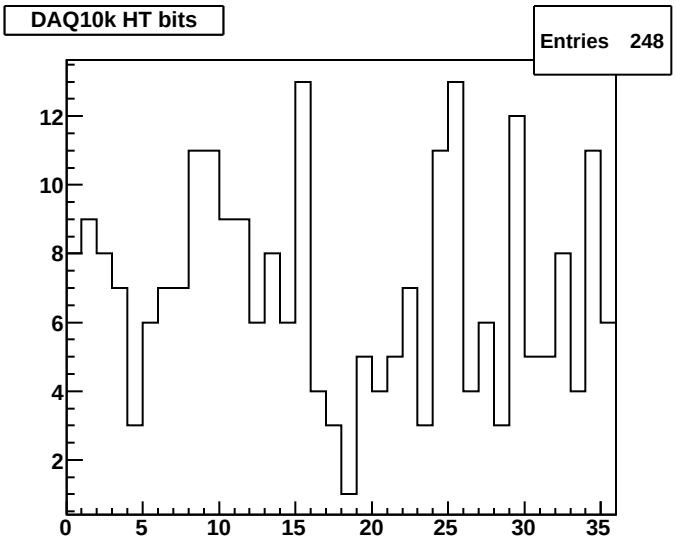

EMC L2 Input - Partial JP Sum
Entries 4
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

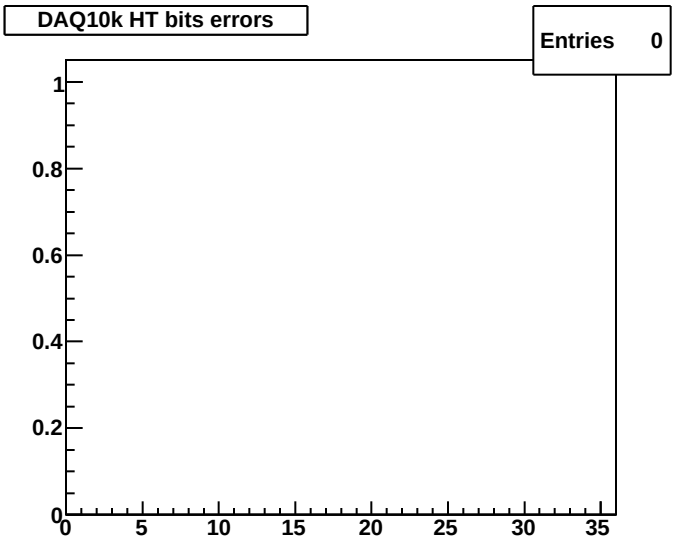

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3994976
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

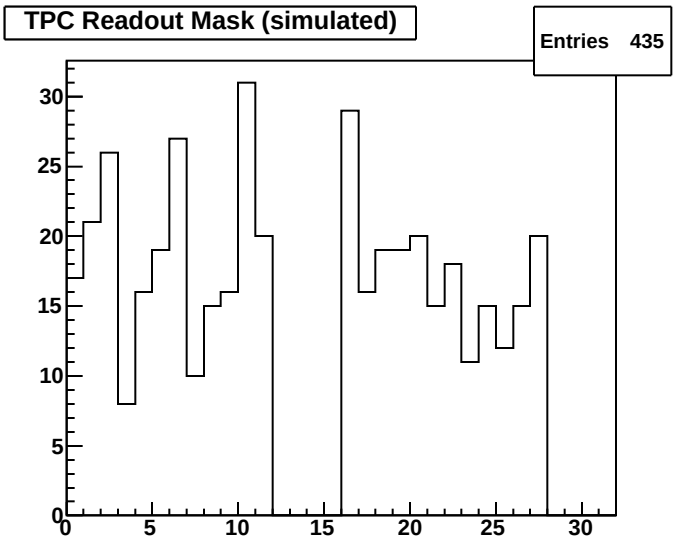

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

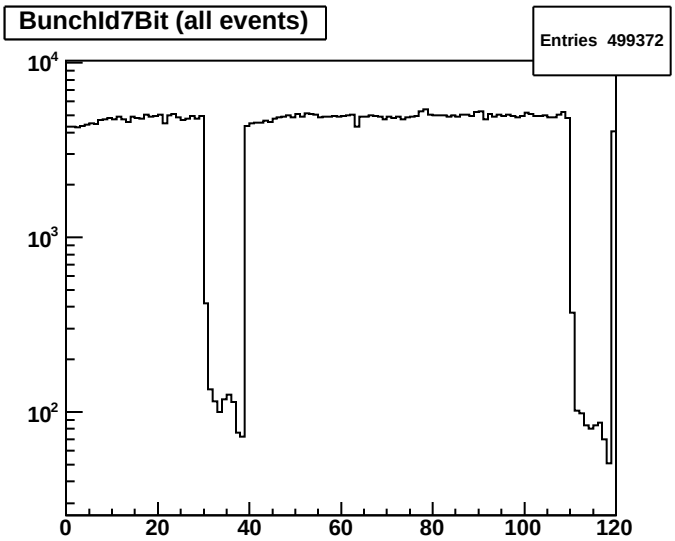

EMC L2 Input - HT23 bits
Entries 3994976
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

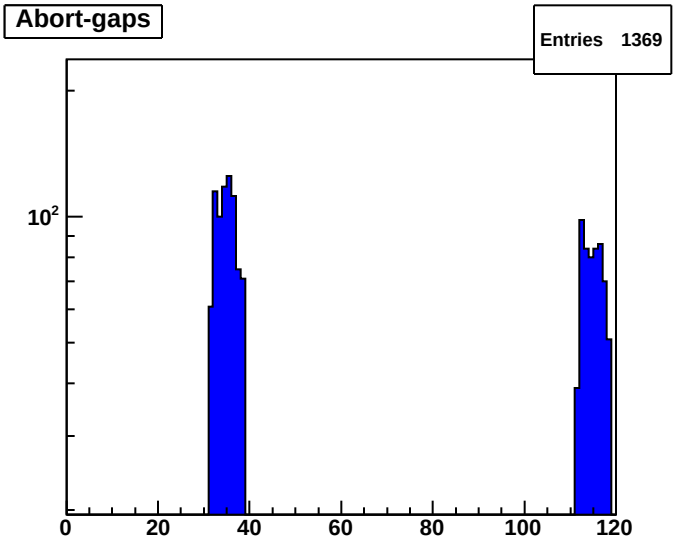

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

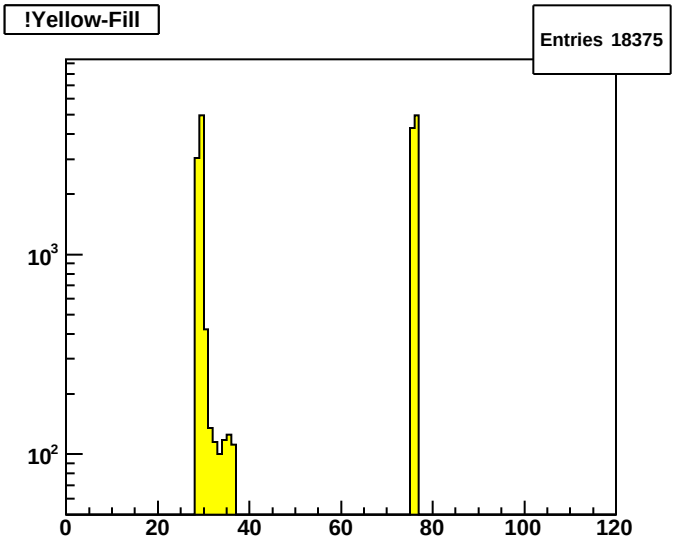

DAQ10k HT bits
Entries 248

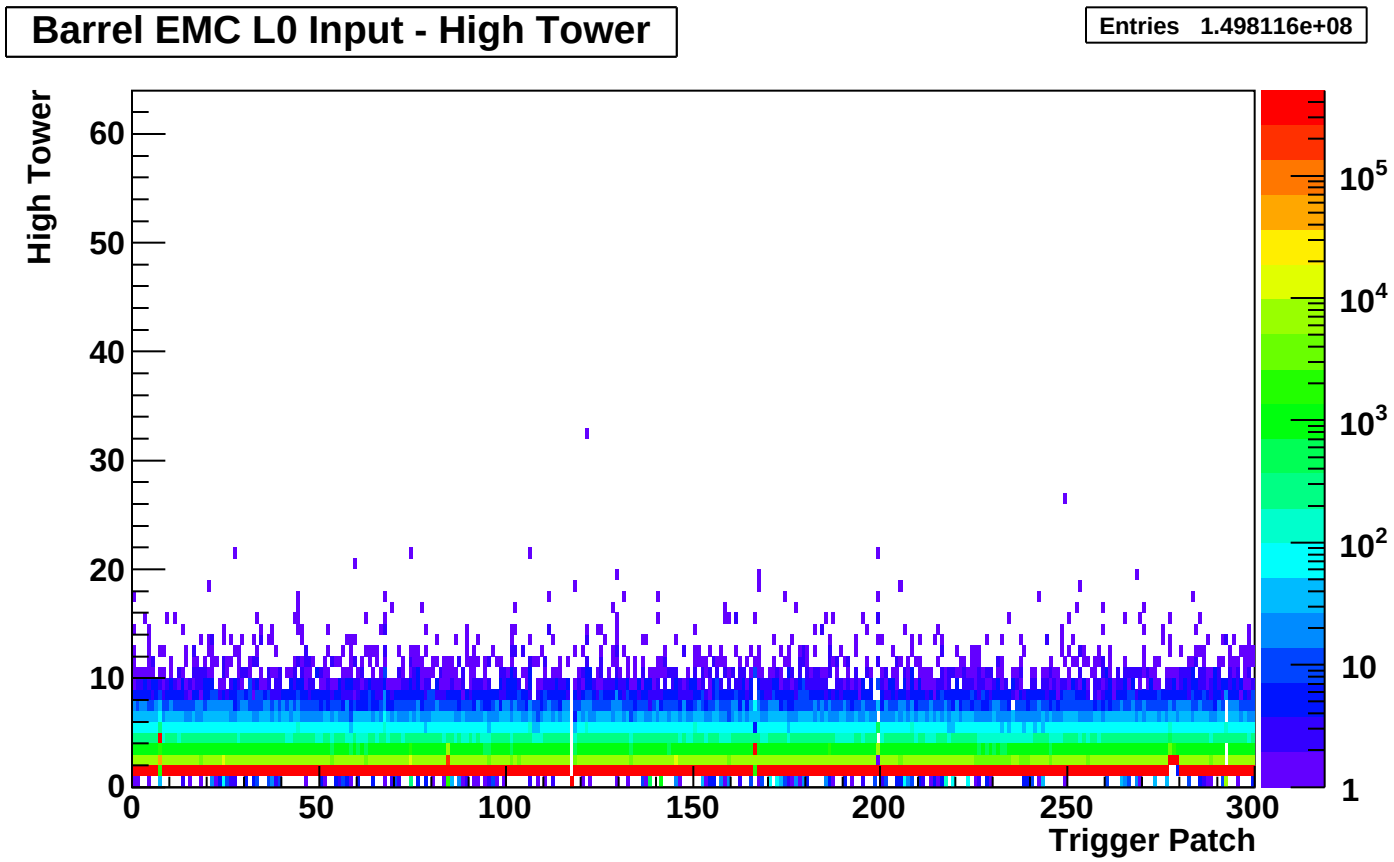

DAQ10k HT bits errors
Entries 0

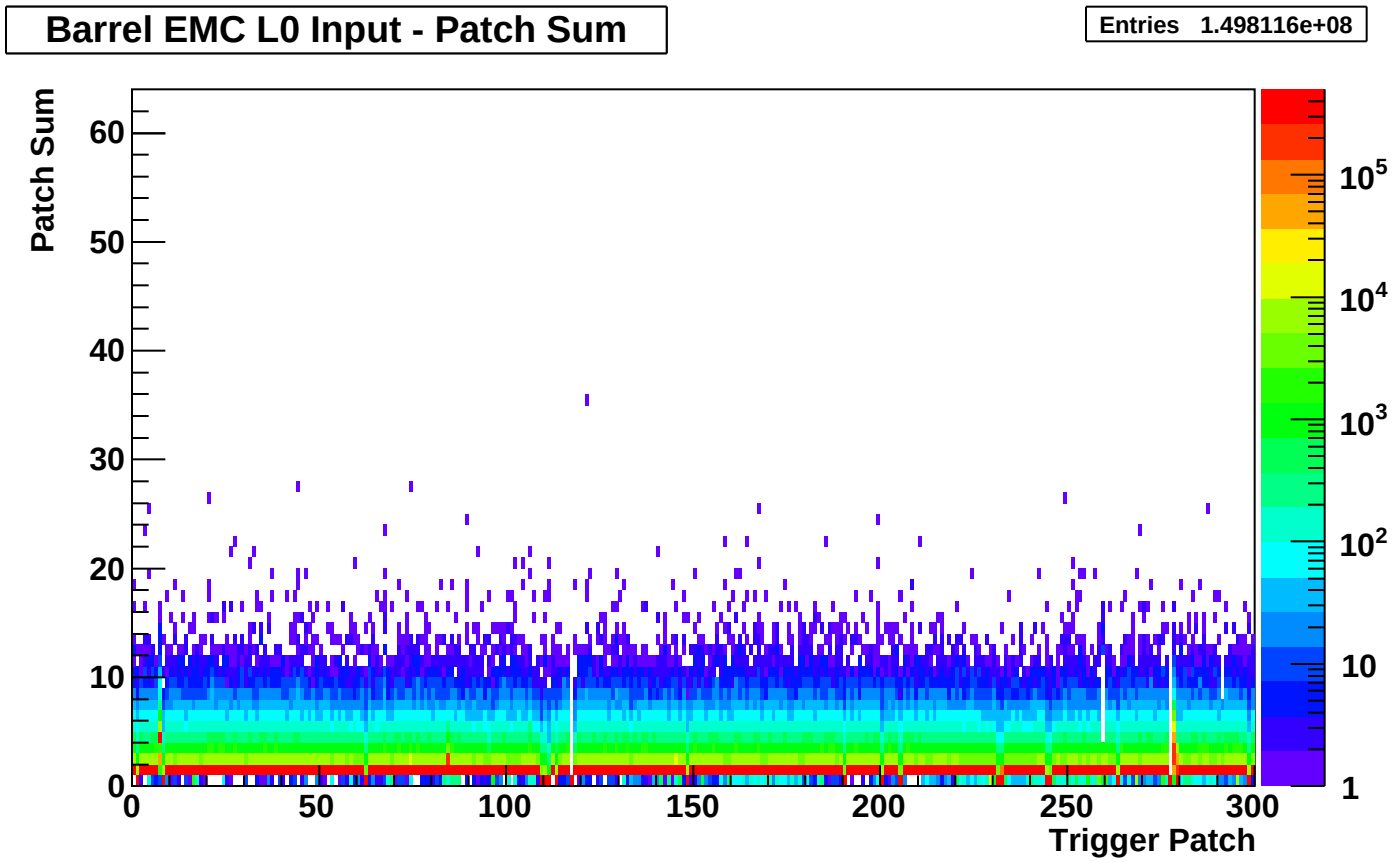

TPC Readout Mask (simulated)
Entries 435

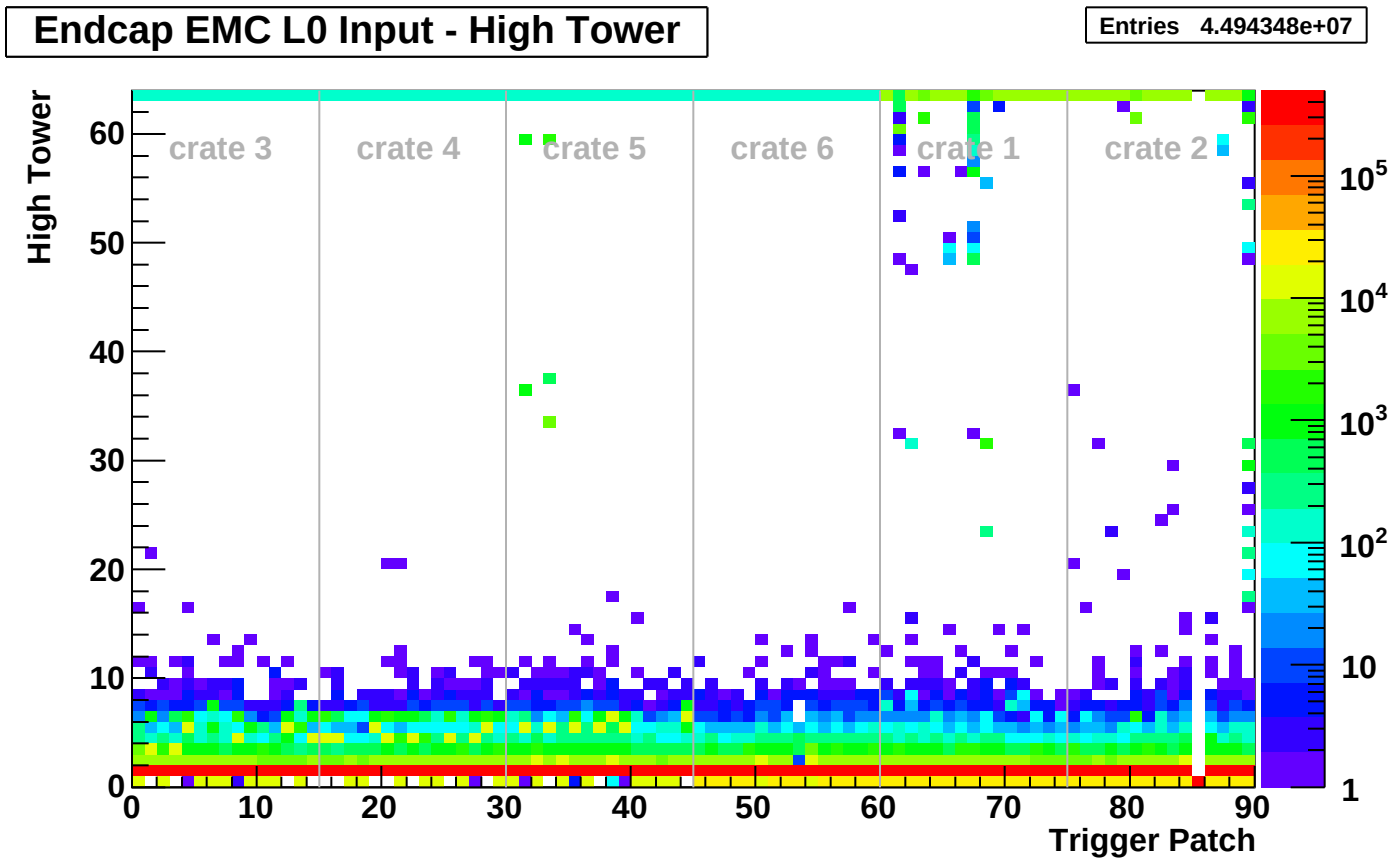

BunchId7Bit (all events)
Entries 499372

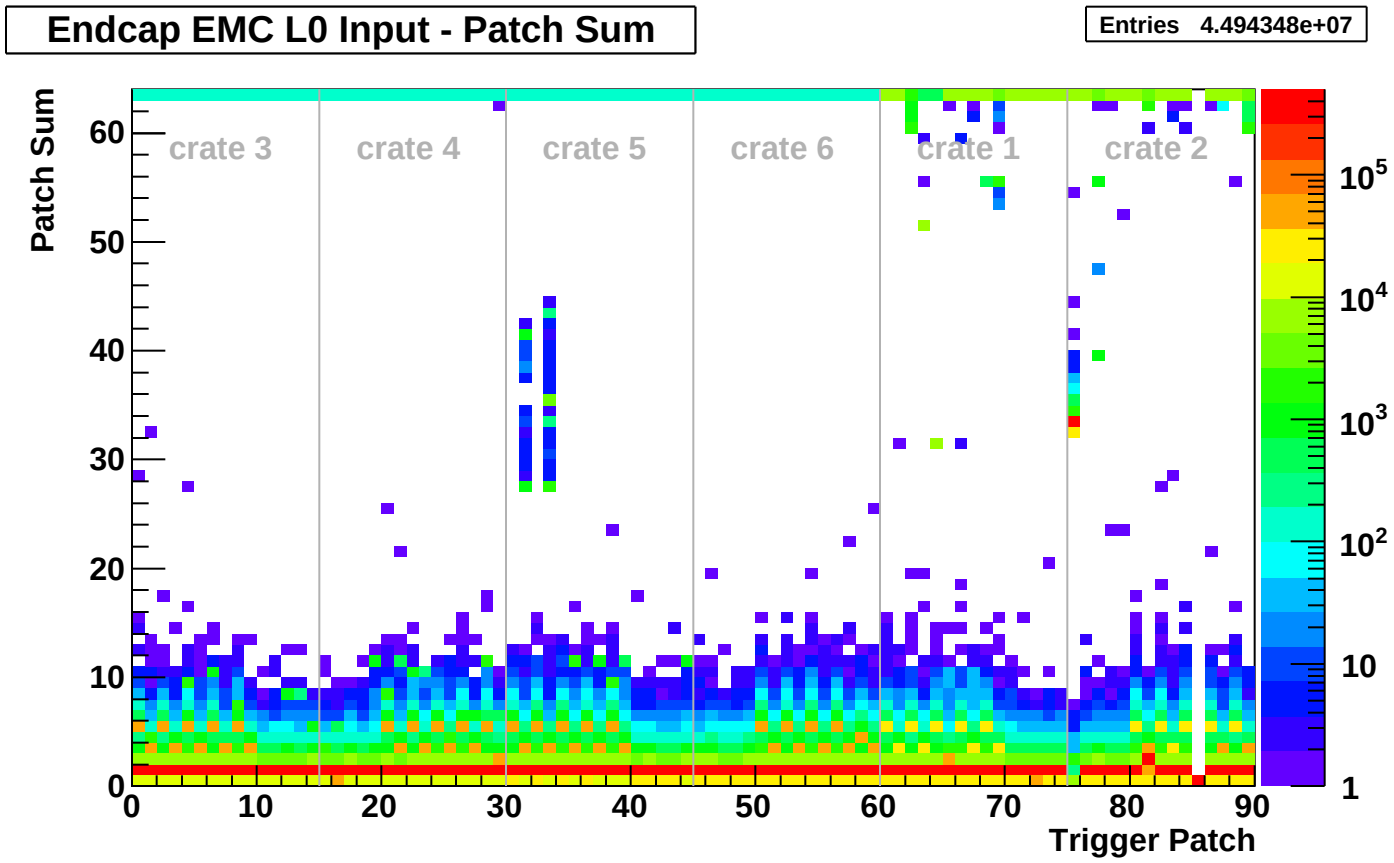

Abort-gaps
Entries 1369

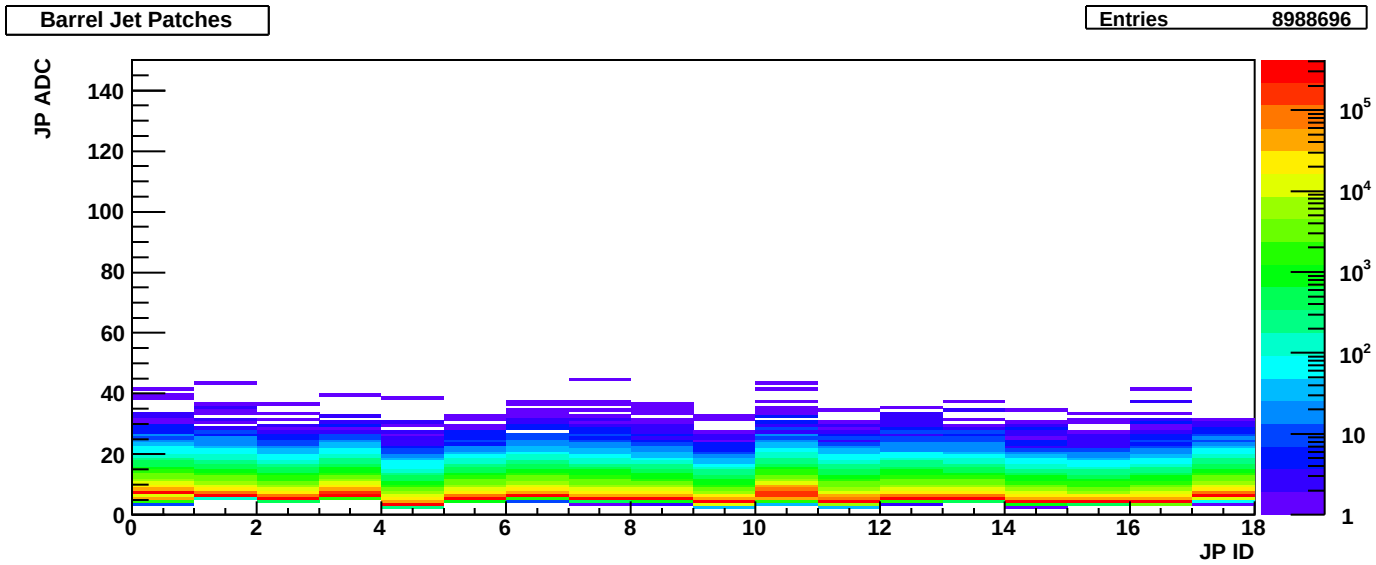

!Yellow-Fill
Entries 18375

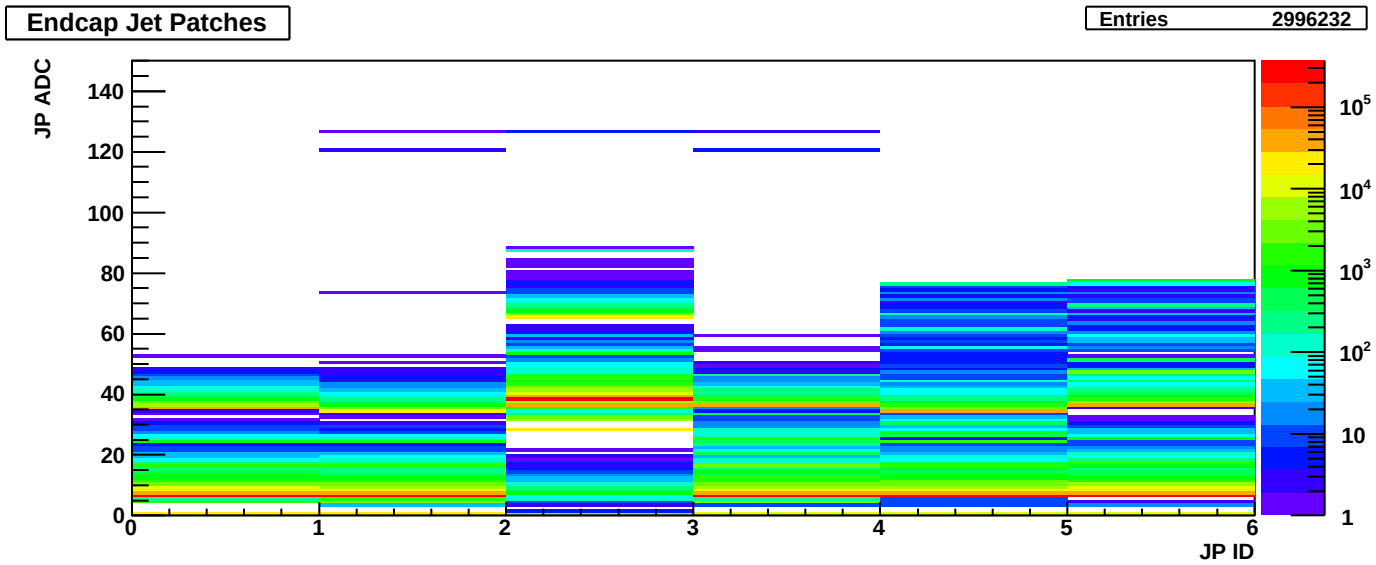

Barrel EMC L0 Input - High Tower
Entries 1.498116e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

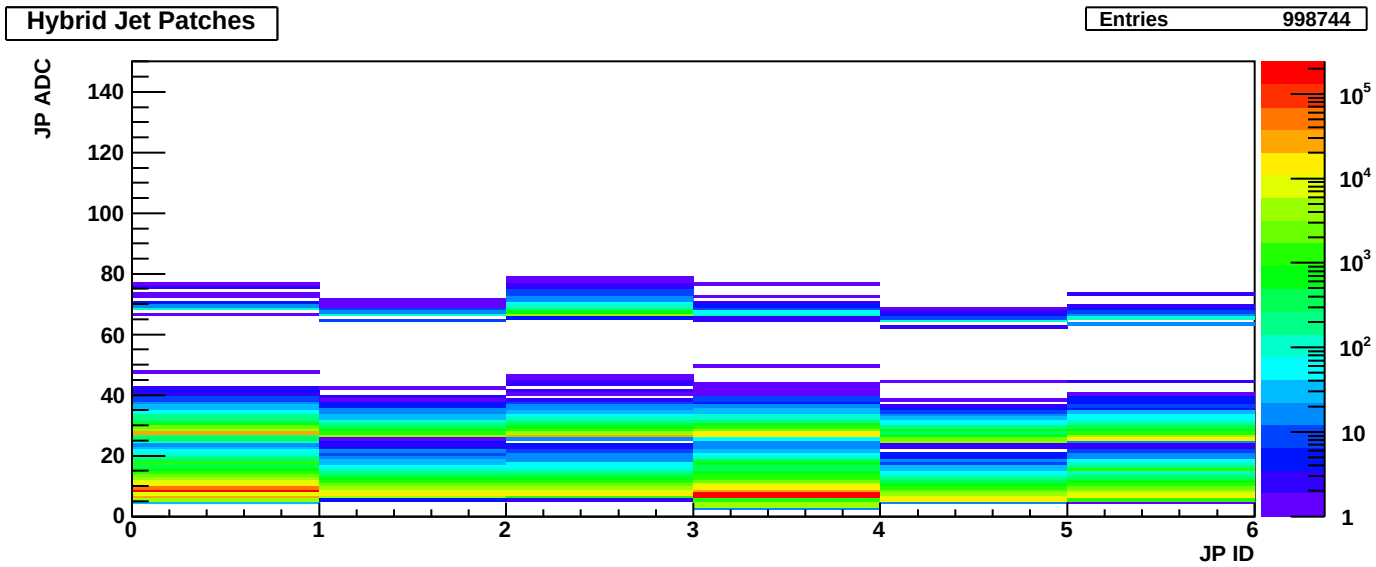

Barrel EMC L0 Input - Patch Sum
Entries 1.498116e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

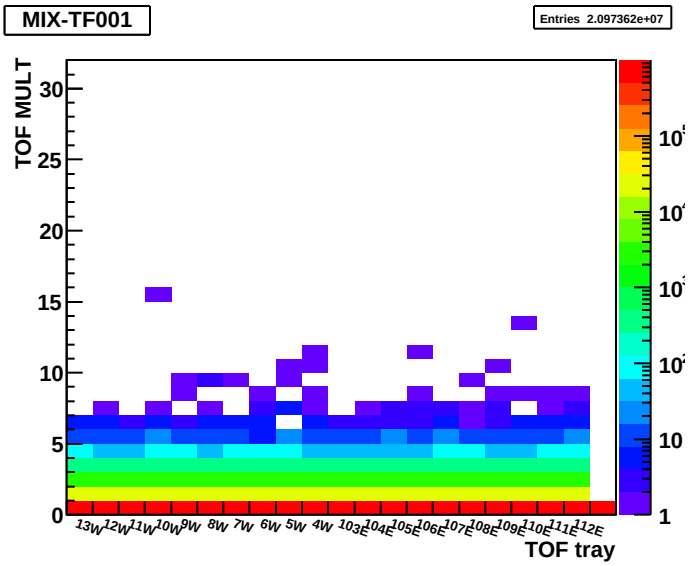

Endcap EMC L0 Input - High Tower
Entries 4.494348e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

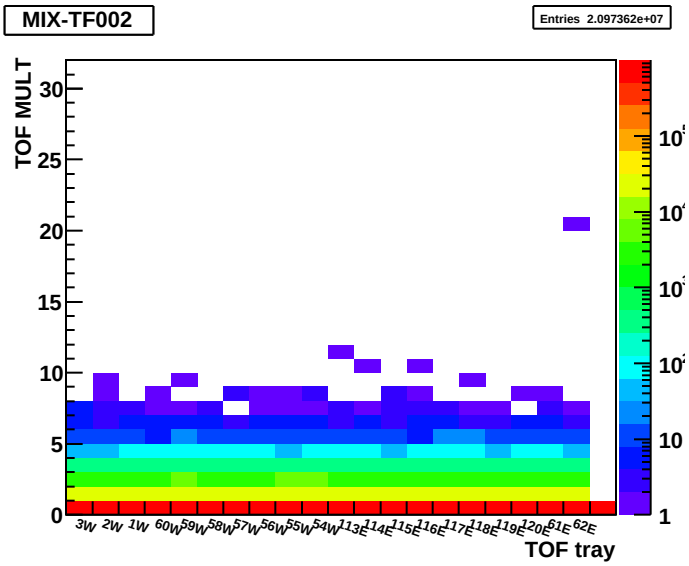

Endcap EMC L0 Input - Patch Sum
Entries 4.494348e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

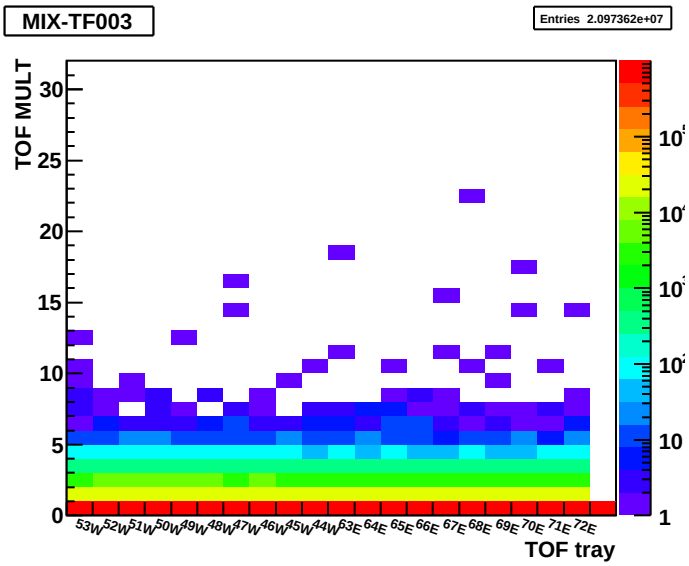

Barrel Jet Patches
Entries 8988696
JP ADC
JP ID

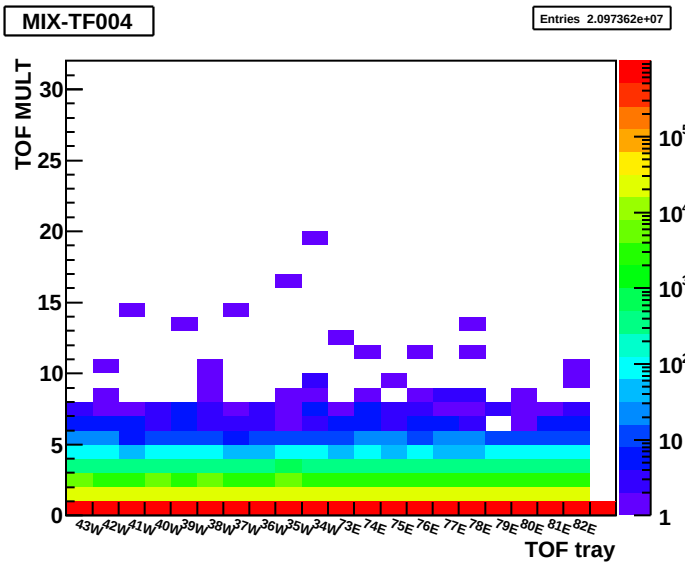

Endcap Jet Patches
Entries 2996232
JP ADC
JP ID

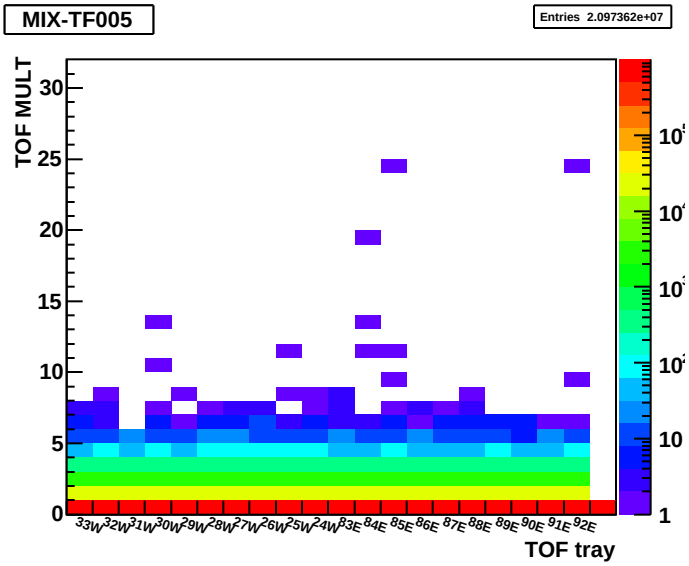

Hybrid Jet Patches
Entries 998744
JP ADC
JP ID

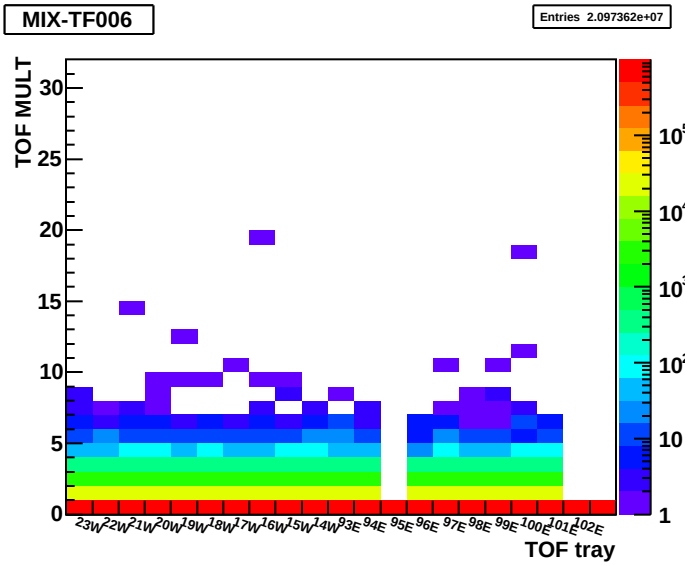

MIX-TF001
Entries 2.097362e+07
TOF MULT
TOF tray

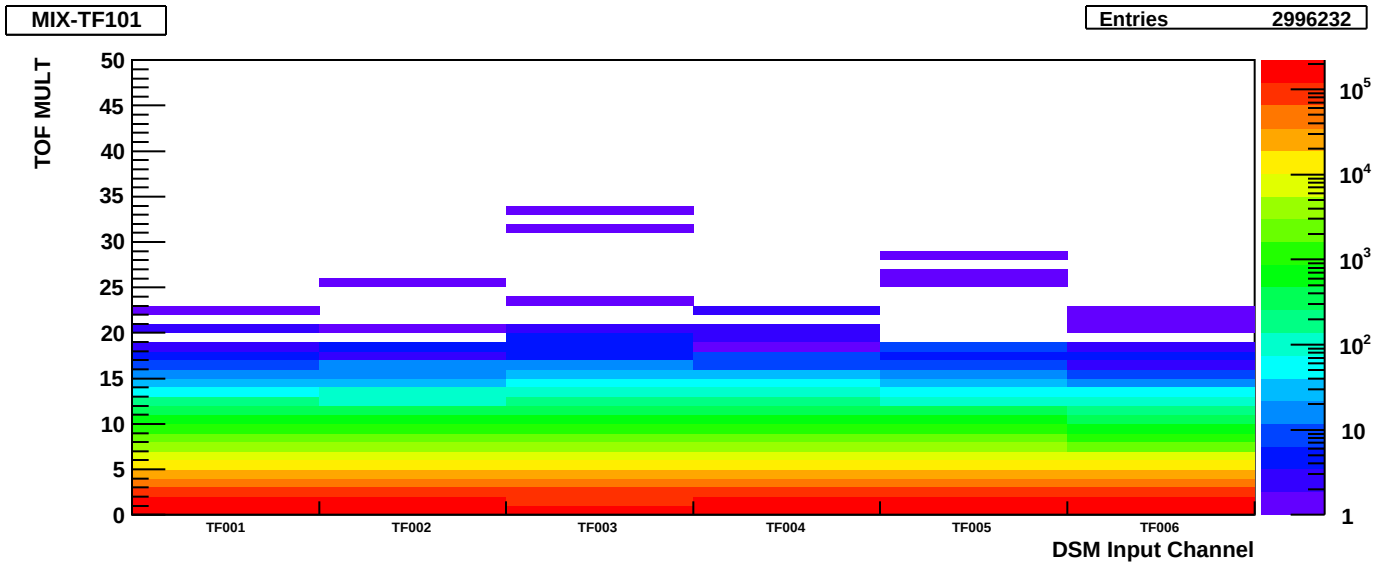

MIX-TF002
Entries 2.097362e+07
TOF MULT
TOF tray

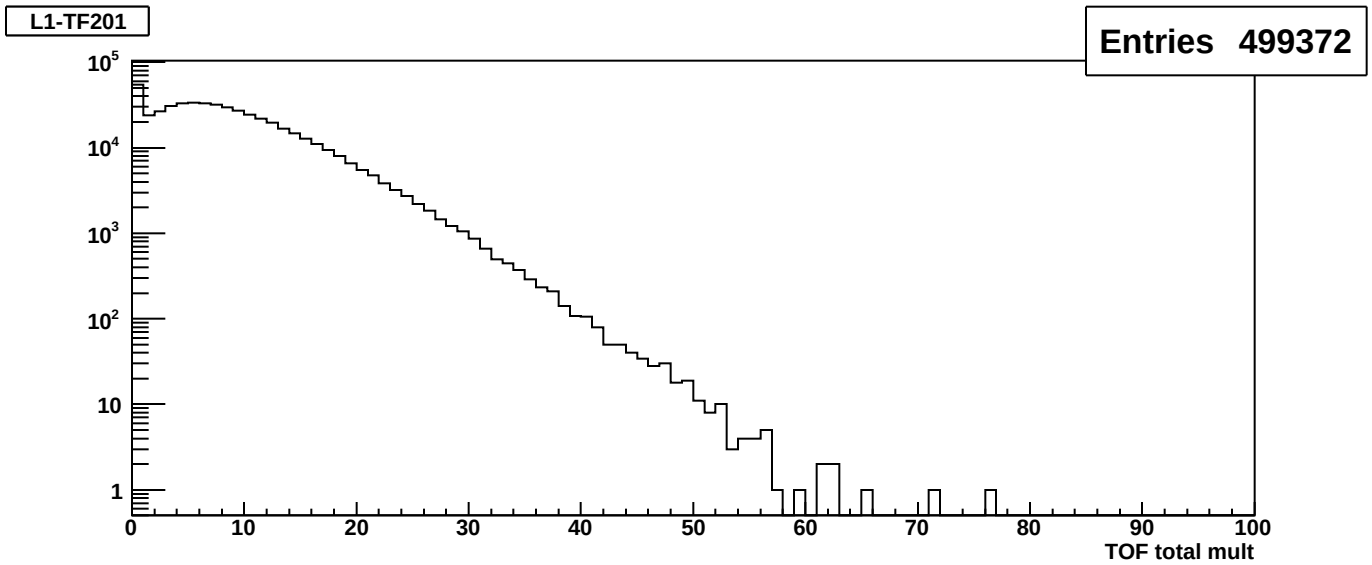

MIX-TF003
Entries 2.097362e+07
TOF MULT
TOF tray

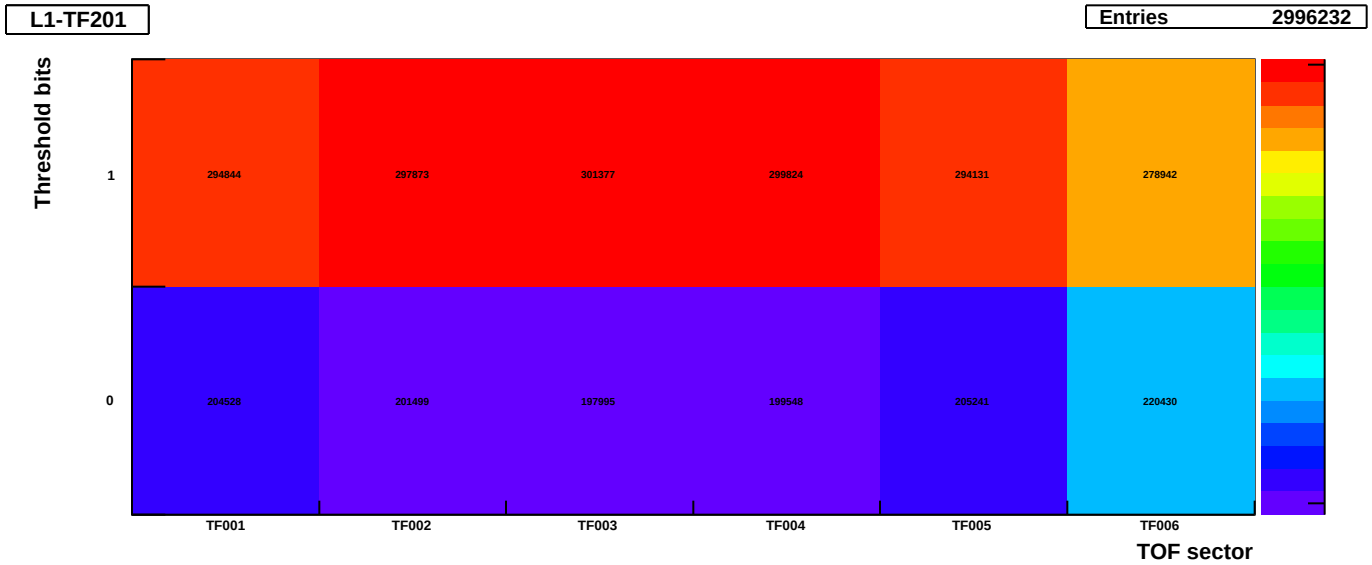

MIX-TF004
Entries 2.097362e+07
TOF MULT
TOF tray

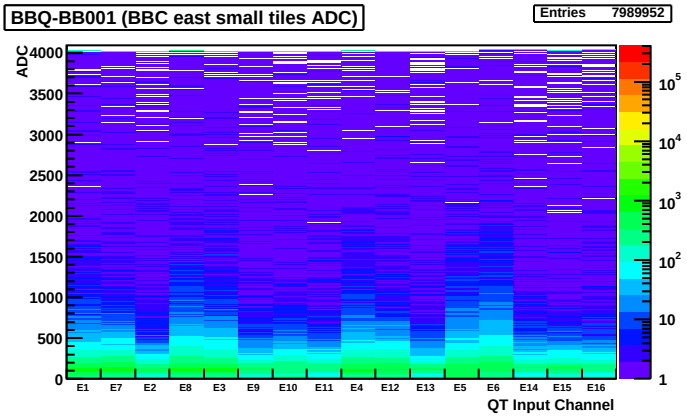

MIX-TF005
Entries 2.097362e+07
TOF MULT
TOF tray

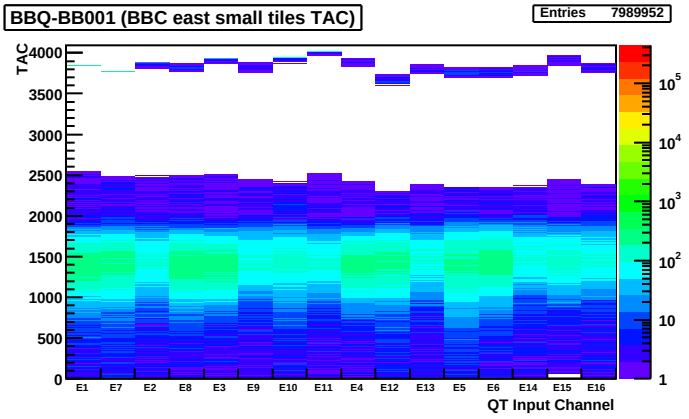

MIX-TF006
Entries 2.097362e+07
TOF MULT
TOF tray

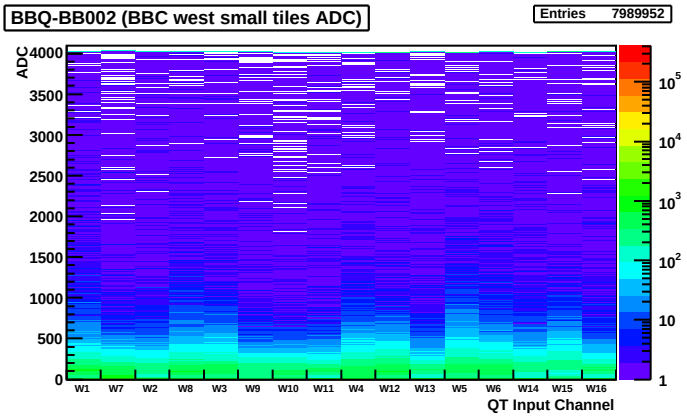

MIX-TF101
Entries 2996232
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

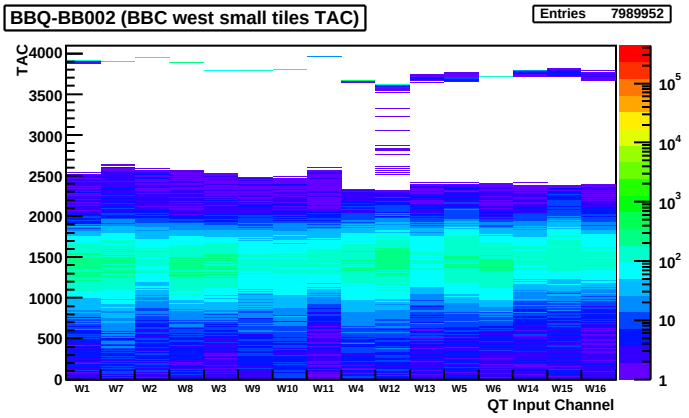

L1-TF201
Entries 499372
TOF total mult

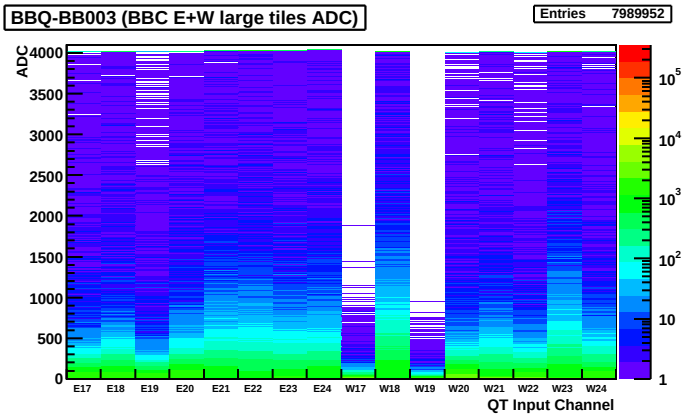

L1-TF201
Entries 2996232
Threshold bits
294844
297873
301377
299824
294131
278942
204528
201499
197995
199548
205241
220430
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

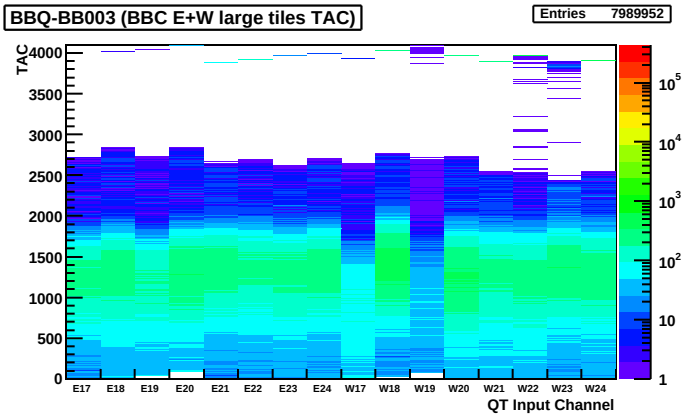

BBQ-BB001 (BBC east small tiles ADC)
Entries 7989952
ADC
QT Input Channel

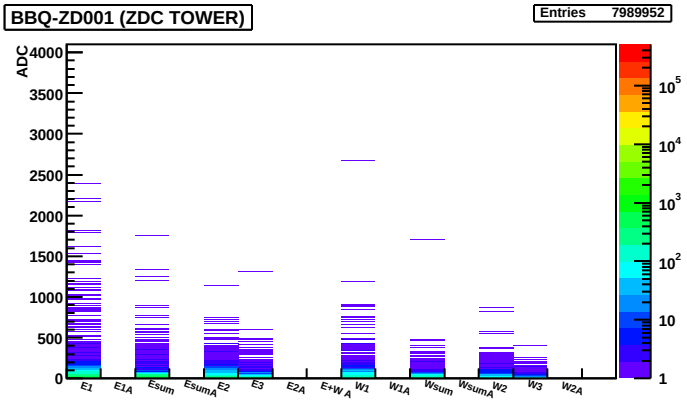

BBQ-BB001 (BBC east small tiles TAC)
Entries 7989952
TAC
QT Input Channel

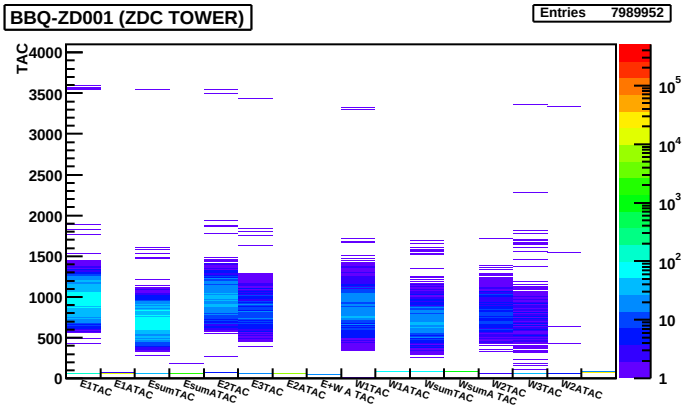

BBQ-BB002 (BBC west small tiles ADC)
Entries 7989952
ADC
QT Input Channel

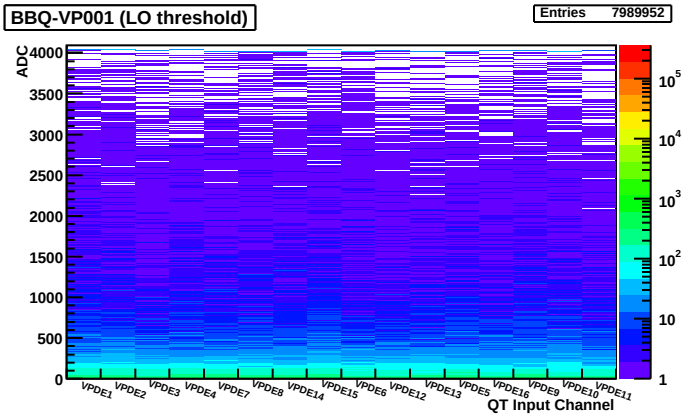

BBQ-BB002 (BBC west small tiles TAC)
Entries 7989952
TAC
QT Input Channel

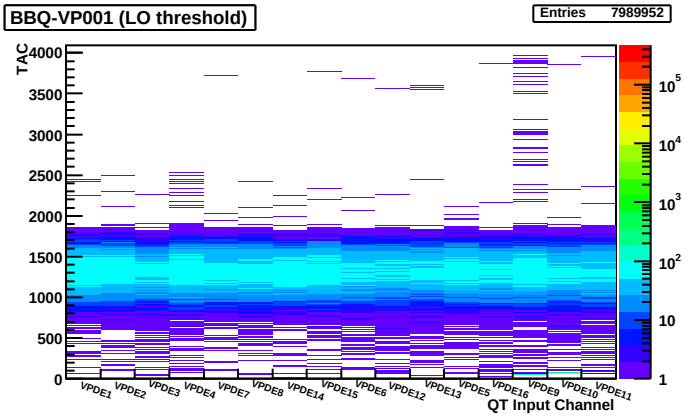

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7989952
ADC
QT Input Channel

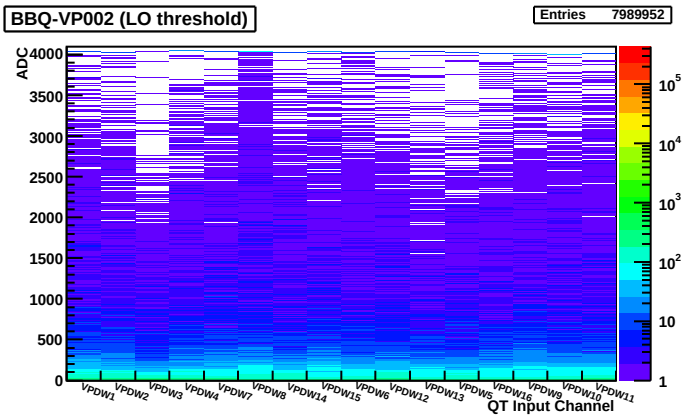

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7989952
TAC
QT Input Channel

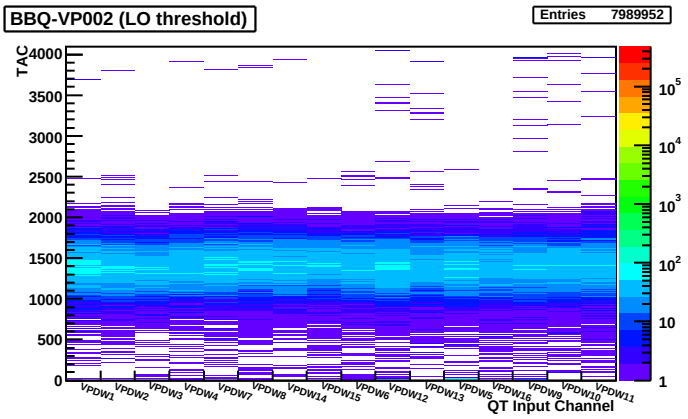

BBQ-ZD001 (ZDC TOWER)
Entries 7989952
ADC

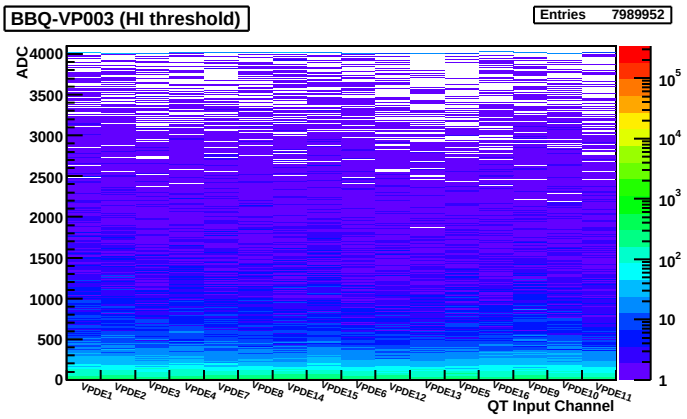

BBQ-ZD001 (ZDC TOWER)
Entries 7989952
TAC

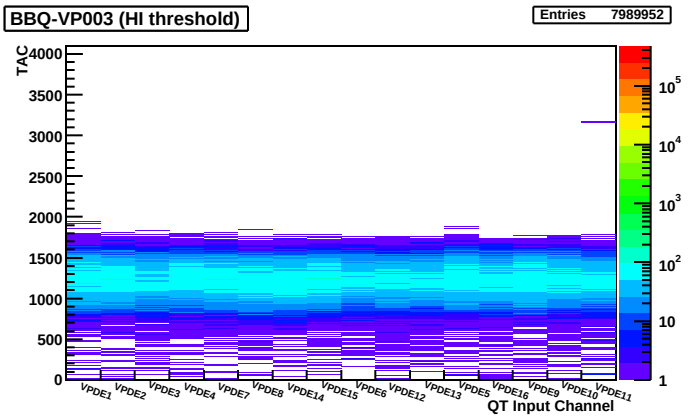

BBQ-VP001 (LO threshold)
Entries 7989952
ADC
QT Input Channel

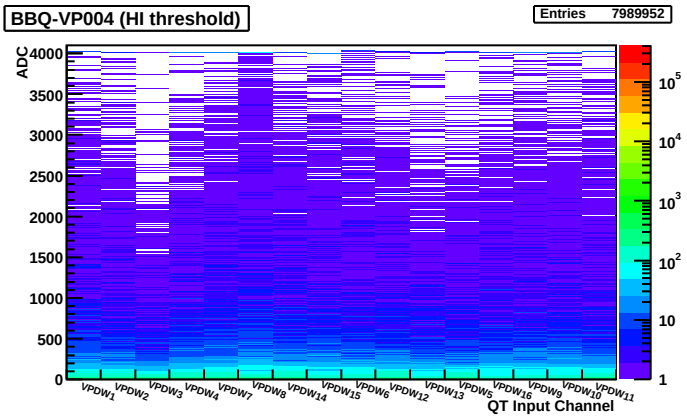

BBQ-VP001 (LO threshold)
Entries 7989952
TAC
QT Input Channel

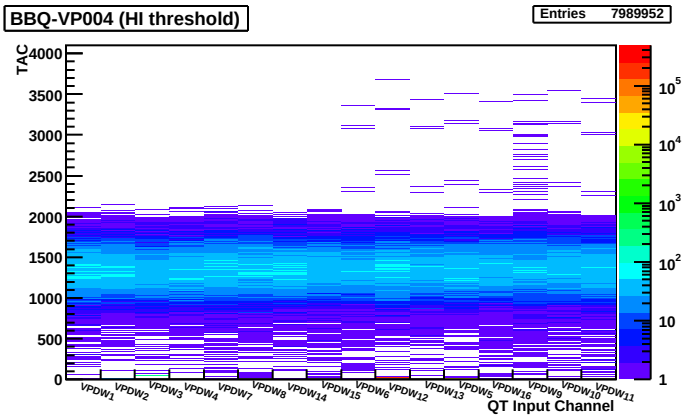

BBQ-VP002 (LO threshold)
Entries 7989952
ADC
QT Input Channel

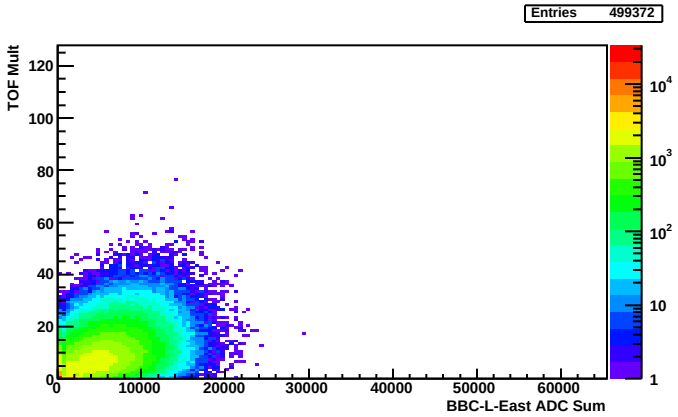

BBQ-VP002 (LO threshold)
Entries 7989952
TAC
QT Input Channel

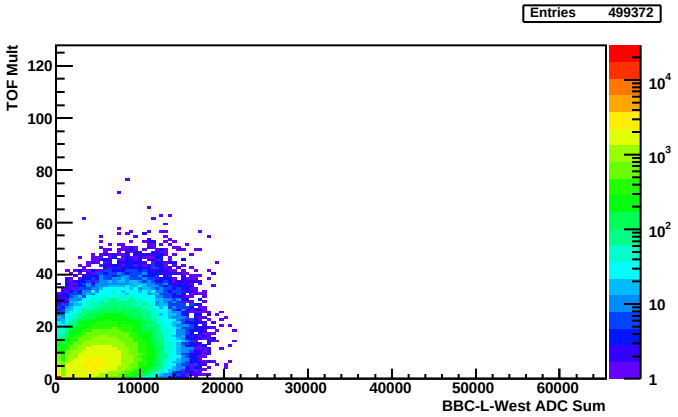

BBQ-VP003 (HI threshold)
Entries 7989952
ADC
QT Input Channel

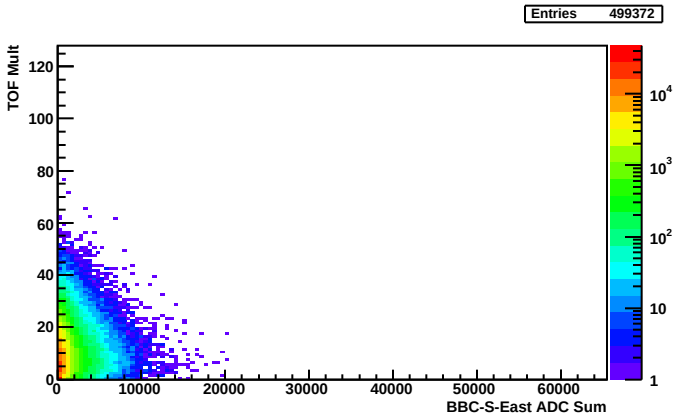

BBQ-VP003 (HI threshold)
Entries 7989952
TAC
QT Input Channel

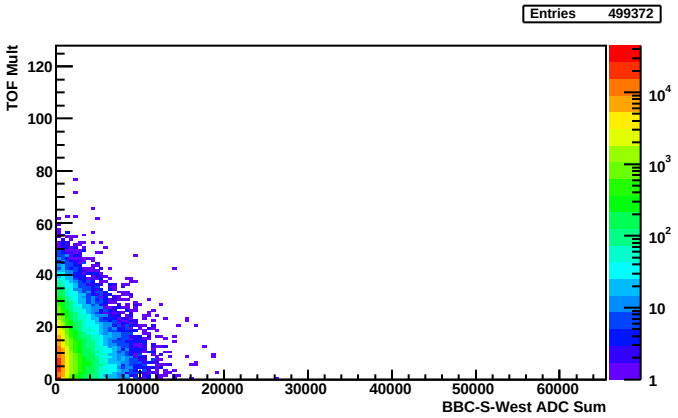

BBQ-VP004 (HI threshold)
Entries 7989952
ADC
QT Input Channel

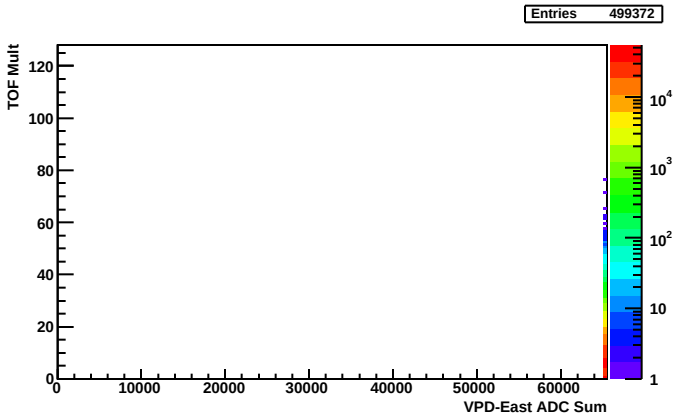

BBQ-VP004 (HI threshold)
Entries 7989952
TAC
QT Input Channel

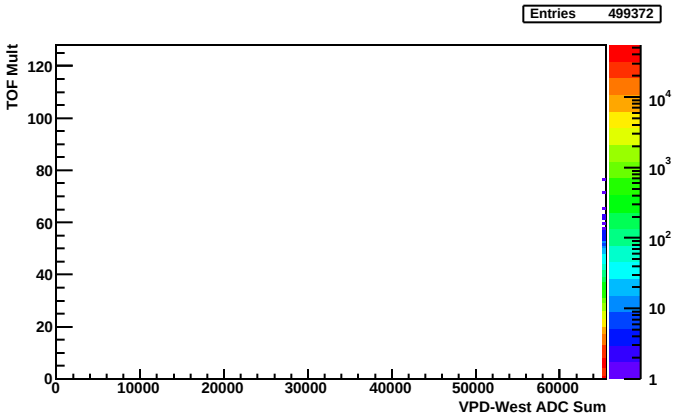

Entries 499372
TOF Mult
BBC-L-East ADC Sum

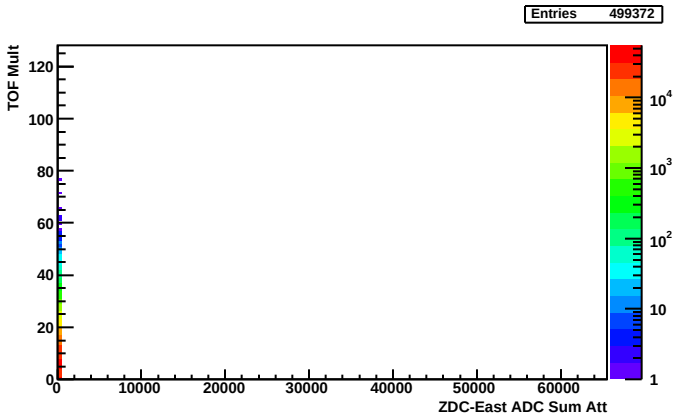

Entries 499372
TOF Mult
BBC-L-West ADC Sum

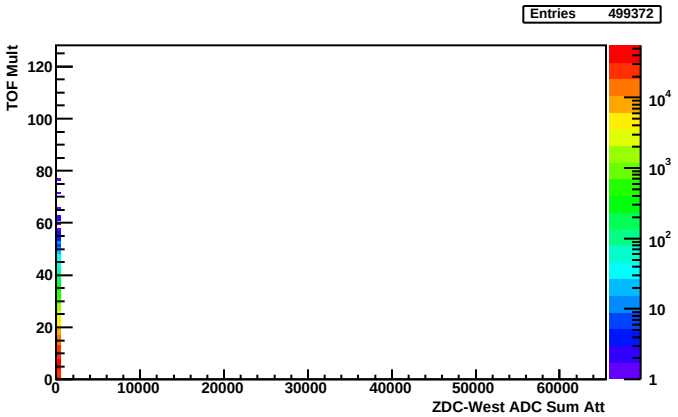

Entries 499372
TOF Mult
BBC-S-East ADC Sum

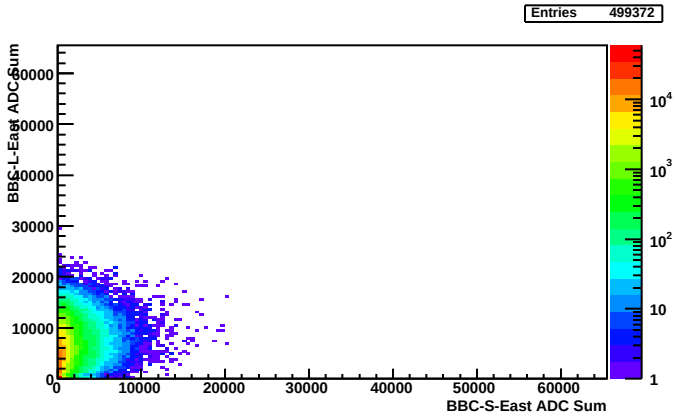

Entries 499372
TOF Mult
BBC-S-West ADC Sum

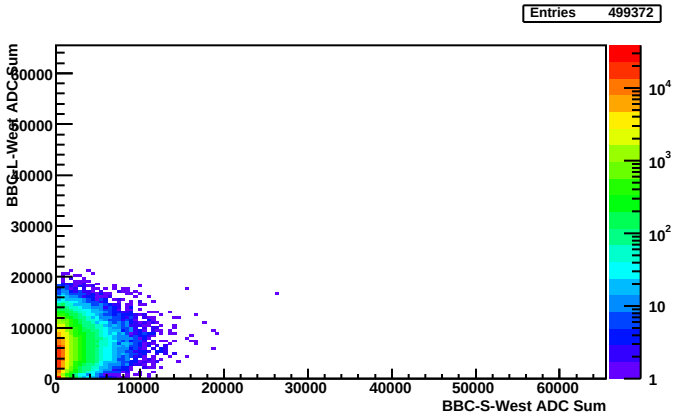

Entries 499372
TOF Mult
VPD-East ADC Sum

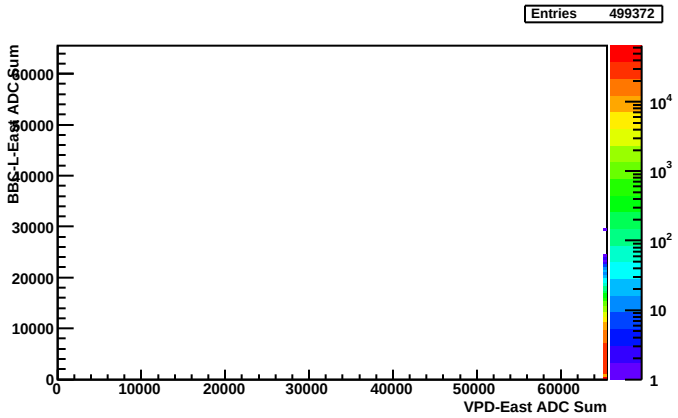

Entries 499372
TOF Mult
VPD-West ADC Sum

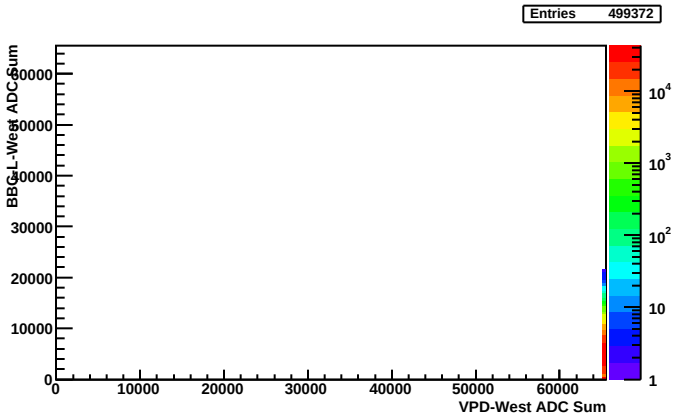

Entries 499372
TOF Mult
ZDC-East ADC Sum Att

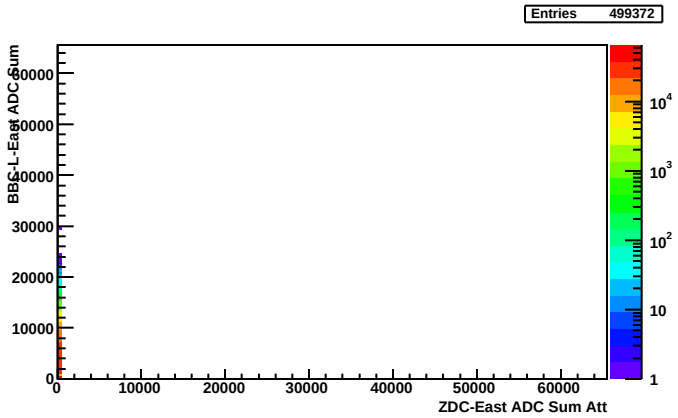

Entries 499372
TOF Mult
ZDC-West ADC Sum Att

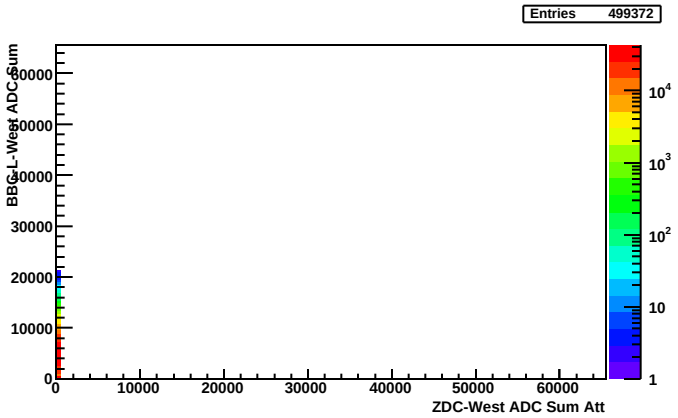

Entries 499372
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 499372
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 499372
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 499372
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 499372
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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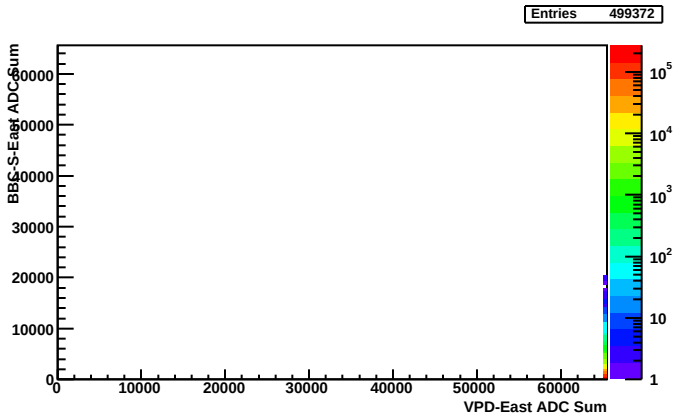
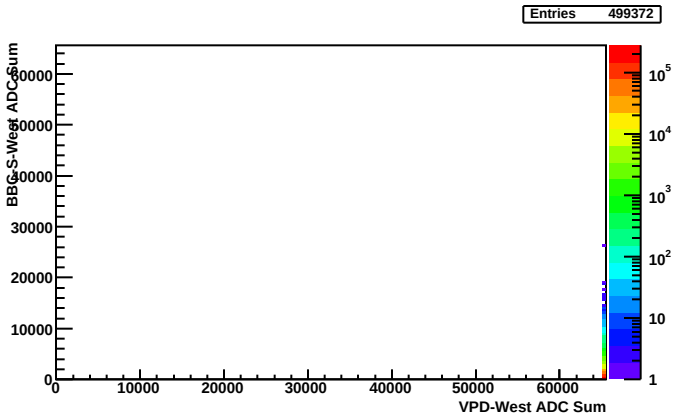
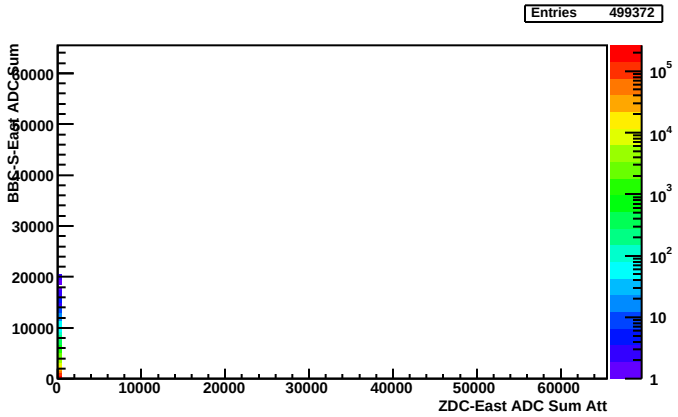
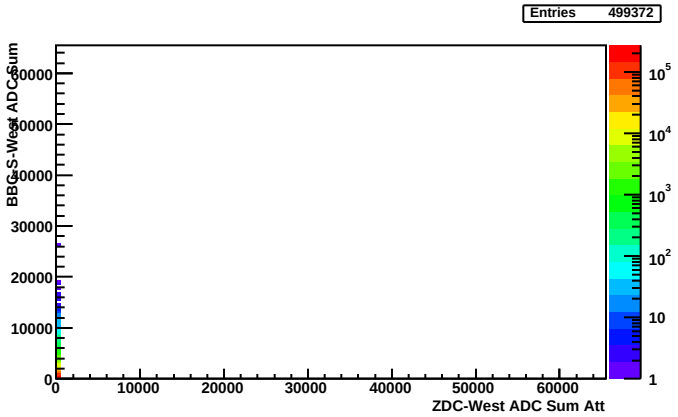
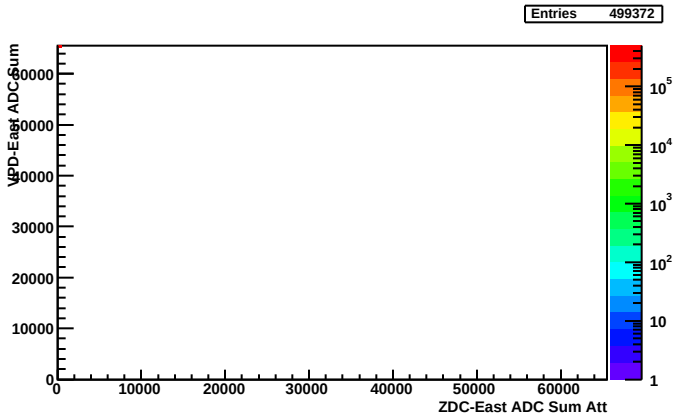
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

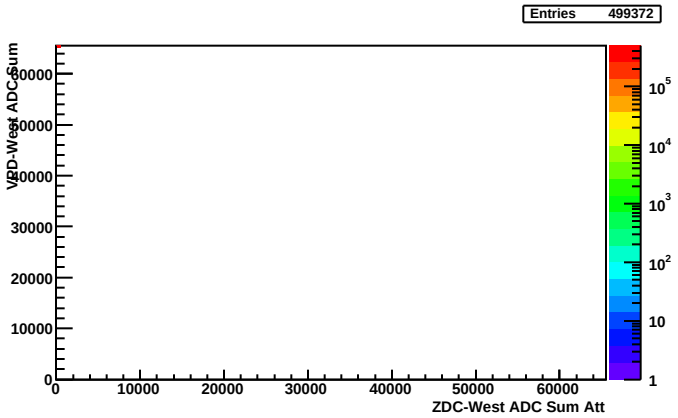

Entries 499372
BBC-S-East ADC Sum
VPD-East ADC Sum

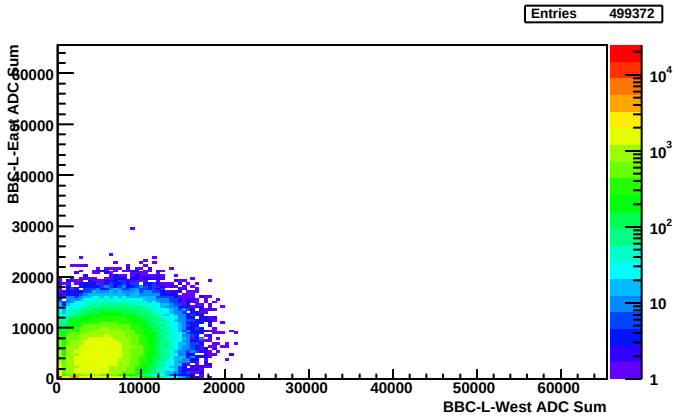

Entries 499372
BBC-S-West ADC Sum
VPD-West ADC Sum

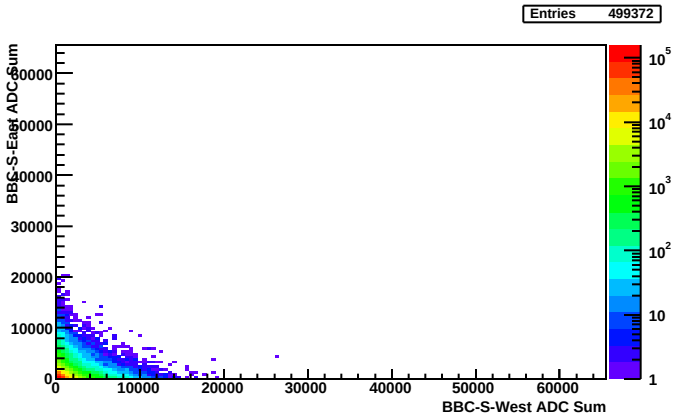

Entries 499372
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

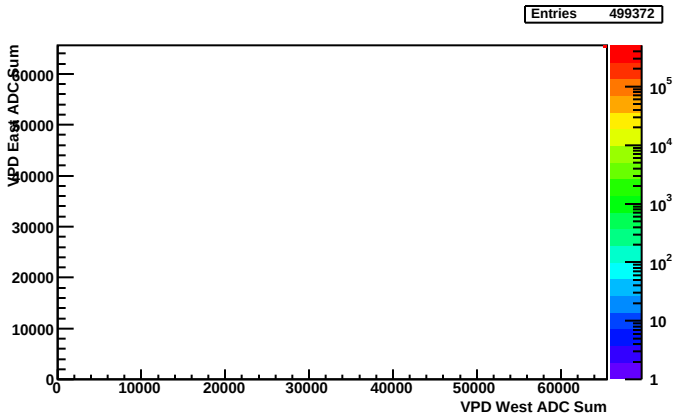

Entries 499372
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

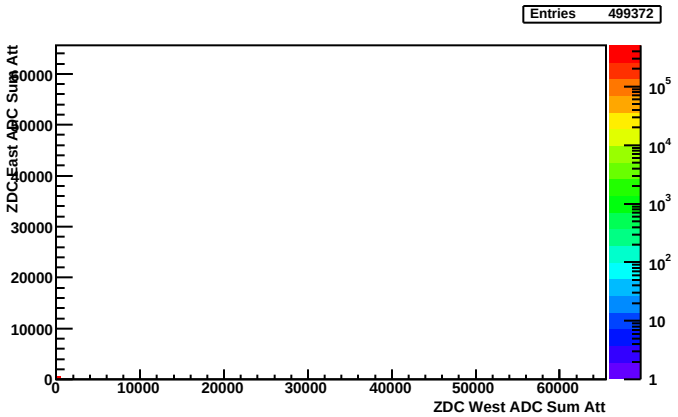

Entries 499372
VPD-East ADC Sum
ZDC-East ADC Sum Att

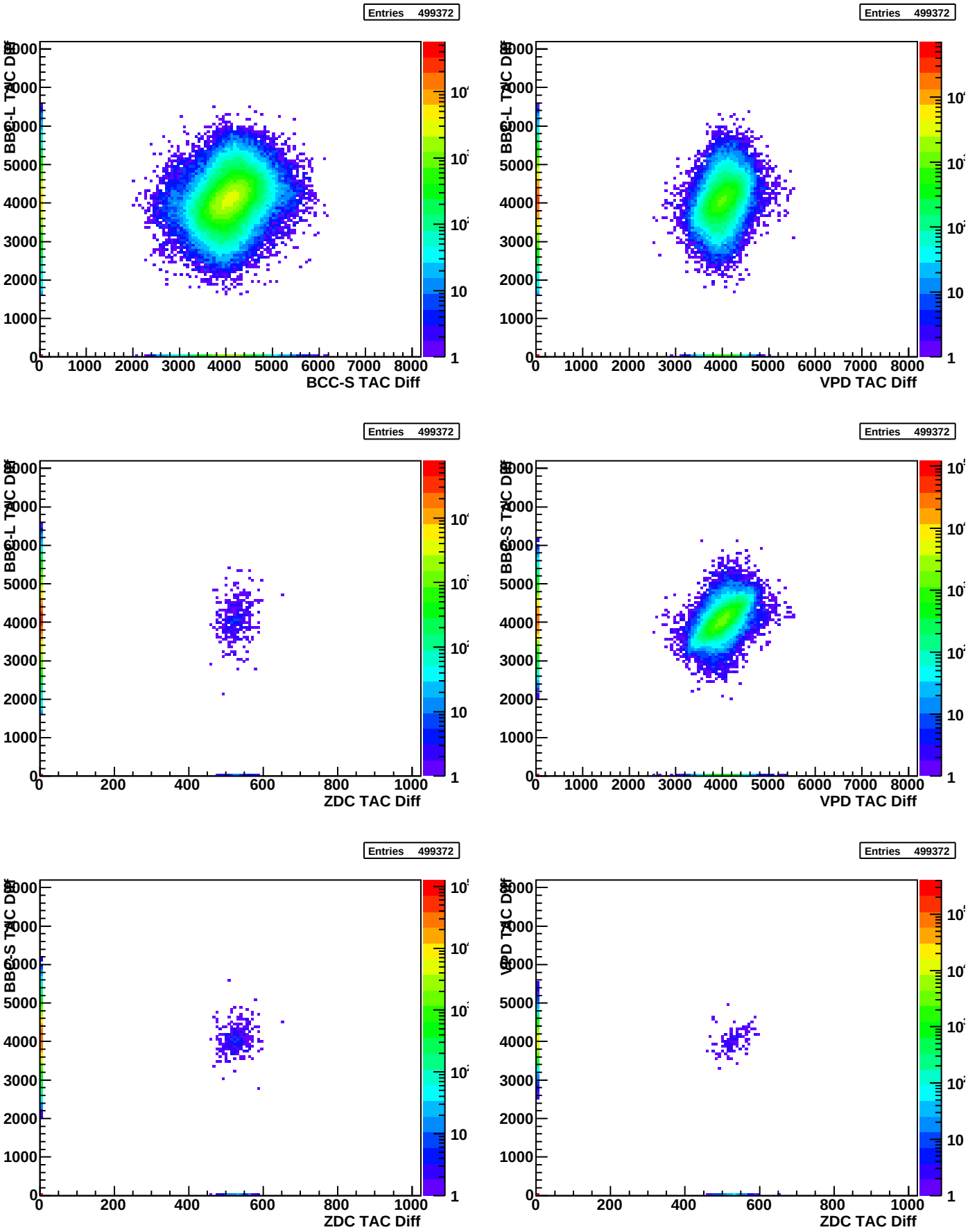

Entries 499372
VPD-West ADC Sum
ZDC-West ADC Sum Att

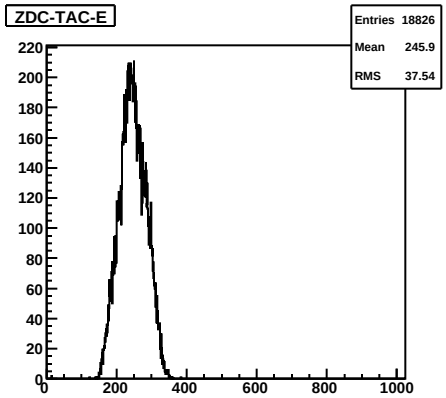

Entries 499372
BBC-L-East ADC Sum
BBC-L-West ADC Sum

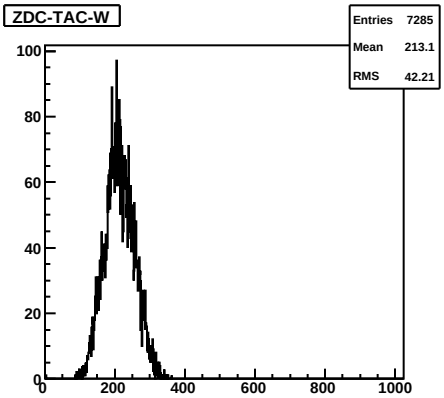

Entries 499372
BBC-S-East ADC Sum
BBC-S-West ADC Sum

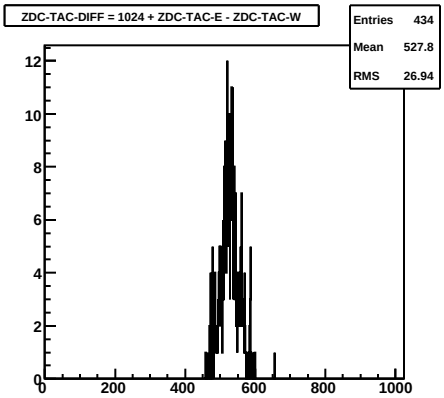

Entries 499372
VPD East ADC Sum
VPD West ADC Sum

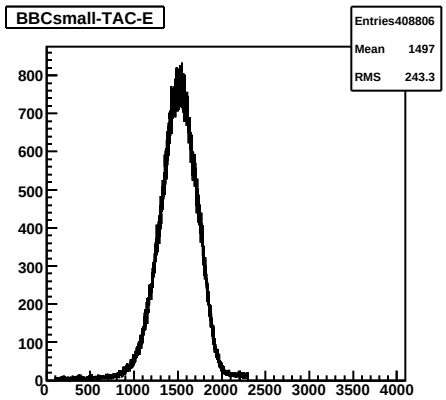

Entries 499372
ZDC East ADC Sum Att
ZDC West ADC Sum Att

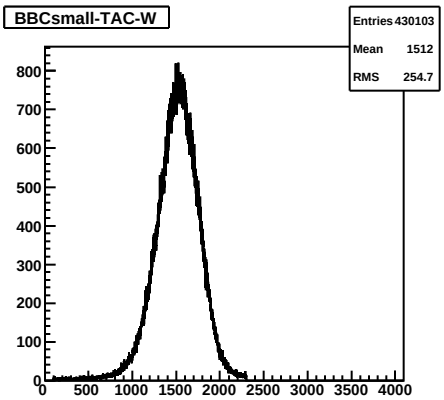

Entries 499372
BBC-L TAC Diff
BCC-S TAC Diff
Entries 499372
BBC-L TAC Diff
VPD TAC Diff
Entries 499372
BBC-L TAC Diff
ZDC TAC Diff
Entries 499372
BBC-S TAC Diff
VPD TAC Diff
Entries 499372
BBC-S TAC Diff
ZDC TAC Diff
Entries 499372
VPD TAC Diff
ZDC TAC Diff

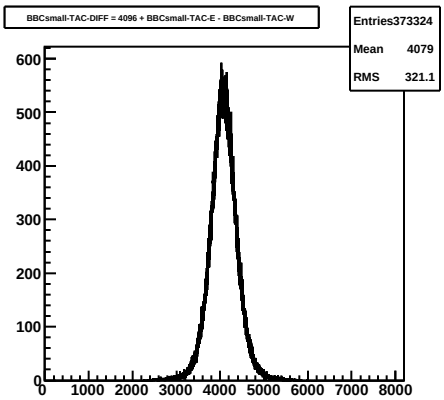

ZDC-TAC-E
Entries 18826
Mean 245.9
RMS 37.54

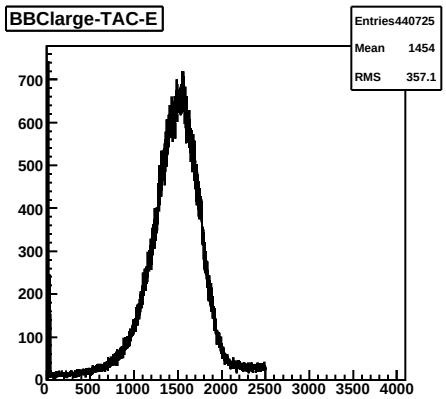

ZDC-TAC-W
Entries 7285
Mean 213.1
RMS 42.21

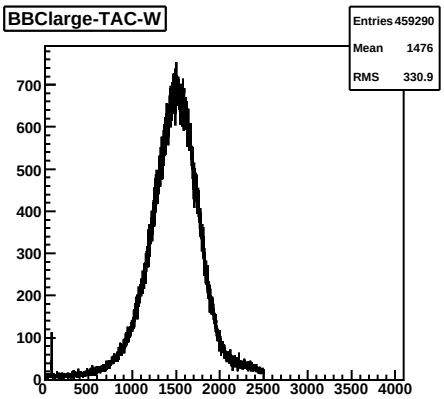

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 434
Mean 527.8
RMS 26.94

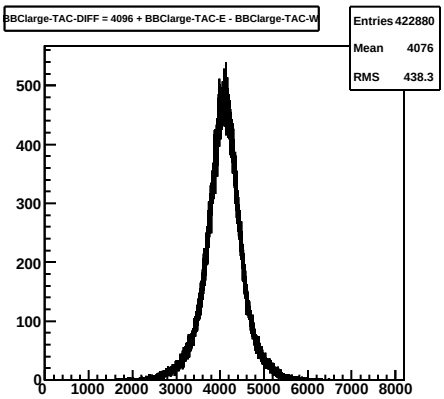

BBCsmall-TAC-E
Entries 408806
Mean 1497
RMS 243.3

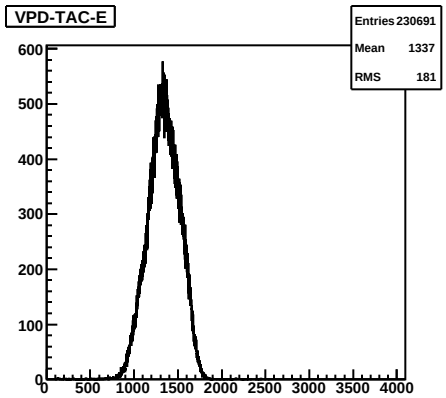

BBCsmall-TAC-W
Entries 430103
Mean 1512
RMS 254.7

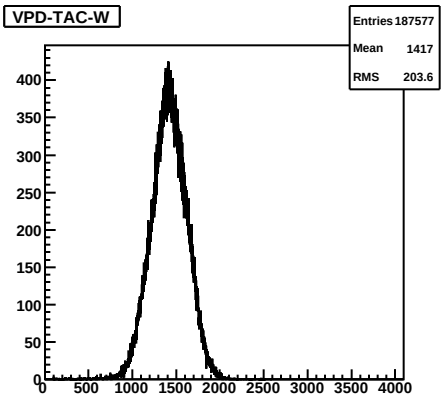

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 373324
Mean 4079
RMS 321.1

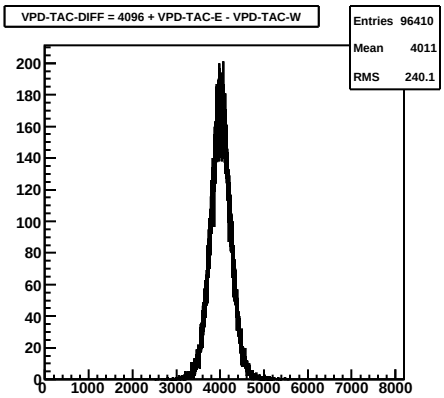

BBClarge-TAC-E
Entries 440725
Mean 1454
RMS 357.1

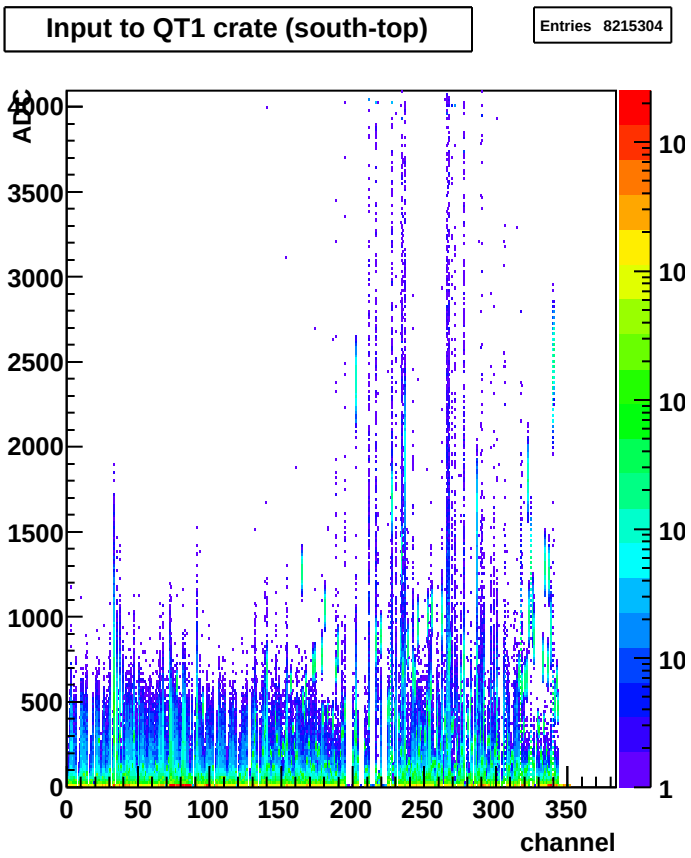

BBClarge-TAC-W
Entries 459290
Mean 1476
RMS 330.9

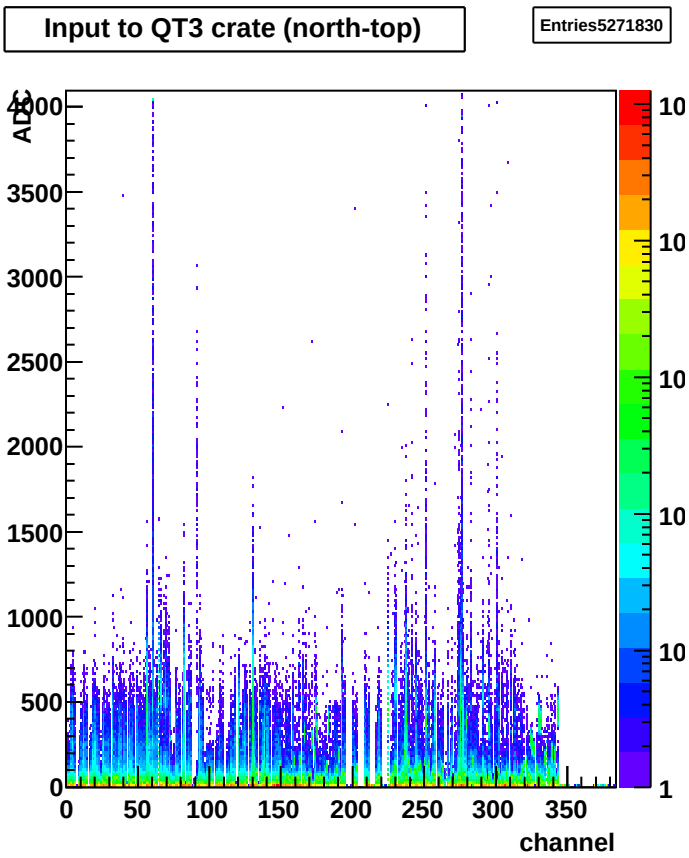

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 422880
Mean 4076
RMS 438.3

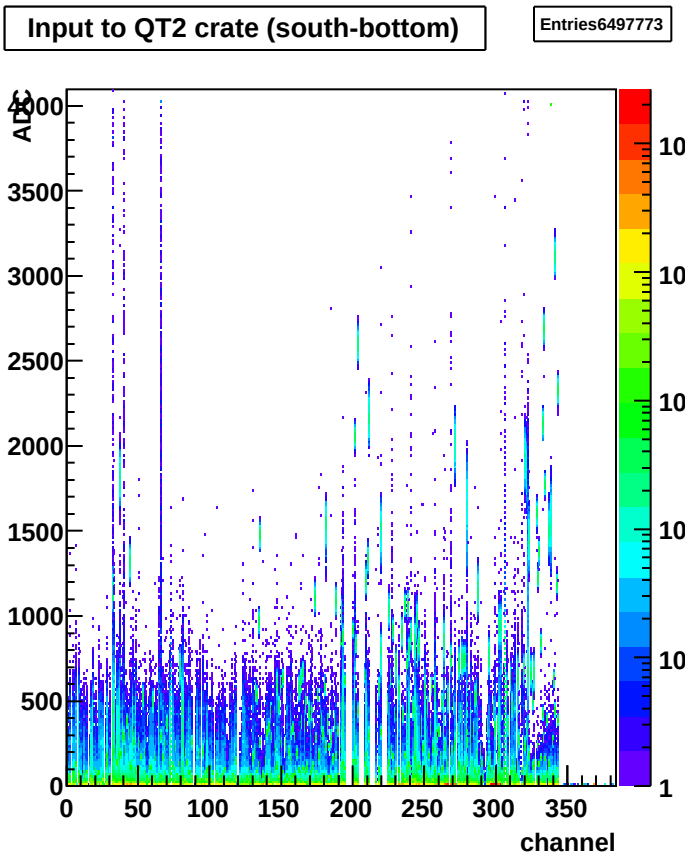

VPD-TAC-E
Entries 230691
Mean 1337
RMS 181

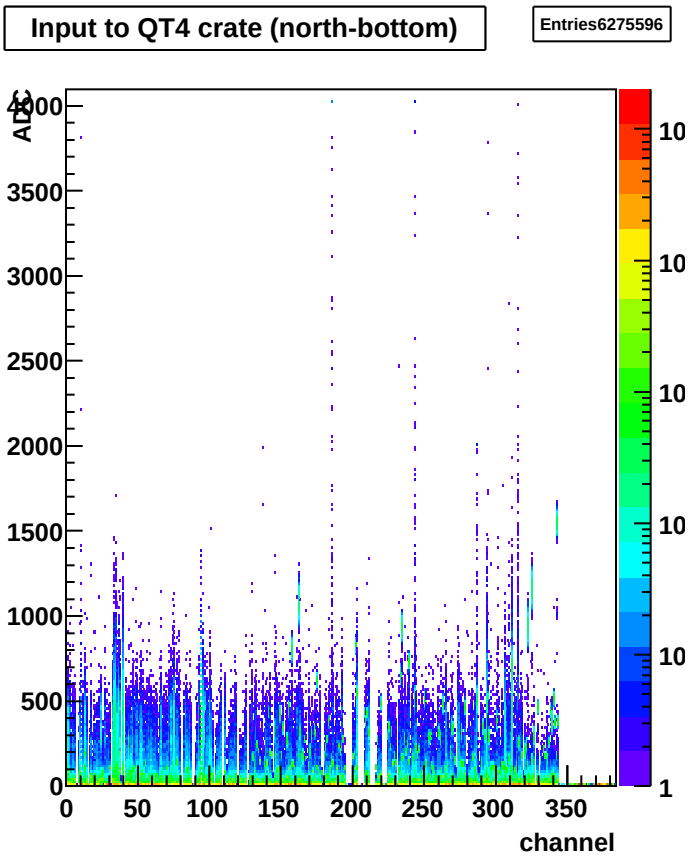

VPD-TAC-W
Entries 187577
Mean 1417
RMS 203.6

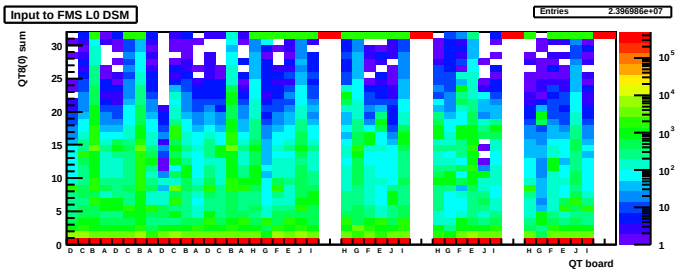

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 96410
Mean 4011
RMS 240.1

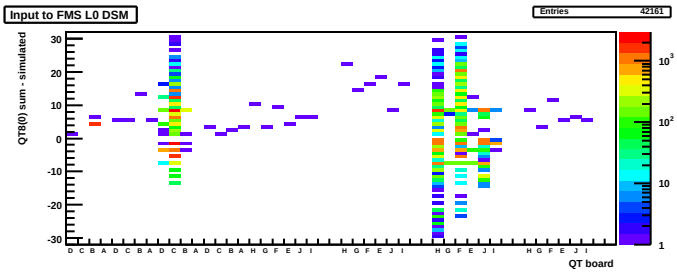

Input to QT1 crate (south-top)
Entries 8215304
ADC
channel

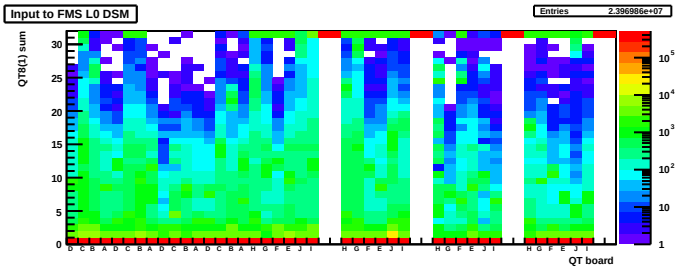

Input to QT3 crate (north-top)
Entries5271830
ADC
channel

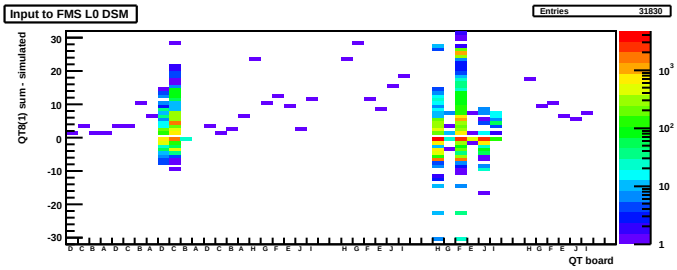

Input to QT2 crate (south-bottom)
Entries6497773
ADC
channel

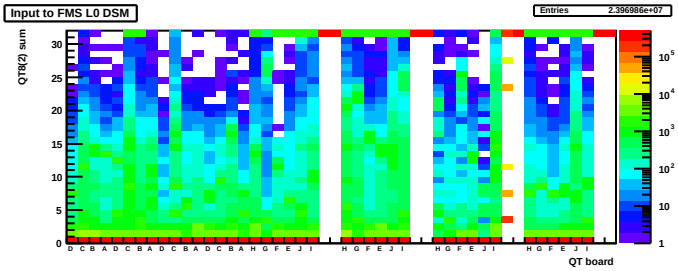

Input to QT4 crate (north-bottom)
Entries6275596
ADC
channel

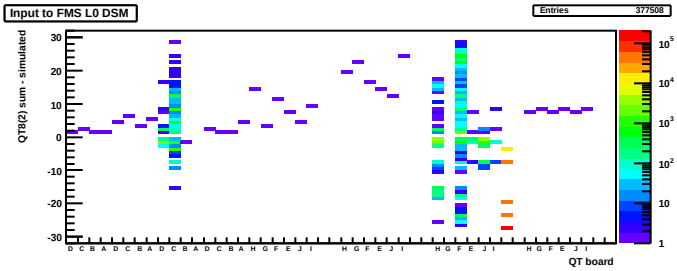

Input to FMS L0 DSM
Entries 2.396986e+07
QT8(0) sum
QT board

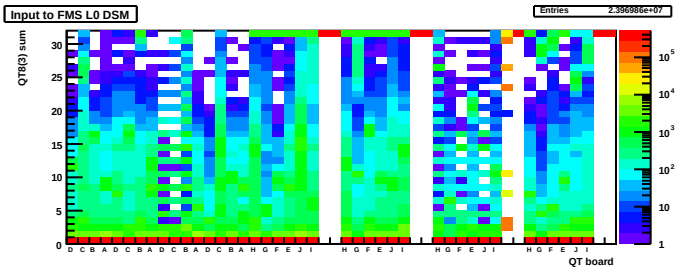

Input to FMS L0 DSM
Entries 42161
QT8(0) sum - simulated
QT board

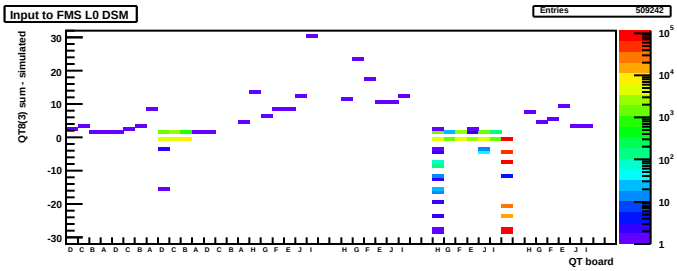

Input to FMS L0 DSM
Entries 2.396986e+07
QT8(1) sum
QT board

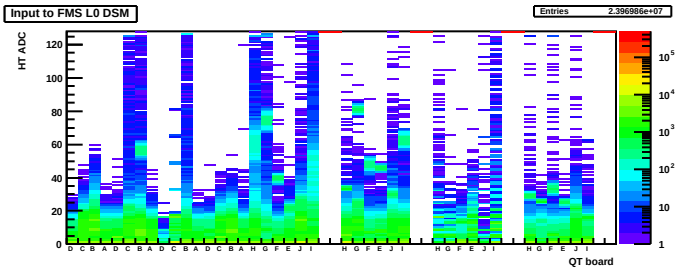

Input to FMS L0 DSM
Entries 31830
QT8(1) sum - simulated
QT board

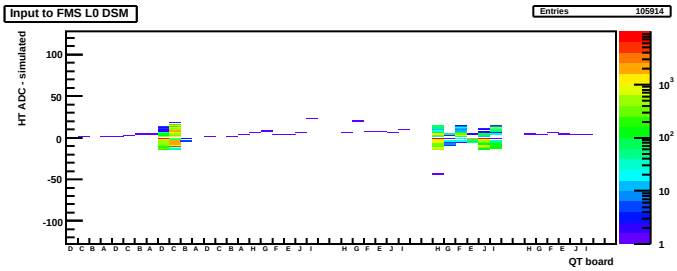

Input to FMS L0 DSM
Entries 2.396986e+07
QT8(2) sum
QT board

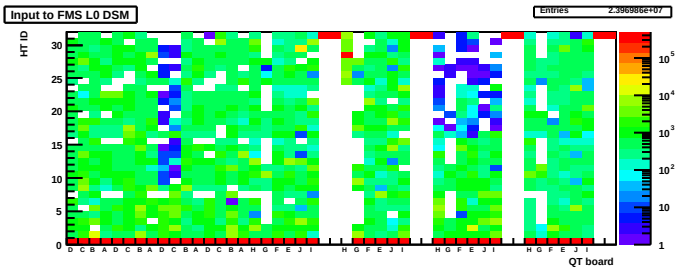

Input to FMS L0 DSM
Entries 377508
QT8(2) sum - simulated
QT board

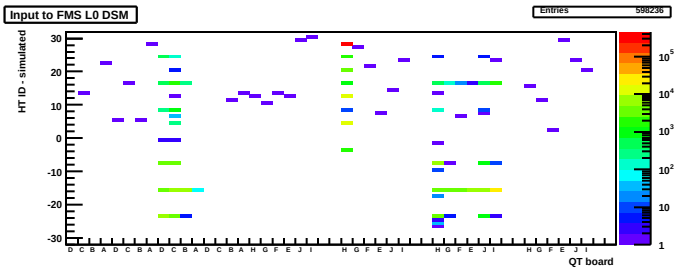

Input to FMS L0 DSM
Entries 2.396986e+07
QT8(3) sum
QT board

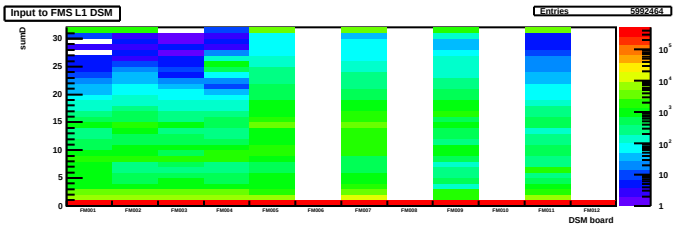

Input to FMS L0 DSM
Entries 509242
QT8(3) sum - simulated
QT board

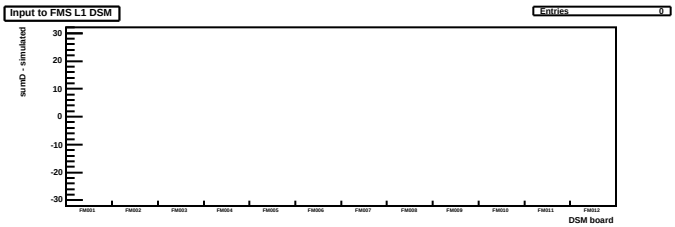

Input to FMS L0 DSM
Entries 2.396986e+07
HT ADC
QT board

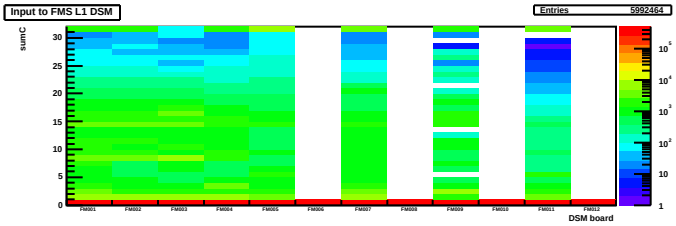

Input to FMS L0 DSM
Entries 105914
HT ADC - simulated
QT board

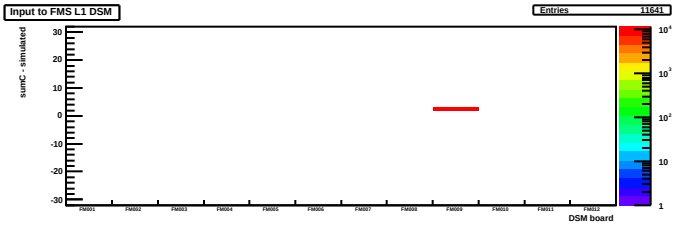

Input to FMS L0 DSM
Entries 2.396986e+07
HT ID
QT board

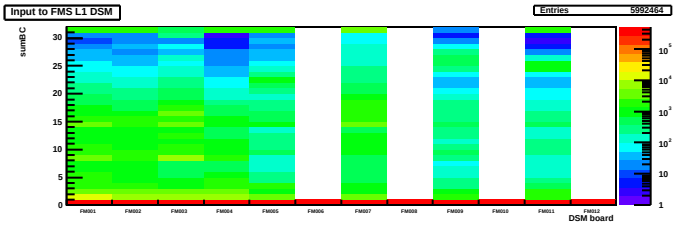

Input to FMS L0 DSM
Entries 598236
HT ID - simulated
QT board

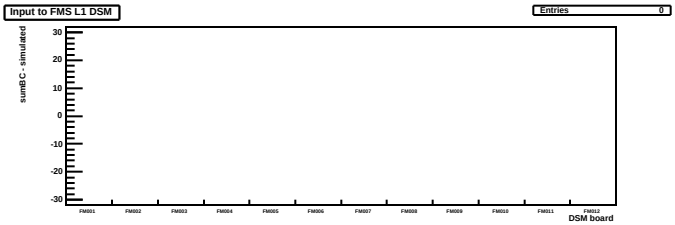

Input to FMS L1 DSM
Entries 5992464
sumD
DSM board

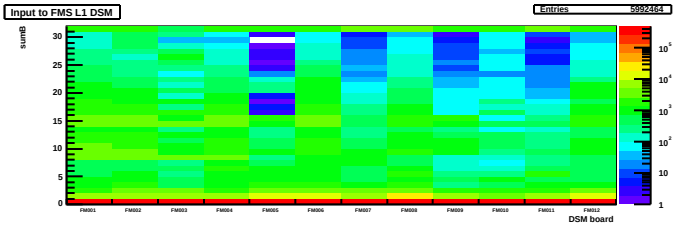

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

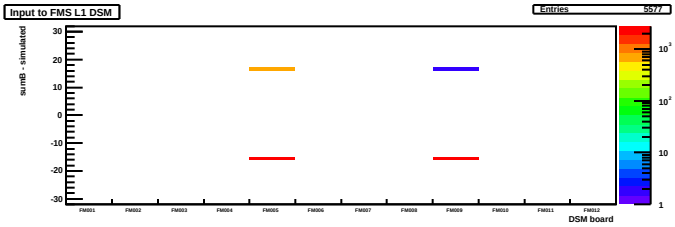

Input to FMS L1 DSM
Entries 5992464
sumC
DSM board

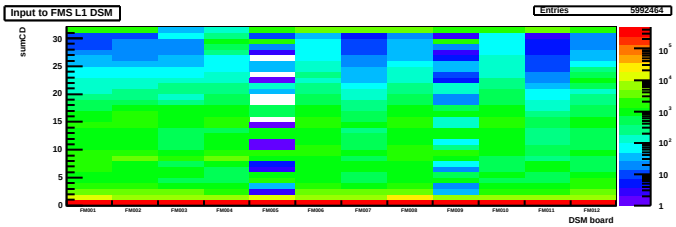

Input to FMS L1 DSM
Entries 11641
sumC - simulated
DSM board

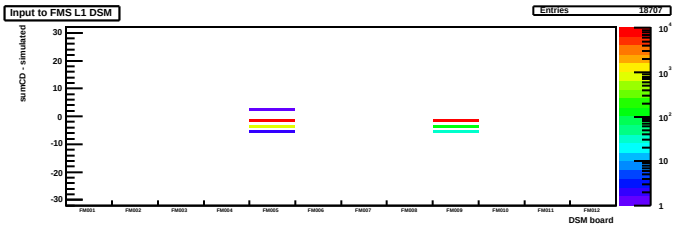

Input to FMS L1 DSM
Entries 5992464
sumBC
DSM board

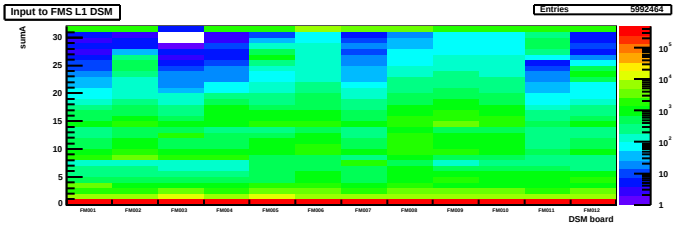

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

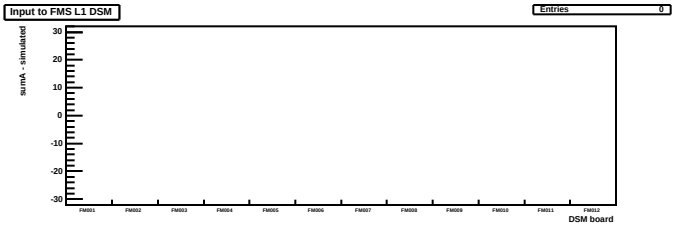

Input to FMS L1 DSM
Entries 5992464
sumB
DSM board

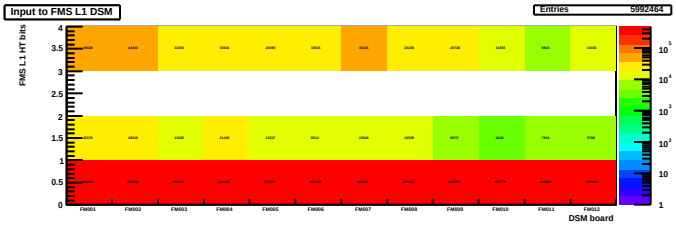

Input to FMS L1 DSM
Entries 5577
sumB - simulated
DSM board

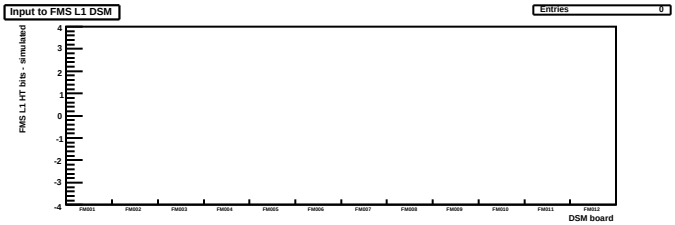

Input to FMS L1 DSM
Entries 5992464
sumCD
DSM board

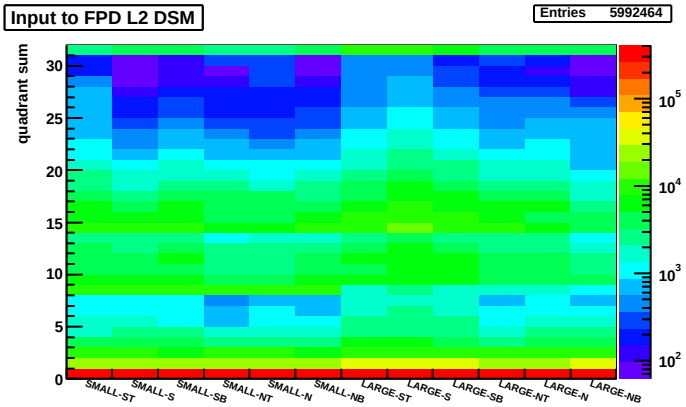

Input to FMS L1 DSM
Entries 18707
sumCD - simulated
DSM board

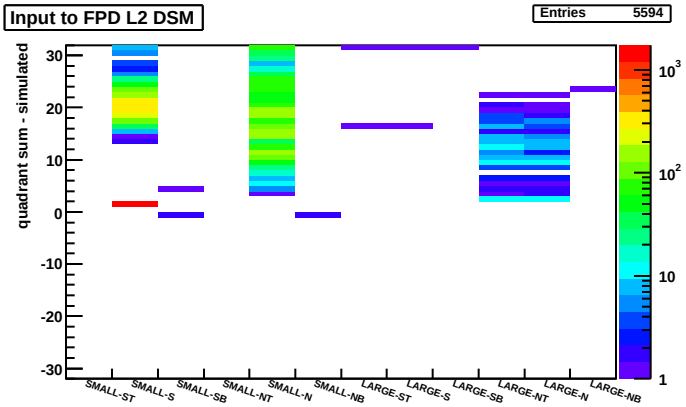

Input to FMS L1 DSM
Entries 5992464
sumA
DSM board

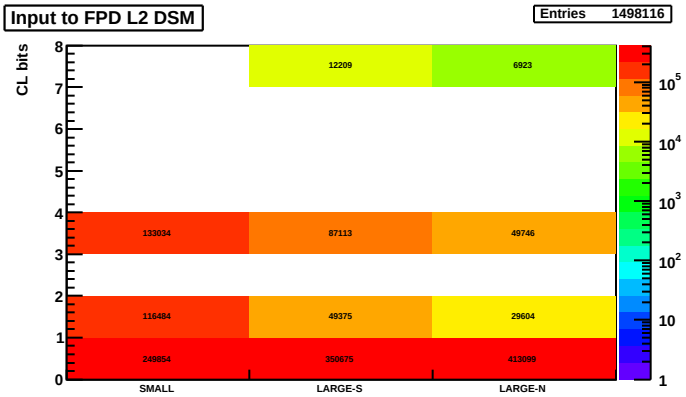

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

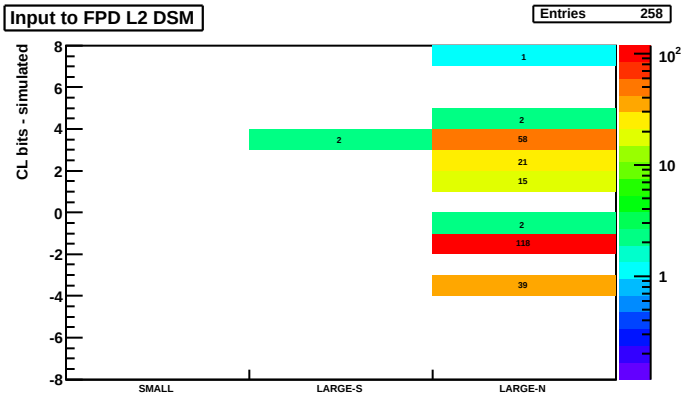

Input to FMS L1 DSM
Entries 5992464
FMS L1 HT bits
DSM board

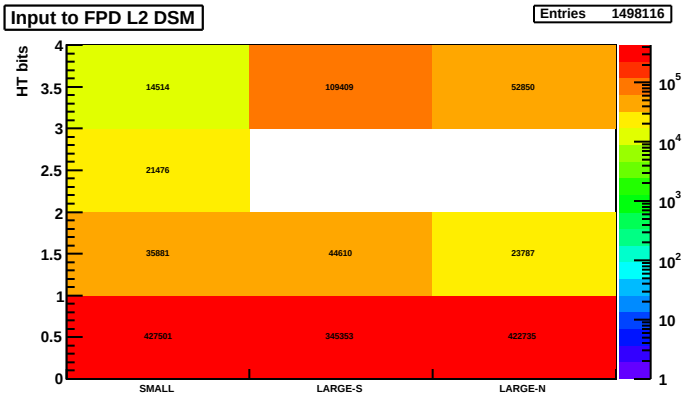

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

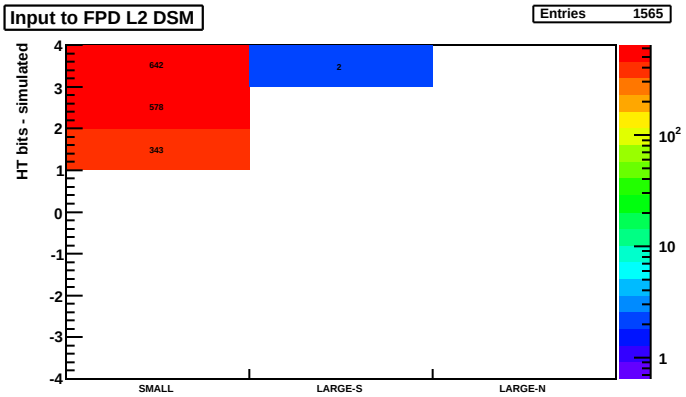

Input to FPD L2 DSM
Entries 5992464
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

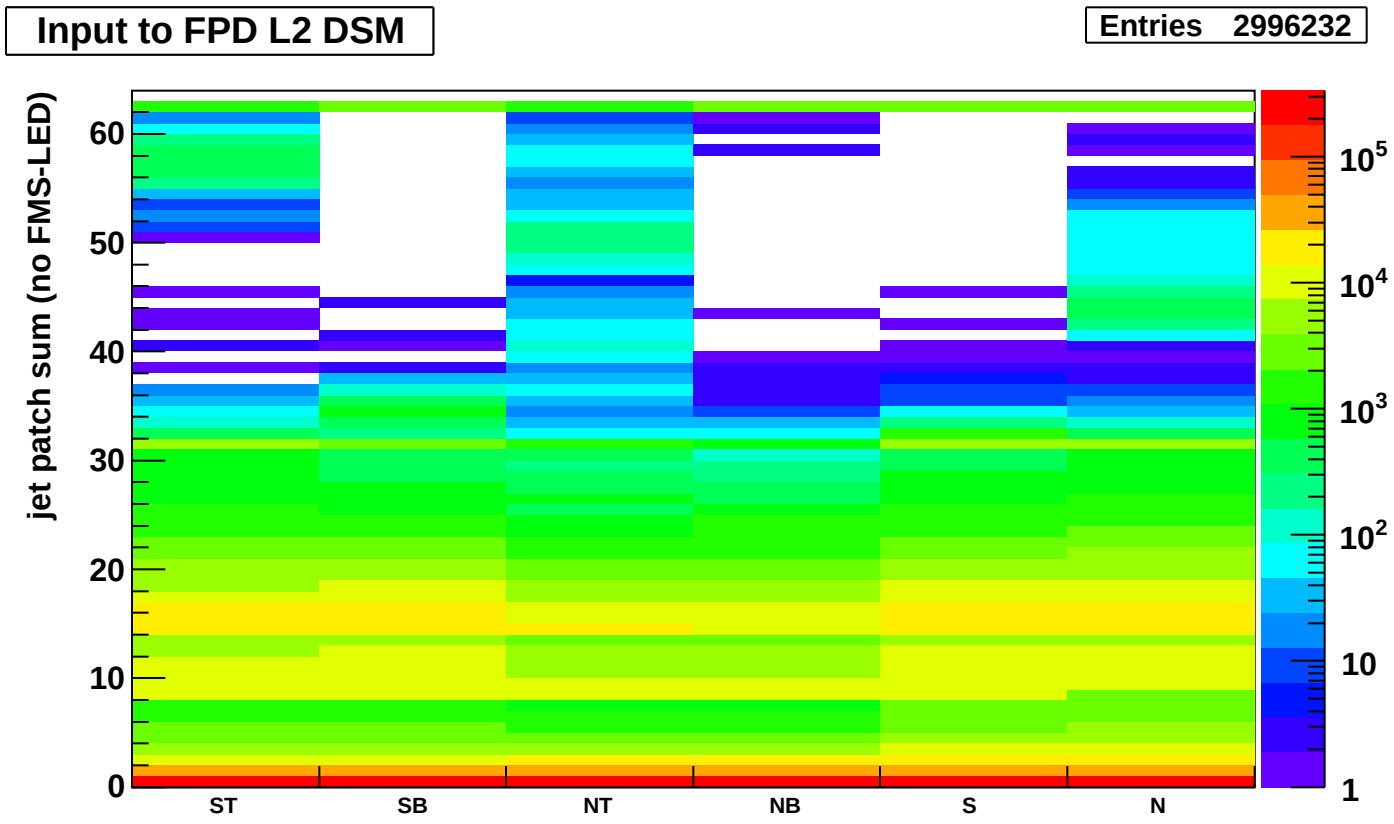

Input to FPD L2 DSM
Entries 5594
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

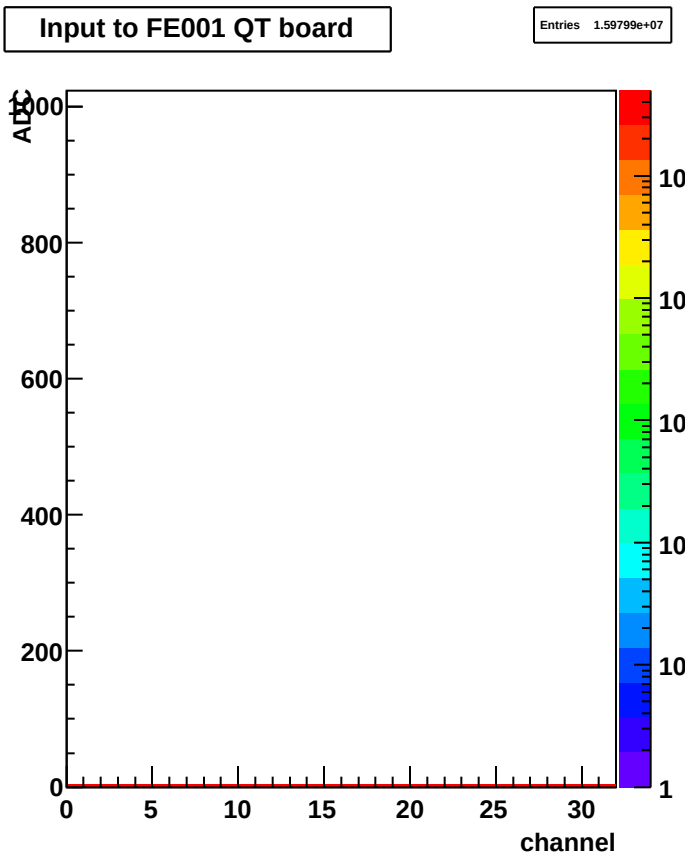

Input to FPD L2 DSM
Entries 1498116
CL bits
12209
6923
133034
87113
49746
116484
49375
29604
249854
350675
413099
SMALL
LARGE-S
LARGE-N

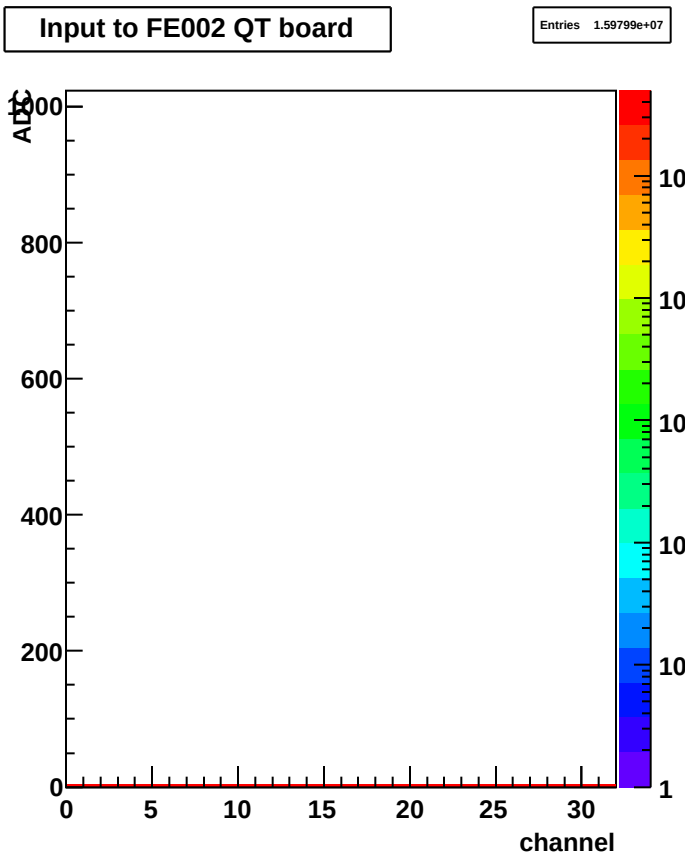

Input to FPD L2 DSM
Entries 258
CL bits - simulated
1
2
2
58
21
15
2
110
39
SMALL
LARGE-S
LARGE-N

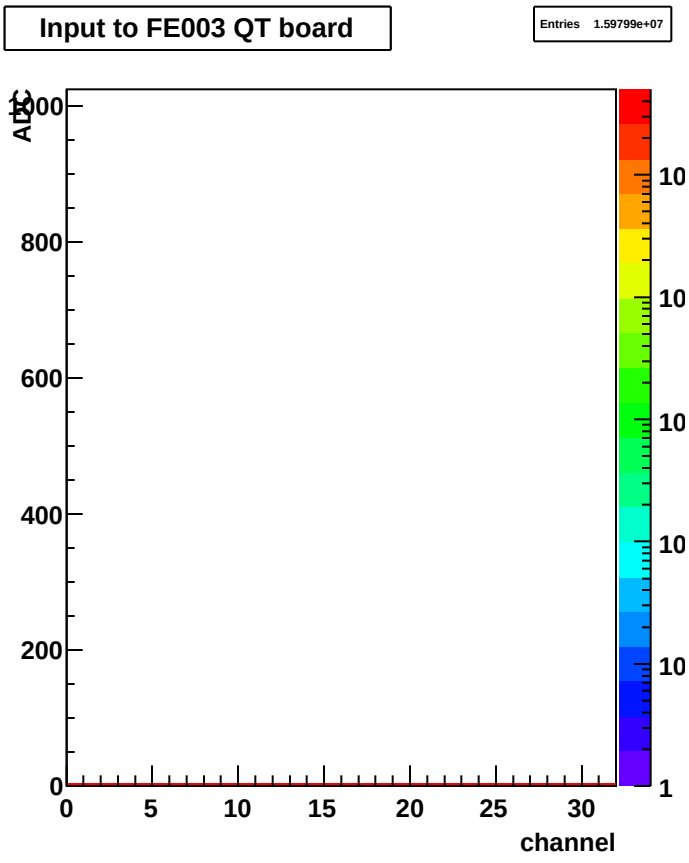

Input to FPD L2 DSM
Entries 1498116
HT bits
14514
109409
52850
21476
35881
44610
23787
427501
345353
422735
SMALL
LARGE-S
LARGE-N

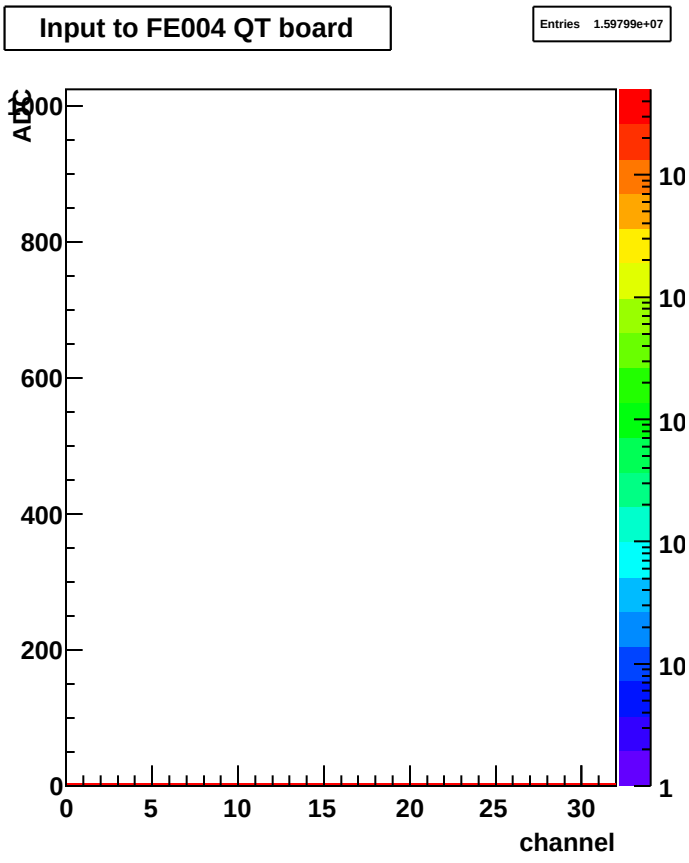

Input to FPD L2 DSM
Entries 1565
HT bits - simulated
642
2
578
343
SMALL
LARGE-S
LARGE-N

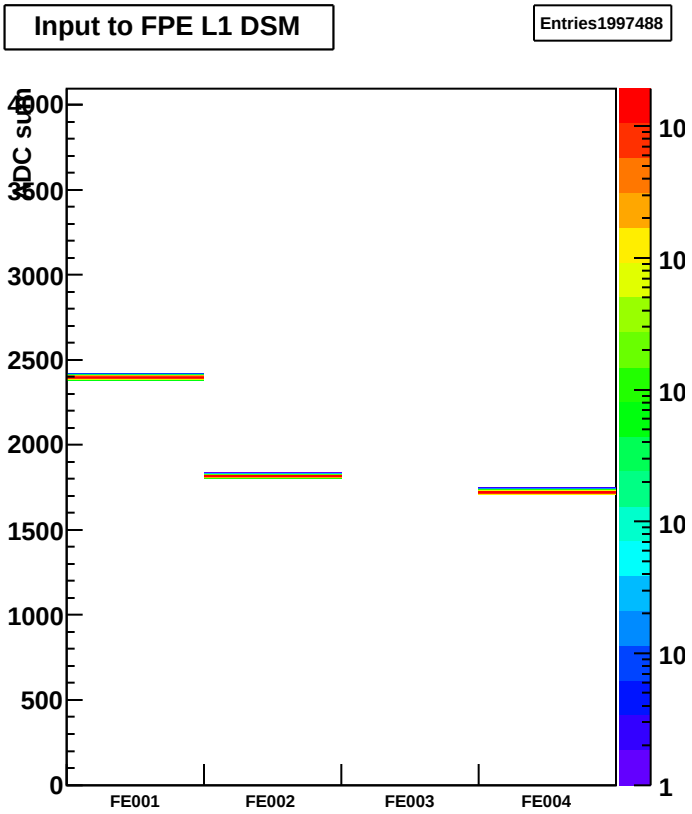

Input to FPD L2 DSM
Entries 2996232
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 1.59799e+07
ADC
channel

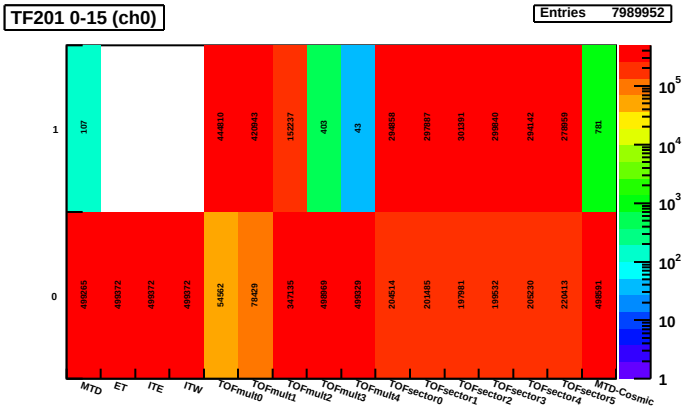

Input to FE002 QT board
Entries 1.59799e+07
ADC
channel


Input to FE003 QT board
Entries 1.59799e+07
ADC
channel

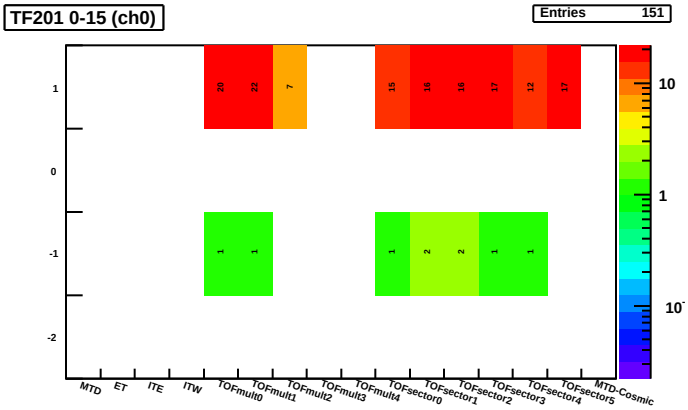

Input to FE004 QT board
Entries 1.59799e+07
ADC
channel


Input to FPE L1 DSM
Entries1997488
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

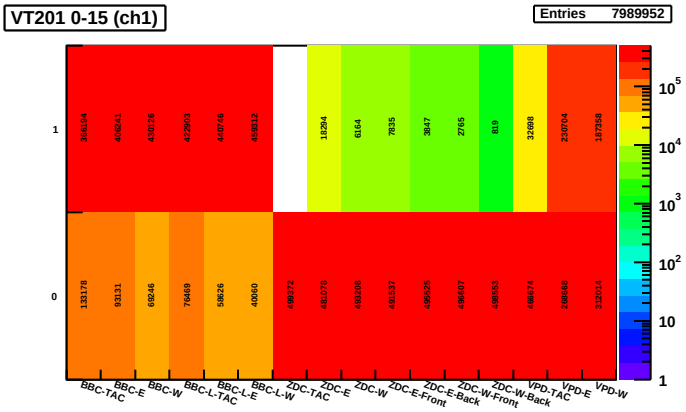
TF201 0-15 (ch0)



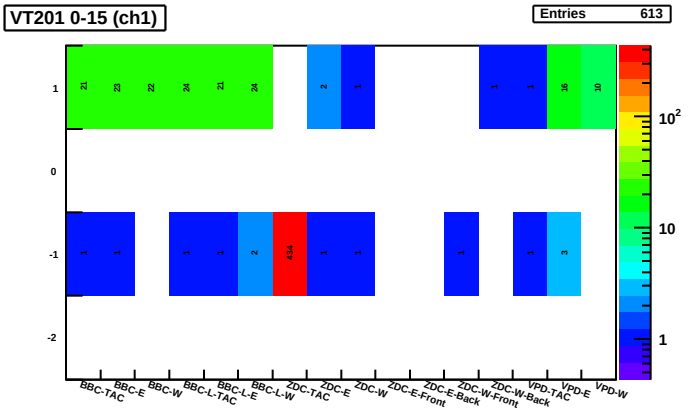
TF201 0-15 (ch0)



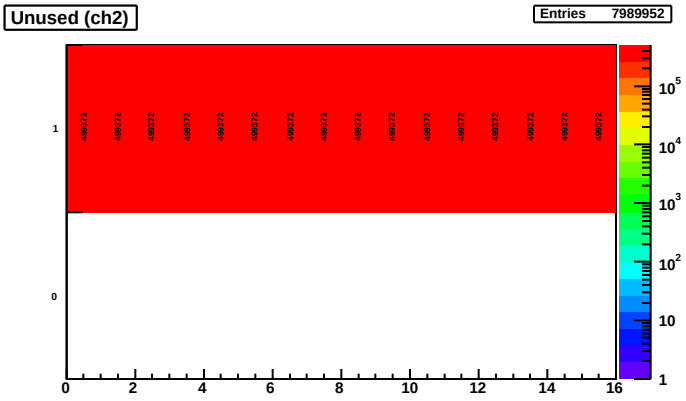
VT201 0-15 (ch1)

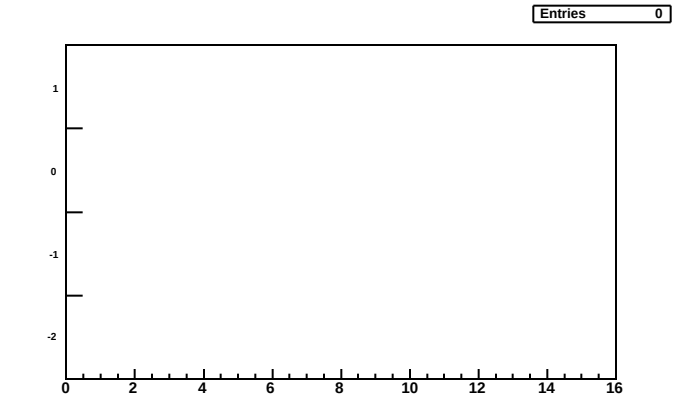


VT201 0-15 (ch1)

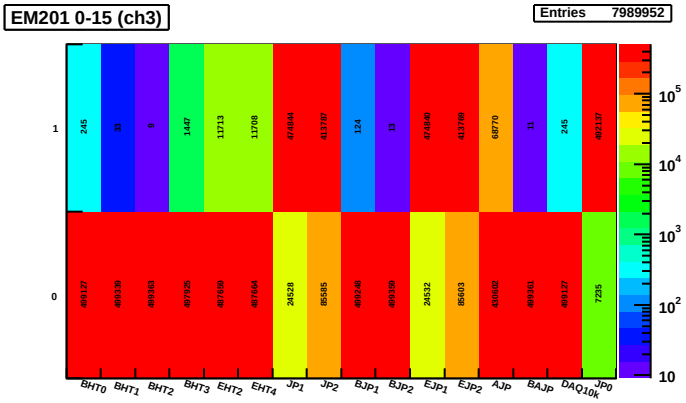


Unused (ch2)

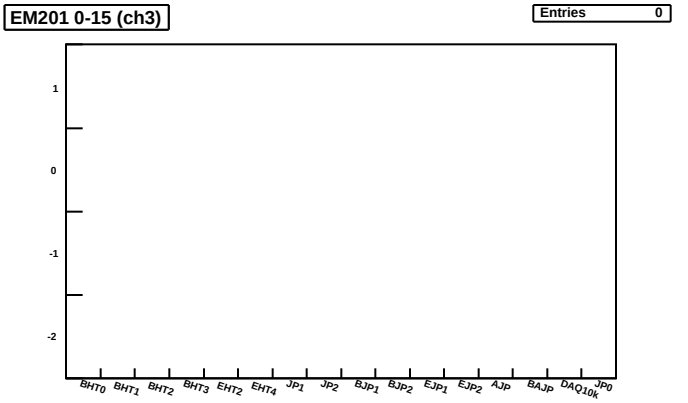




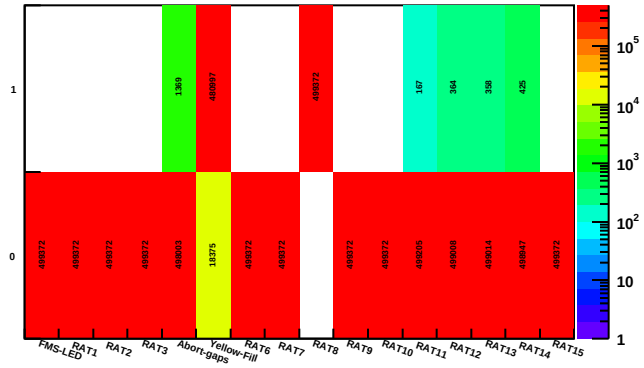
EM201 0-15 (ch3)

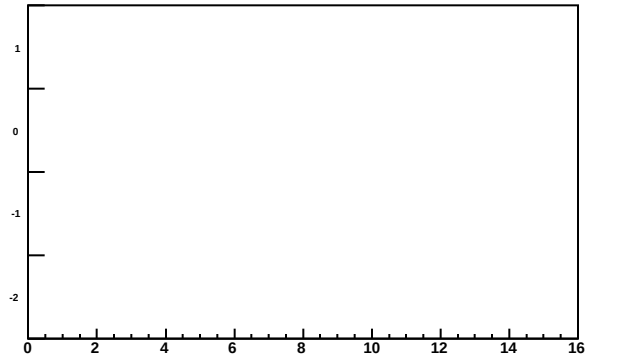


EM201 0-15 (ch3)

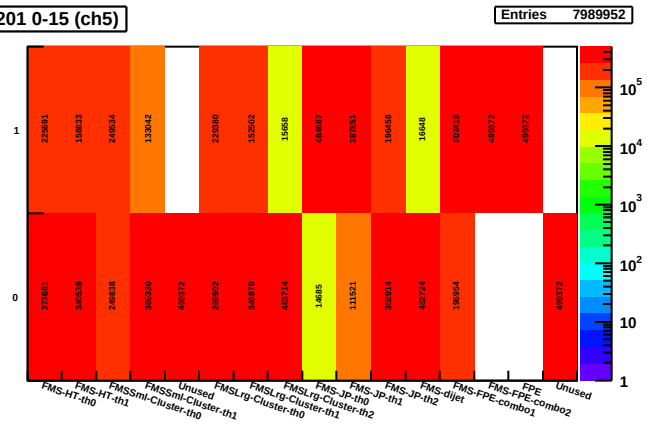


RAT board (ch4)





FP201 0-15 (ch5)



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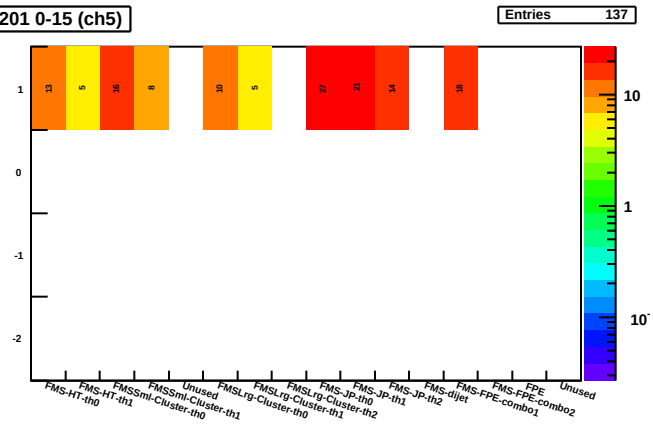
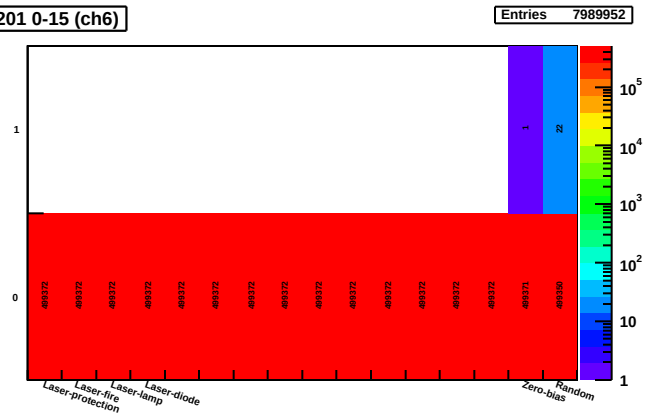
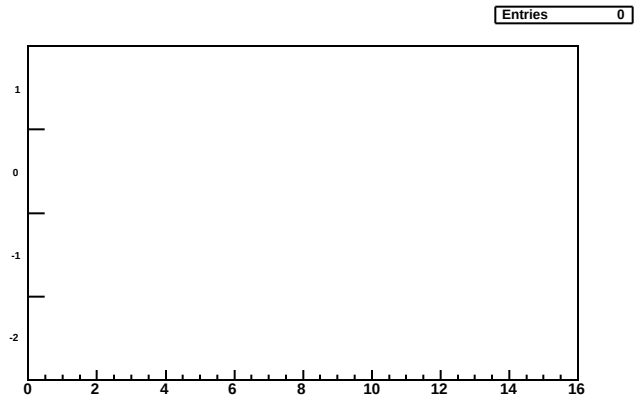
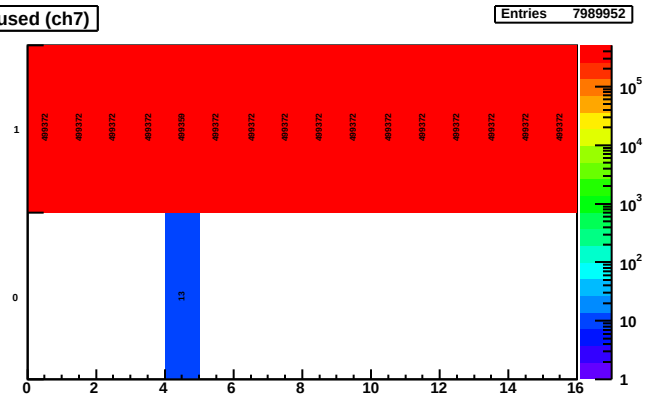
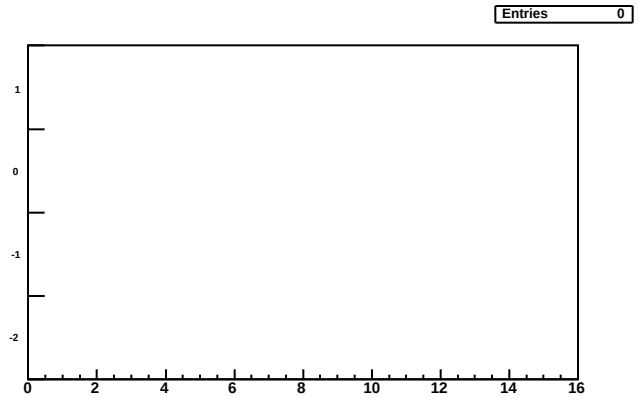


ST201 0-15 (ch6)





Unused (ch7)