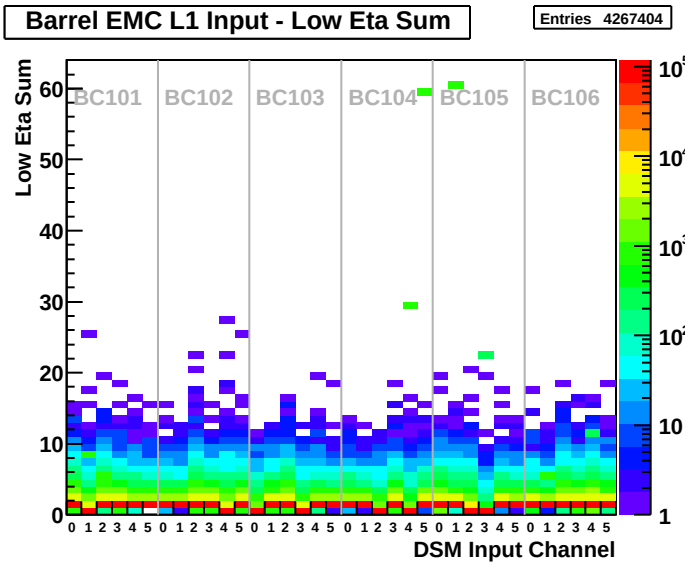
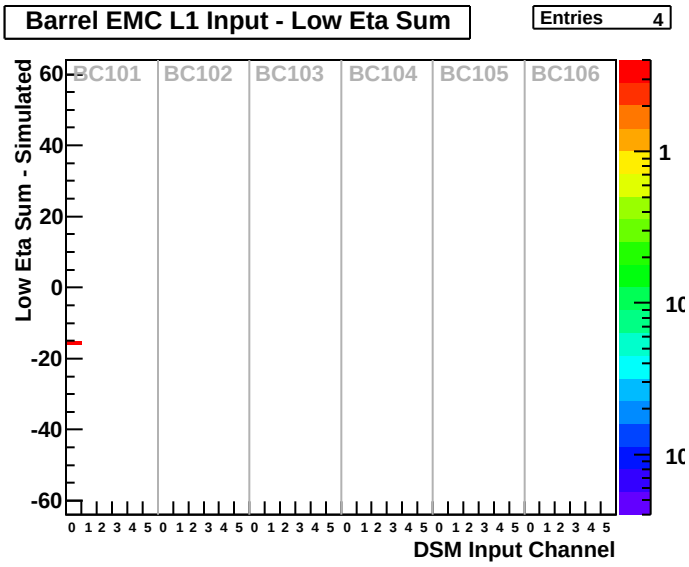
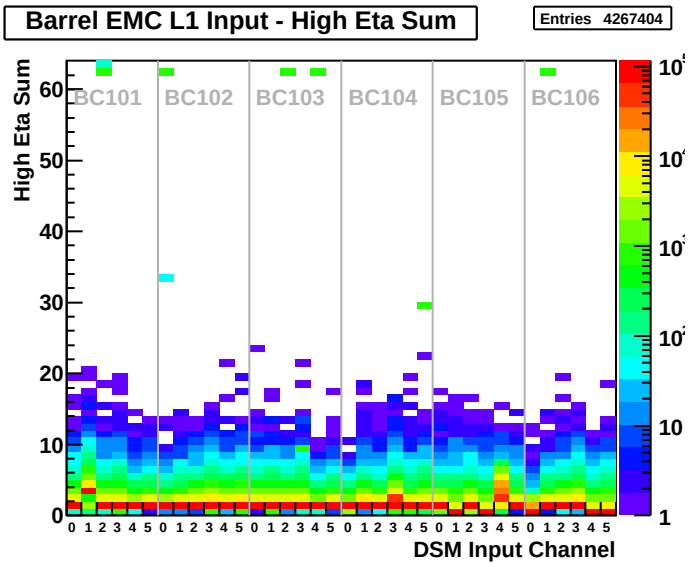
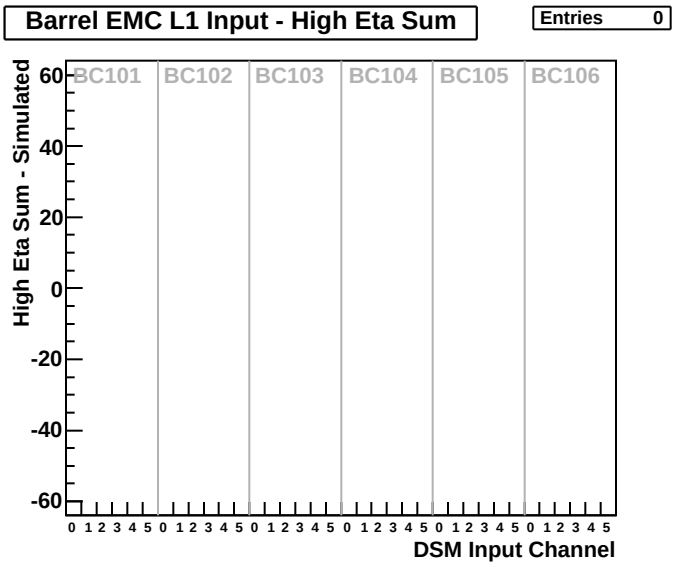
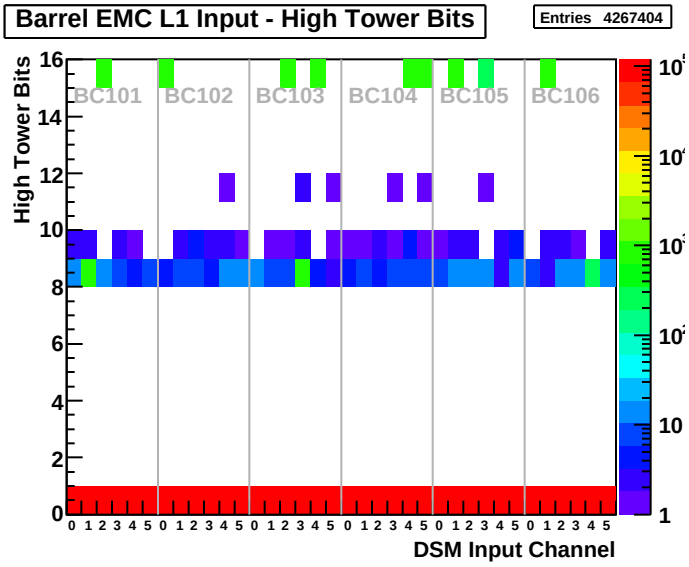
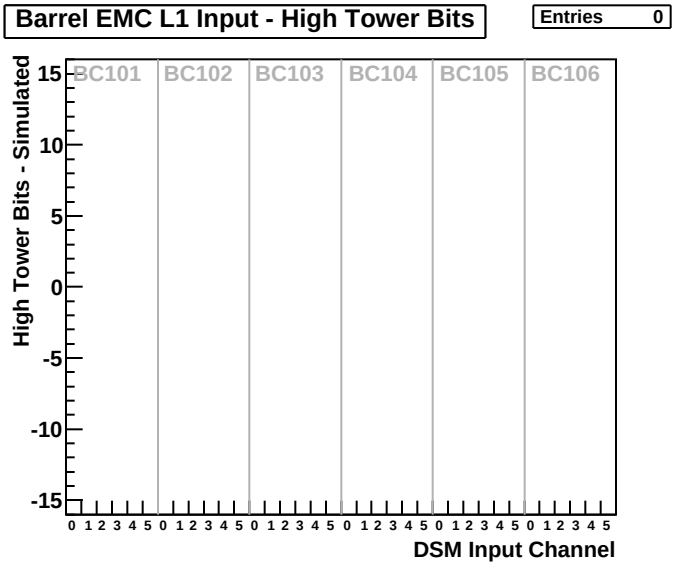

Barrel EMC L1 Input - Low Eta Sum
Entries 4267404
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

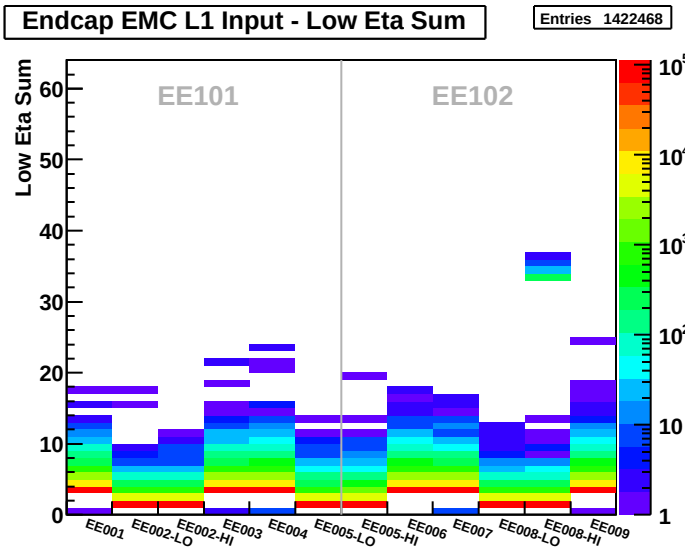

Barrel EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

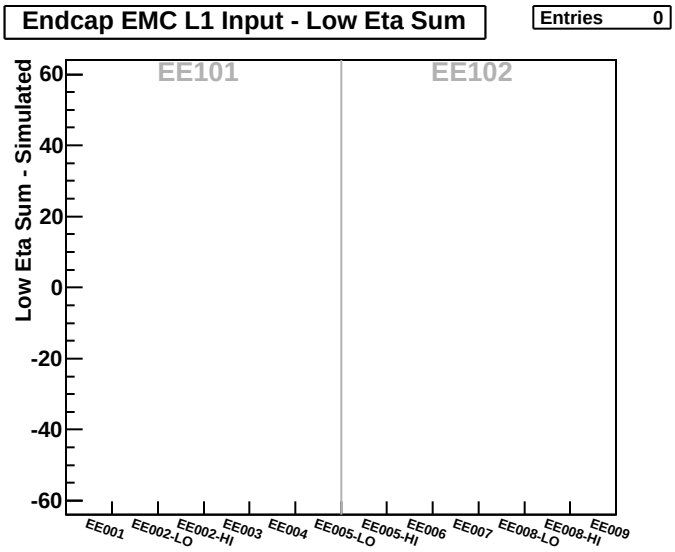

Barrel EMC L1 Input - High Eta Sum
Entries 4267404
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

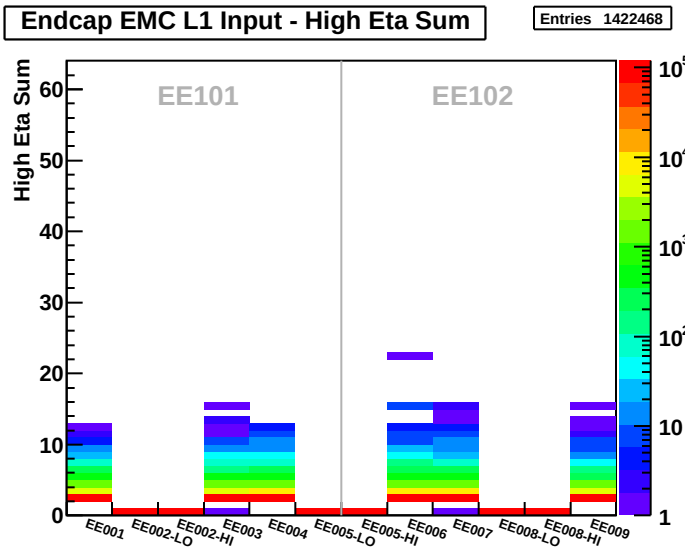

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

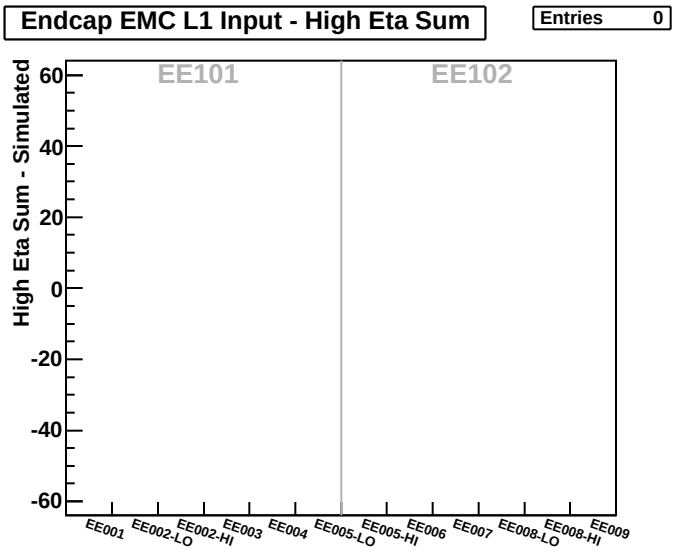

Barrel EMC L1 Input - High Tower Bits
Entries 4267404
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

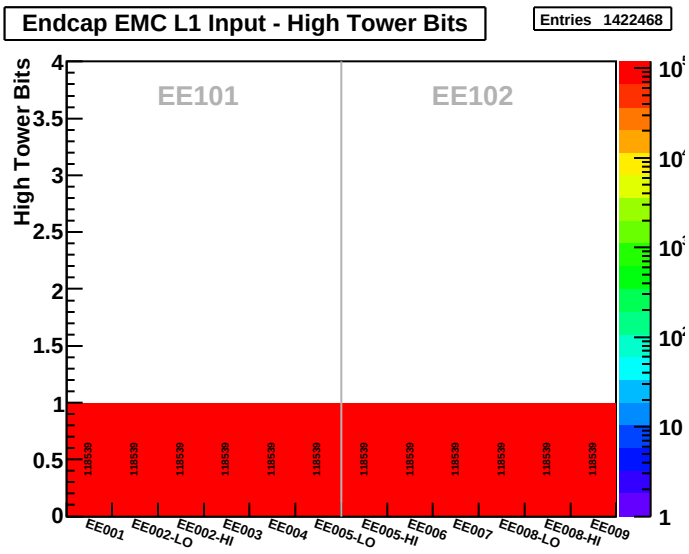

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

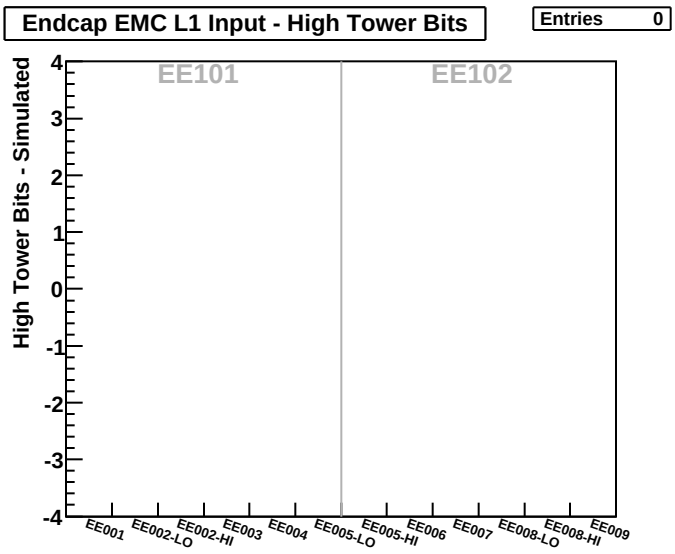

Endcap EMC L1 Input - Low Eta Sum
Entries 1422468
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

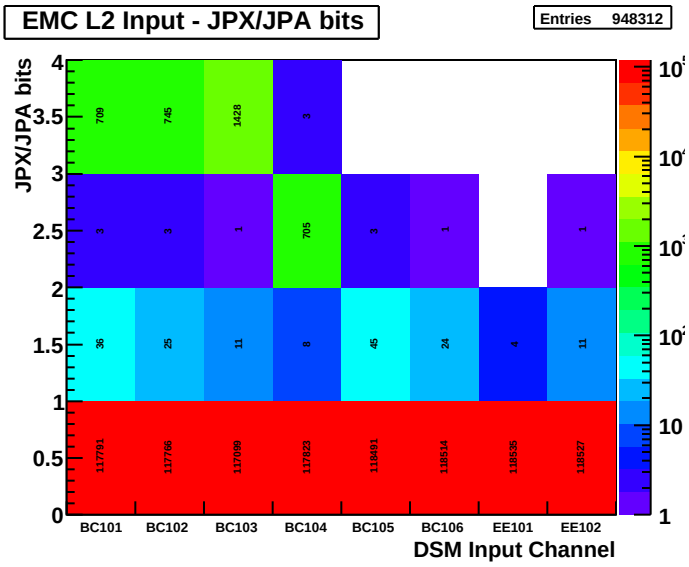

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

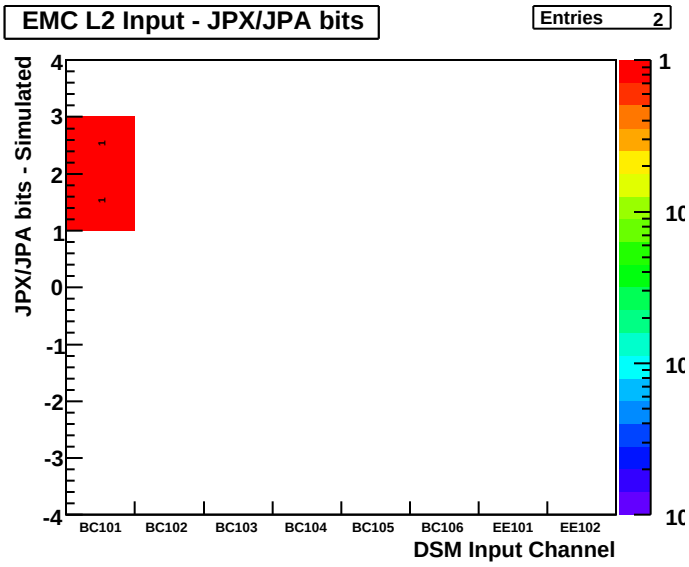

Endcap EMC L1 Input - High Eta Sum
Entries 1422468
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

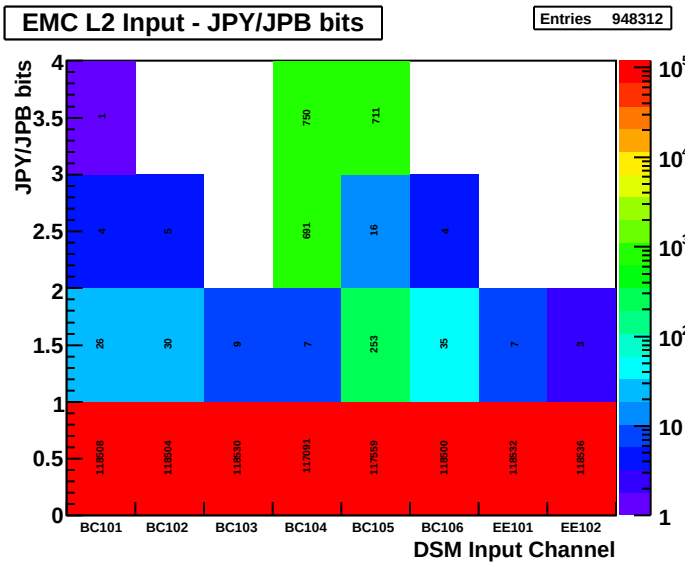

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

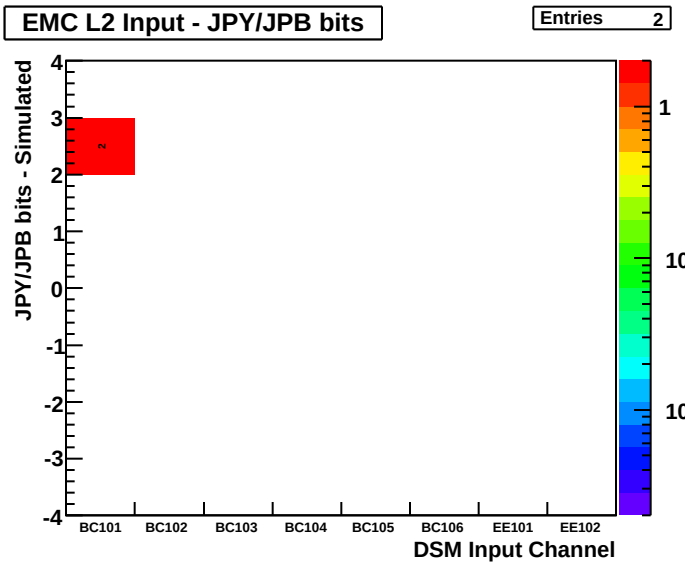

Endcap EMC L1 Input - High Tower Bits
Entries 1422468
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

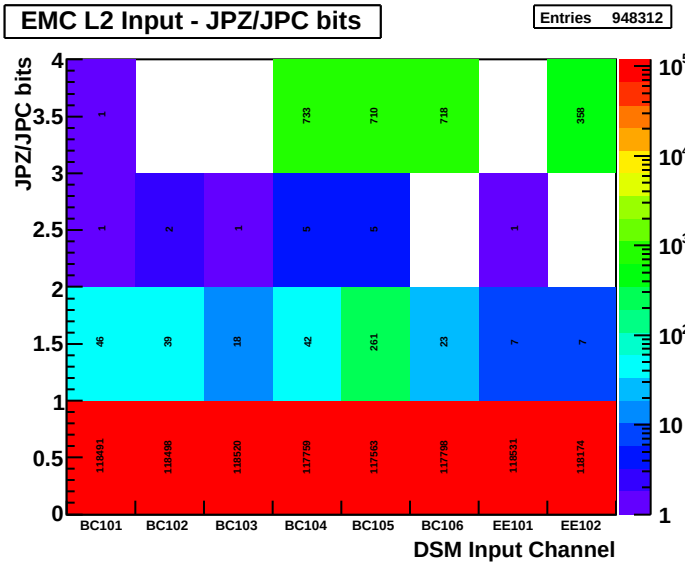

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

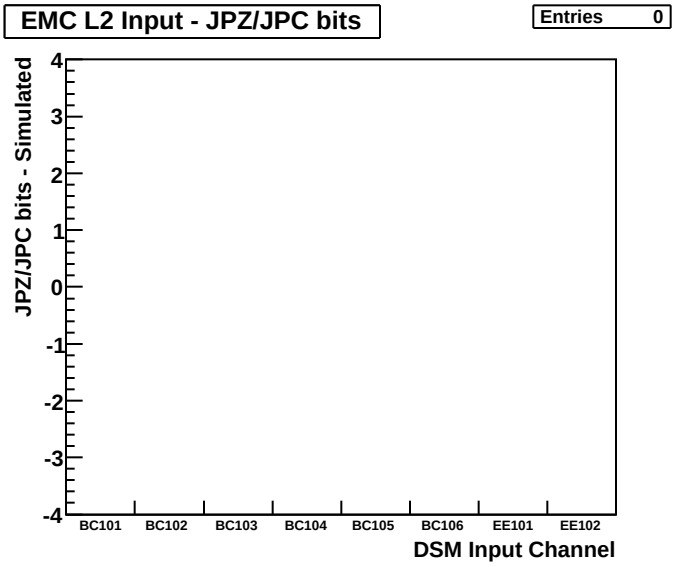

EMC L2 Input - JPX/JPA bits
Entries 948312
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

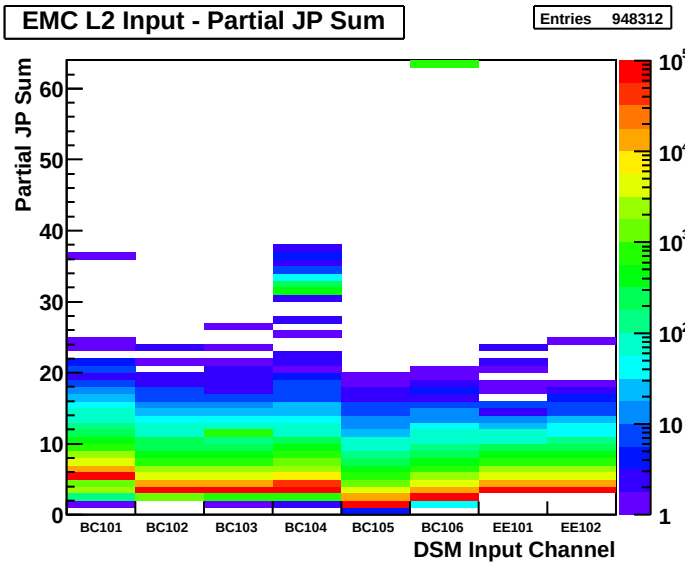

EMC L2 Input - JPX/JPA bits
Entries 2
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

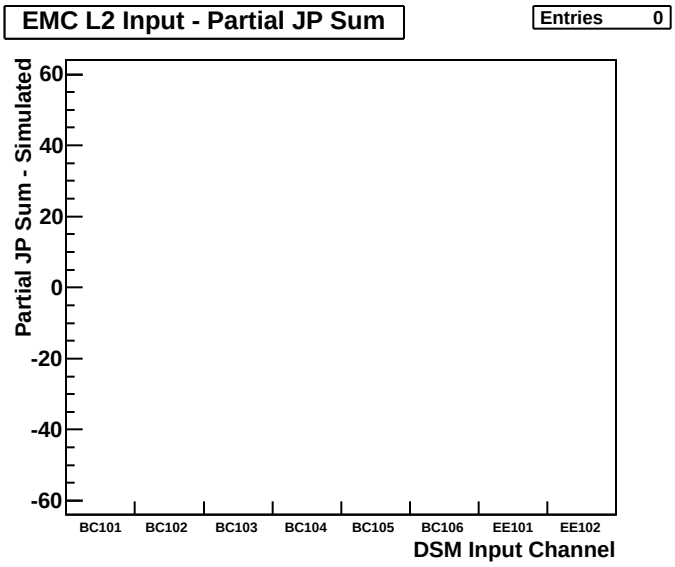

EMC L2 Input - JPY/JPB bits
Entries 948312
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

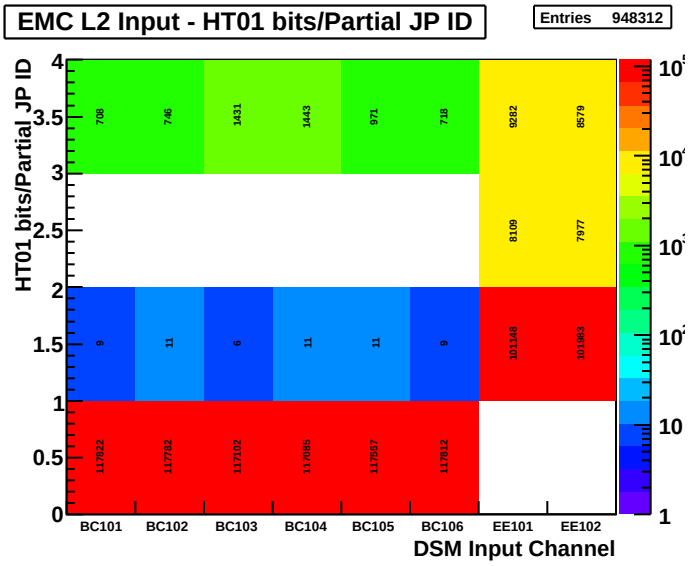

EMC L2 Input - JPY/JPB bits
Entries 2
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

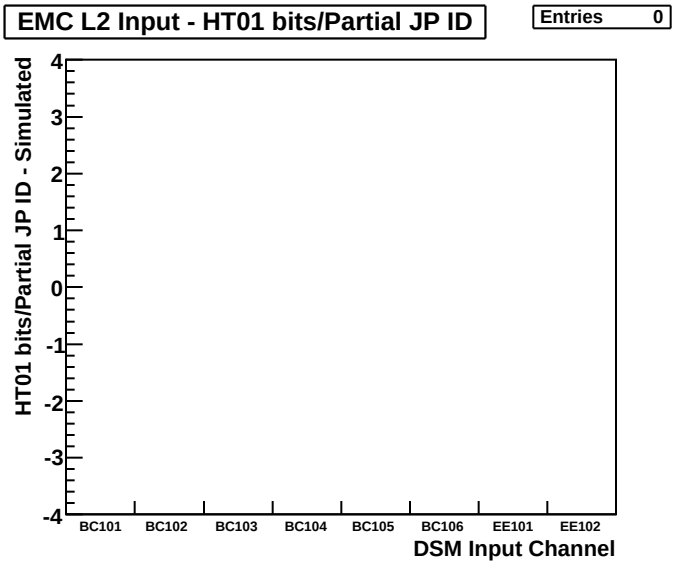

EMC L2 Input - JPZ/JPC bits
Entries 948312
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

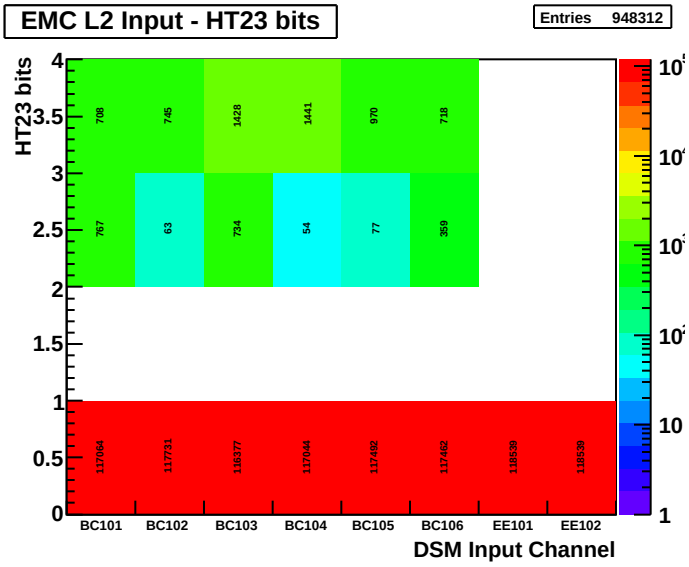

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

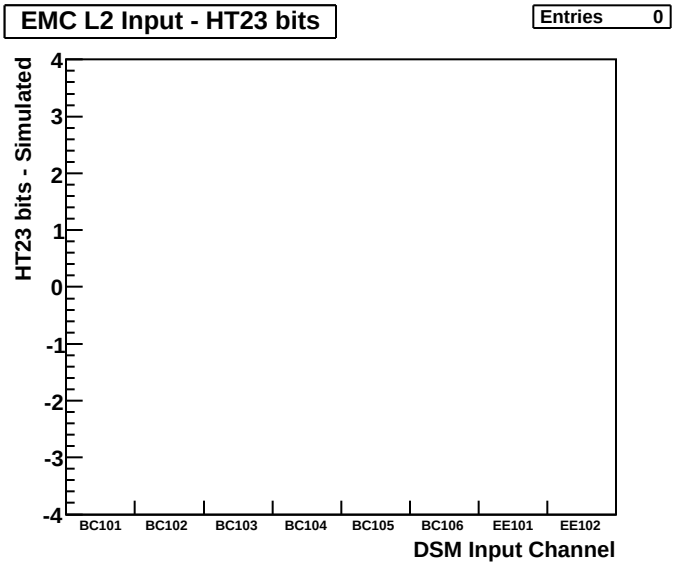

EMC L2 Input - Partial JP Sum
Entries 948312
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

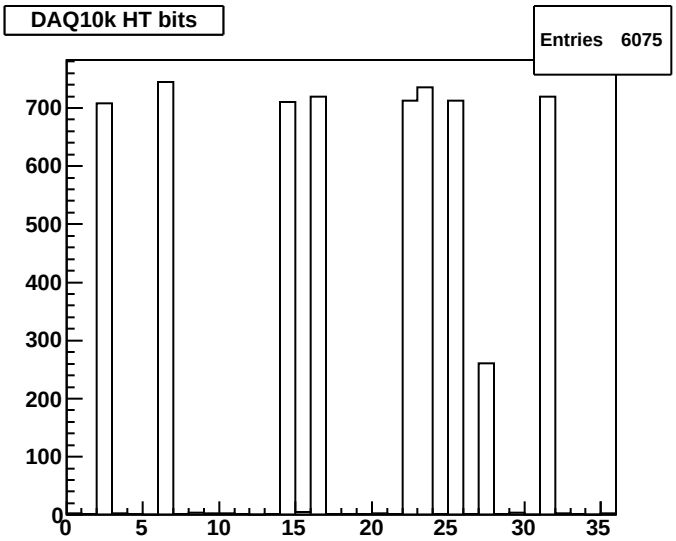

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

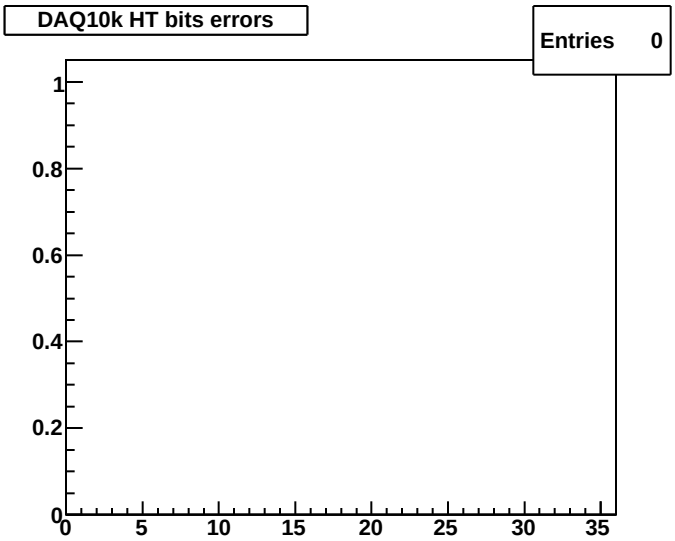

EMC L2 Input - HT01 bits/Partial JP ID
Entries 948312
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

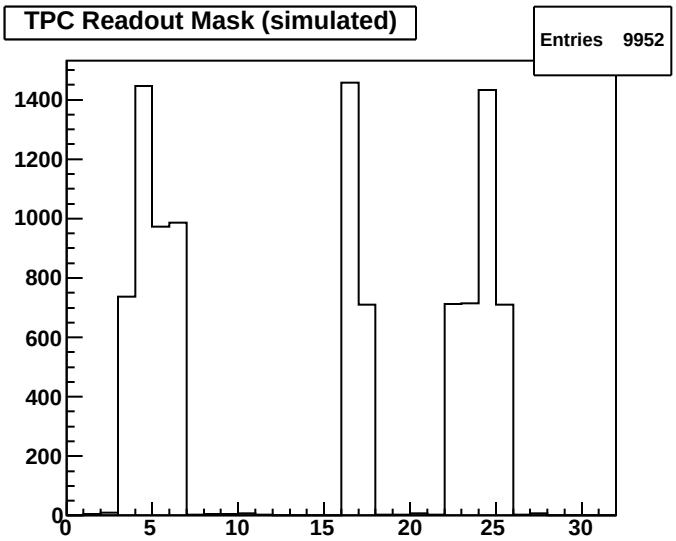

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

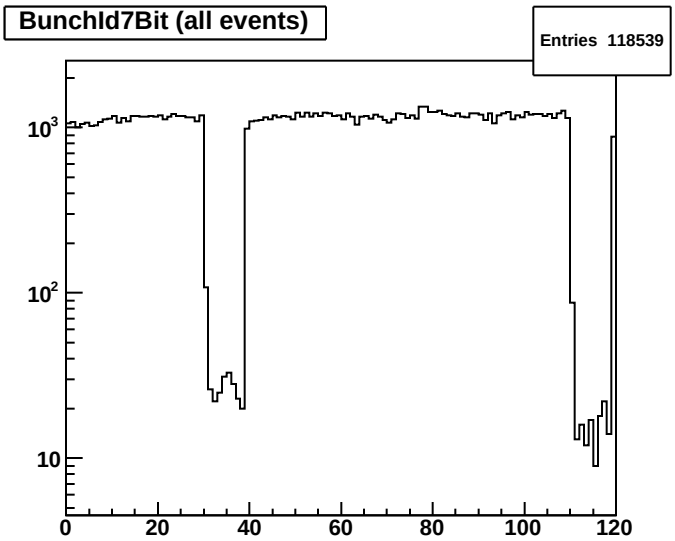

EMC L2 Input - HT23 bits
Entries 948312
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

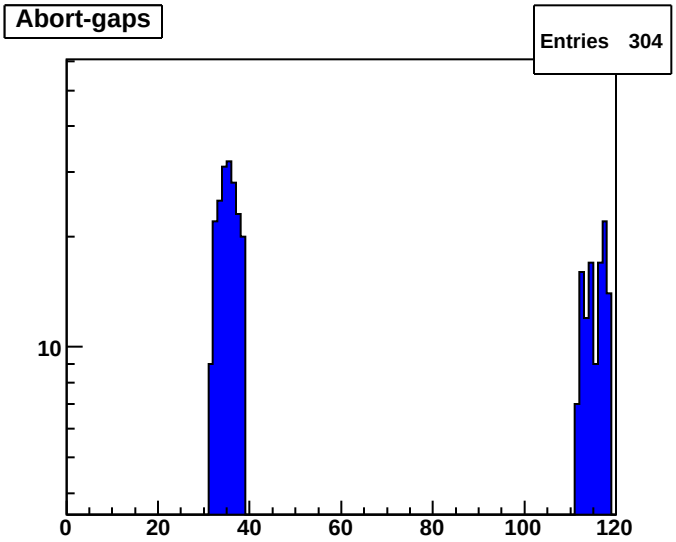

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

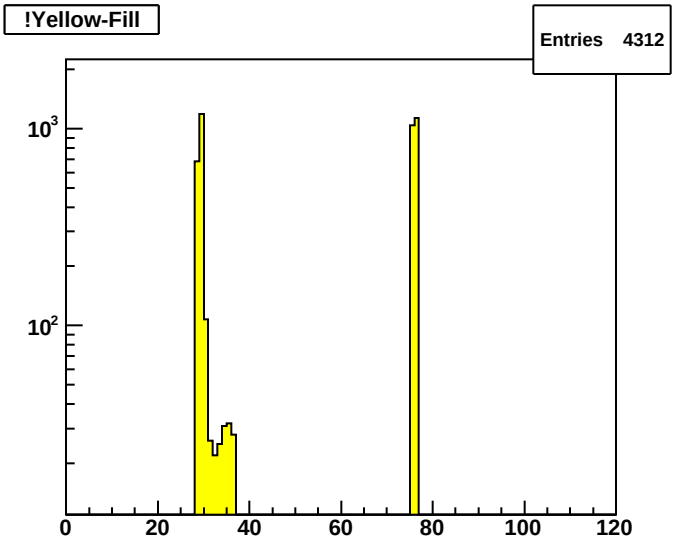

DAQ10k HT bits
Entries 6075

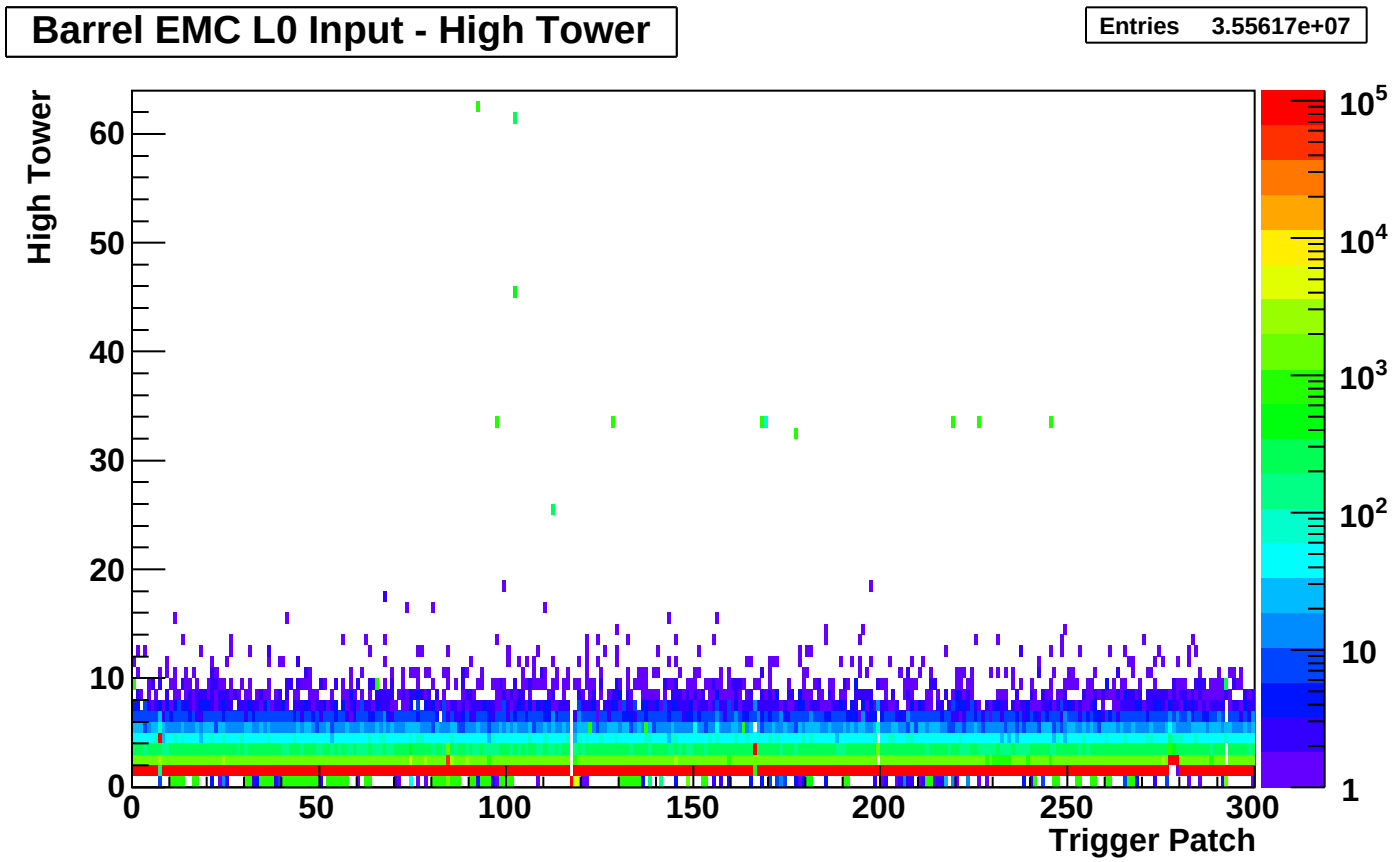

DAQ10k HT bits errors
Entries 0

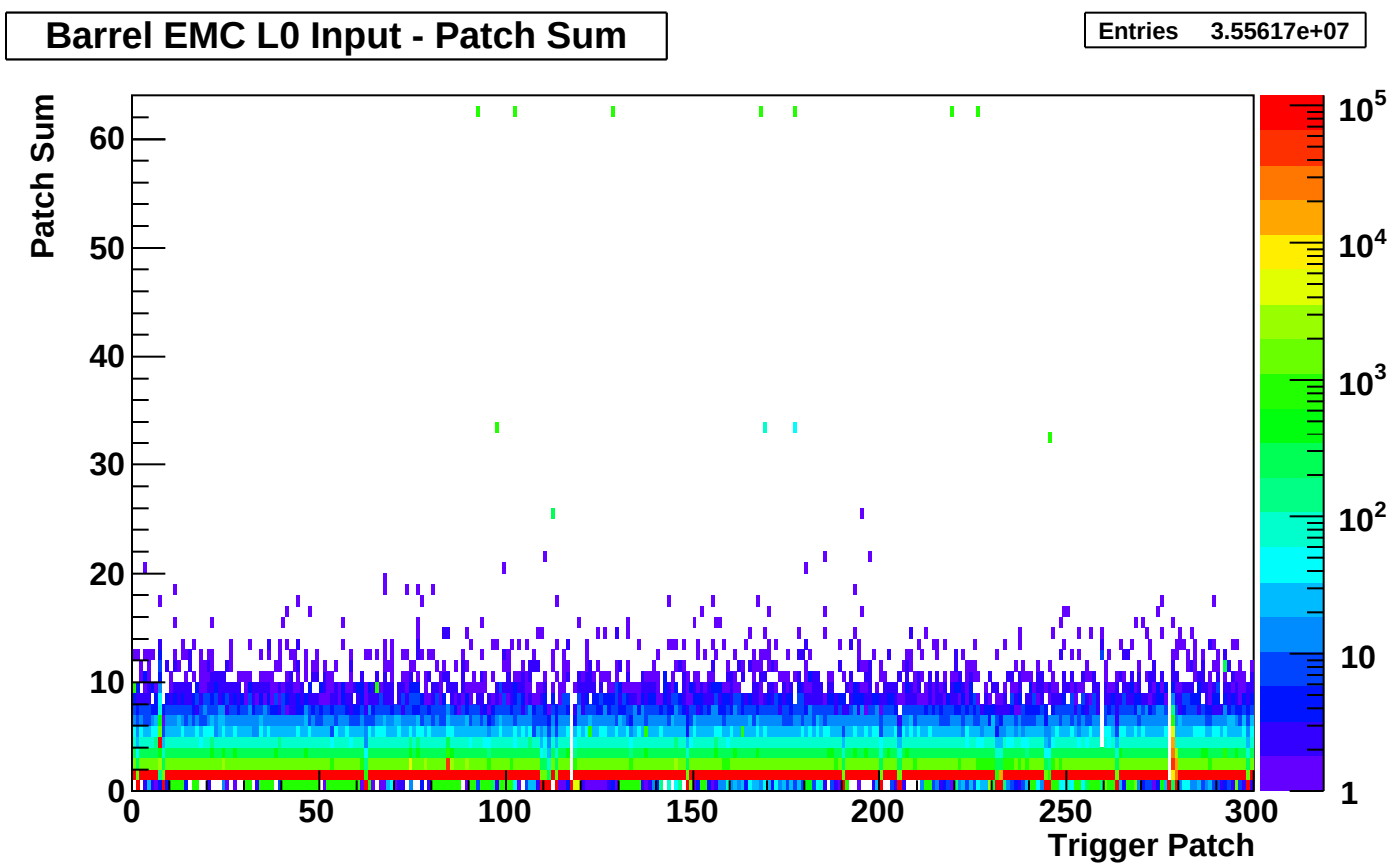

TPC Readout Mask (simulated)
Entries 9952

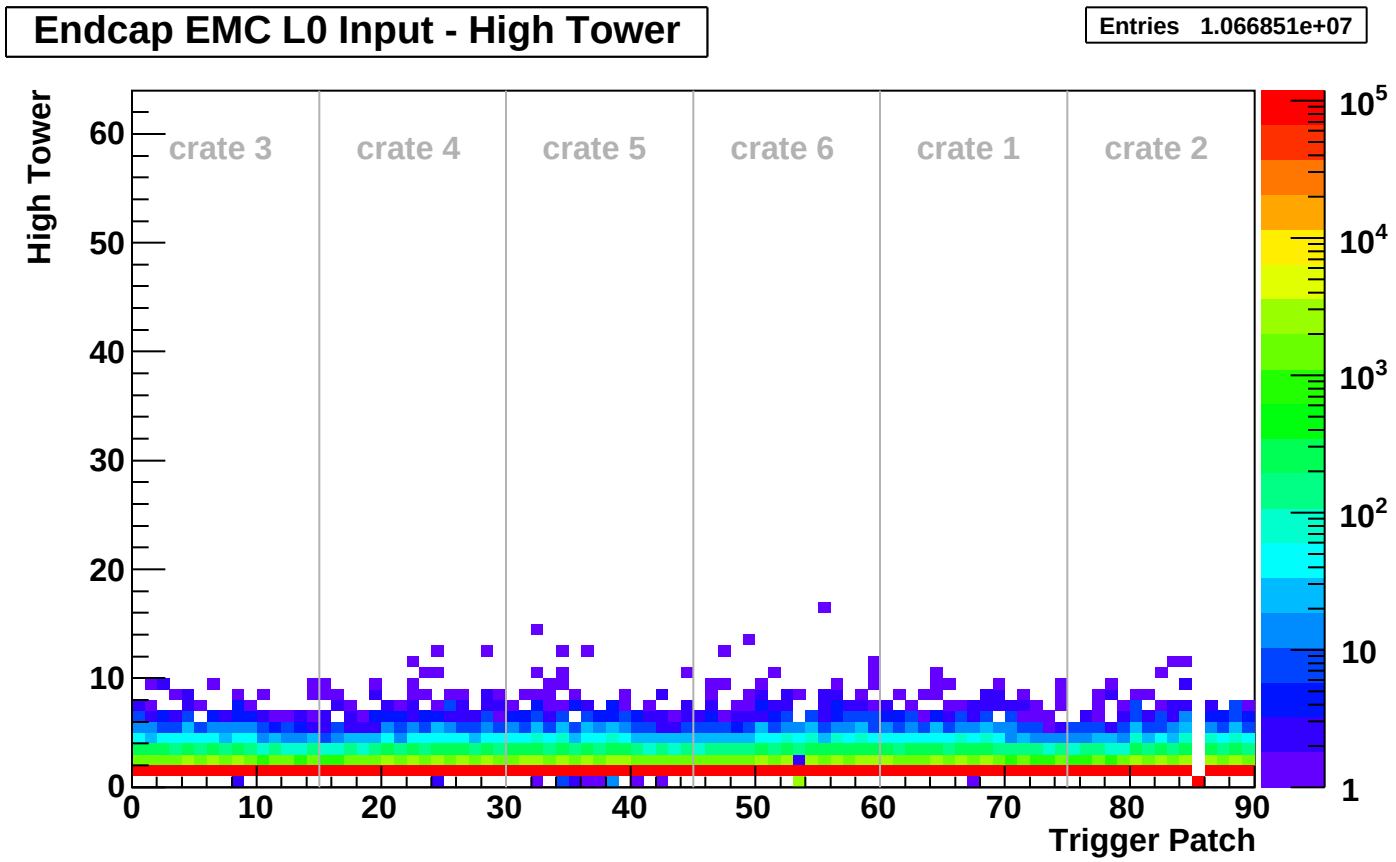

BunchId7Bit (all events)
Entries 118539

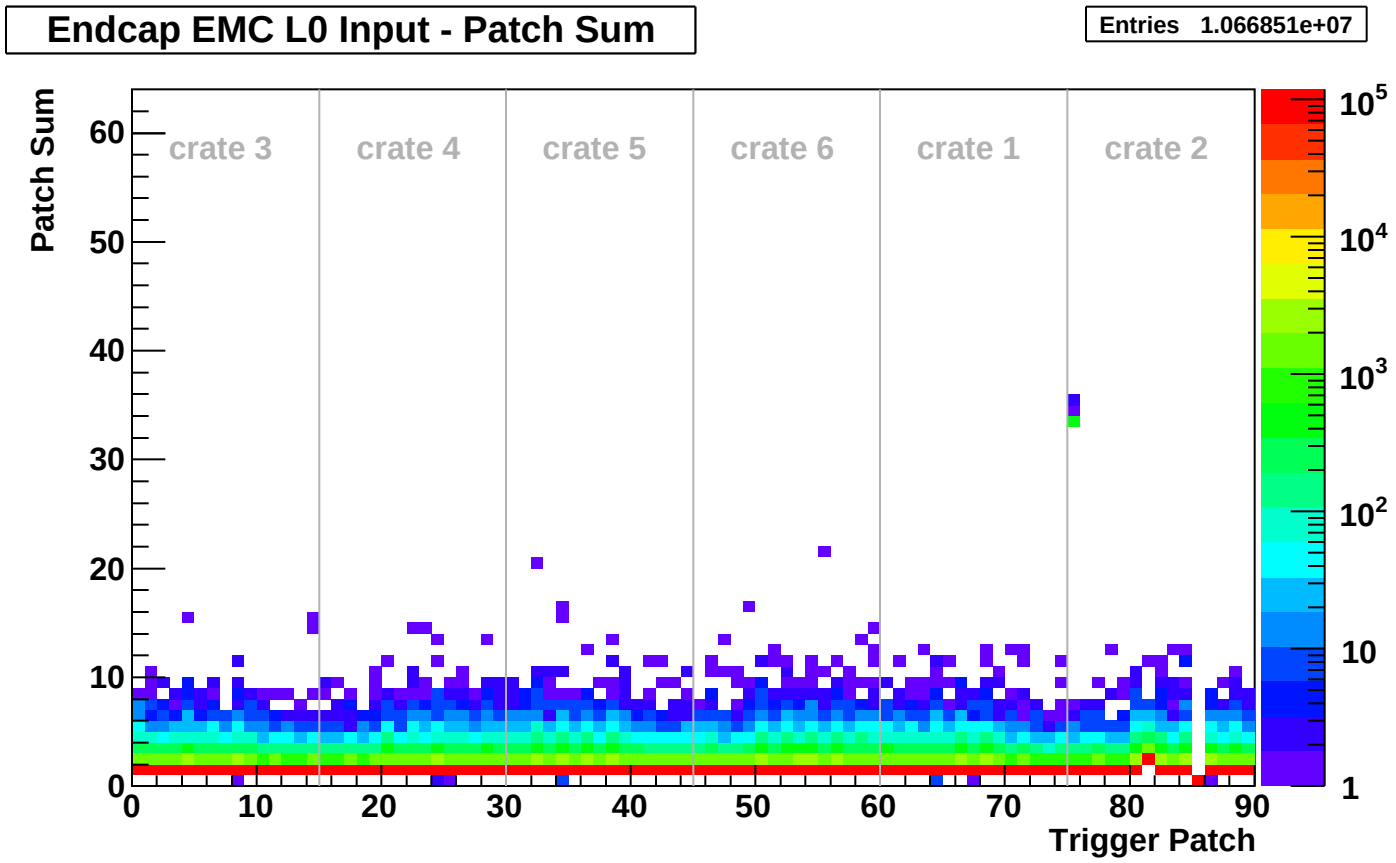

Abort-gaps
Entries 304

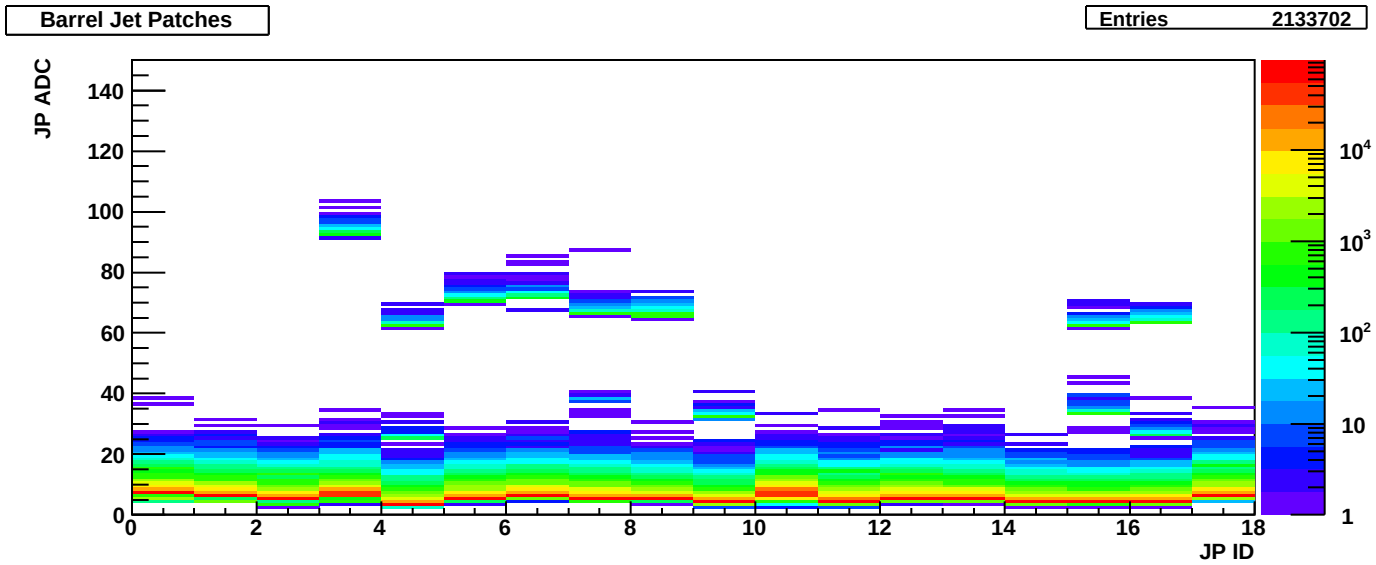

!Yellow-Fill
Entries 4312

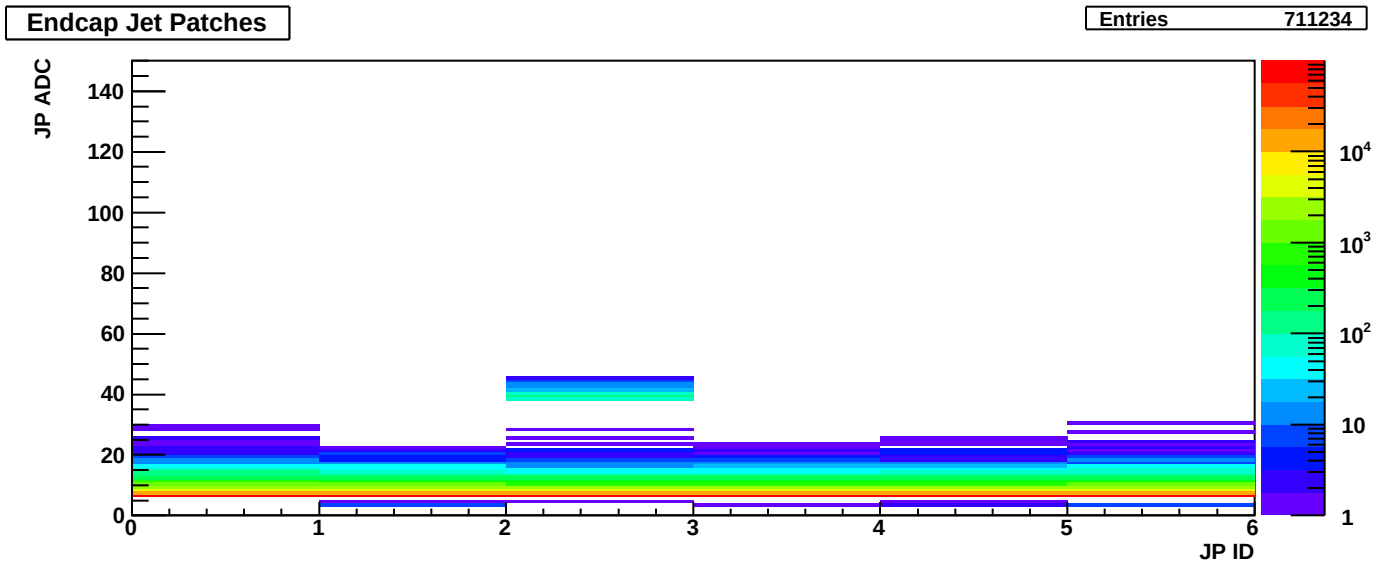

Barrel EMC L0 Input - High Tower
Entries 3.55617e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

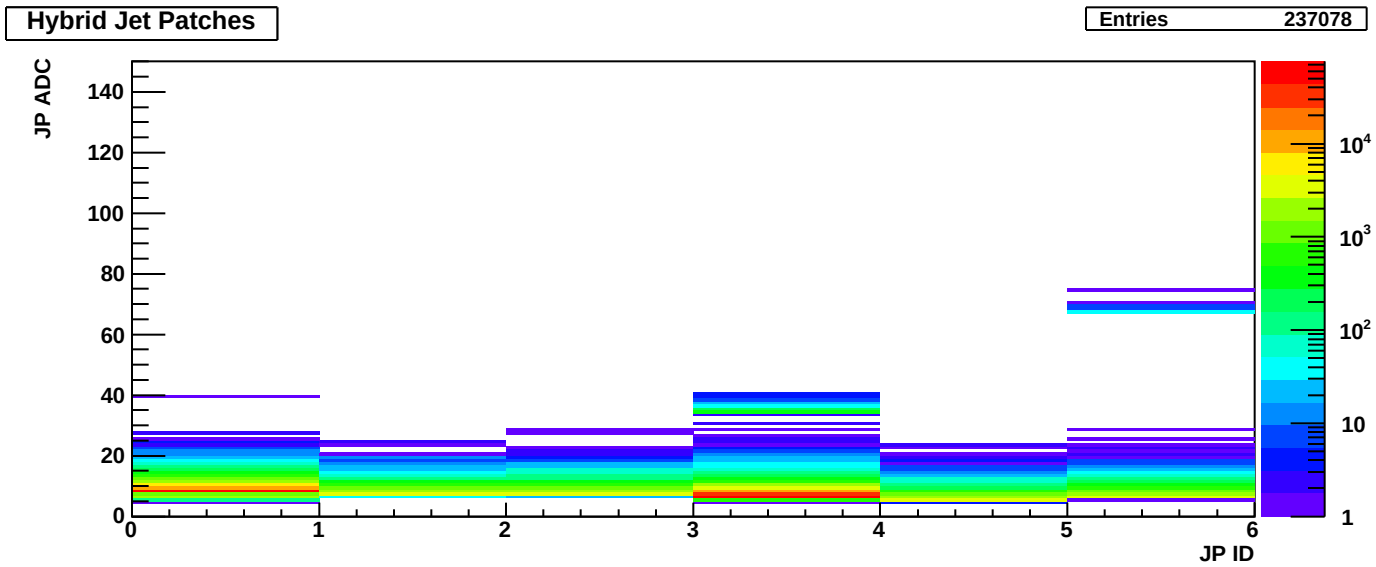

Barrel EMC L0 Input - Patch Sum
Entries 3.55617e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

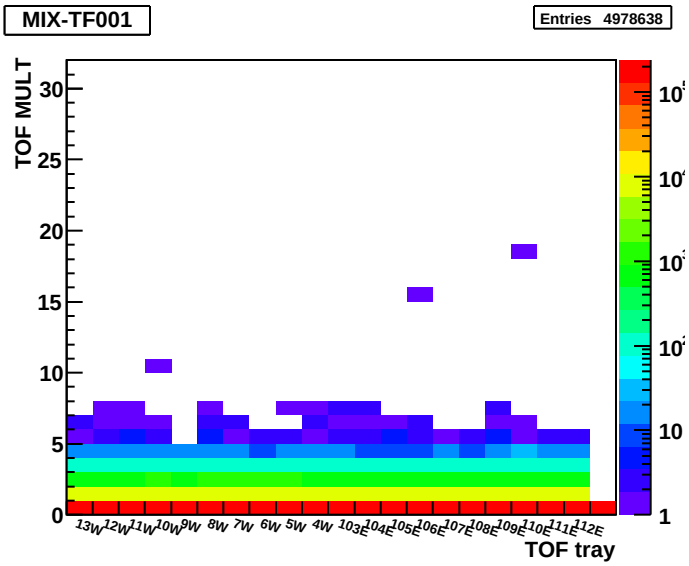

Endcap EMC L0 Input - High Tower
Entries 1.066851e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

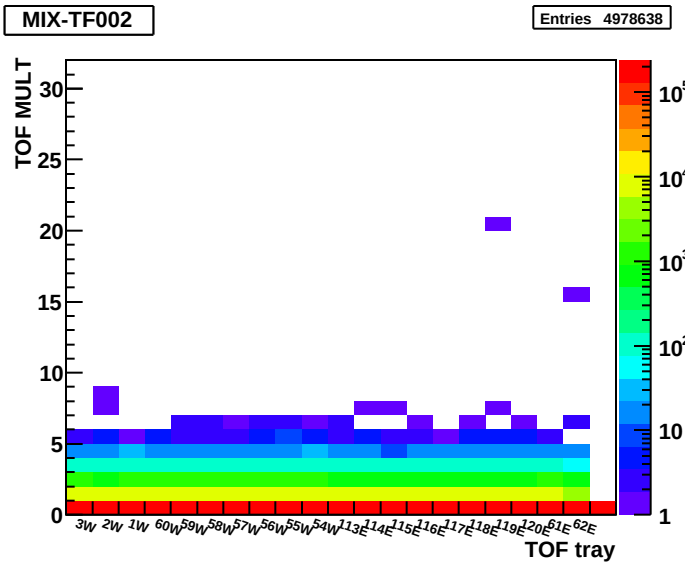

Endcap EMC L0 Input - Patch Sum
Entries 1.066851e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

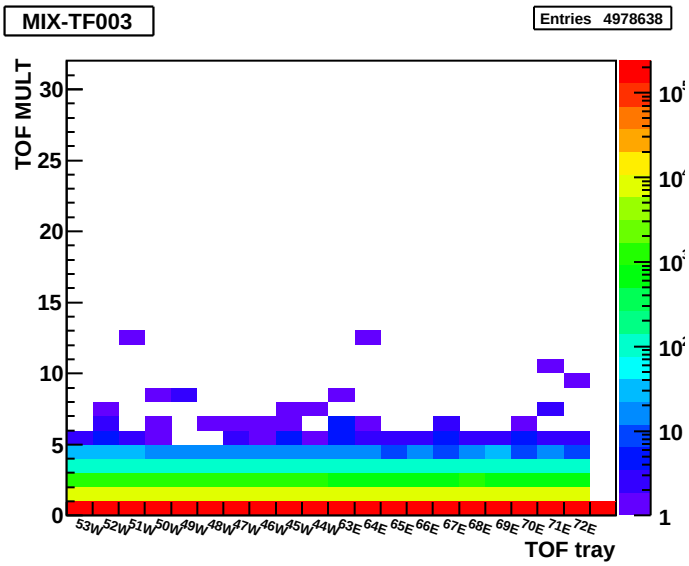

Barrel Jet Patches
Entries 2133702
JP ADC
JP ID

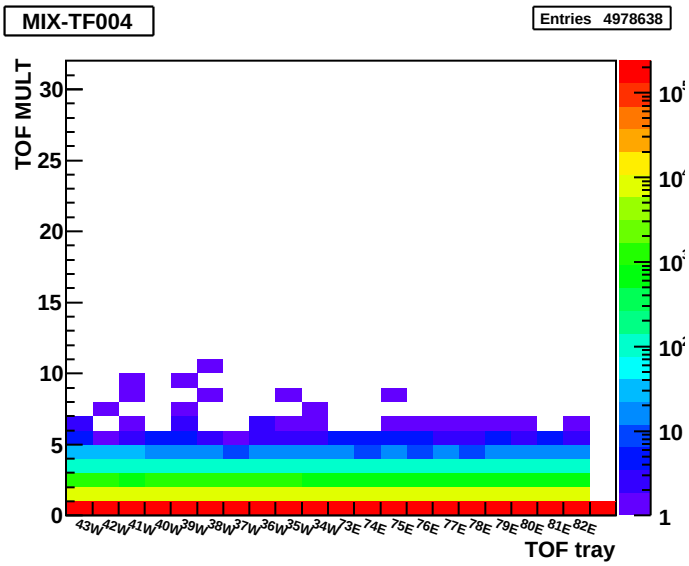

Endcap Jet Patches
Entries 711234
JP ADC
JP ID

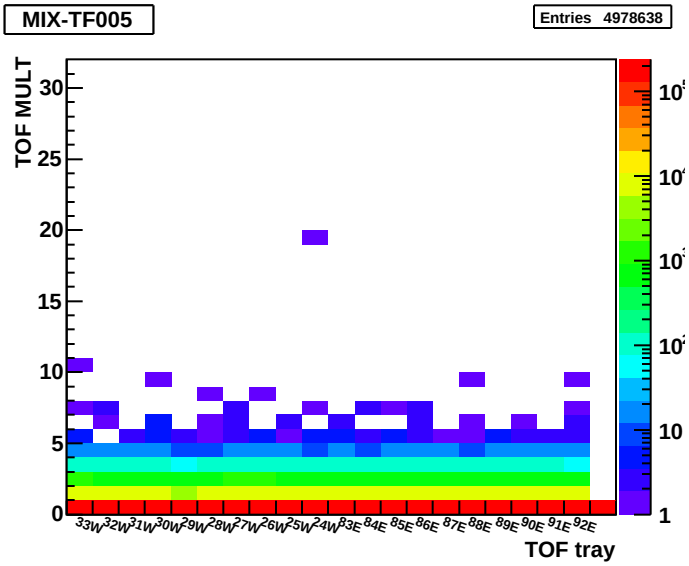

Hybrid Jet Patches
Entries 237078
JP ADC
JP ID

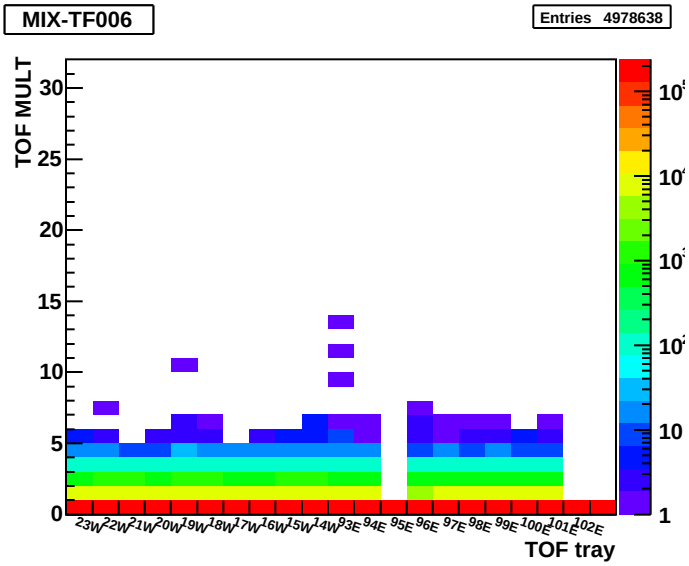

MIX-TF001
Entries 4978638
TOF MULT
TOF tray

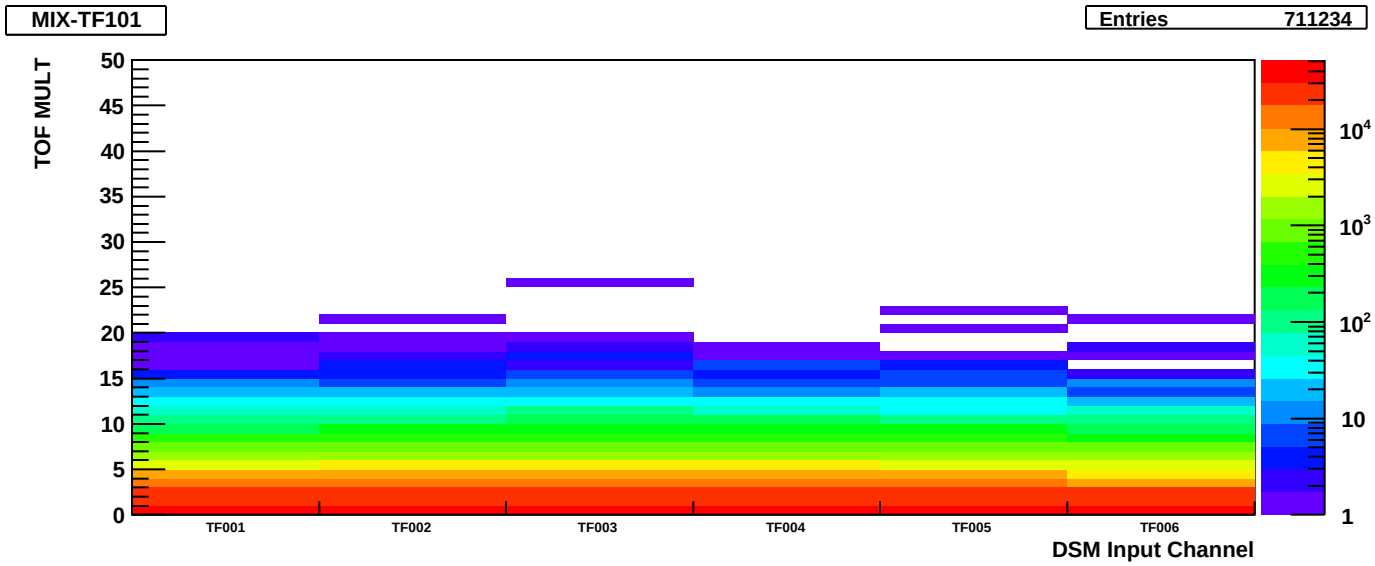

MIX-TF002
Entries 4978638
TOF MULT
TOF tray

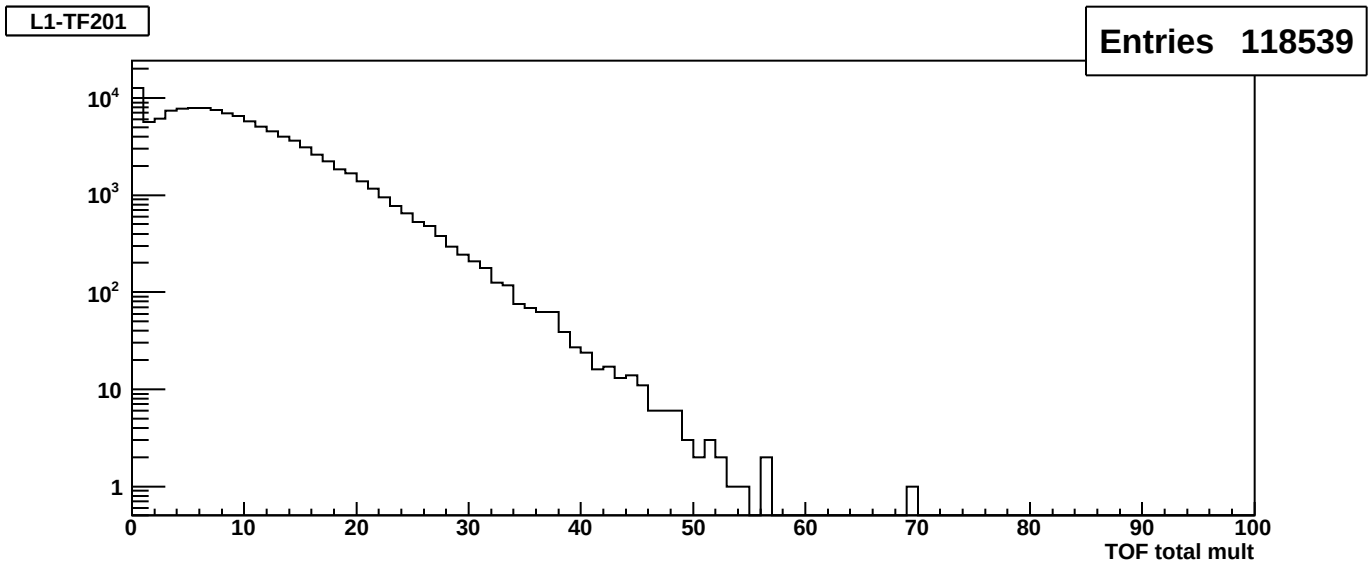

MIX-TF003
Entries 4978638
TOF MULT
TOF tray

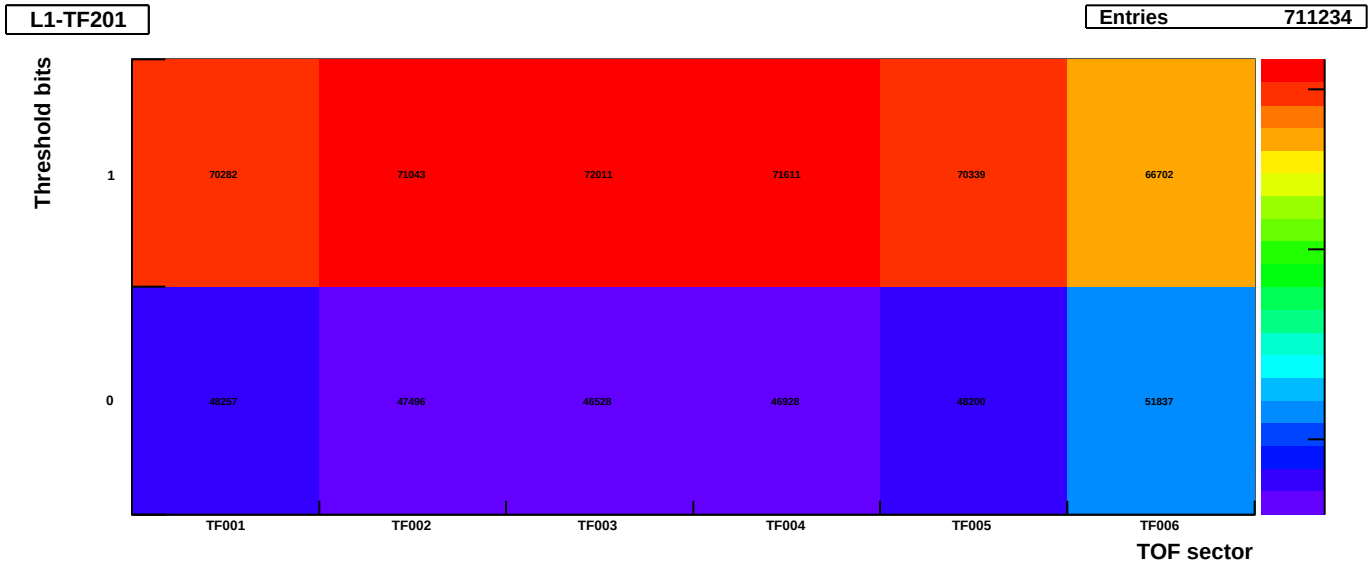

MIX-TF004
Entries 4978638
TOF MULT
TOF tray

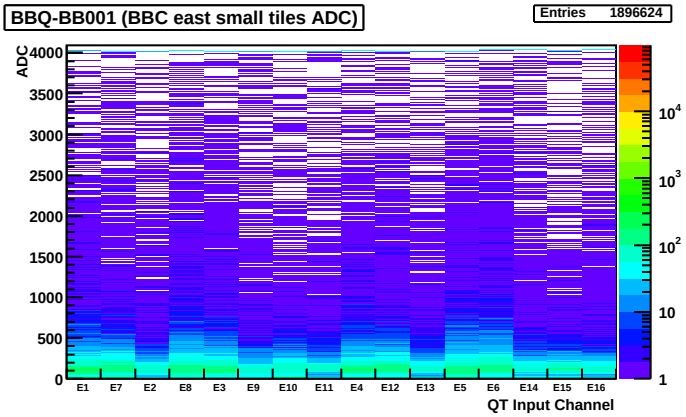

MIX-TF005
Entries 4978638
TOF MULT
TOF tray

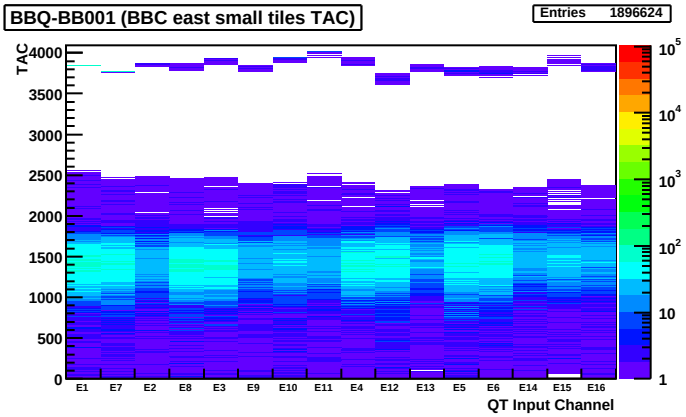

MIX-TF006
Entries 4978638
TOF MULT
TOF tray

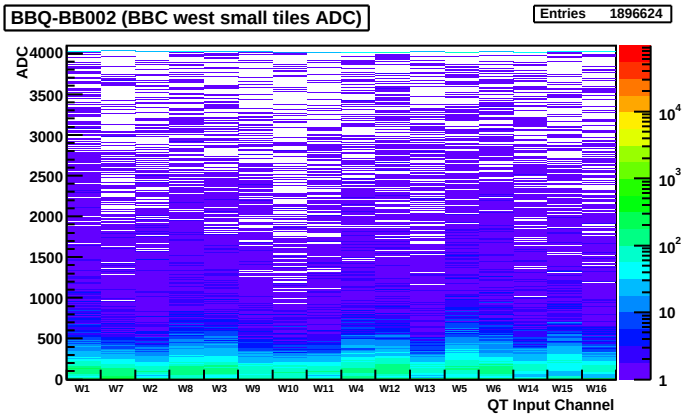

MIX-TF101
Entries 711234
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

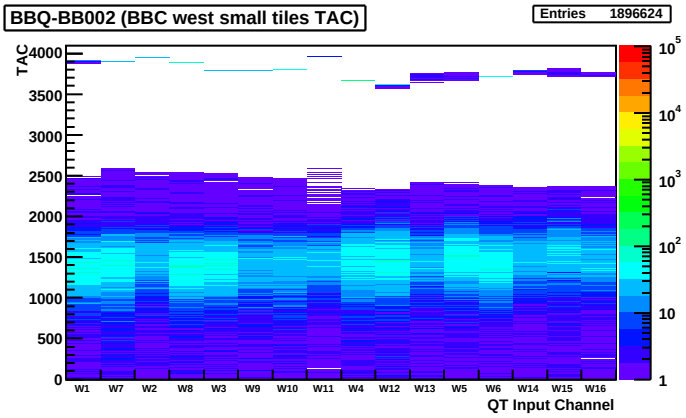

L1-TF201
Entries 118539
TOF total mult

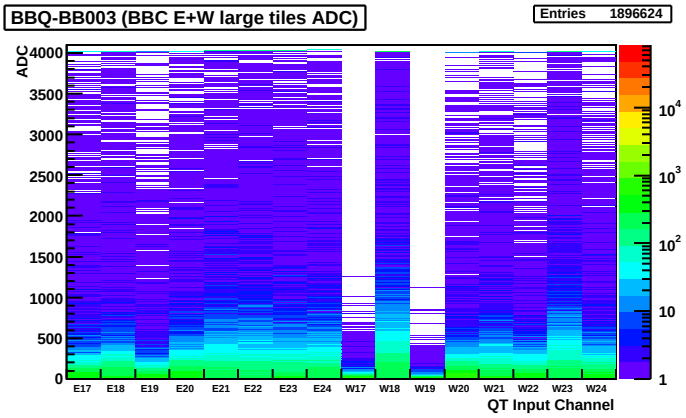

L1-TF201
Entries 711234
Threshold bits
70282
71043
72011
71611
70339
66702
48257
47496
46528
46928
48200
51837
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

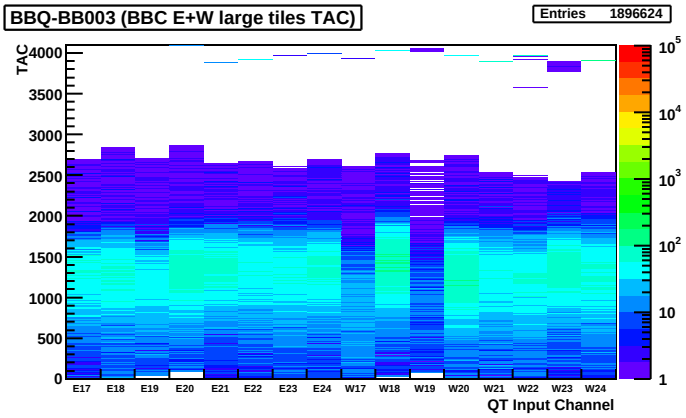

BBQ-BB001 (BBC east small tiles ADC)
Entries 1896624
ADC
QT Input Channel

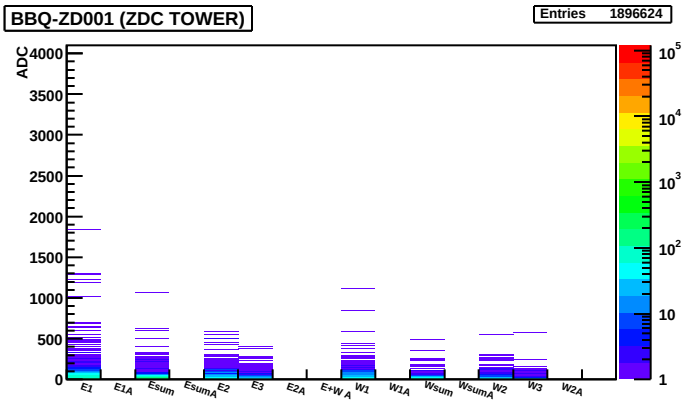

BBQ-BB001 (BBC east small tiles TAC)
Entries 1896624
TAC
QT Input Channel

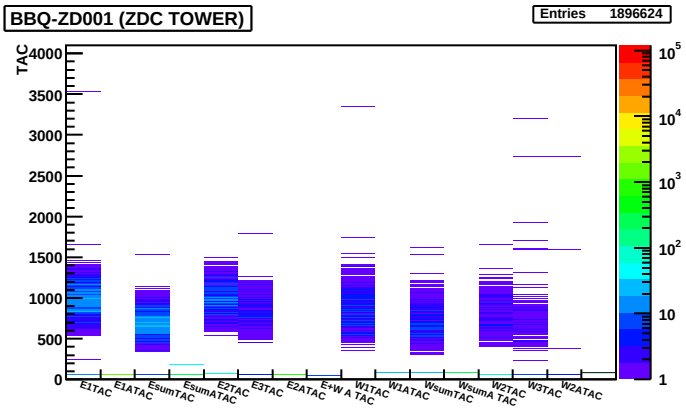

BBQ-BB002 (BBC west small tiles ADC)
Entries 1896624
ADC
QT Input Channel

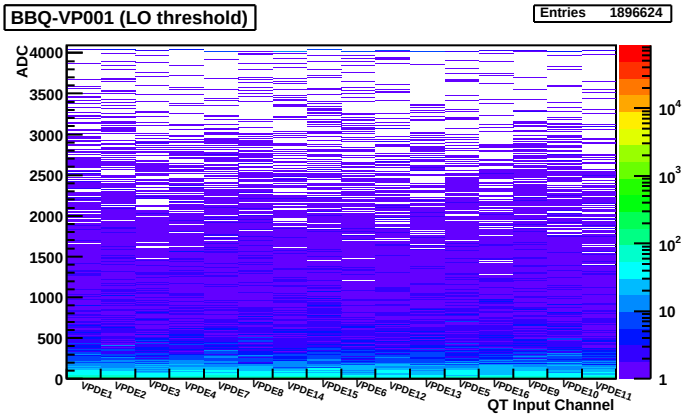

BBQ-BB002 (BBC west small tiles TAC)
Entries 1896624
TAC
QT Input Channel

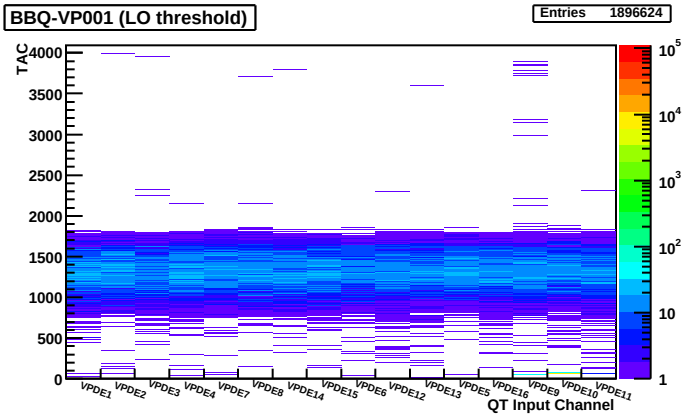

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1896624
ADC
QT Input Channel

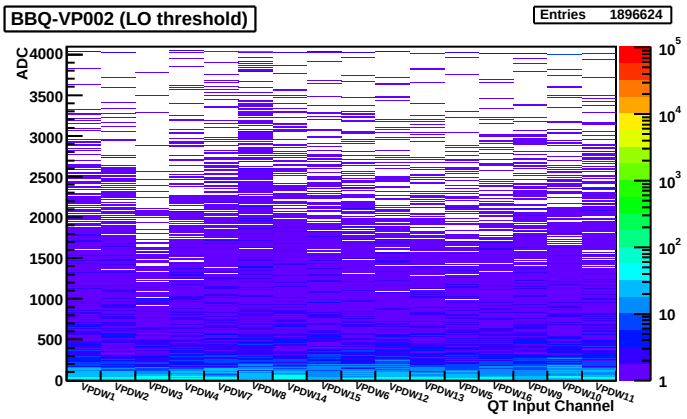

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1896624
TAC
QT Input Channel

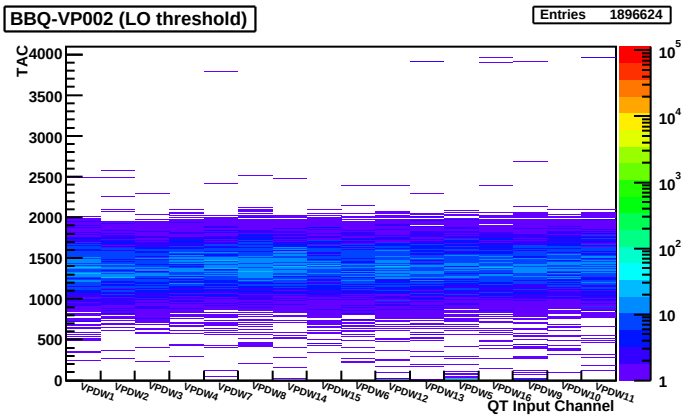

BBQ-ZD001 (ZDC TOWER)
Entries 1896624
ADC

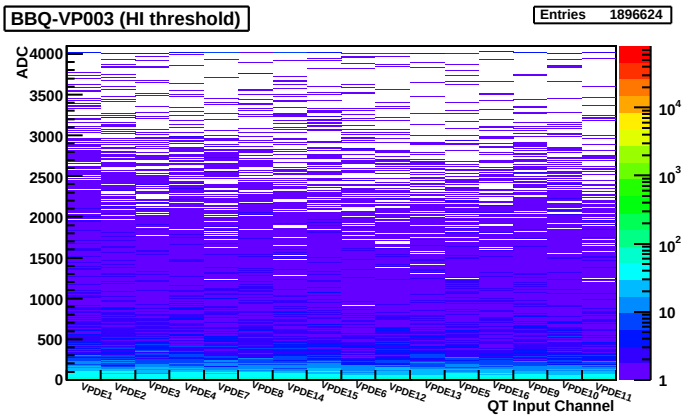

BBQ-ZD001 (ZDC TOWER)
Entries 1896624
TAC

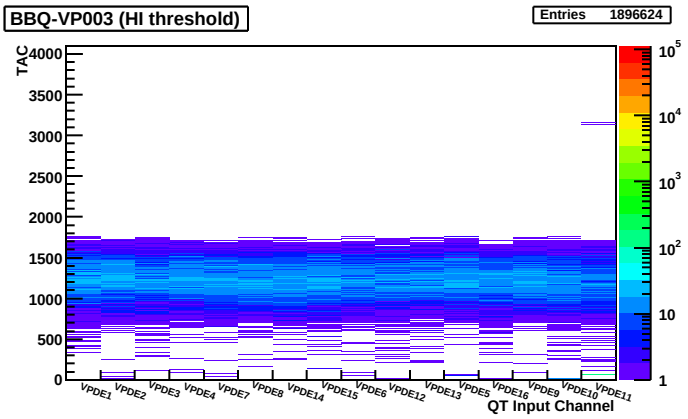

BBQ-VP001 (LO threshold)
Entries 1896624
ADC
QT Input Channel

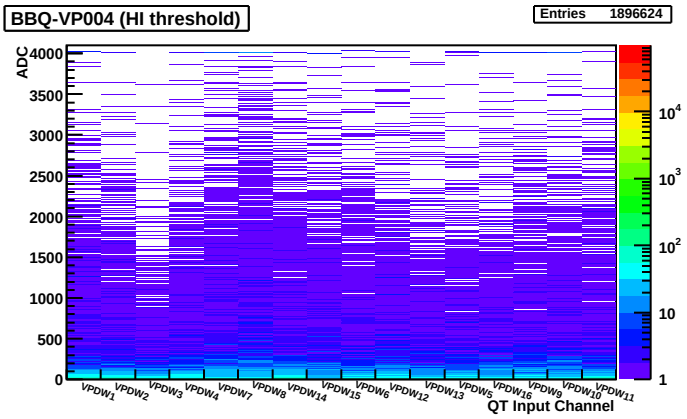

BBQ-VP001 (LO threshold)
Entries 1896624
TAC
QT Input Channel

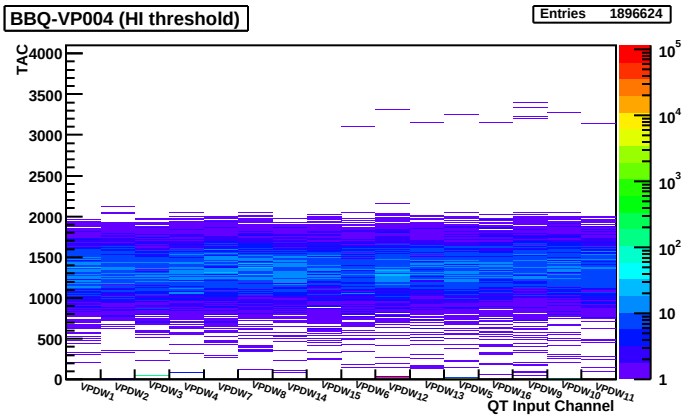

BBQ-VP002 (LO threshold)
Entries 1896624
ADC
QT Input Channel

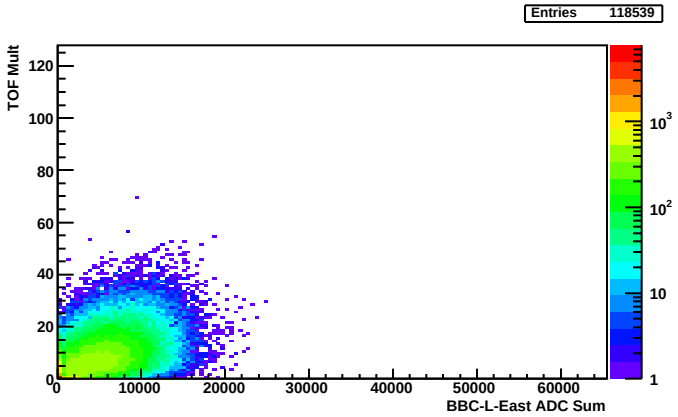

BBQ-VP002 (LO threshold)
Entries 1896624
TAC
QT Input Channel

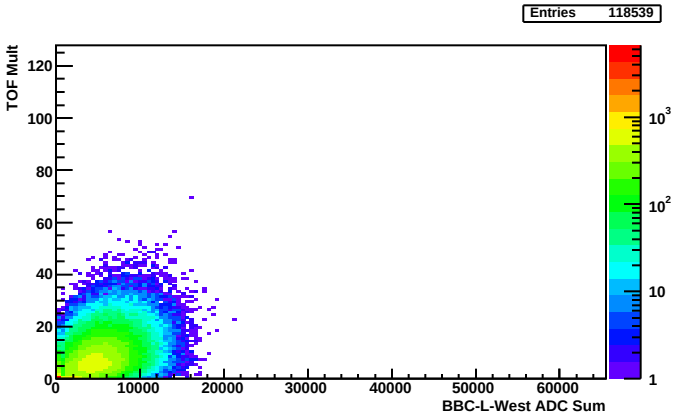

BBQ-VP003 (HI threshold)
Entries 1896624
ADC
QT Input Channel

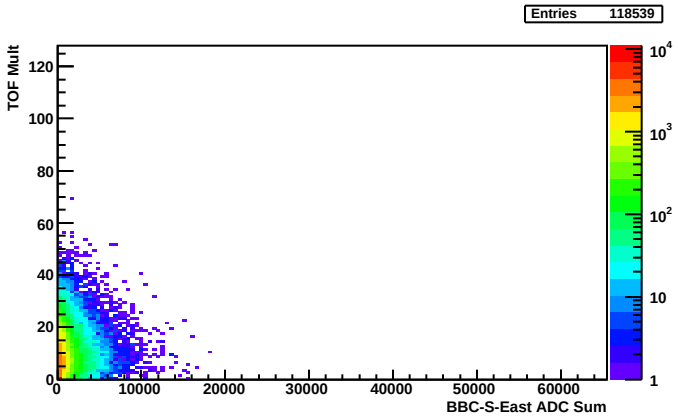

BBQ-VP003 (HI threshold)
Entries 1896624
TAC
QT Input Channel

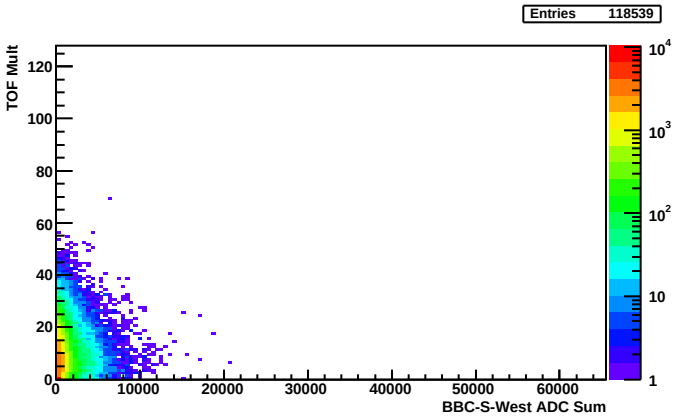

BBQ-VP004 (HI threshold)
Entries 1896624
ADC
QT Input Channel

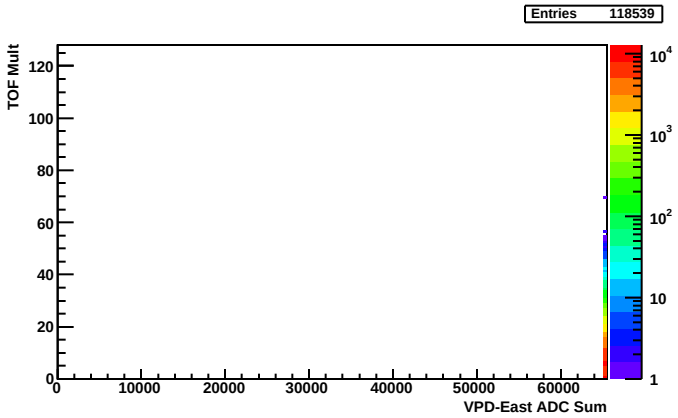

BBQ-VP004 (HI threshold)
Entries 1896624
TAC
QT Input Channel

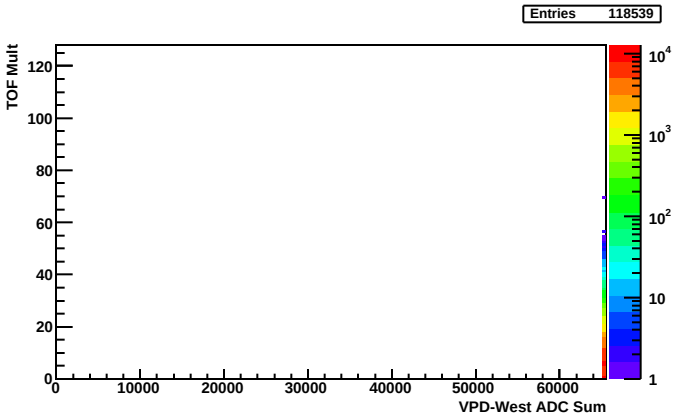

Entries 118539
TOF Mult
BBC-L-East ADC Sum

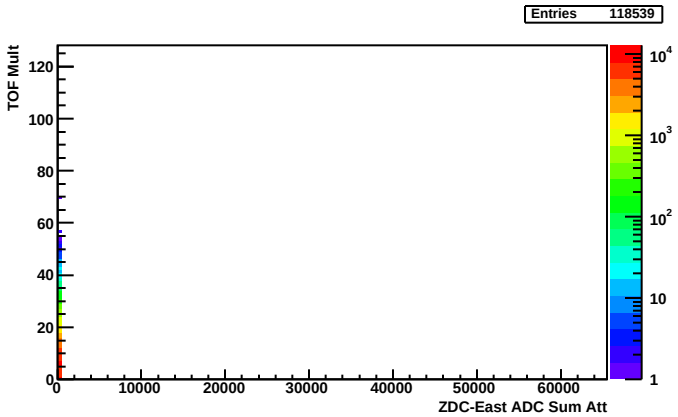

Entries 118539
TOF Mult
BBC-L-West ADC Sum

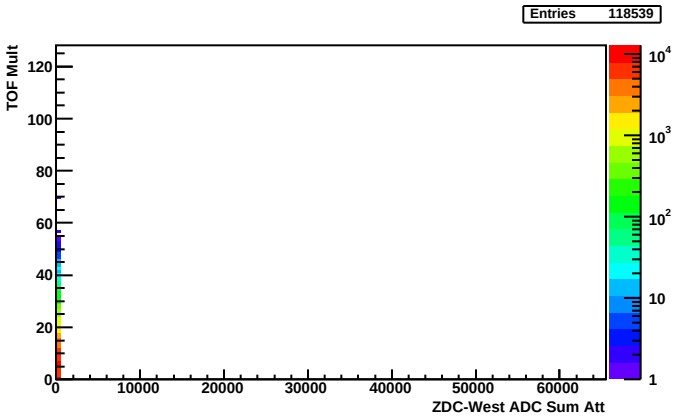

Entries 118539
TOF Mult
BBC-S-East ADC Sum

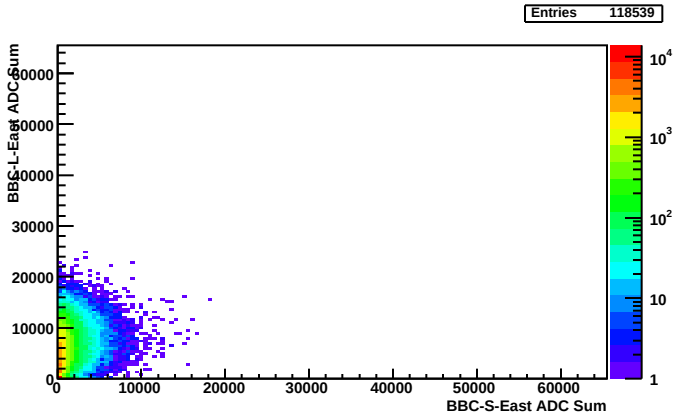

Entries 118539
TOF Mult
BBC-S-West ADC Sum

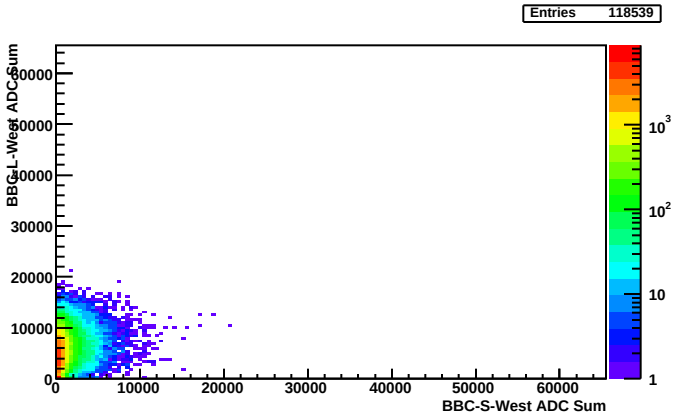

Entries 118539
TOF Mult
VPD-East ADC Sum

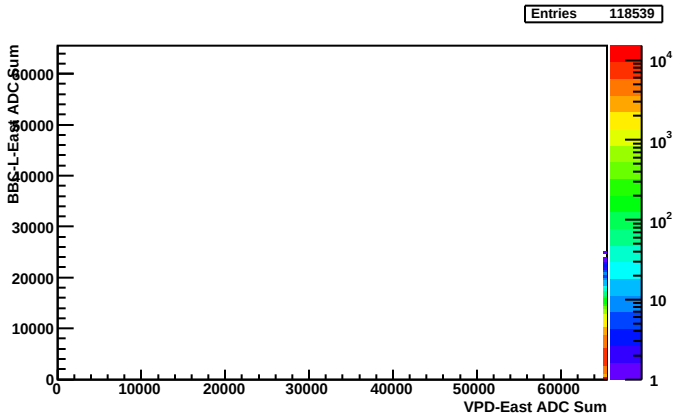

Entries 118539
TOF Mult
VPD-West ADC Sum

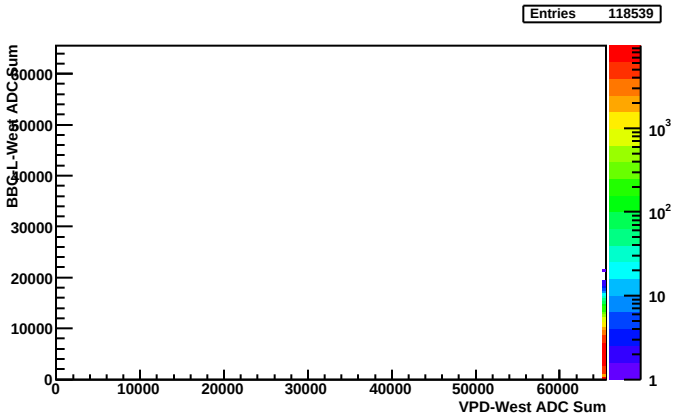

Entries 118539
TOF Mult
ZDC-East ADC Sum Att

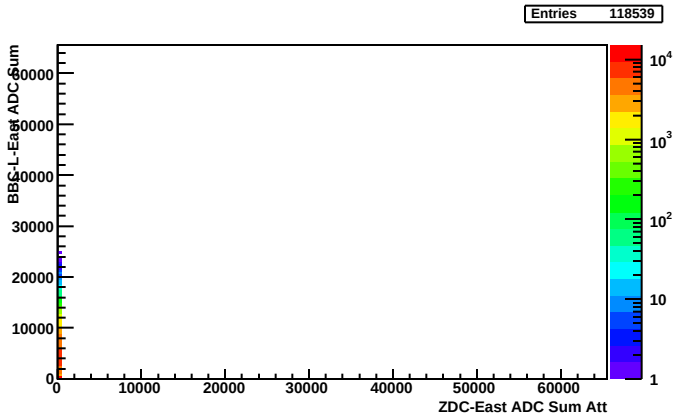

Entries 118539
TOF Mult
ZDC-West ADC Sum Att


Entries 118539
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 118539
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 118539
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 118539
BBC-L-West ADC Sum
VPD-West ADC Sum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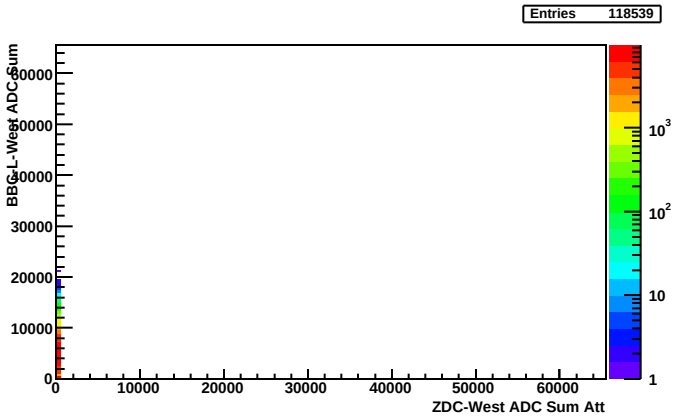
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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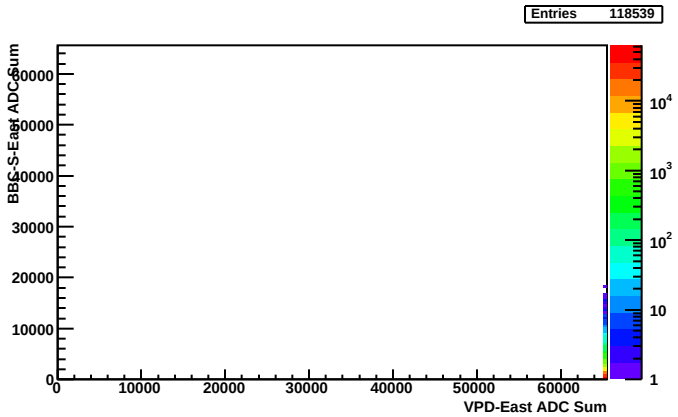
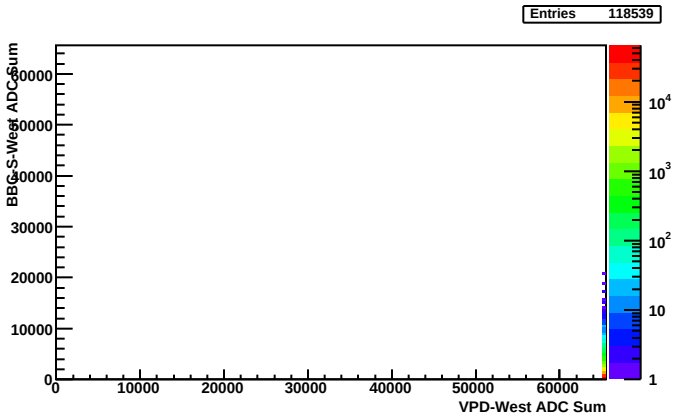
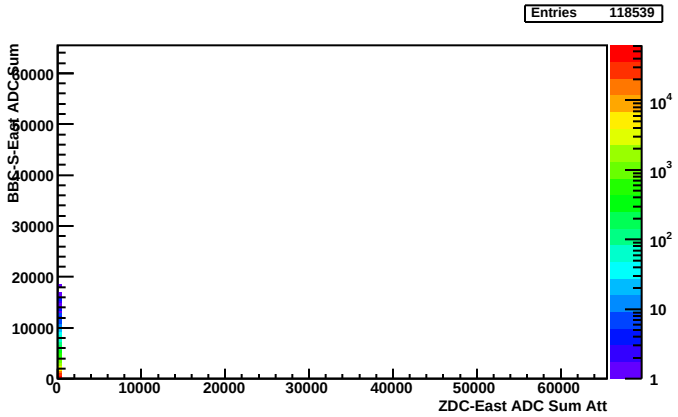
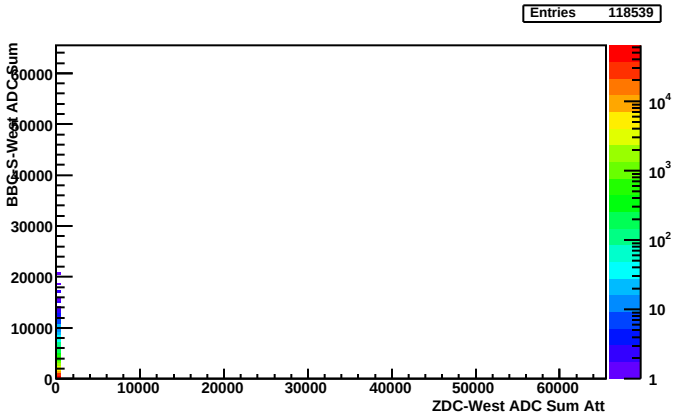
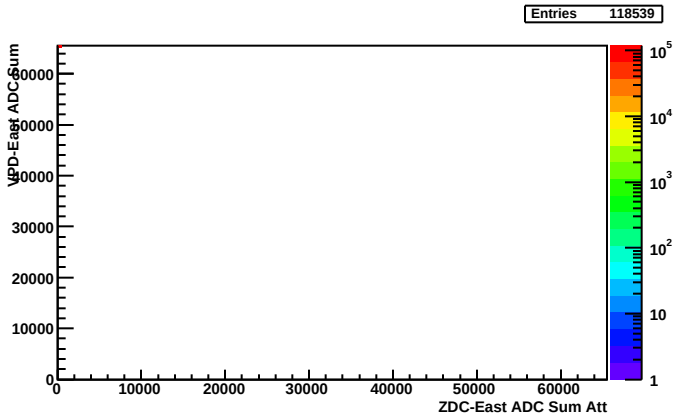
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

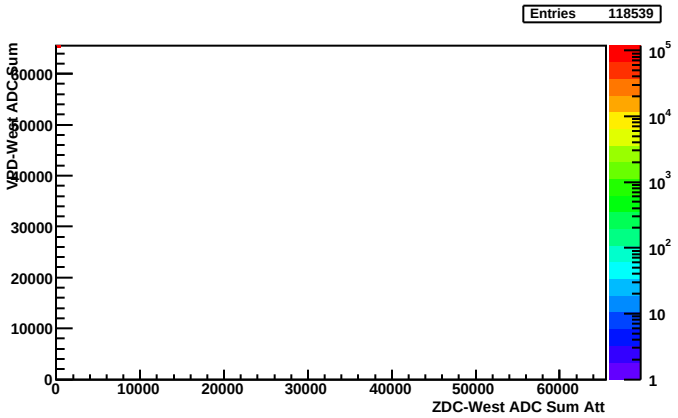

Entries 118539
BBC-S-East ADC Sum
VPD-East ADC Sum

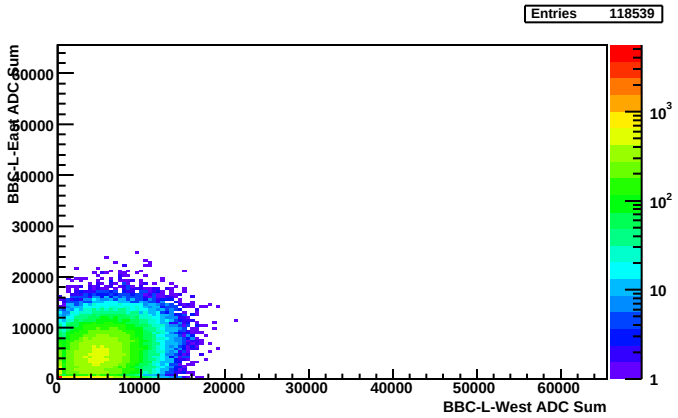

Entries 118539
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 118539
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 118539
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 118539
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 118539
VPD-West ADC Sum
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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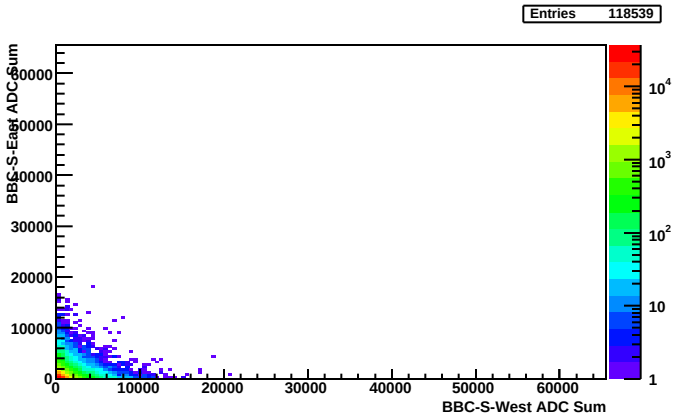
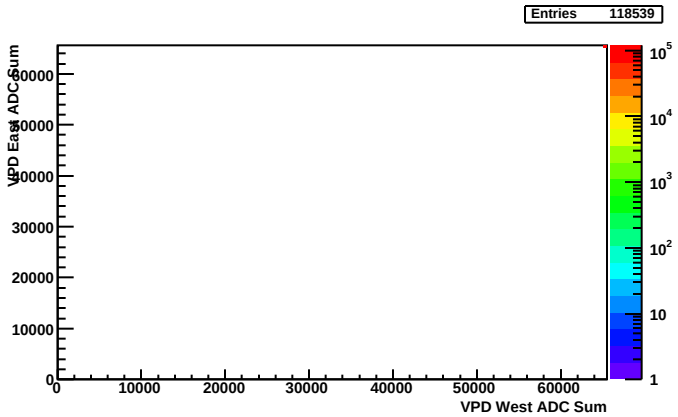
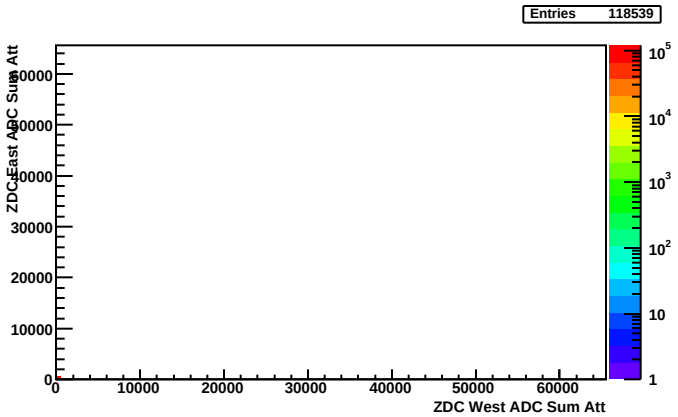
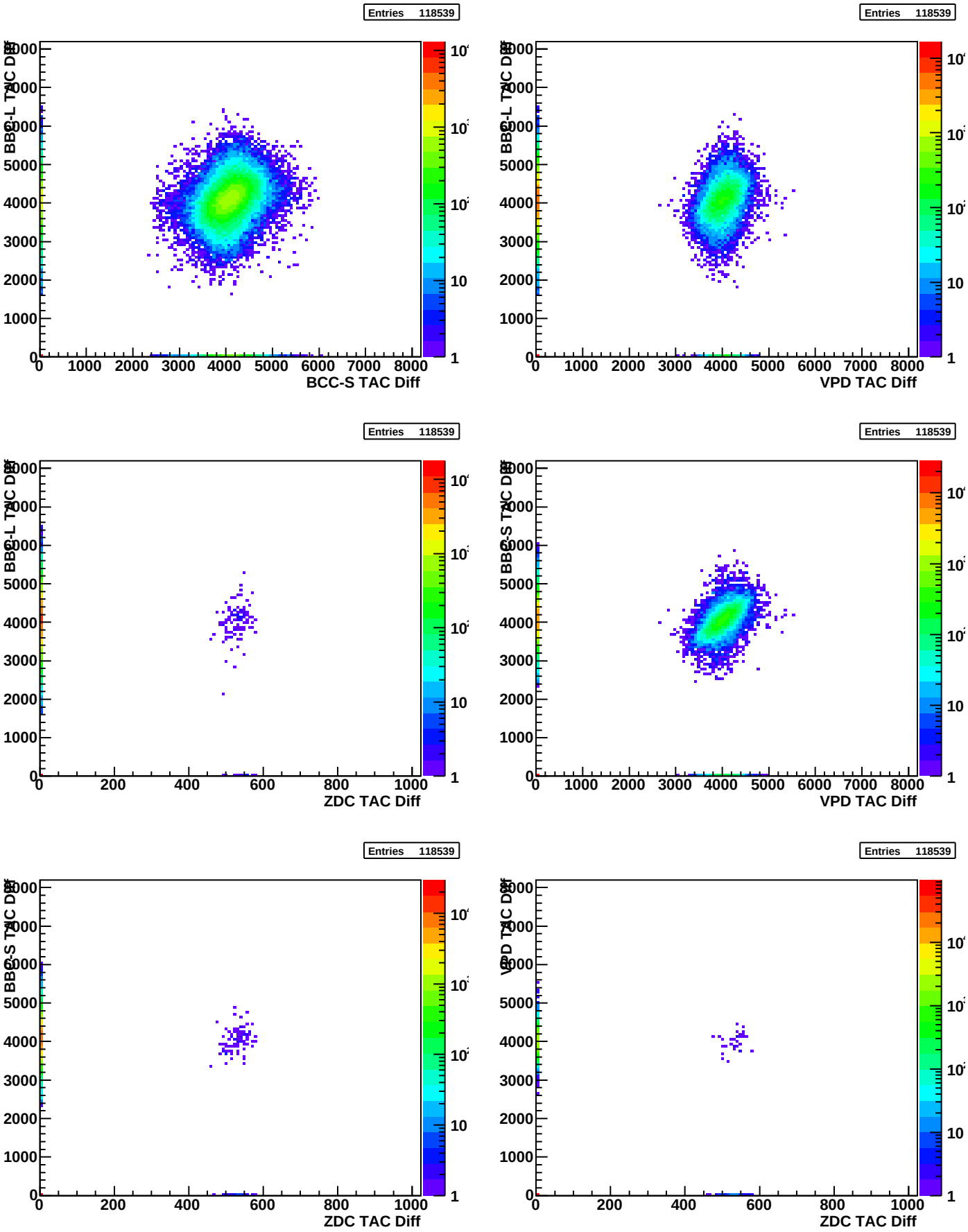
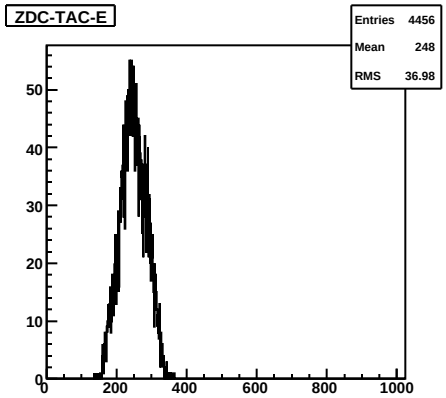
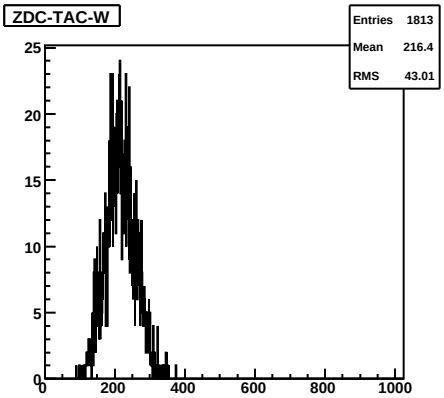

Entries 118539
BBC-S-East ADC Sum
BBC-S-West ADC Sum

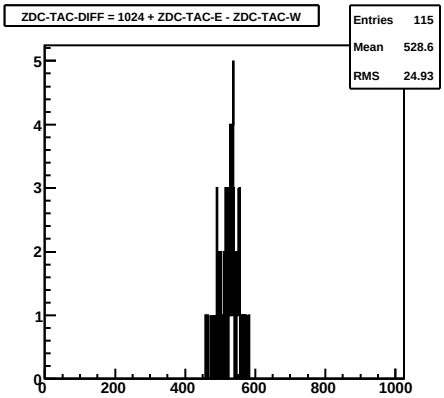

Entries 118539
VPD East ADC Sum
VPD West ADC Sum

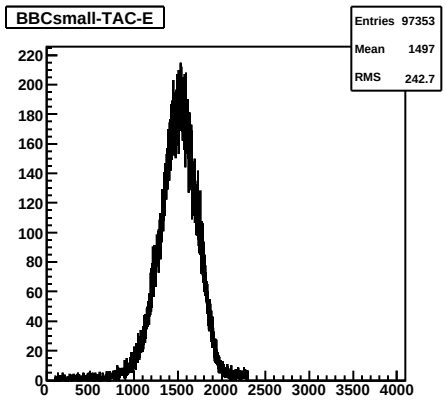

Entries 118539
ZDC East ADC Sum Att
ZDC West ADC Sum Att

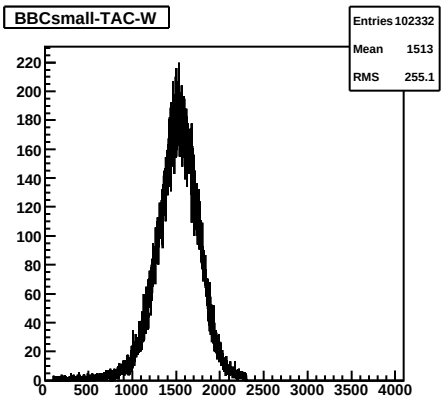

Entries 118539
BBC-L TAC Diff
BCC-S TAC Diff
Entries 118539
BBC-L TAC Diff
VPD TAC Diff
Entries 118539
BBC-L TAC Diff
ZDC TAC Diff
Entries 118539
BBC-S TAC Diff
VPD TAC Diff
Entries 118539
BBC-S TAC Diff
ZDC TAC Diff
Entries 118539
VPD TAC Diff
ZDC TAC Diff

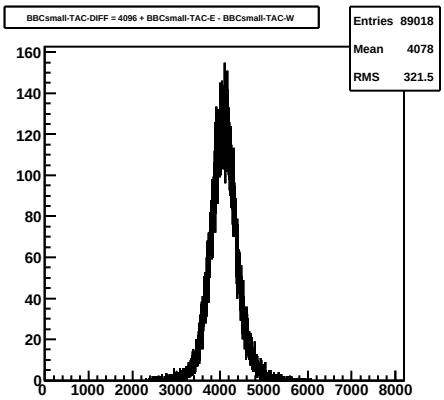

ZDC-TAC-E
Entries 4456
Mean 248
RMS 36.98

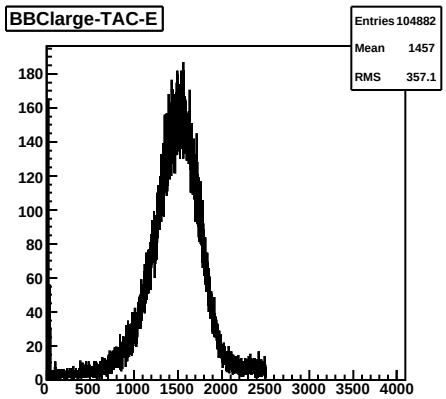

ZDC-TAC-W
Entries 1813
Mean 216.4
RMS 43.01

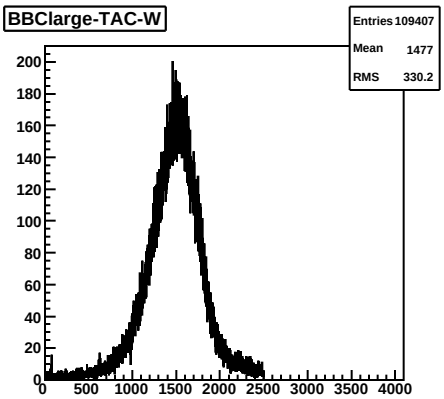

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 115
Mean 528.6
RMS 24.93

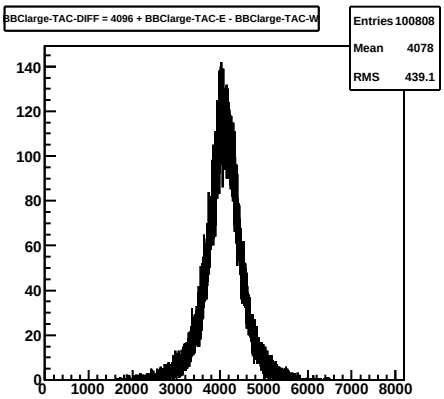

BBCsmall-TAC-E
Entries 97353
Mean 1497
RMS 242.7

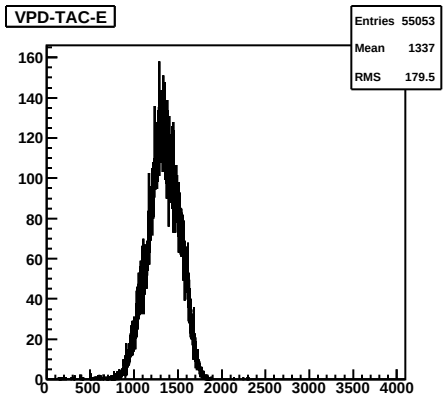

BBCsmall-TAC-W
Entries 102332
Mean 1513
RMS 255.1

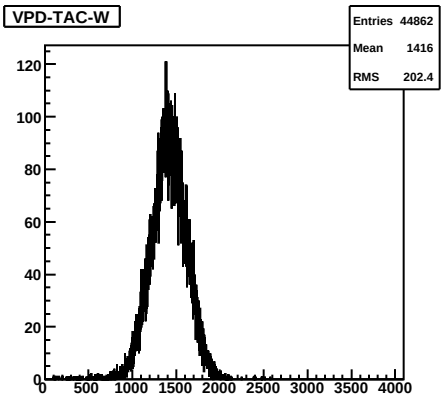

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 89018
Mean 4078
RMS 321.5

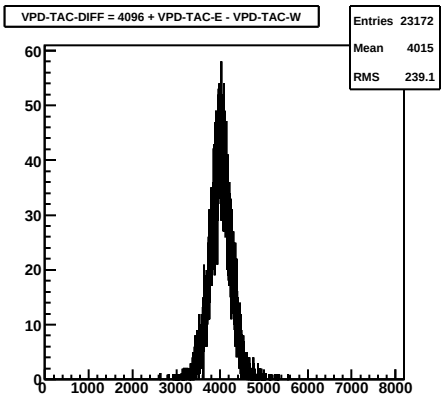

BBClarge-TAC-E
Entries 104882
Mean 1457
RMS 357.1

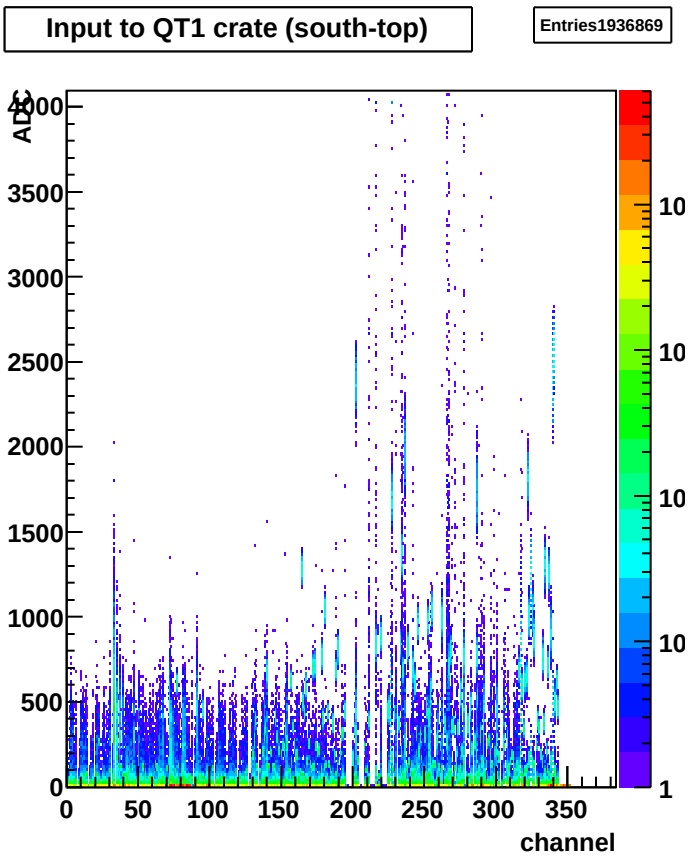

BBClarge-TAC-W
Entries 109407
Mean 1477
RMS 330.2

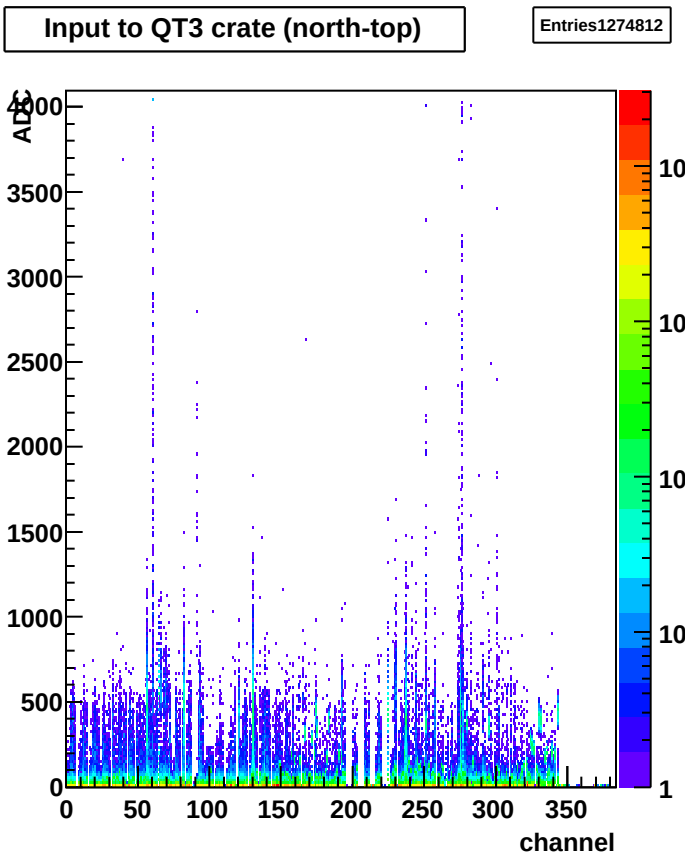

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 100808
Mean 4078
RMS 439.1

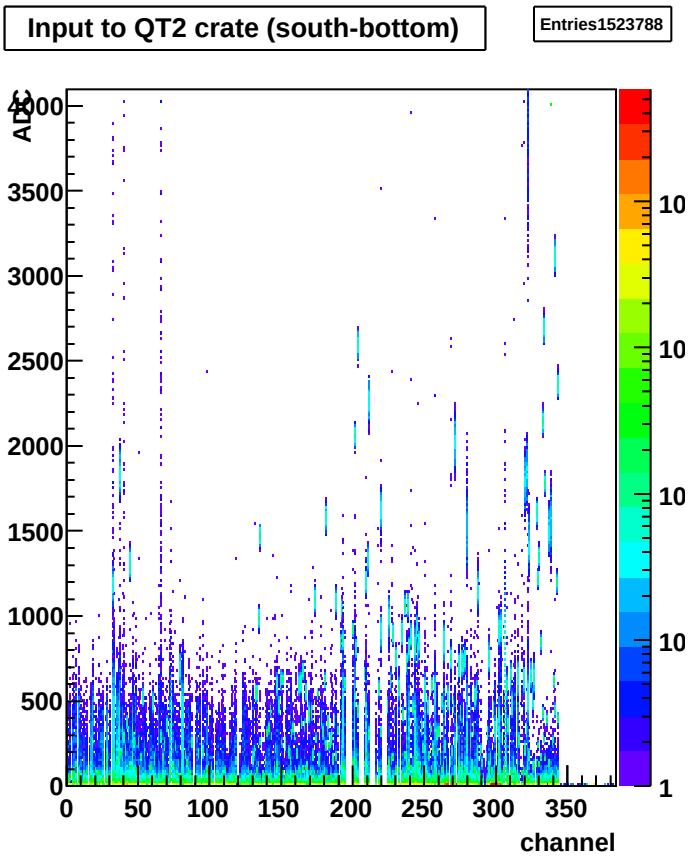

VPD-TAC-E
Entries 55053
Mean 1337
RMS 179.5

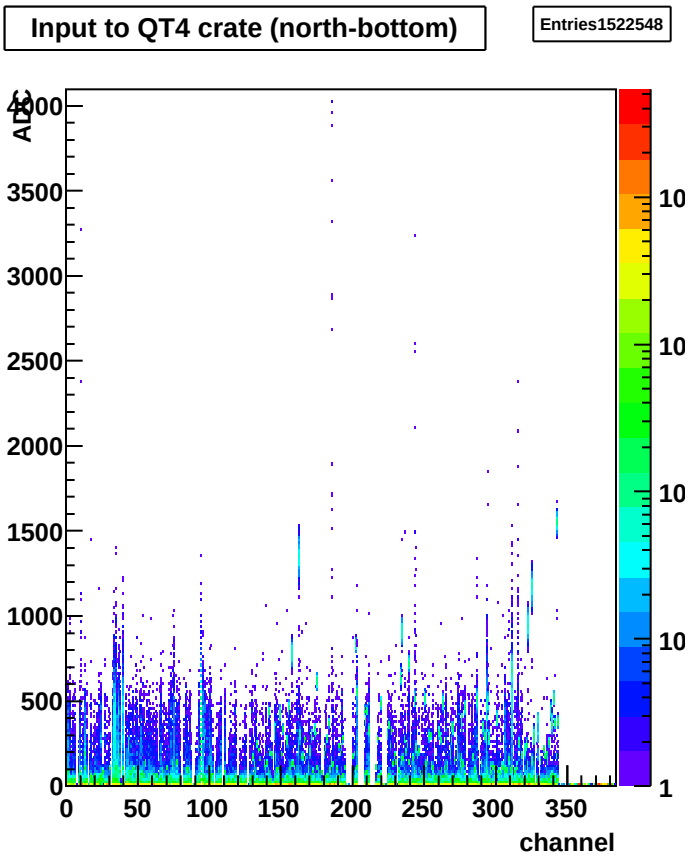

VPD-TAC-W
Entries 44862
Mean 1416
RMS 202.4

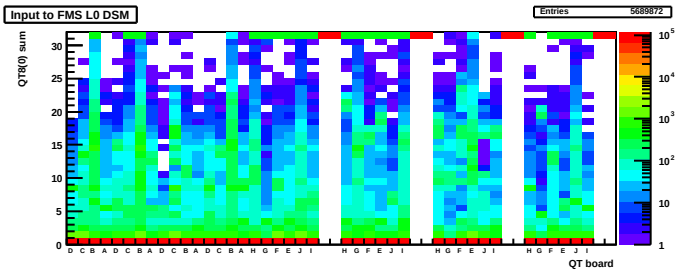

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 23172
Mean 4015
RMS 239.1

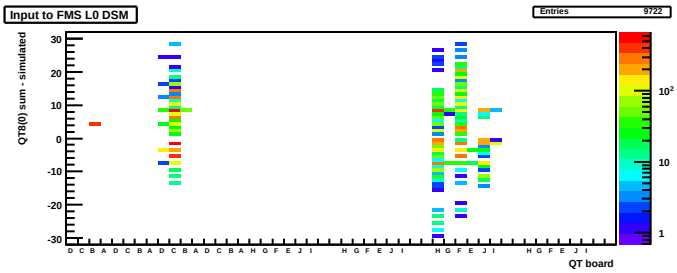

Input to QT1 crate (south-top)
Entries1936869
ADC
channel

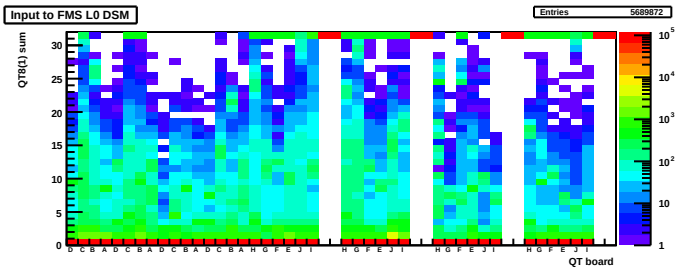

Input to QT3 crate (north-top)
Entries1274812
ADC
channel

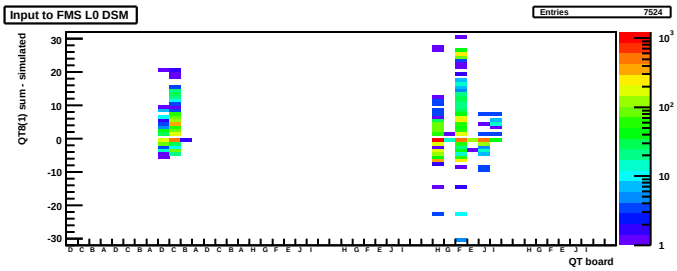

Input to QT2 crate (south-bottom)
Entries1523788
ADC
channel

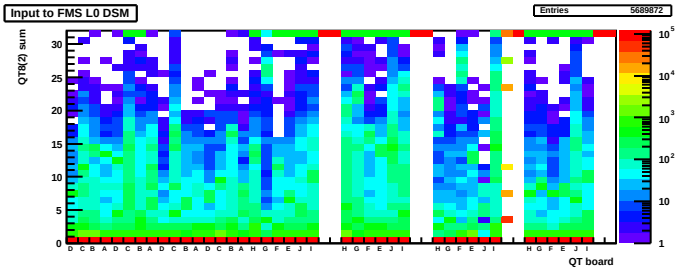

Input to QT4 crate (north-bottom)
Entries1522548
ADC
channel

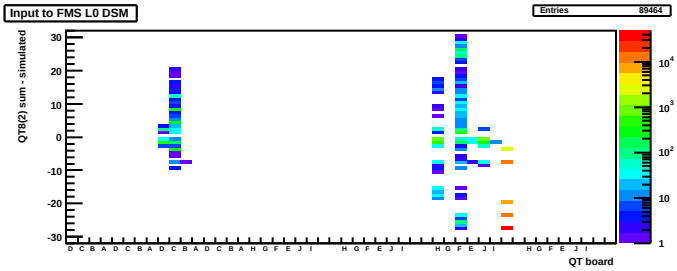

Input to FMS L0 DSM
Entries 5689872
QT8(0) sum
QT board

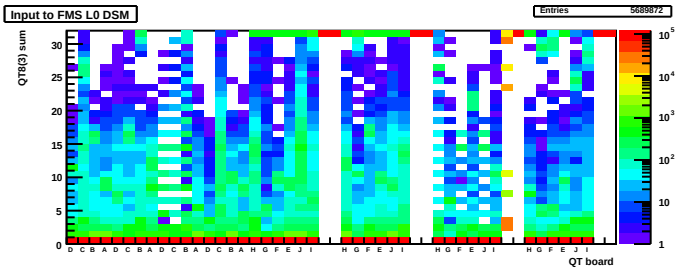

Input to FMS L0 DSM
Entries 9722
QT8(0) sum - simulated
QT board

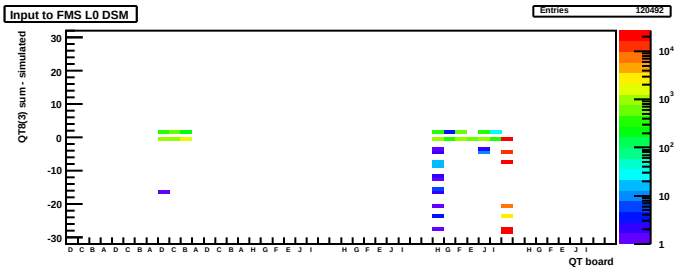

Input to FMS L0 DSM
Entries 5689872
QT8(1) sum
QT board

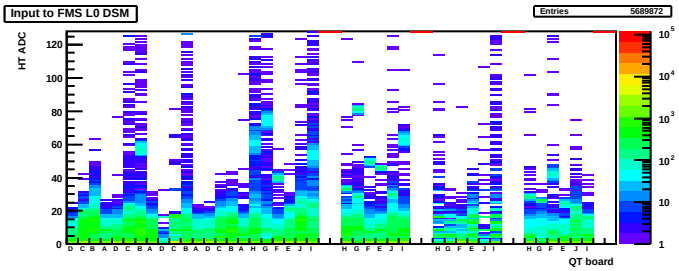

Input to FMS L0 DSM
Entries 7524
QT8(1) sum - simulated
QT board

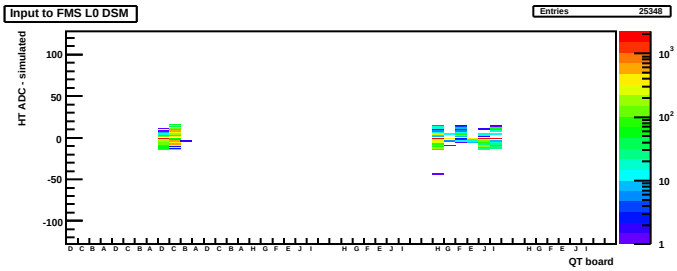

Input to FMS L0 DSM
Entries 5689872
QT8(2) sum
QT board

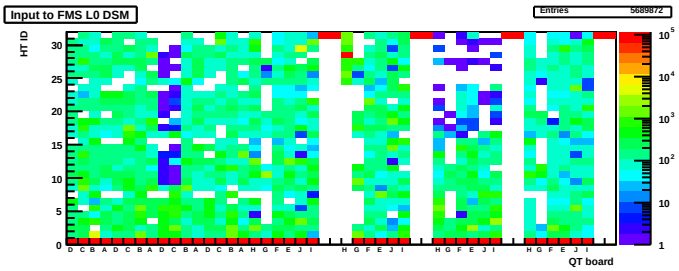

Input to FMS L0 DSM
Entries 89464
QT8(2) sum - simulated
QT board

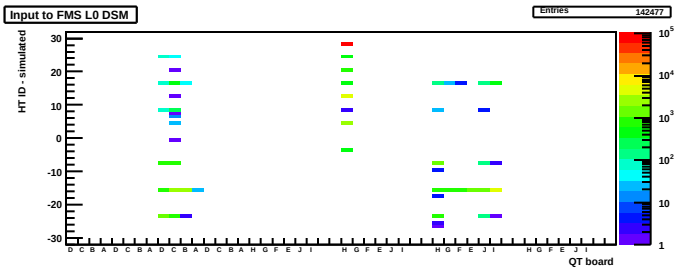

Input to FMS L0 DSM
Entries 5689872
QT8(3) sum
QT board

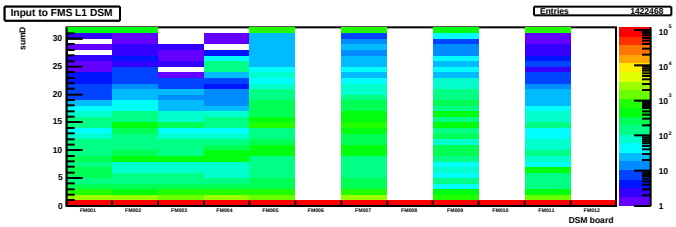

Input to FMS L0 DSM
Entries 120492
QT8(3) sum - simulated
QT board

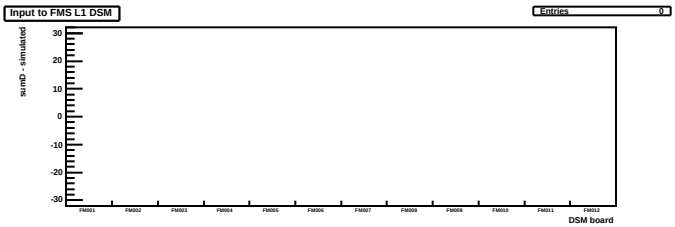

Input to FMS L0 DSM
Entries 5689872
HT ADC
QT board

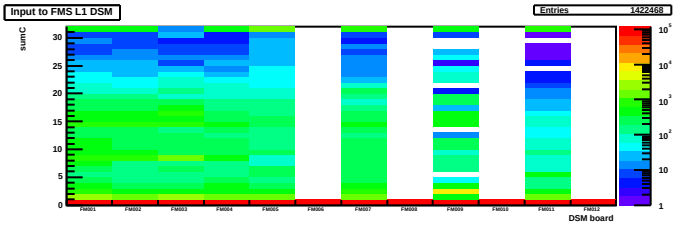

Input to FMS L0 DSM
Entries 25348
HT ADC - simulated
QT board

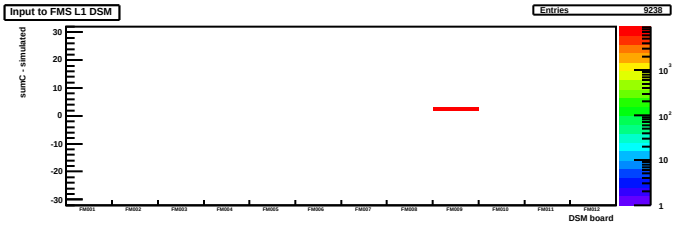

Input to FMS L0 DSM
Entries 5689872
HT ID
QT board

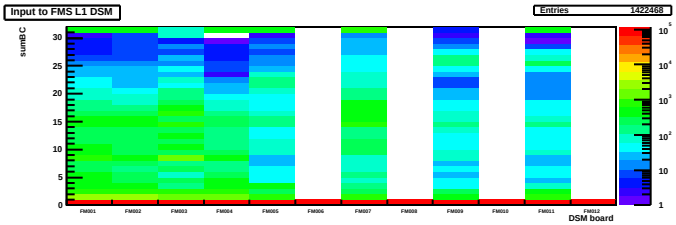

Input to FMS L0 DSM
Entries 142477
HT ID - simulated
QT board

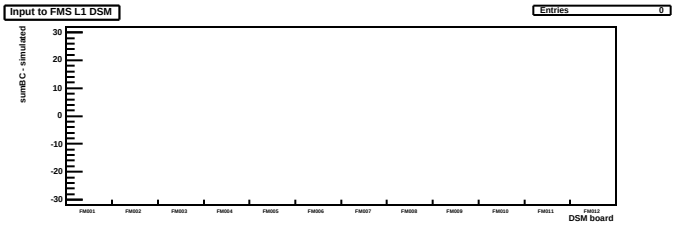

Input to FMS L1 DSM
Entries 1422468
sumD
DSM board

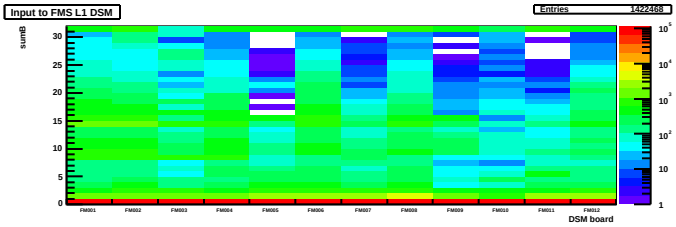

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

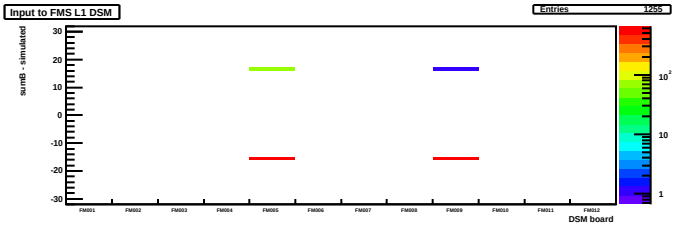

Input to FMS L1 DSM
Entries 1422468
sumC
DSM board

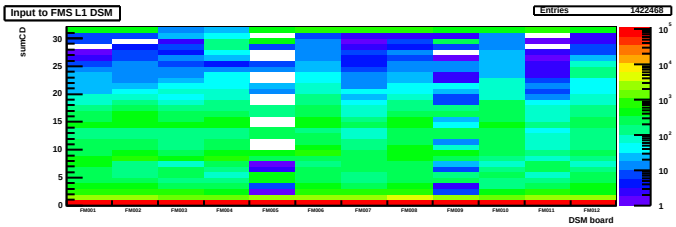

Input to FMS L1 DSM
Entries 9238
sumC - simulated
DSM board

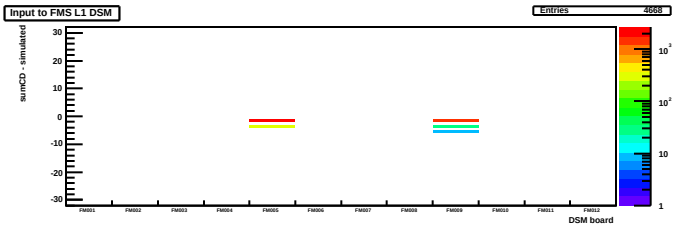

Input to FMS L1 DSM
Entries 1422468
sumBC
DSM board

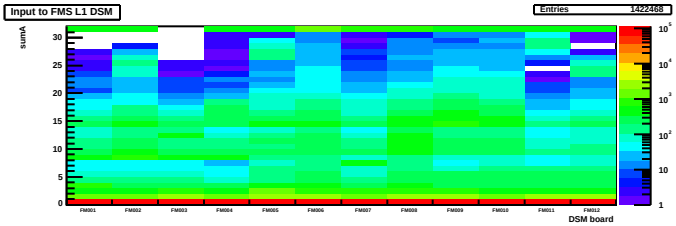

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

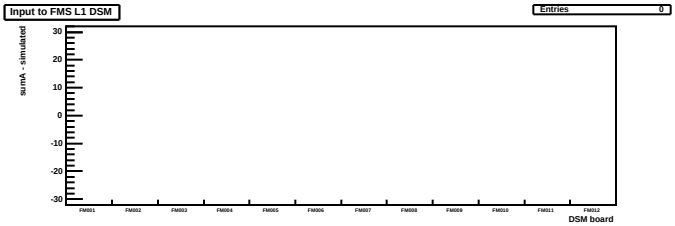

Input to FMS L1 DSM
Entries 1422468
sumB
DSM board

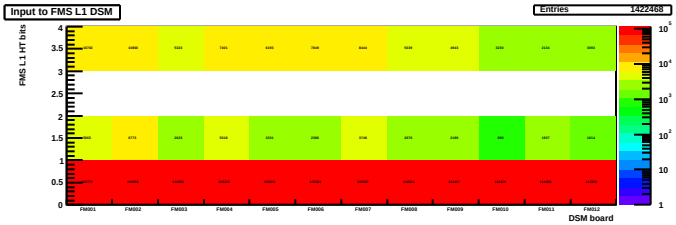

Input to FMS L1 DSM
Entries 1255
sumB - simulated
DSM board

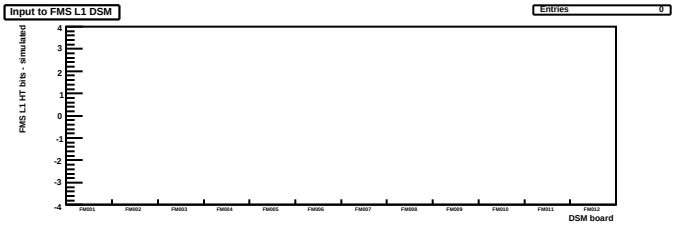

Input to FMS L1 DSM
Entries 1422468
sumCD
DSM board

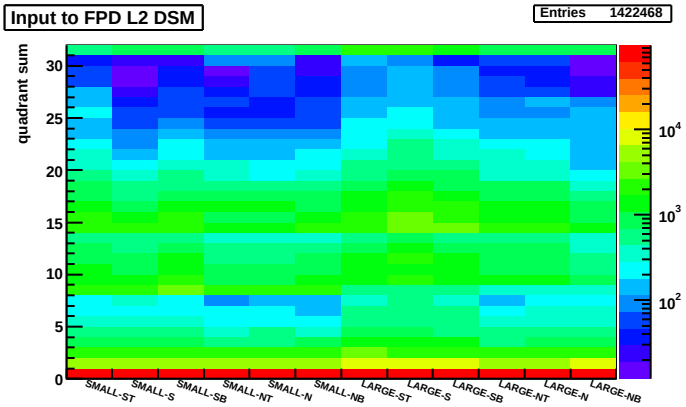

Input to FMS L1 DSM
Entries 4668
sumCD - simulated
DSM board

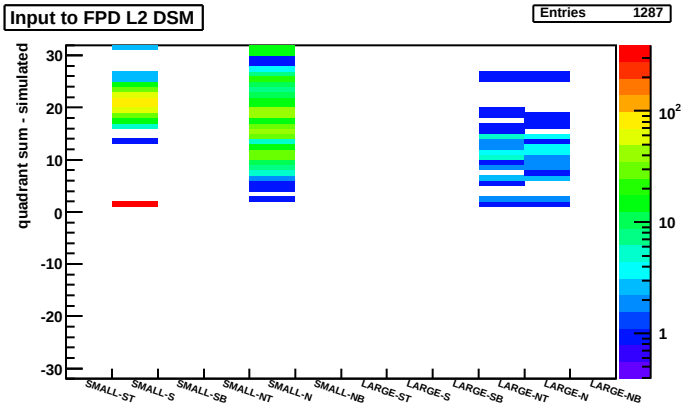

Input to FMS L1 DSM
Entries 1422468
sumA
DSM board

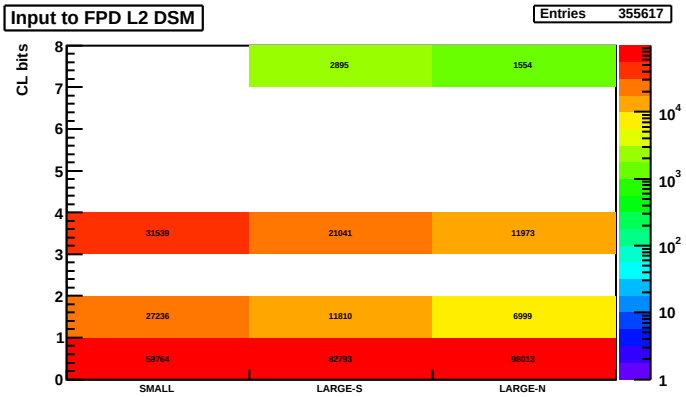

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

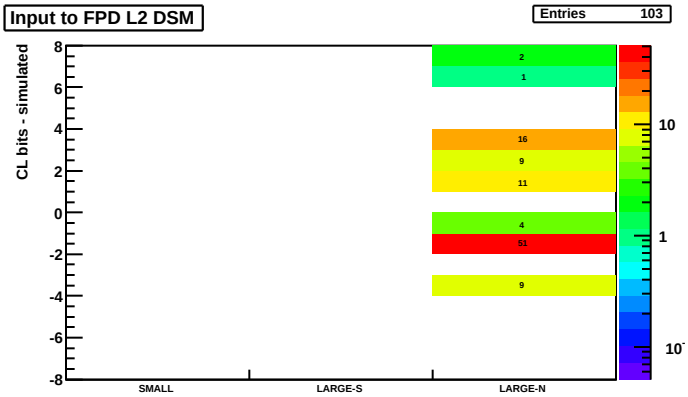

Input to FMS L1 DSM
Entries 1422468
FMS L1 HT bits
DSM board

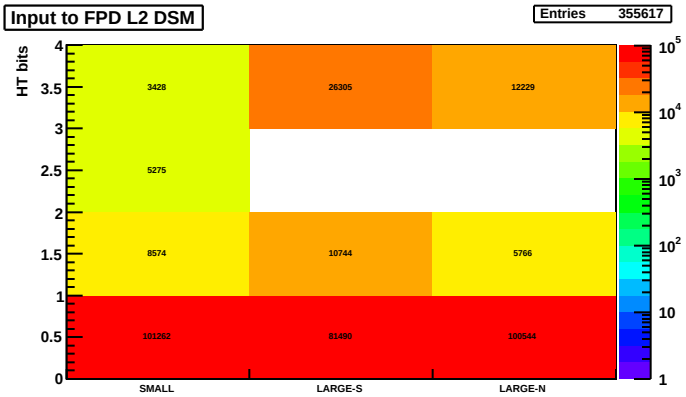

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

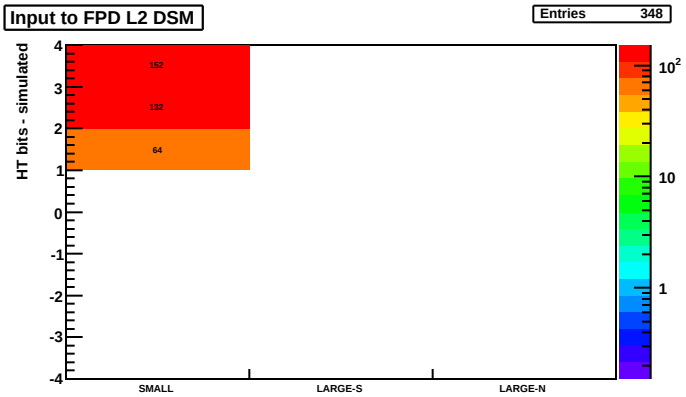

Input to FPD L2 DSM
Entries 1422468
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

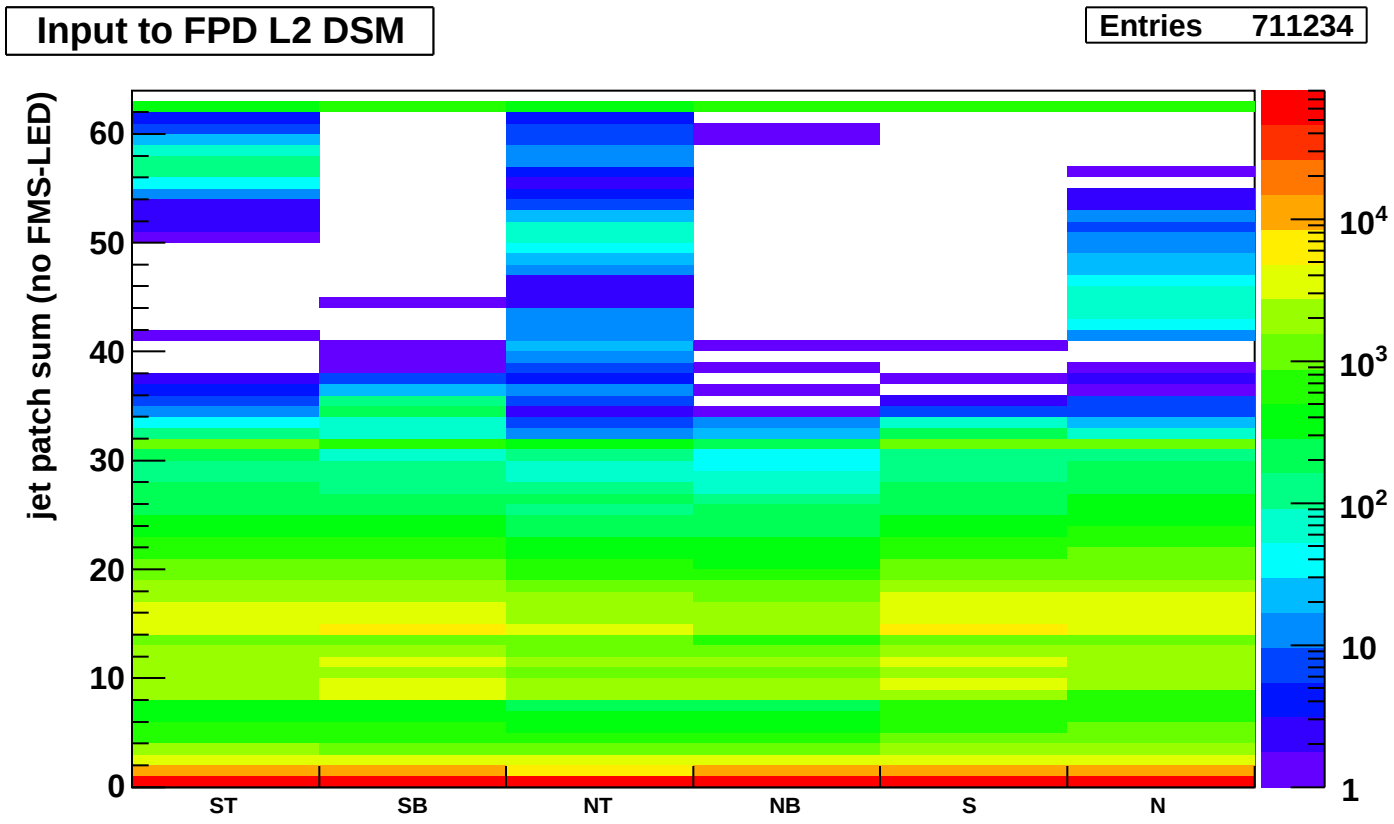

Input to FPD L2 DSM
Entries 1287
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

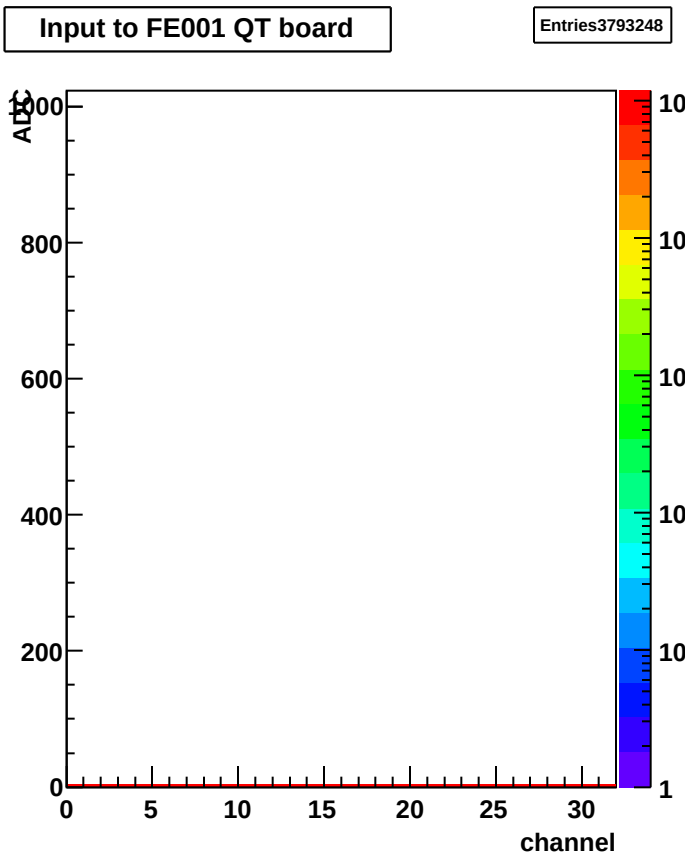

Input to FPD L2 DSM
Entries 355617
CL bits
2895
1554
31539
21041
11973
27236
11810
6999
59764
82793
98013
SMALL
LARGE-S
LARGE-N

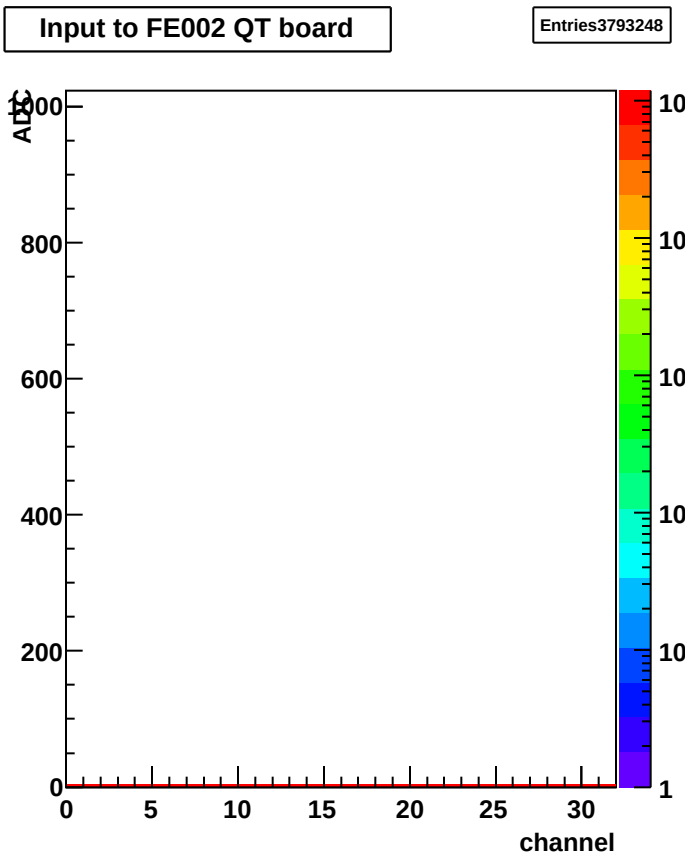

Input to FPD L2 DSM
Entries 103
CL bits - simulated
2
1
16
9
11
4
51
9
SMALL
LARGE-S
LARGE-N

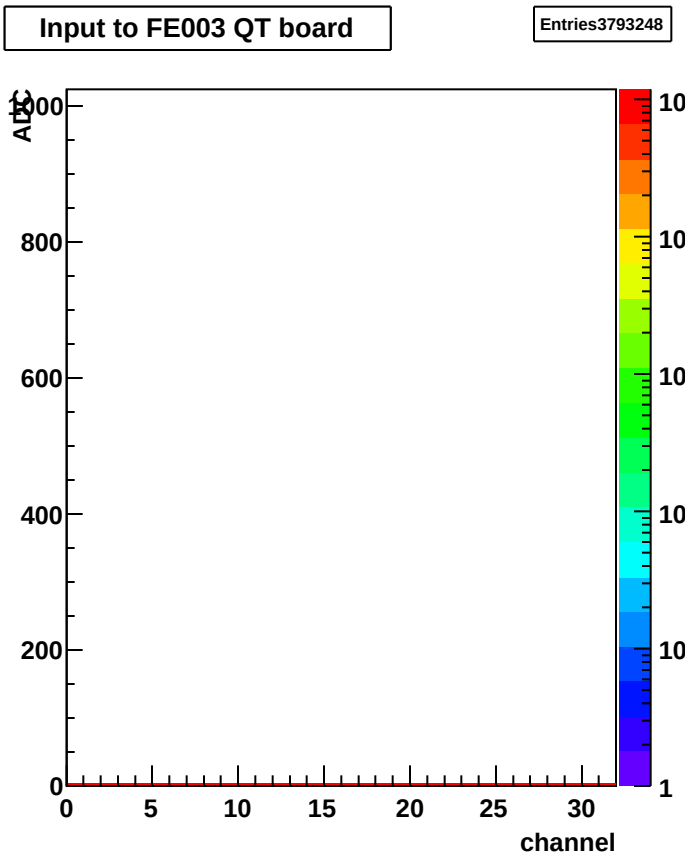

Input to FPD L2 DSM
Entries 355617
HT bits
3428
26305
12229
5275
8574
10744
5766
101262
81490
100544
SMALL
LARGE-S
LARGE-N

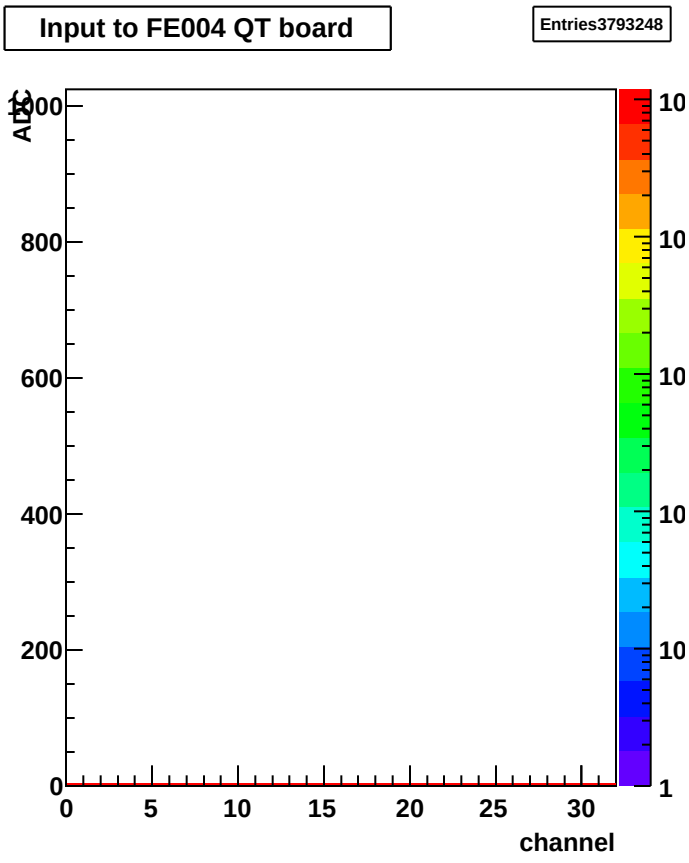

Input to FPD L2 DSM
Entries 348
HT bits - simulated
152
132
64
SMALL
LARGE-S
LARGE-N

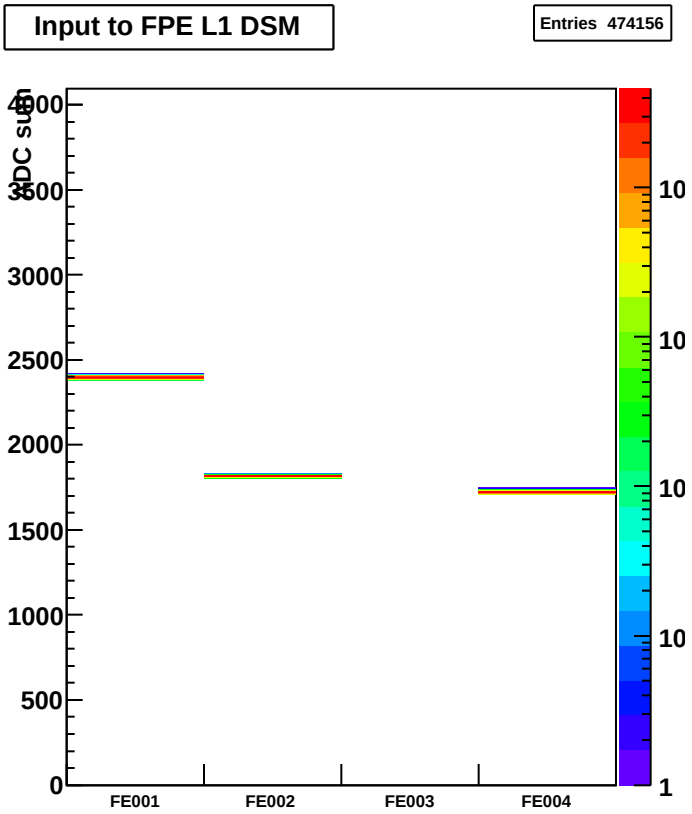

Input to FPD L2 DSM
Entries 711234
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

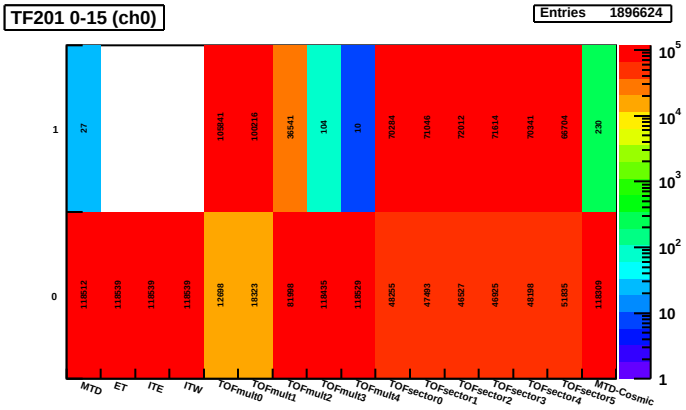

Input to FE001 QT board
Entries3793248
ADC
channel

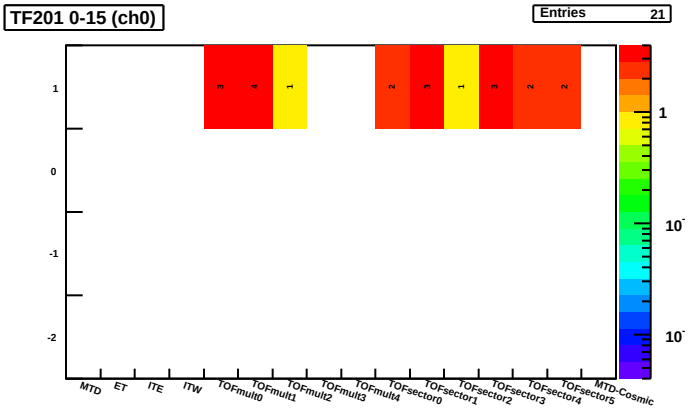

Input to FE002 QT board
Entries3793248
ADC
channel

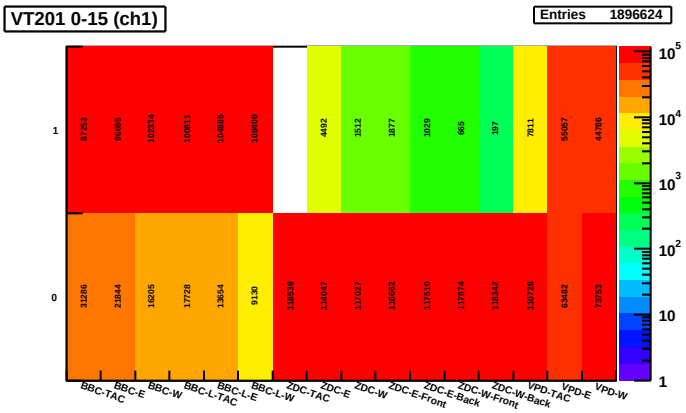

Input to FE003 QT board
Entries3793248
ADC
channel

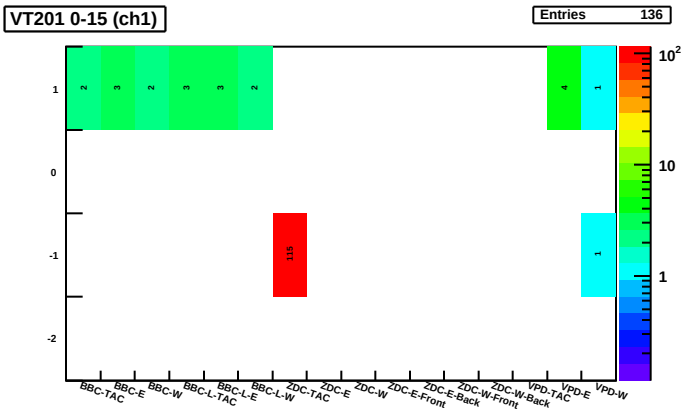

Input to FE004 QT board
Entries3793248
ADC
channel

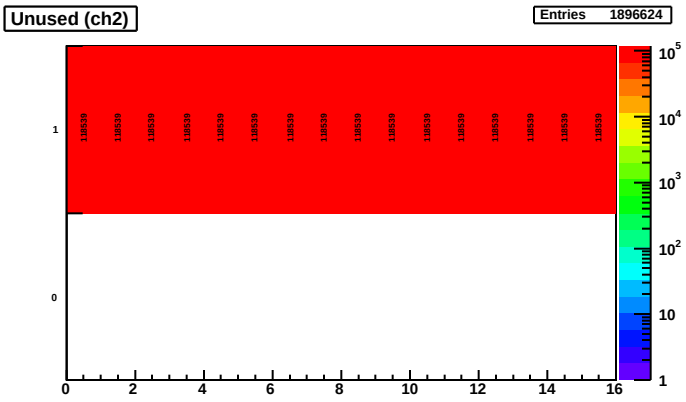

Input to FPE L1 DSM
Entries 474156
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

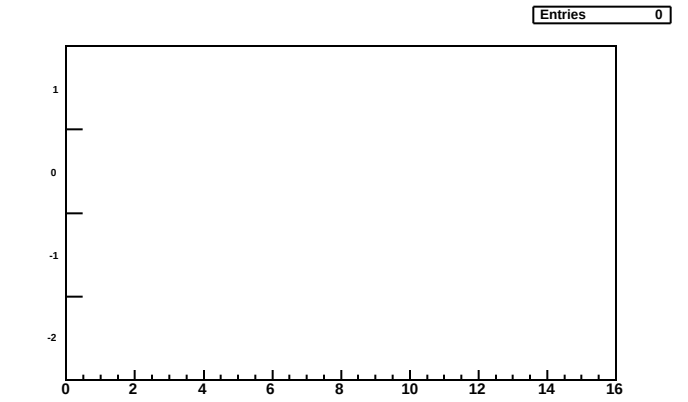

TF201 0-15 (ch0)
Entries 1896624
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

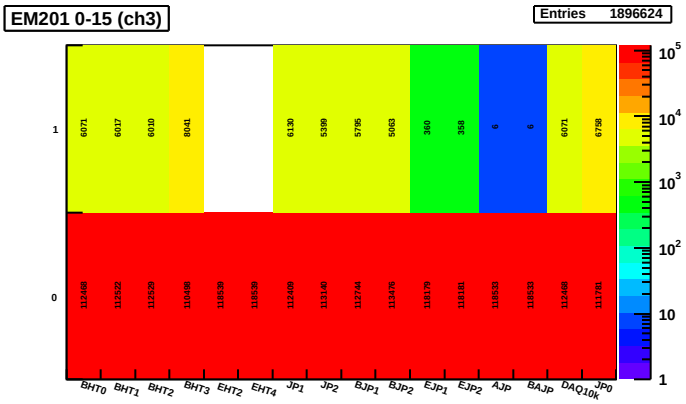

TF201 0-15 (ch0)
Entries 21
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1896624
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 136
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1896624


Entries 0


EM201 0-15 (ch3)
Entries 1896624
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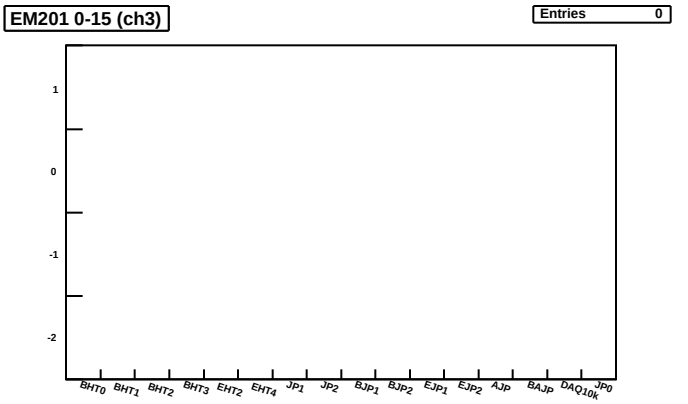
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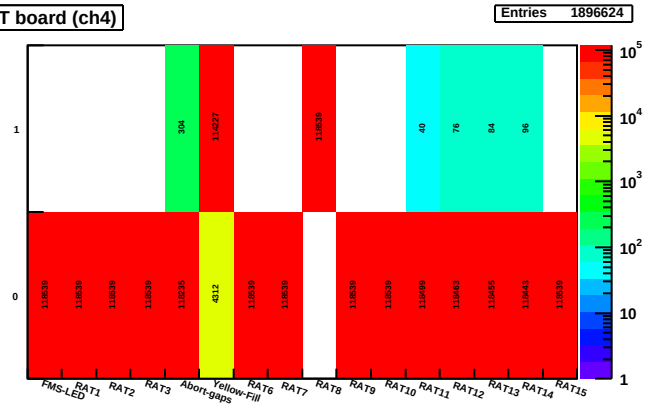
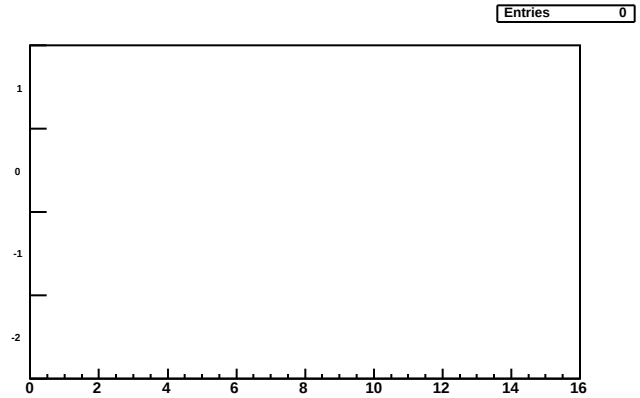
RAT board (ch4)





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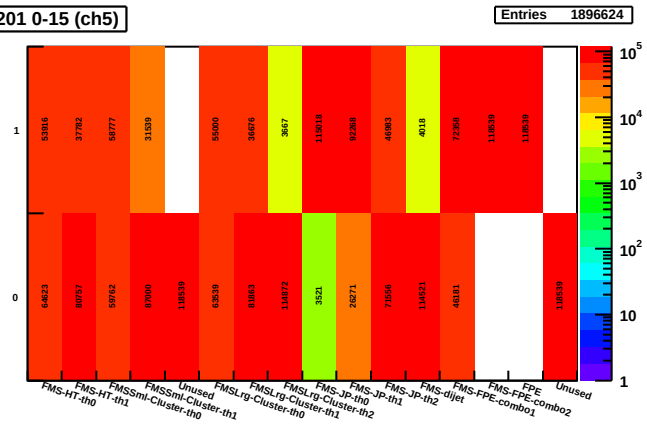

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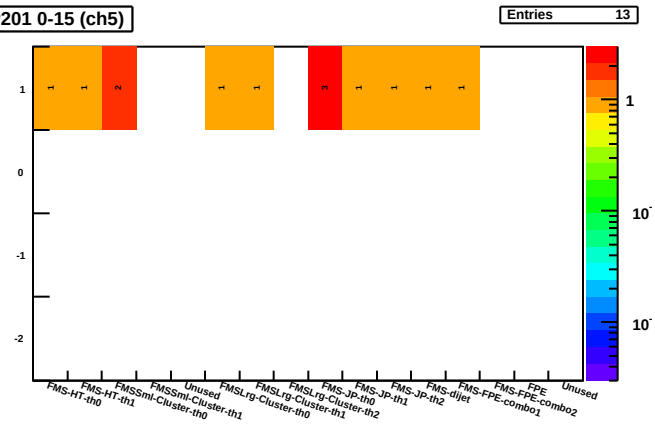

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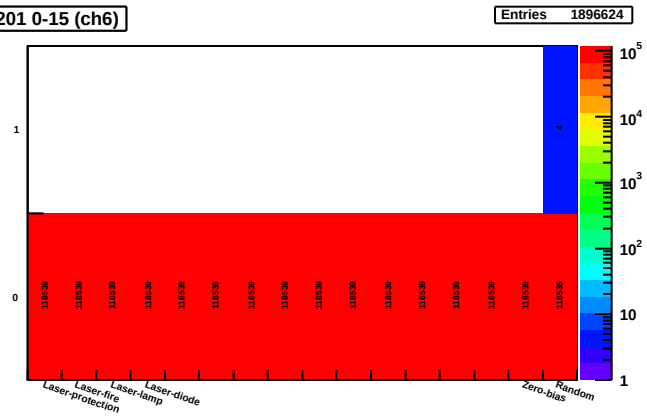

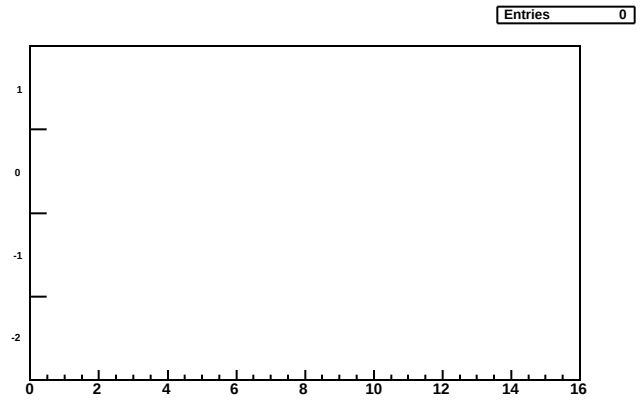
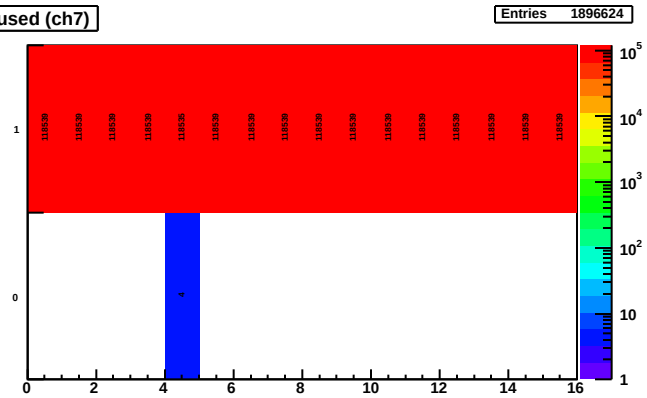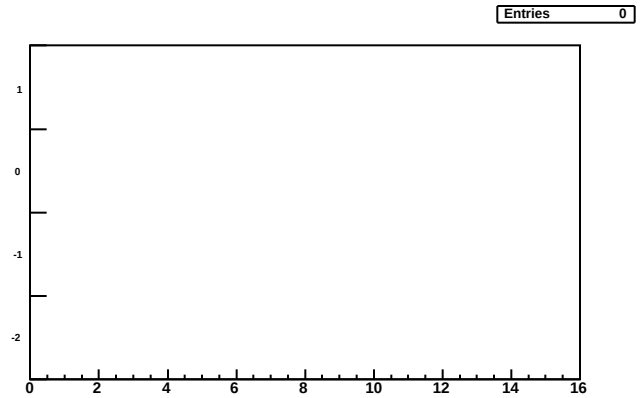
Unused (ch7)