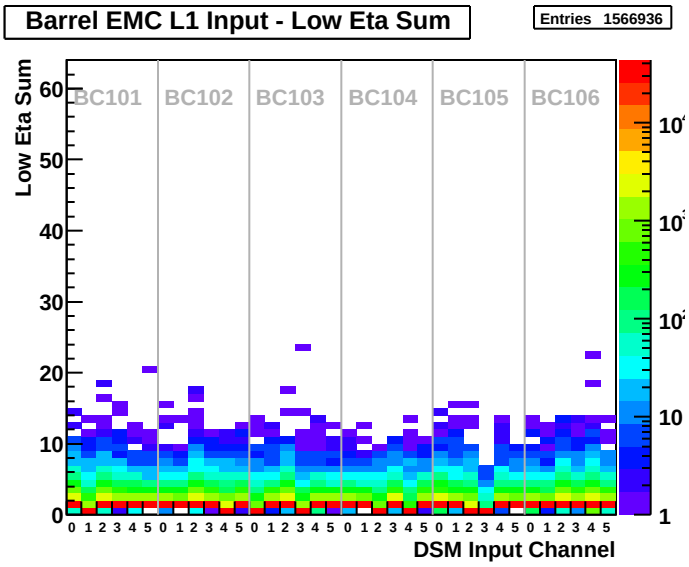
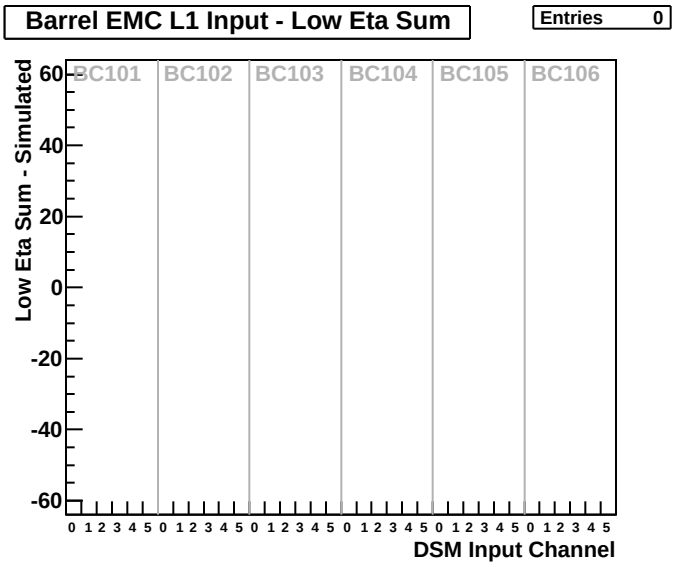
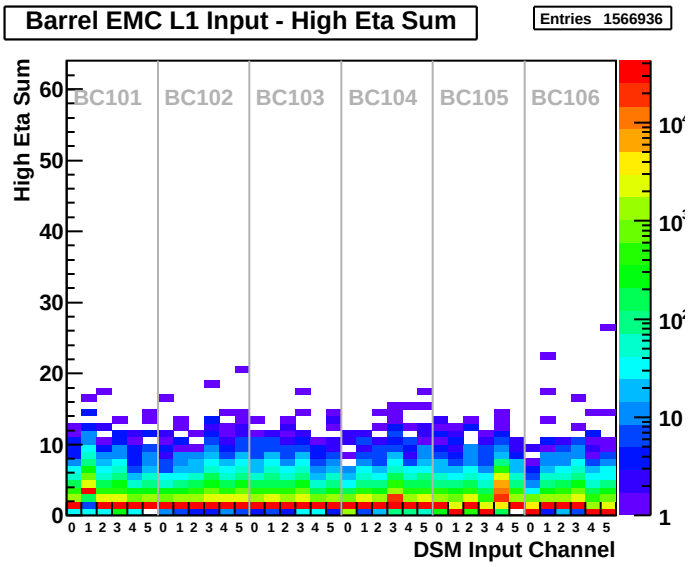
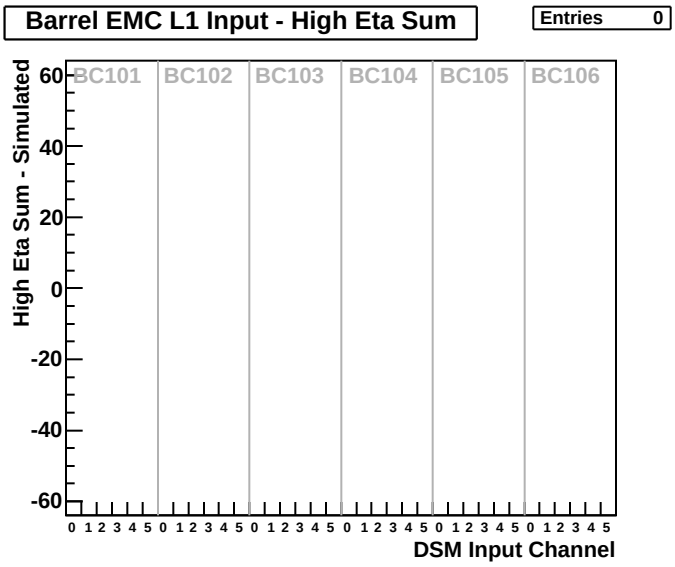
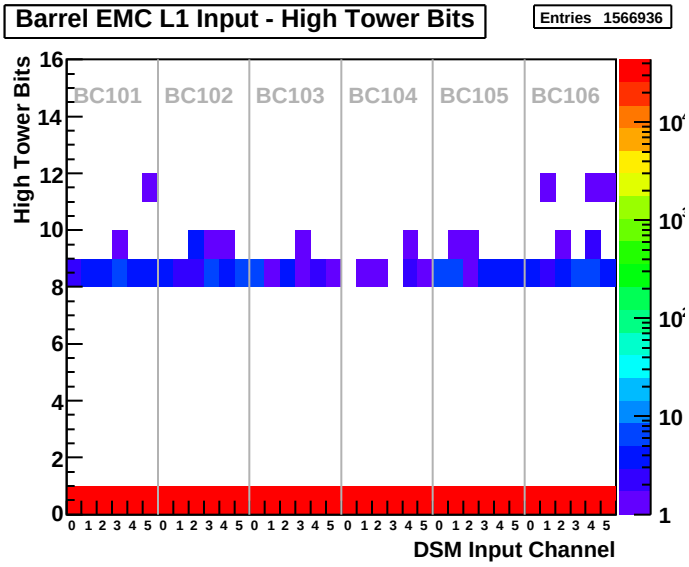
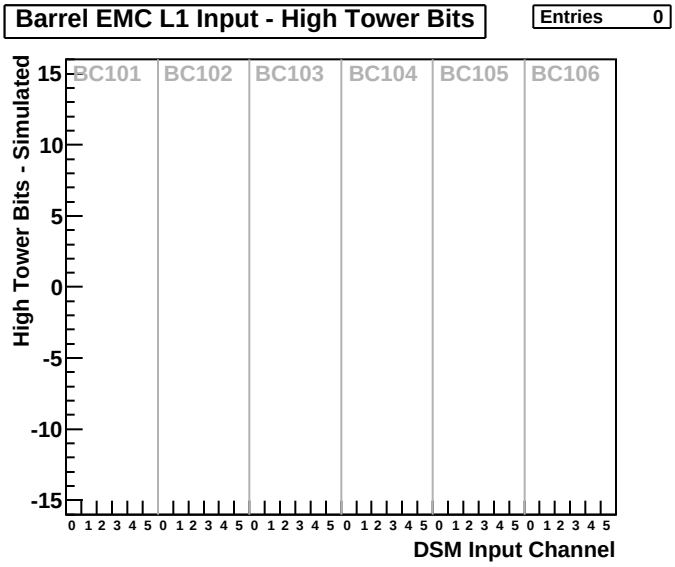

Barrel EMC L1 Input - Low Eta Sum
Entries 1566936
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

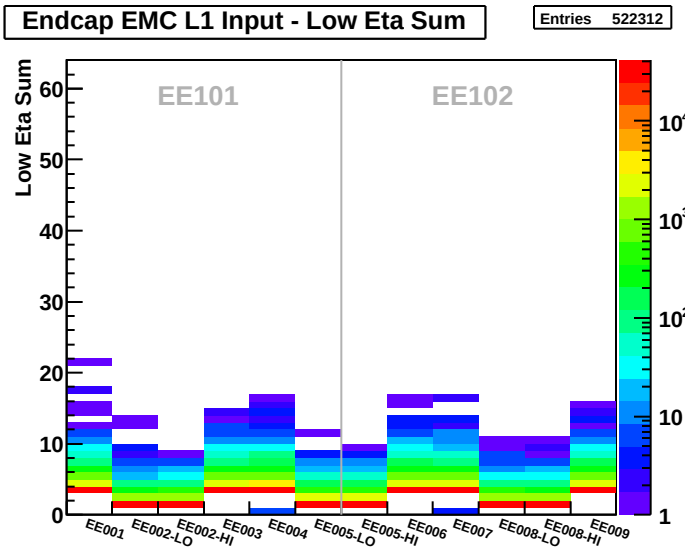

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

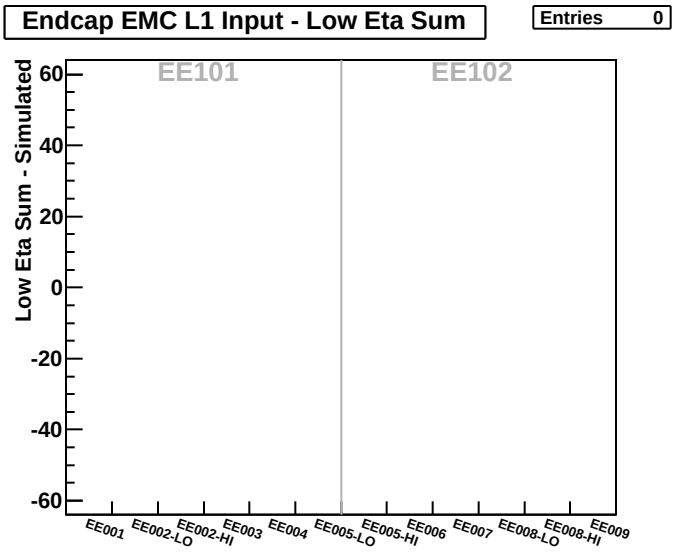

Barrel EMC L1 Input - High Eta Sum
Entries 1566936
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

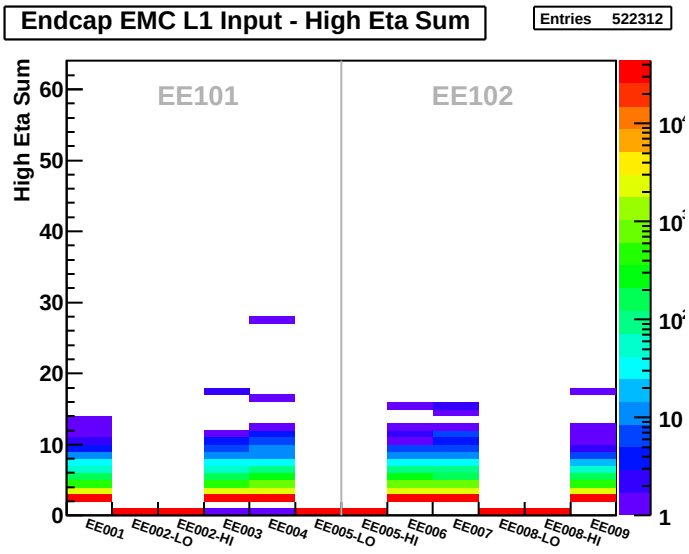

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

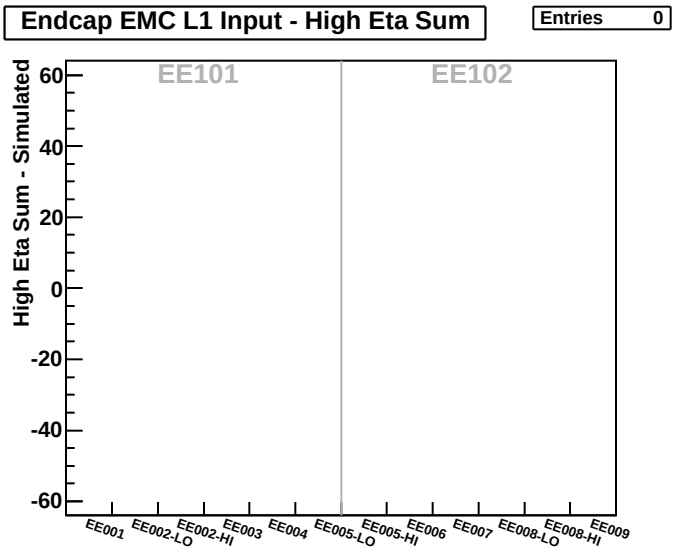

Barrel EMC L1 Input - High Tower Bits
Entries 1566936
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

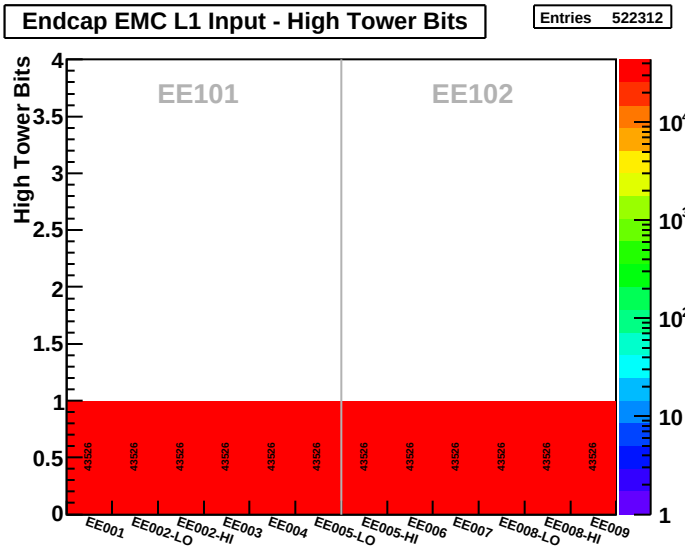

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

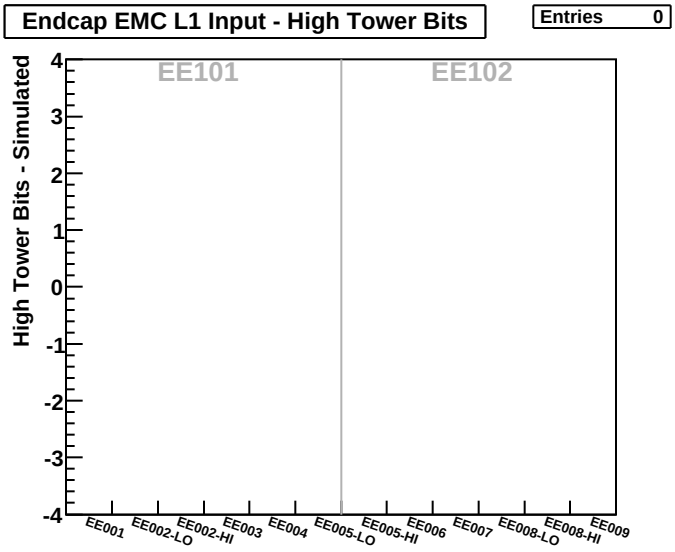

Endcap EMC L1 Input - Low Eta Sum
Entries 522312
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

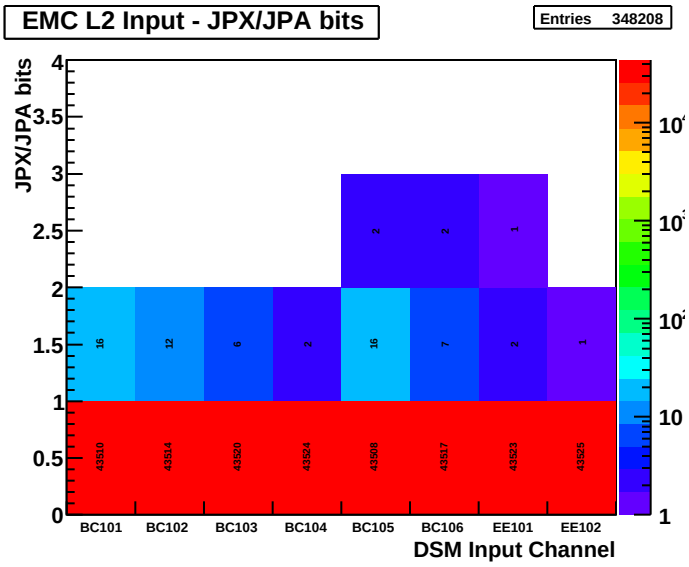

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

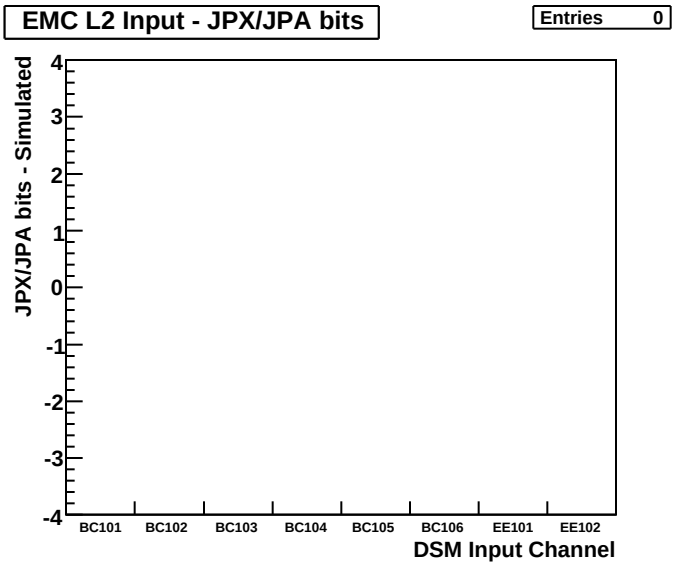

Endcap EMC L1 Input - High Eta Sum
Entries 522312
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

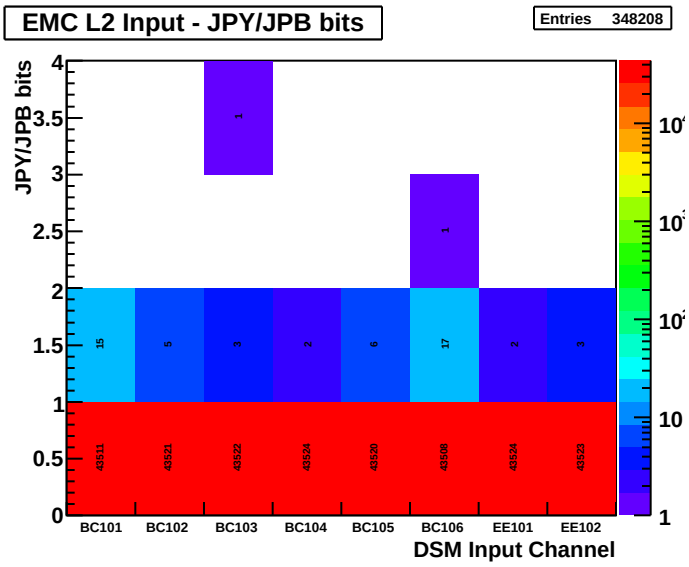

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

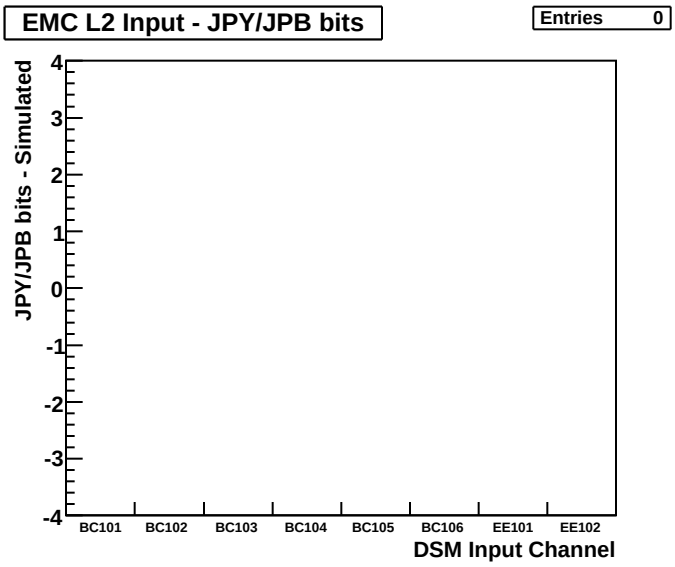

Endcap EMC L1 Input - High Tower Bits
Entries 522312
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

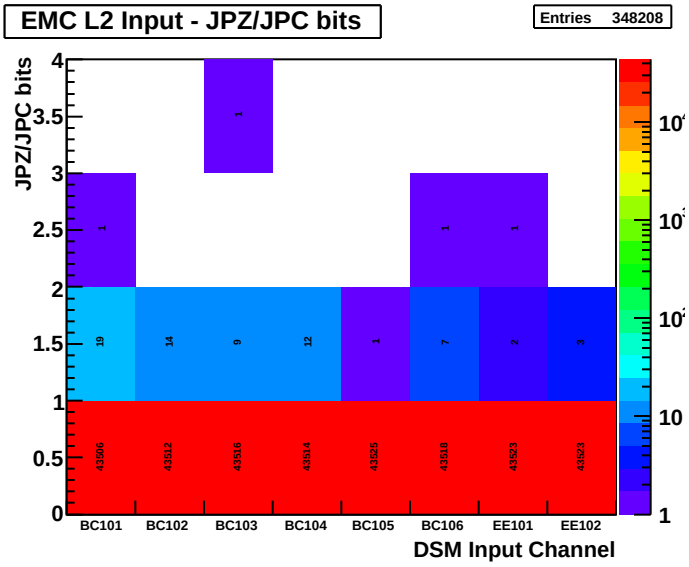

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

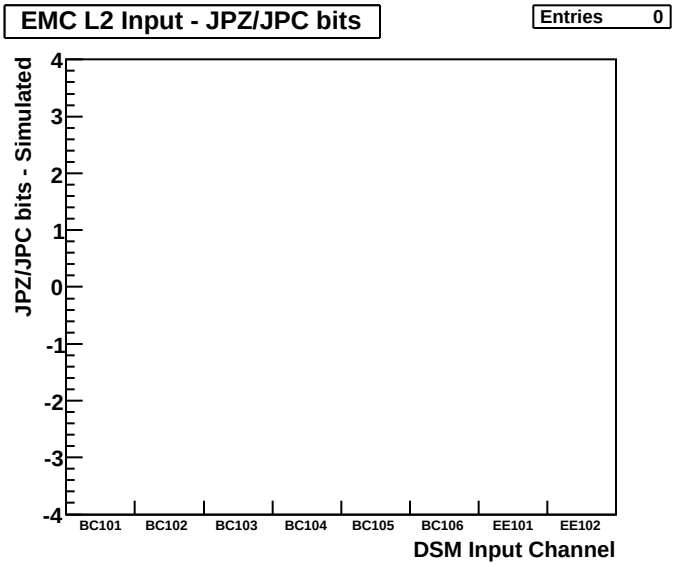

EMC L2 Input - JPX/JPA bits
Entries 348208
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

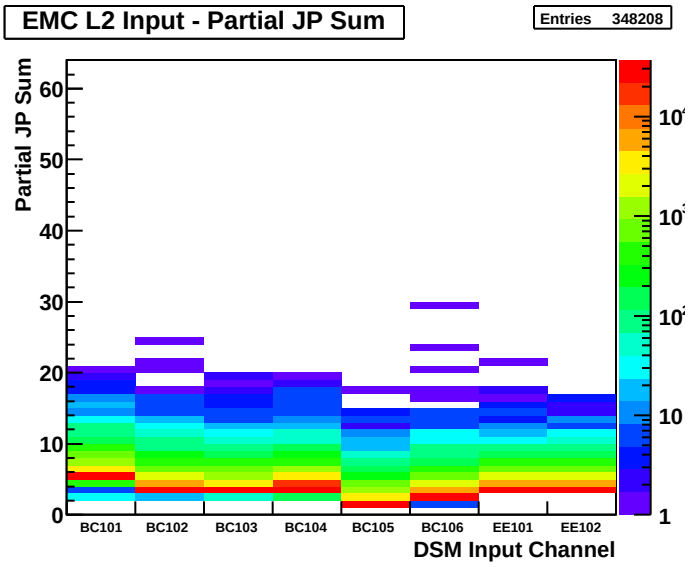

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

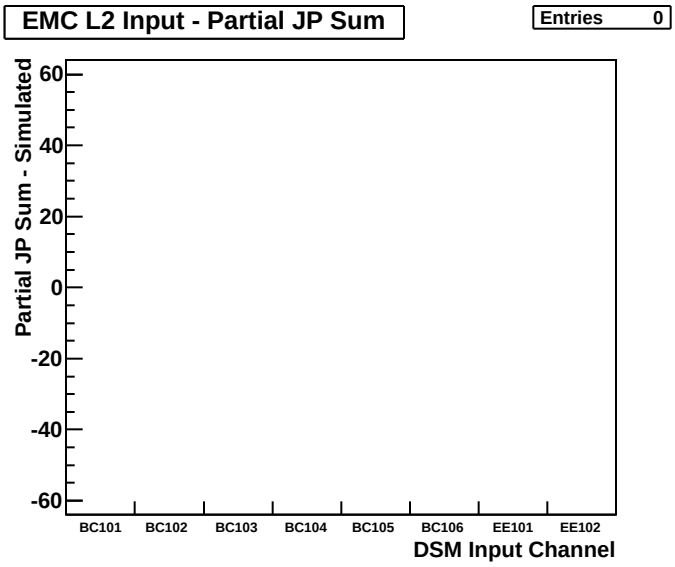

EMC L2 Input - JPY/JPB bits
Entries 348208
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

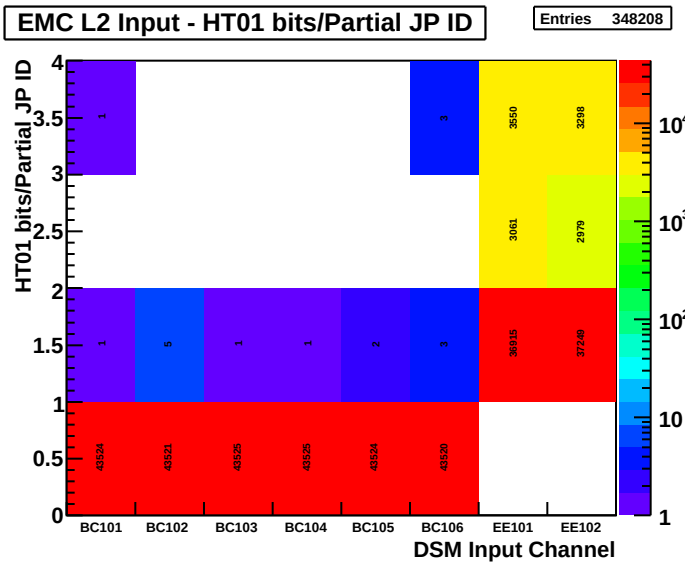

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

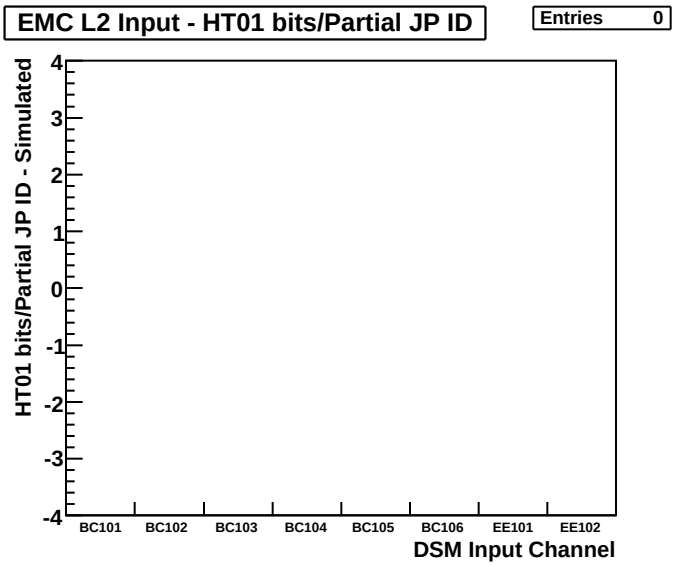

EMC L2 Input - JPZ/JPC bits
Entries 348208
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

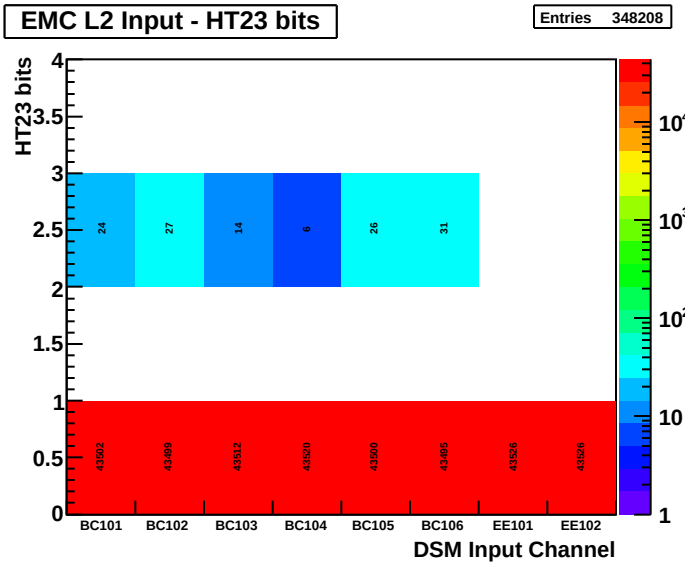

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

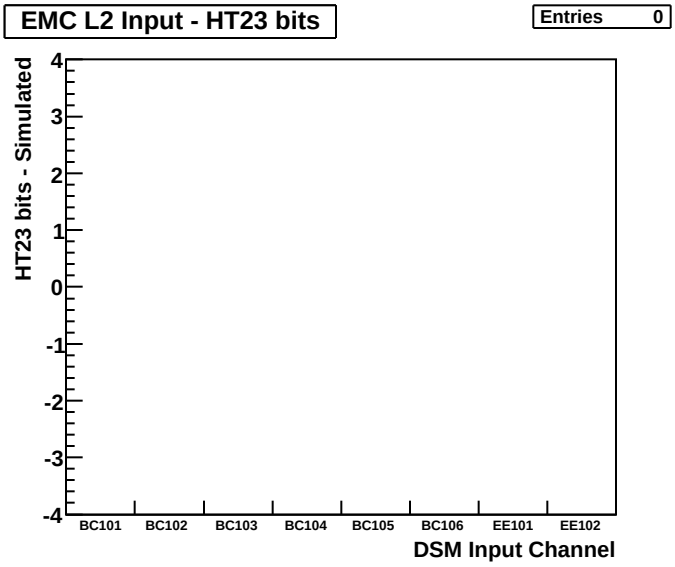

EMC L2 Input - Partial JP Sum
Entries 348208
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

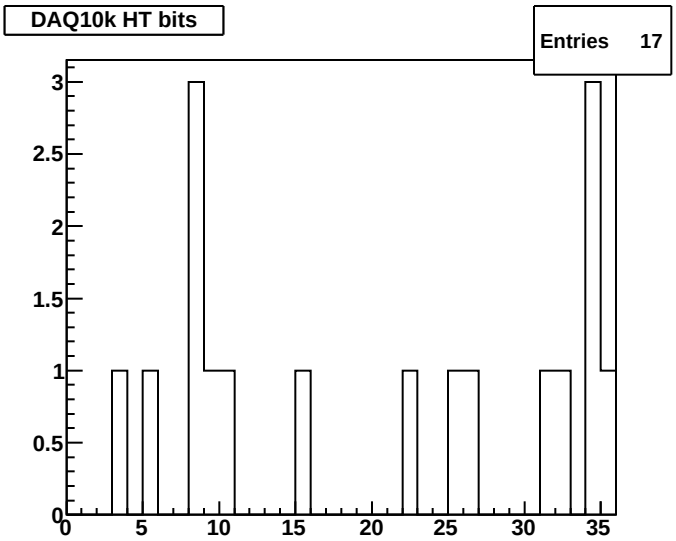

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

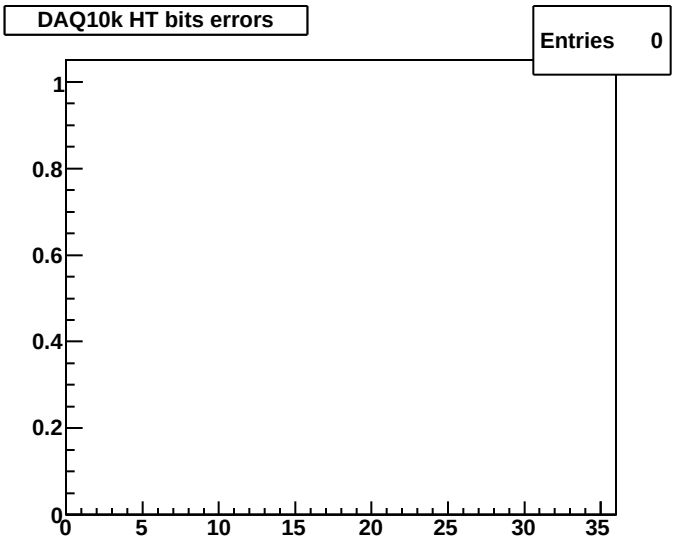

EMC L2 Input - HT01 bits/Partial JP ID
Entries 348208
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

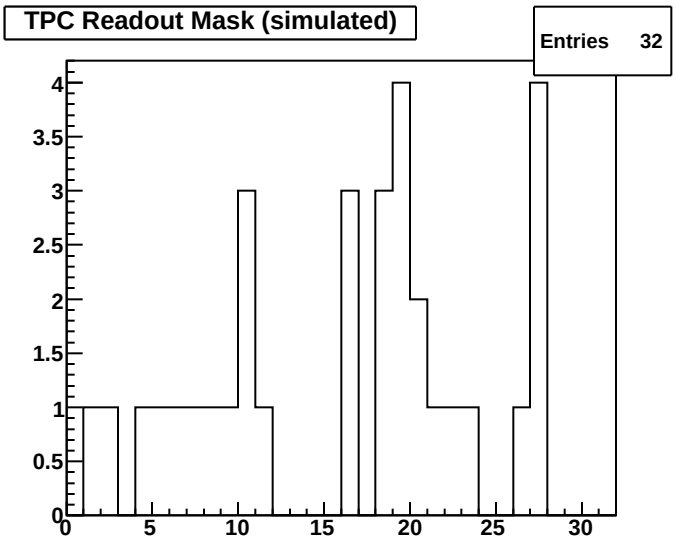

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

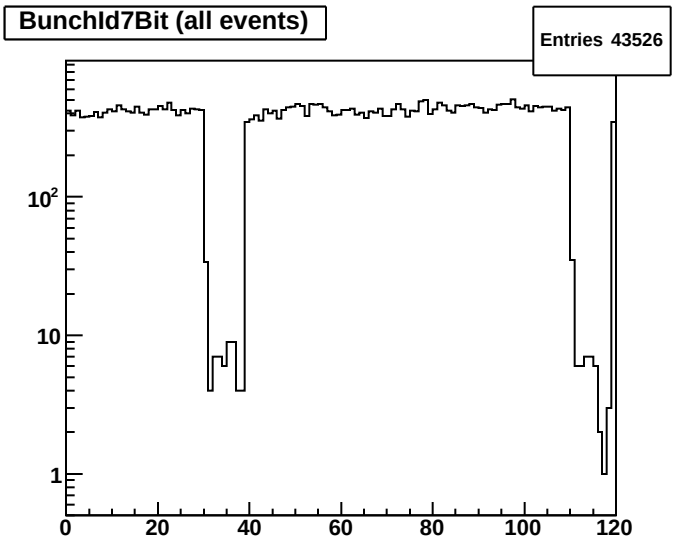

EMC L2 Input - HT23 bits
Entries 348208
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

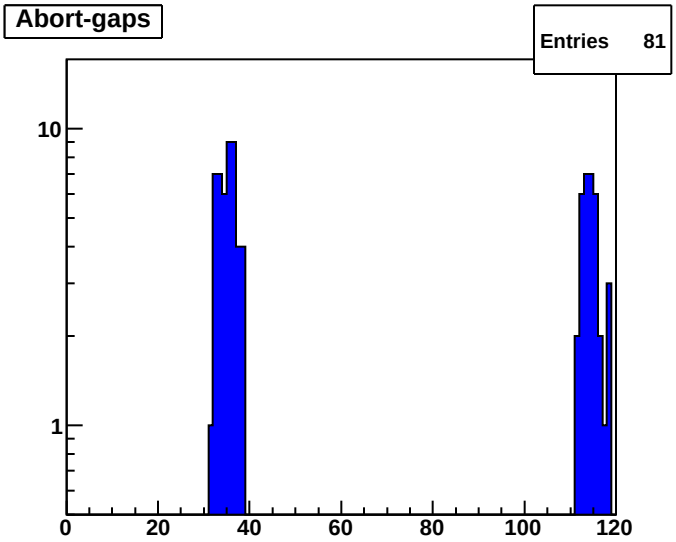

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

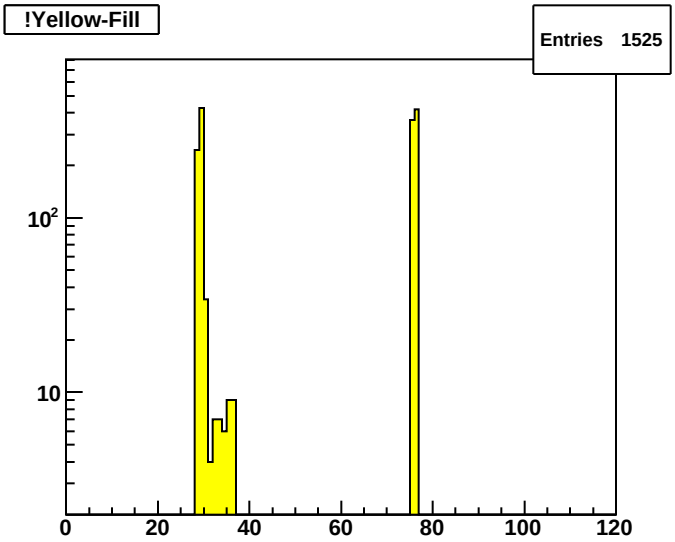

DAQ10k HT bits
Entries 17

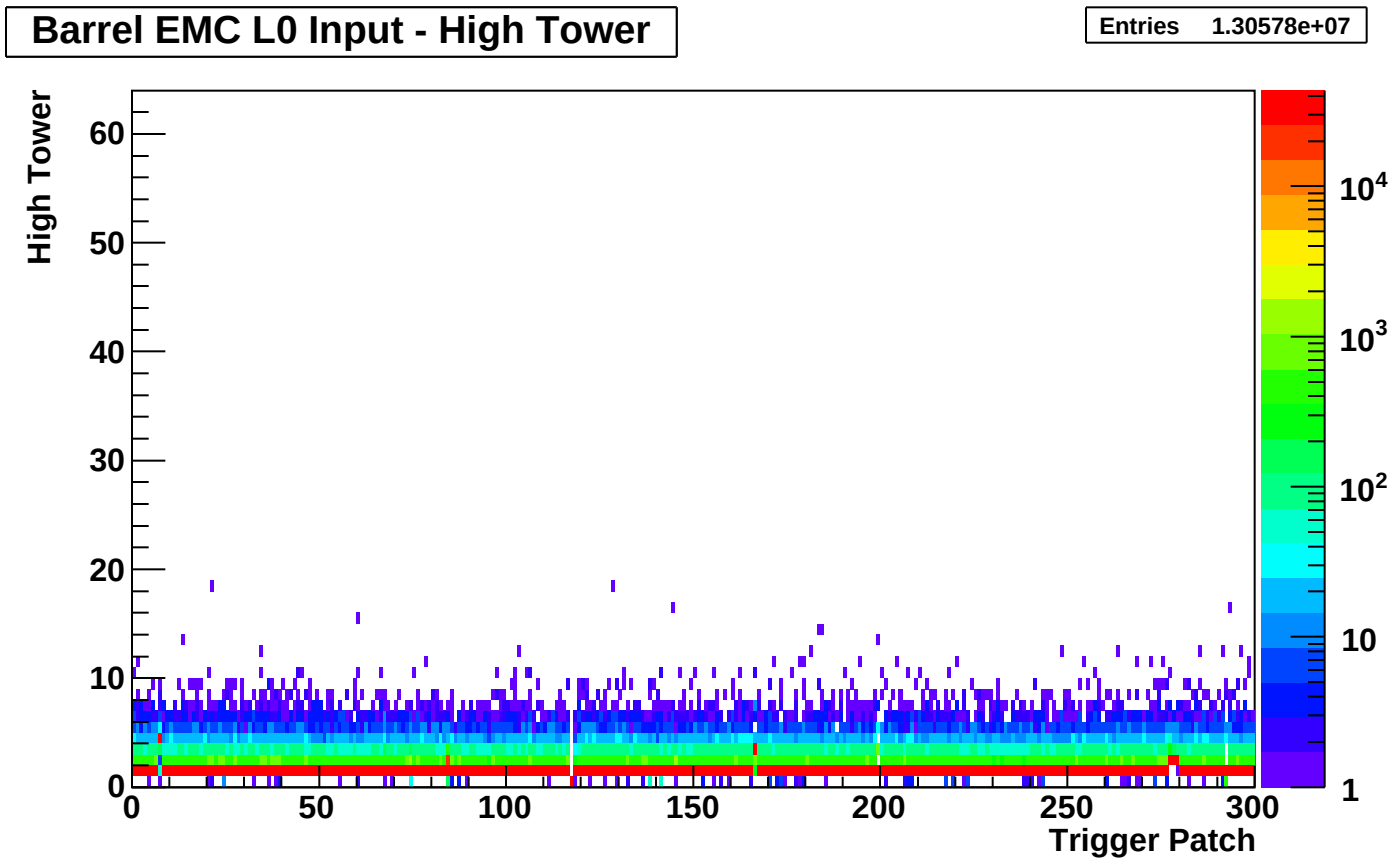

DAQ10k HT bits errors
Entries 0

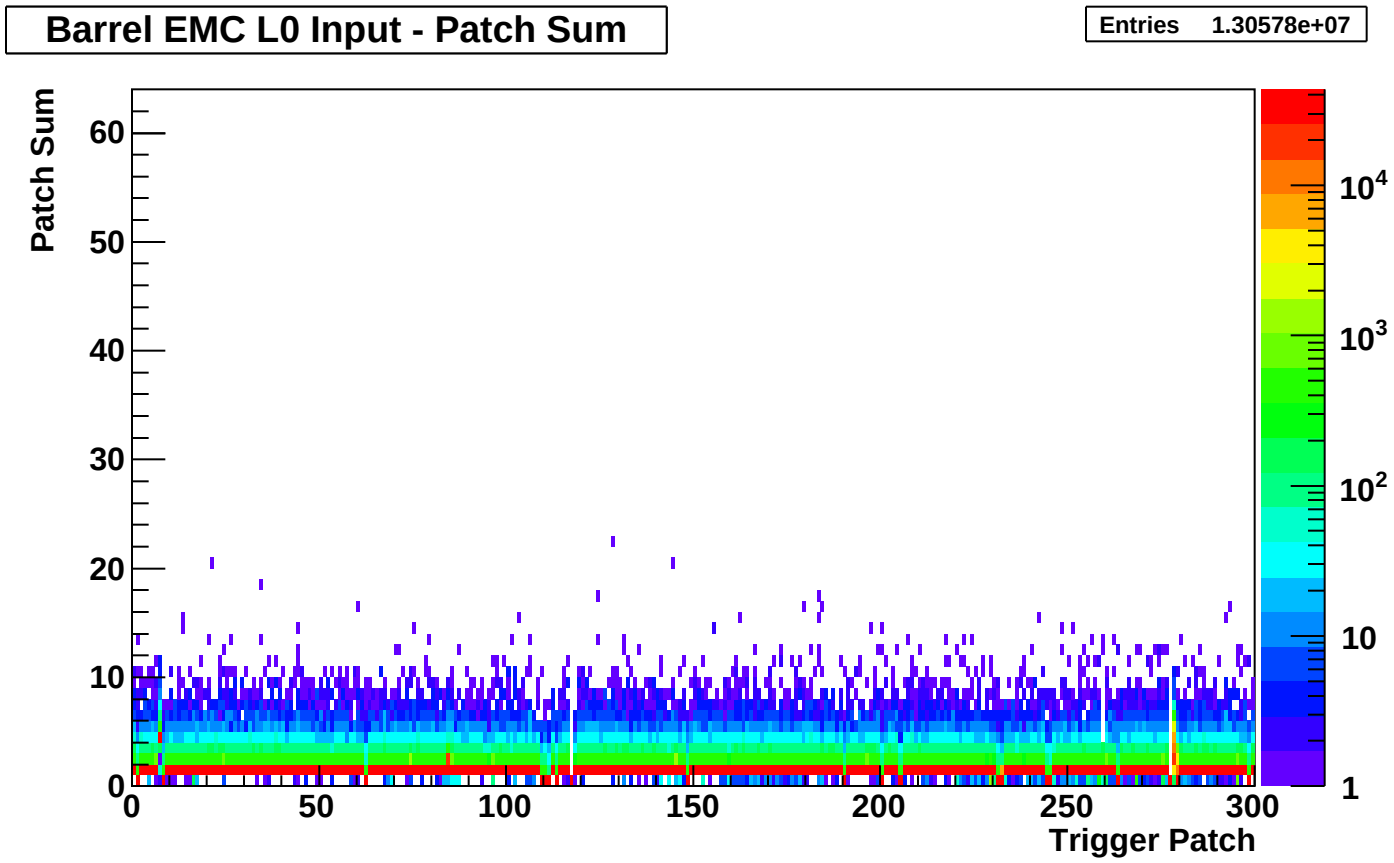

TPC Readout Mask (simulated)
Entries 32

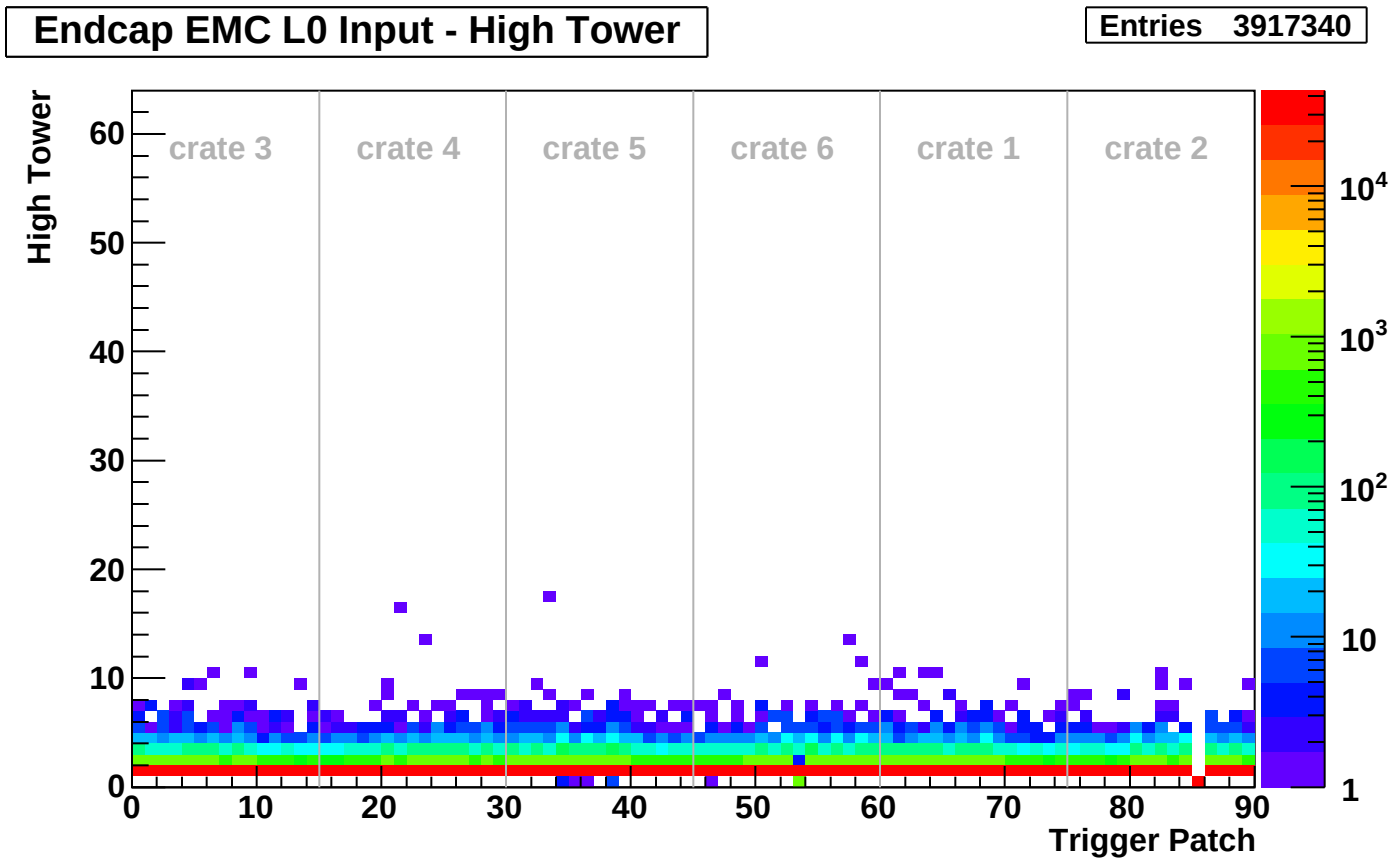

BunchId7Bit (all events)
Entries 43526

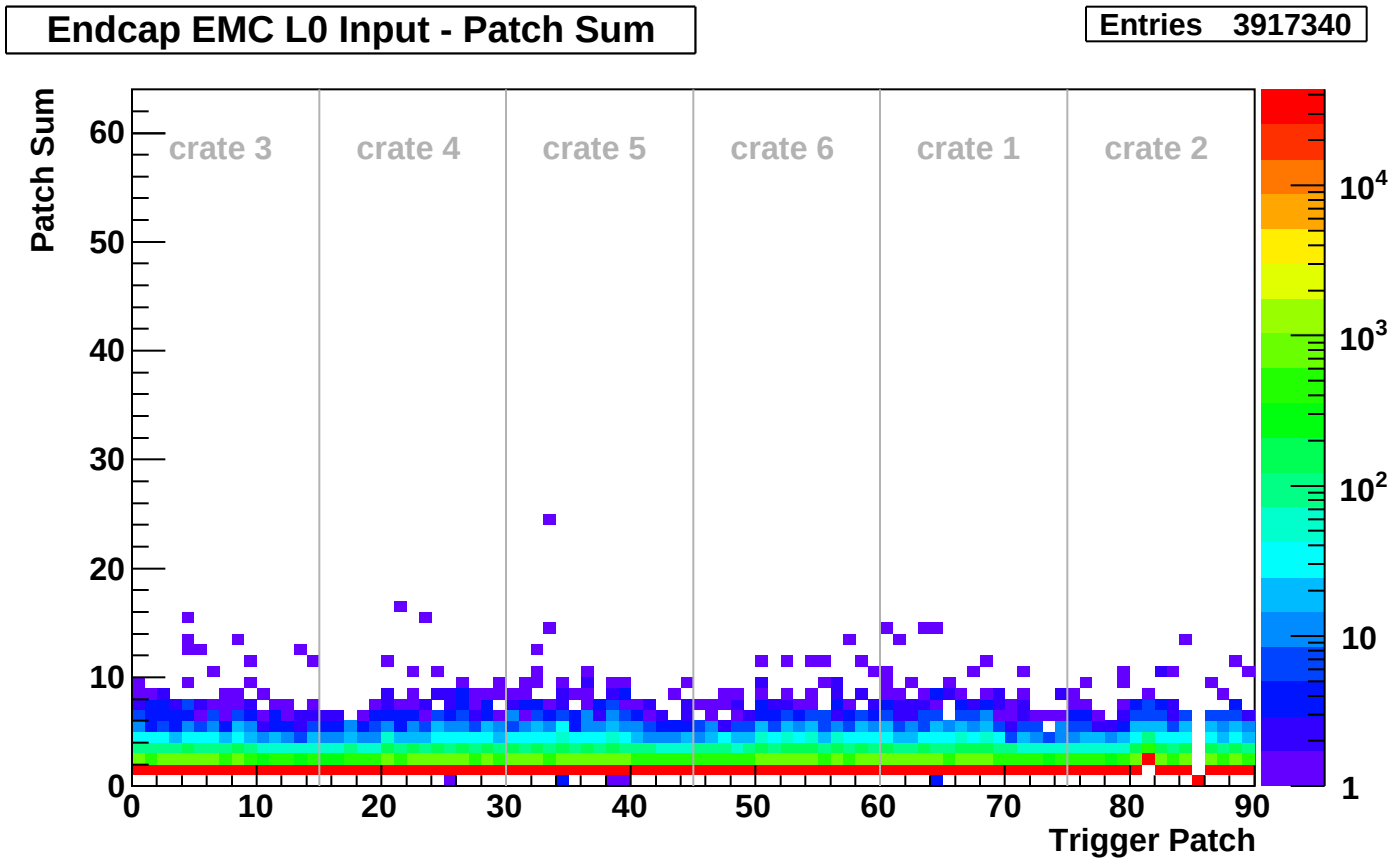

Abort-gaps
Entries 81

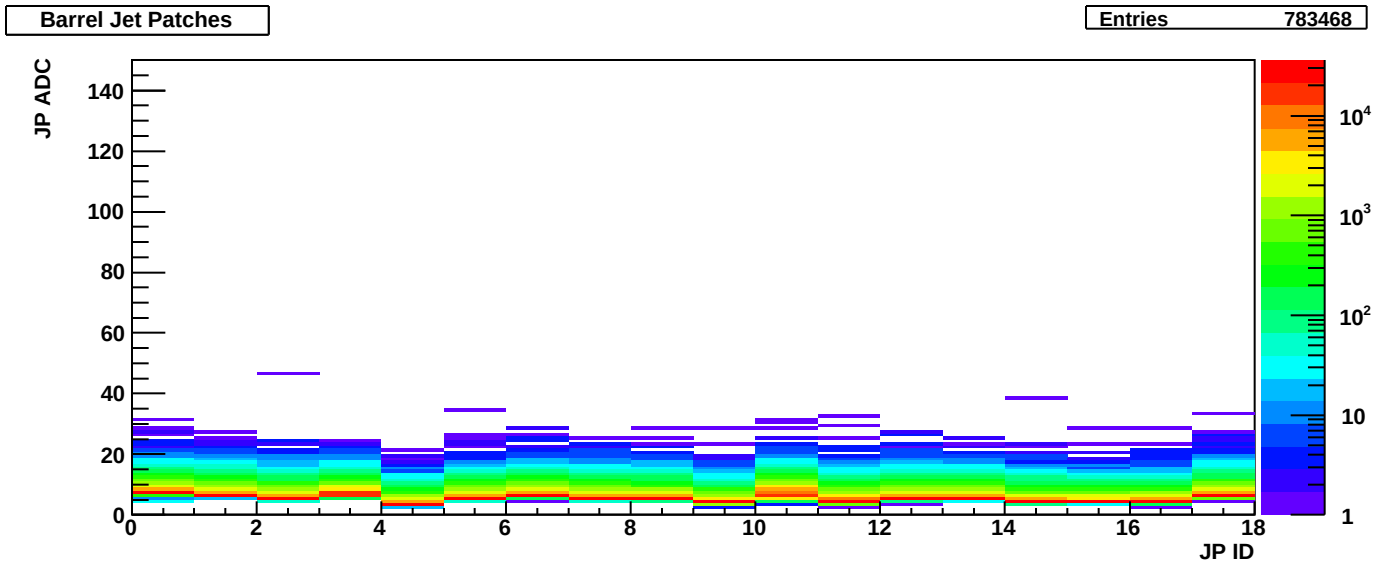

!Yellow-Fill
Entries 1525

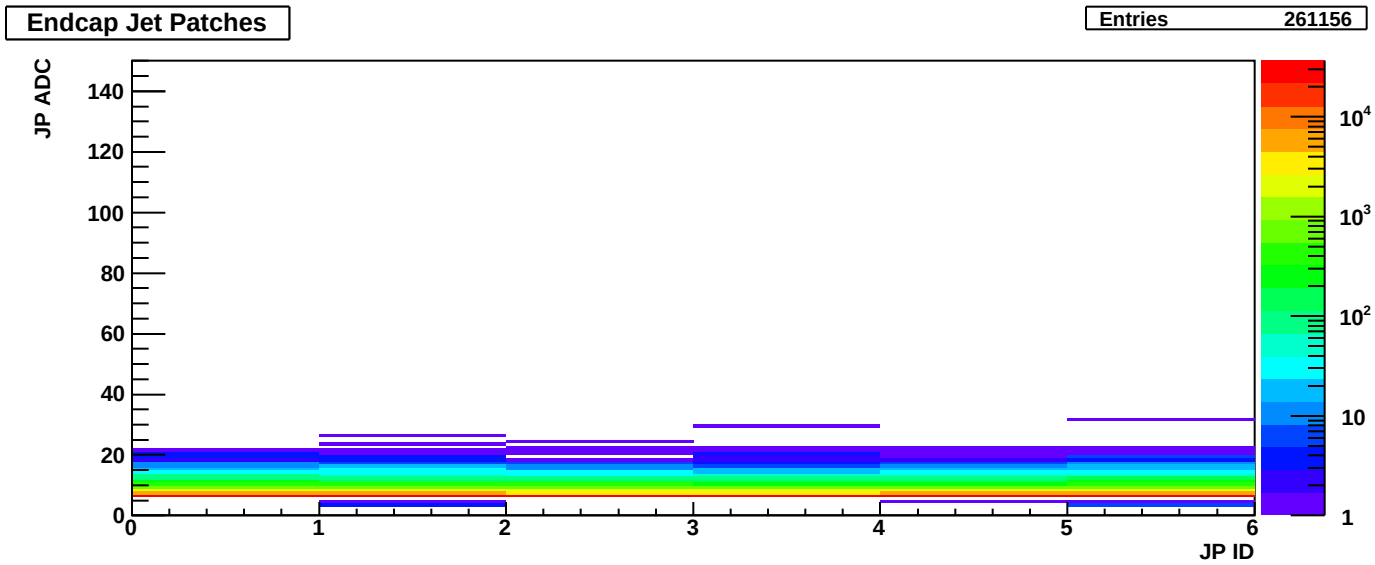

Barrel EMC L0 Input - High Tower
Entries 1.30578e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

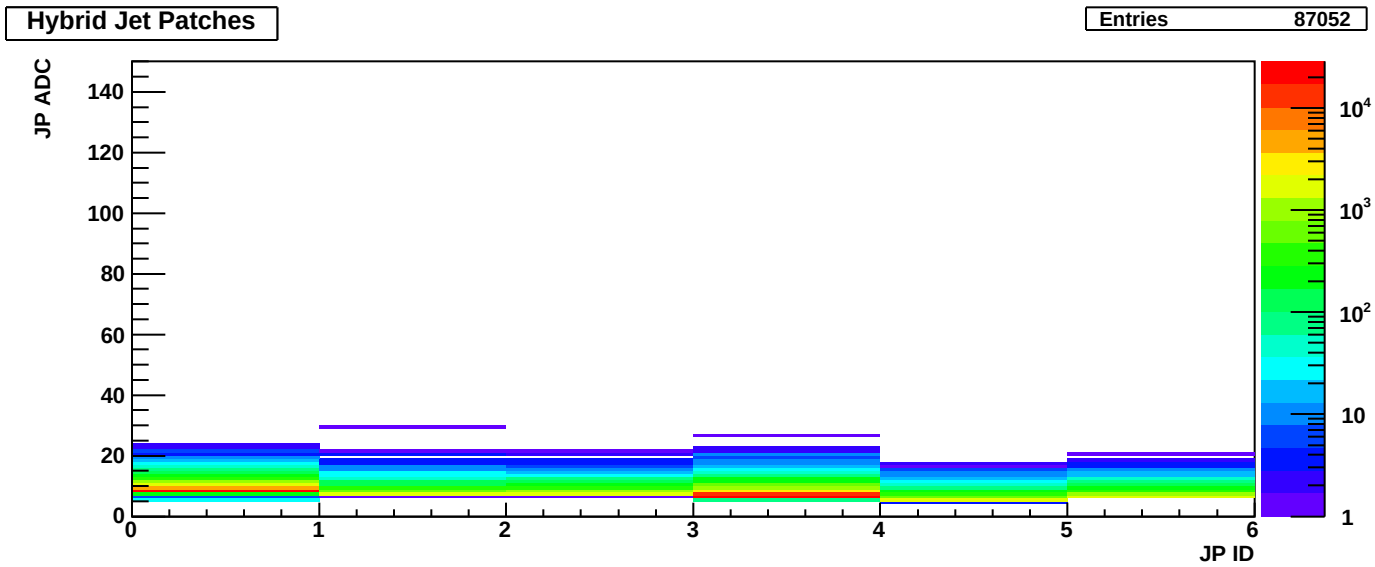

Barrel EMC L0 Input - Patch Sum
Entries 1.30578e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

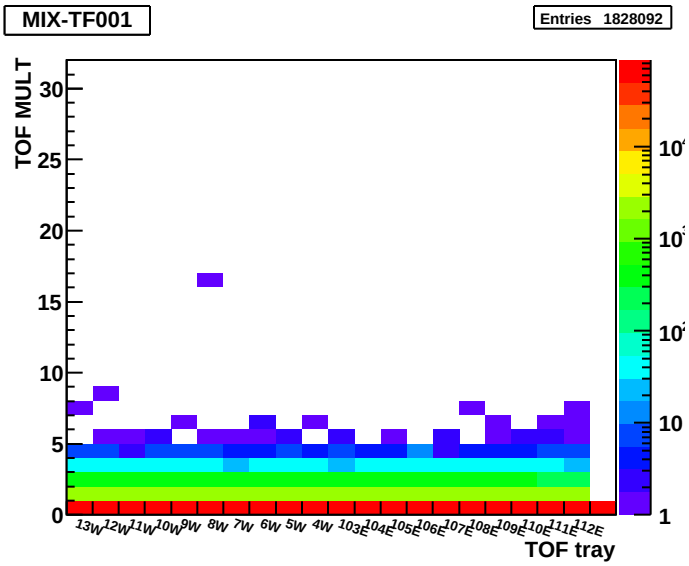

Endcap EMC L0 Input - High Tower
Entries 3917340
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

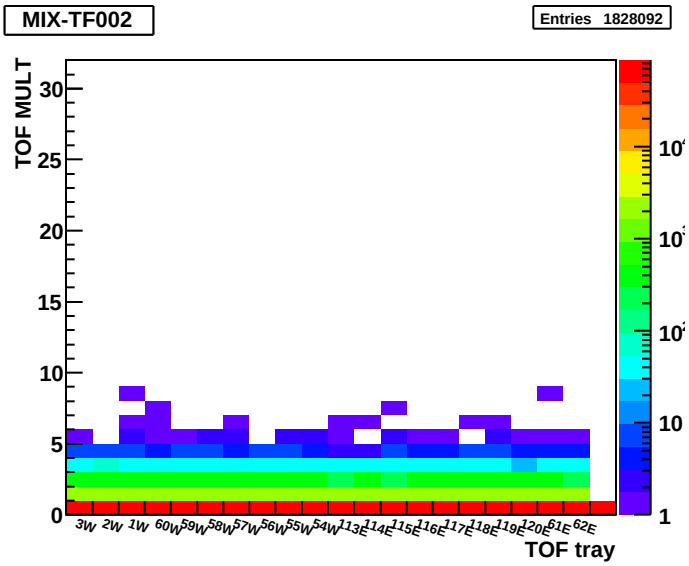

Endcap EMC L0 Input - Patch Sum
Entries 3917340
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

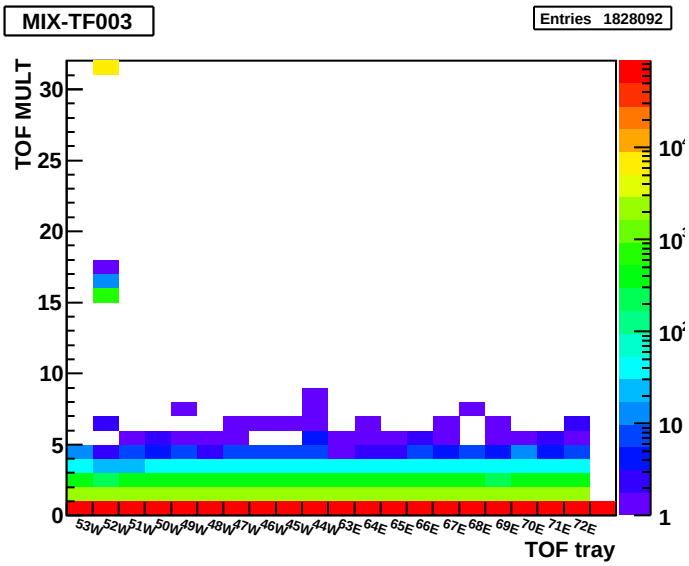

Barrel Jet Patches
Entries 783468
JP ADC
JP ID

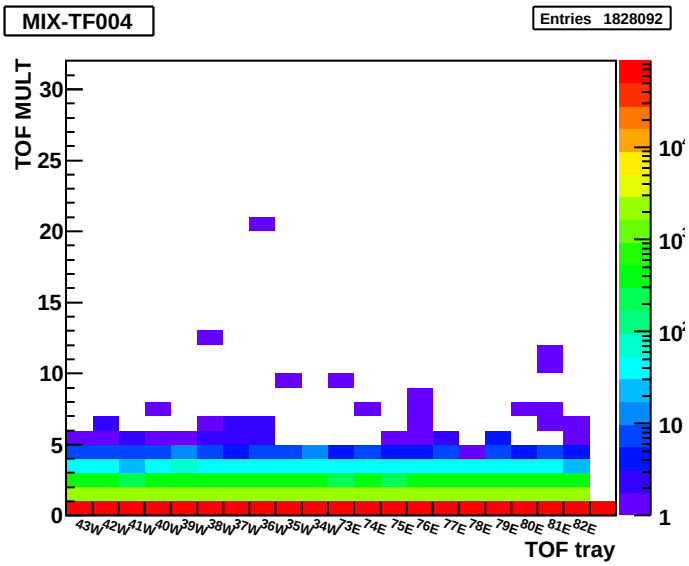

Endcap Jet Patches
Entries 261156
JP ADC
JP ID

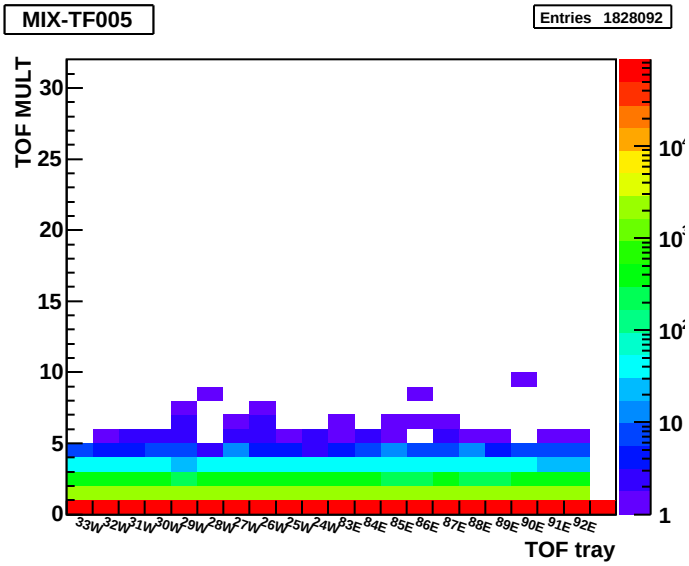

Hybrid Jet Patches
Entries 87052
JP ADC
JP ID

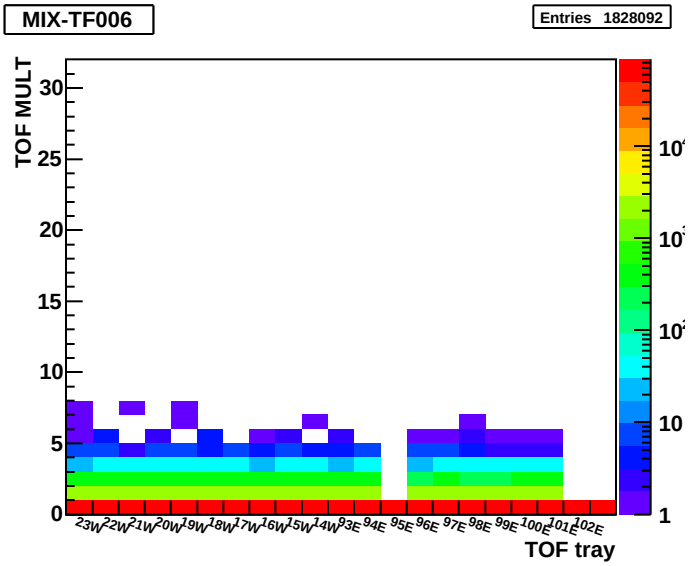

MIX-TF001
Entries 1828092
TOF MULT
TOF tray

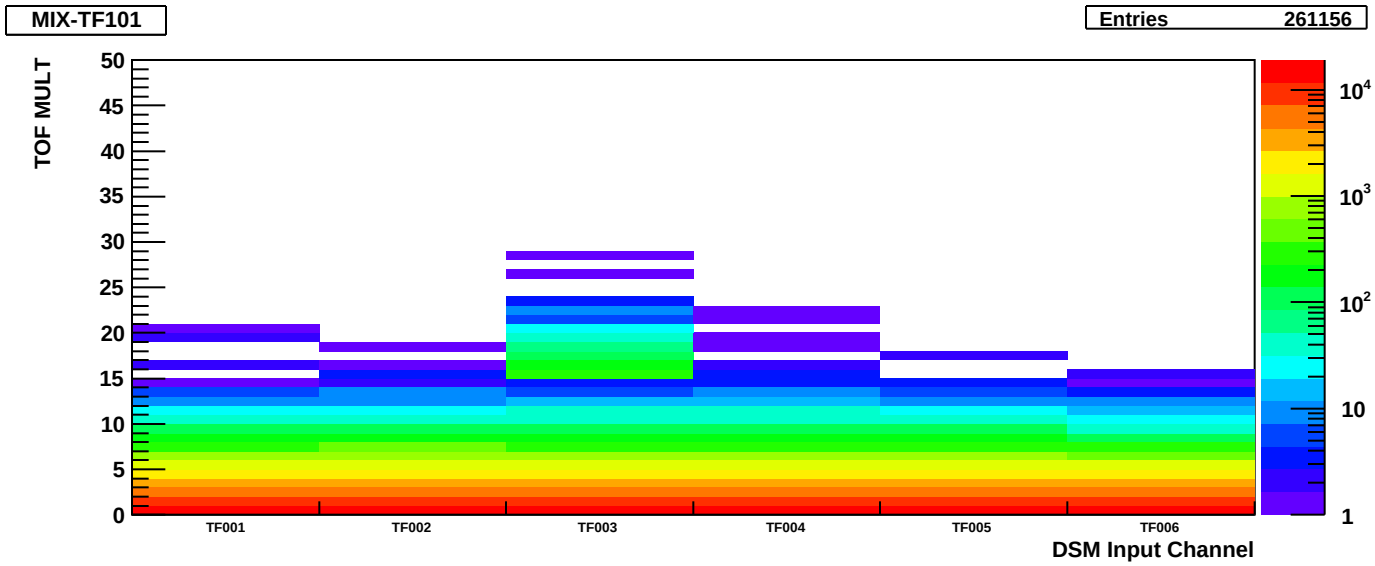

MIX-TF002
Entries 1828092
TOF MULT
TOF tray

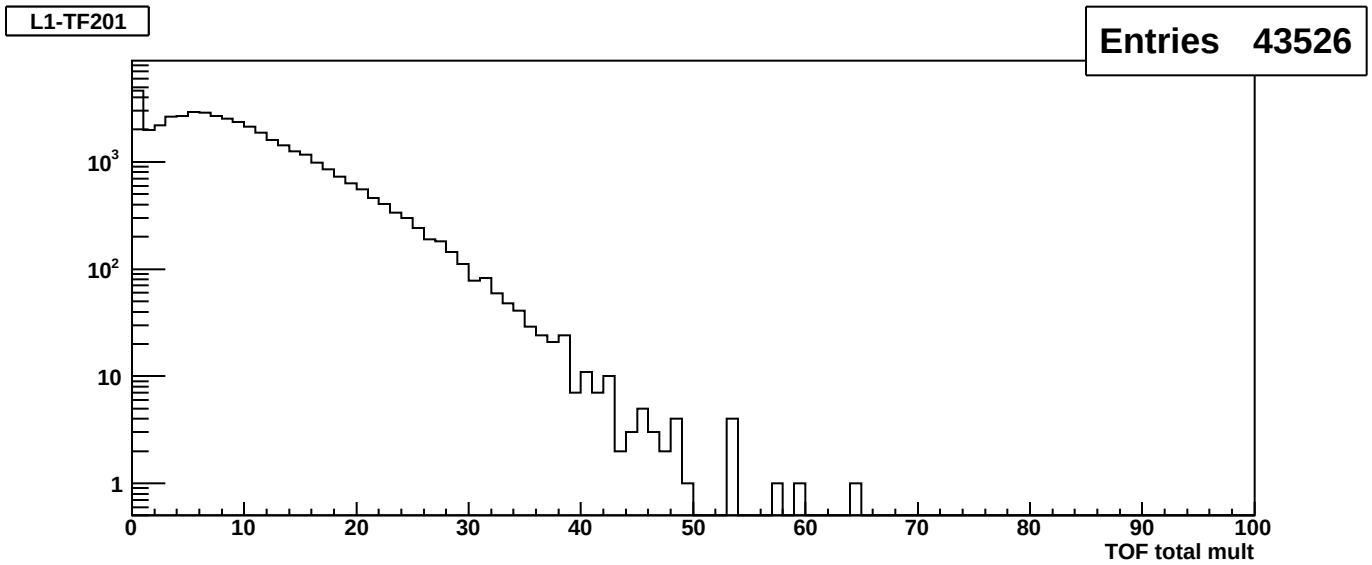

MIX-TF003
Entries 1828092
TOF MULT
TOF tray

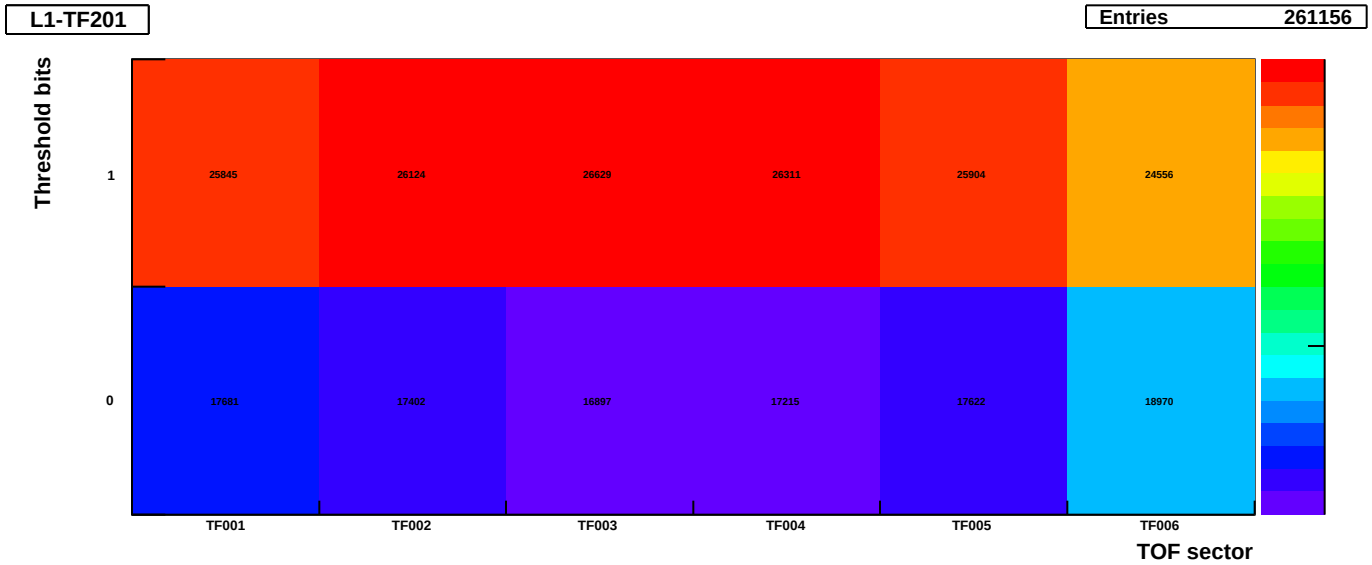

MIX-TF004
Entries 1828092
TOF MULT
TOF tray

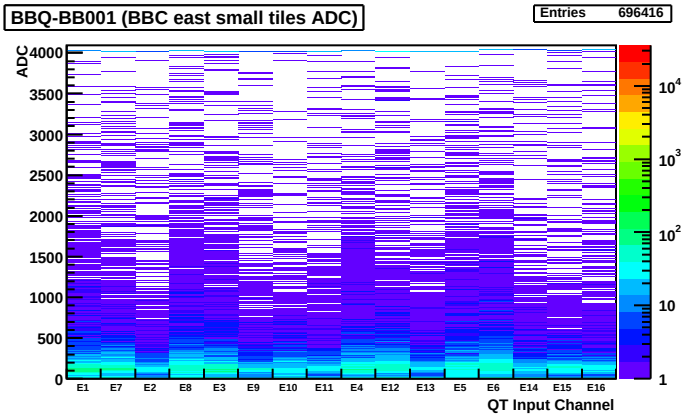

MIX-TF005
Entries 1828092
TOF MULT
TOF tray

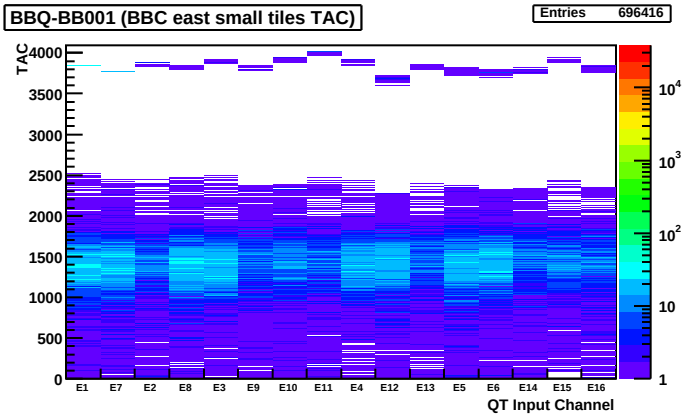

MIX-TF006
Entries 1828092
TOF MULT
TOF tray

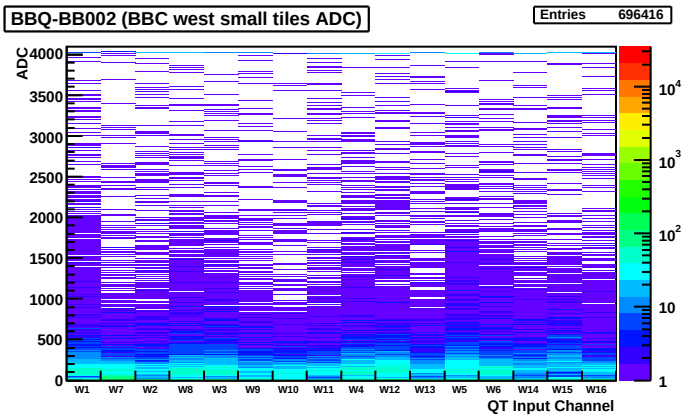

MIX-TF101
Entries 261156
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

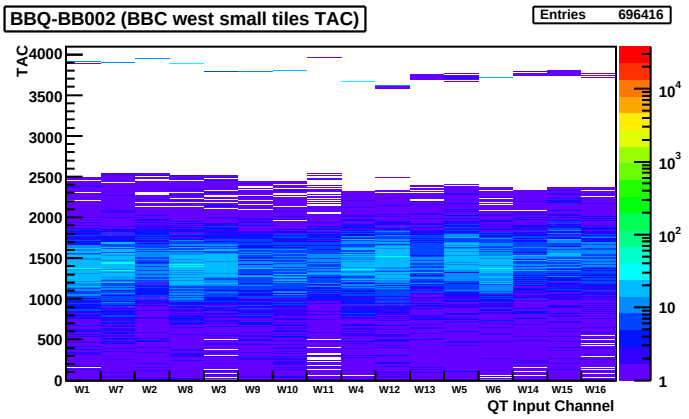

L1-TF201
Entries 43526
TOF total mult

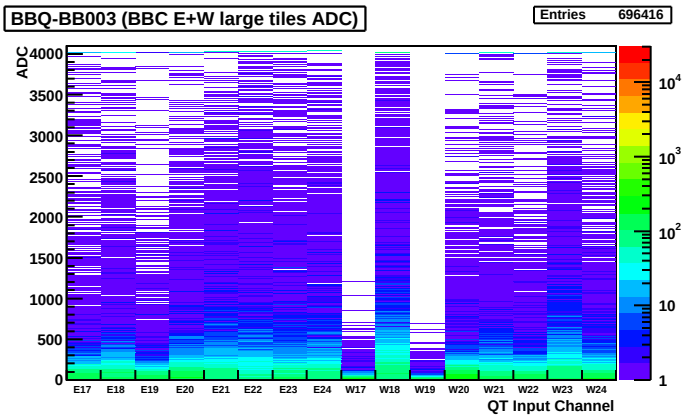

L1-TF201
Entries 261156
Threshold bits
25845
26124
26629
26311
25904
24556
17681
17402
16897
17215
17622
18970
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

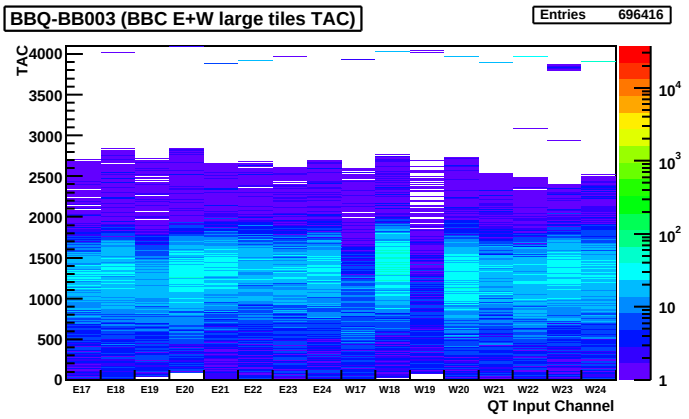

BBQ-BB001 (BBC east small tiles ADC)
Entries 696416
ADC
QT Input Channel

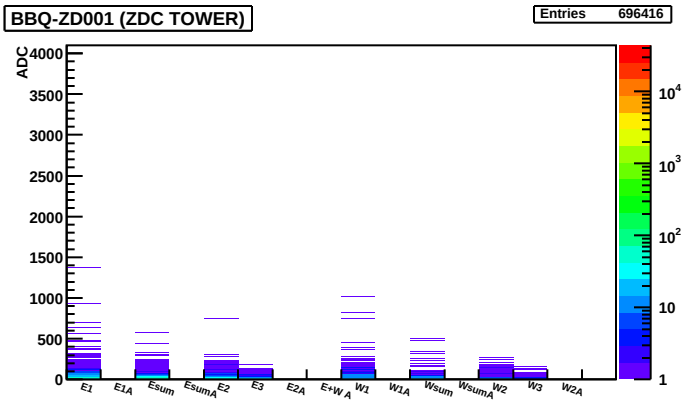

BBQ-BB001 (BBC east small tiles TAC)
Entries 696416
TAC
QT Input Channel

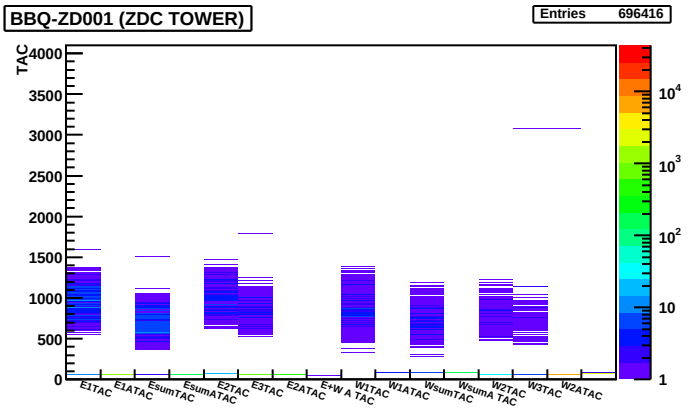

BBQ-BB002 (BBC west small tiles ADC)
Entries 696416
ADC
QT Input Channel

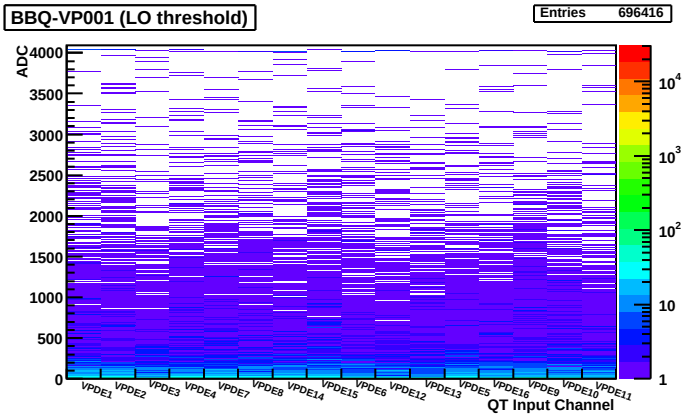

BBQ-BB002 (BBC west small tiles TAC)
Entries 696416
TAC
QT Input Channel

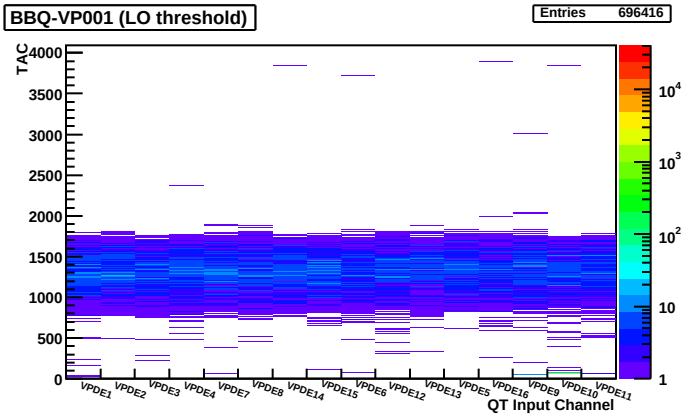

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 696416
ADC
QT Input Channel

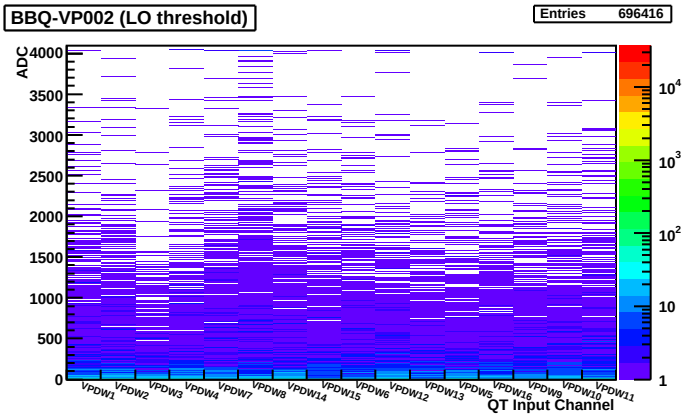

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 696416
TAC
QT Input Channel

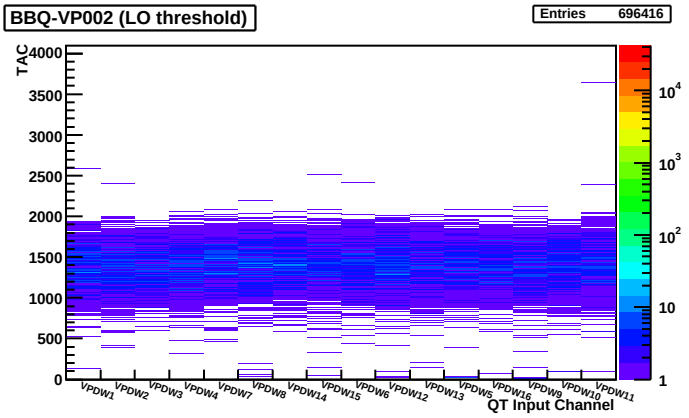

BBQ-ZD001 (ZDC TOWER)
Entries 696416
ADC

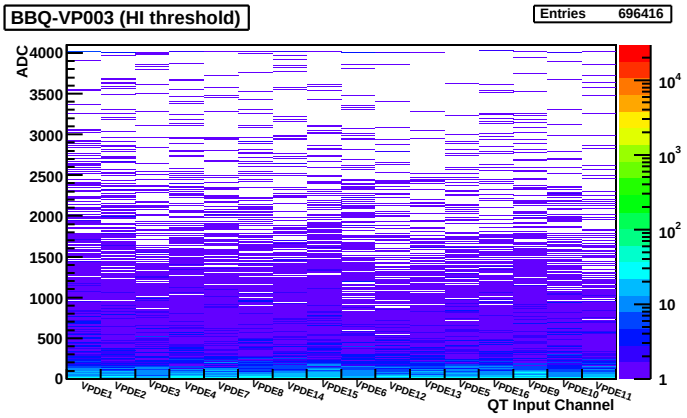

BBQ-ZD001 (ZDC TOWER)
Entries 696416
TAC

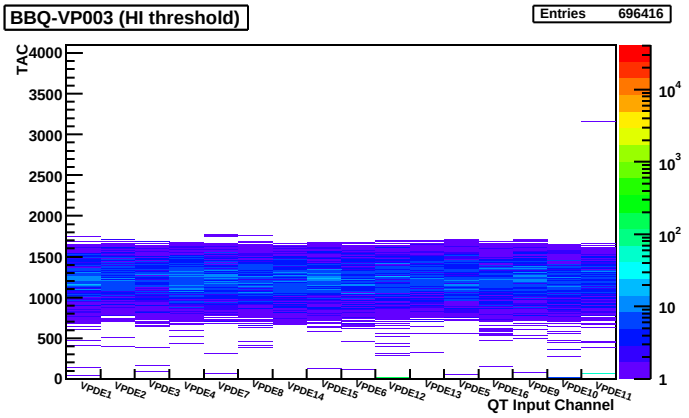

BBQ-VP001 (LO threshold)
Entries 696416
ADC
QT Input Channel

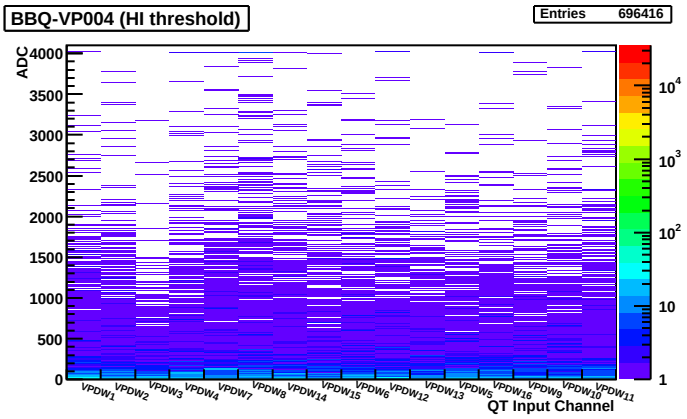

BBQ-VP001 (LO threshold)
Entries 696416
TAC
QT Input Channel

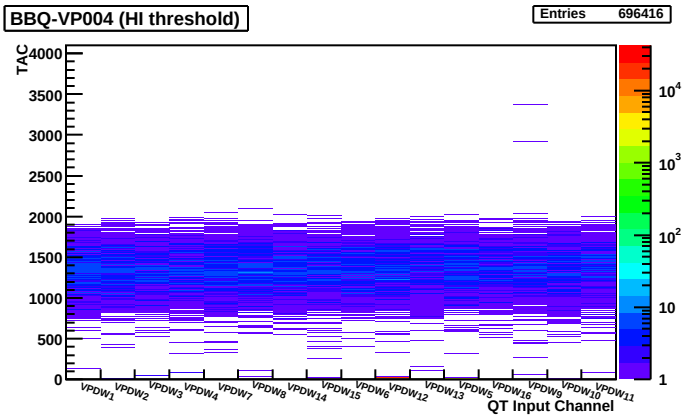

BBQ-VP002 (LO threshold)
Entries 696416
ADC
QT Input Channel

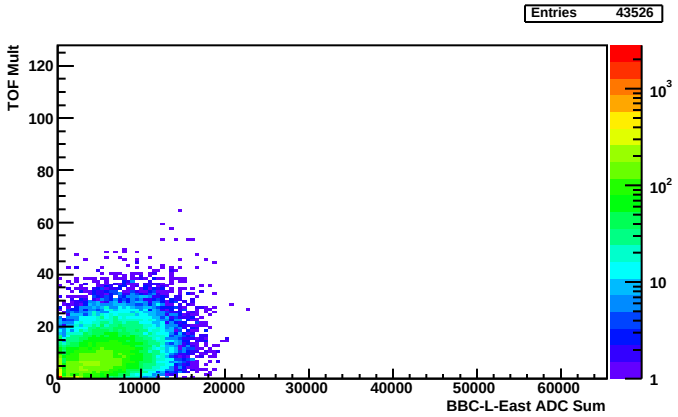

BBQ-VP002 (LO threshold)
Entries 696416
TAC
QT Input Channel

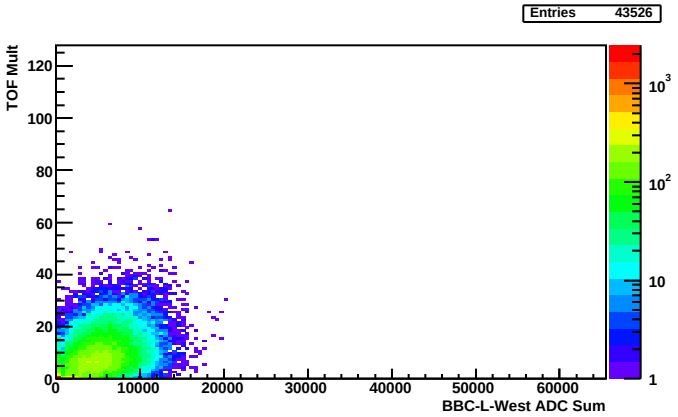

BBQ-VP003 (HI threshold)
Entries 696416
ADC
QT Input Channel

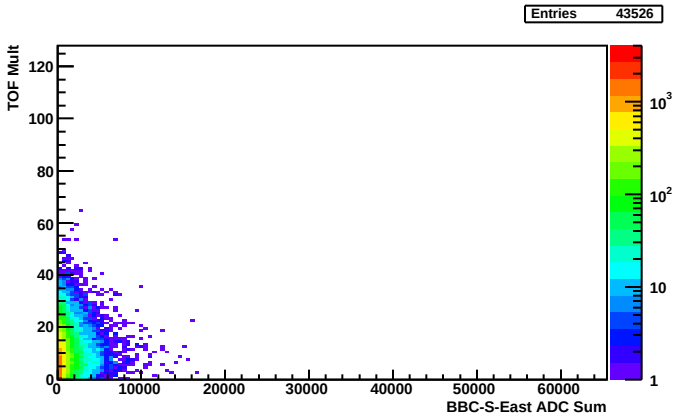

BBQ-VP003 (HI threshold)
Entries 696416
TAC
QT Input Channel

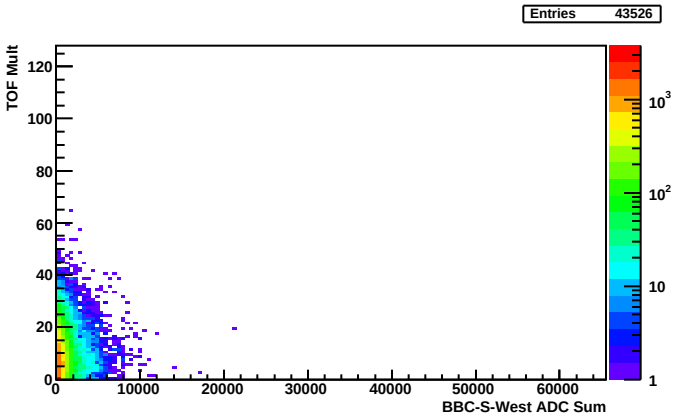

BBQ-VP004 (HI threshold)
Entries 696416
ADC
QT Input Channel

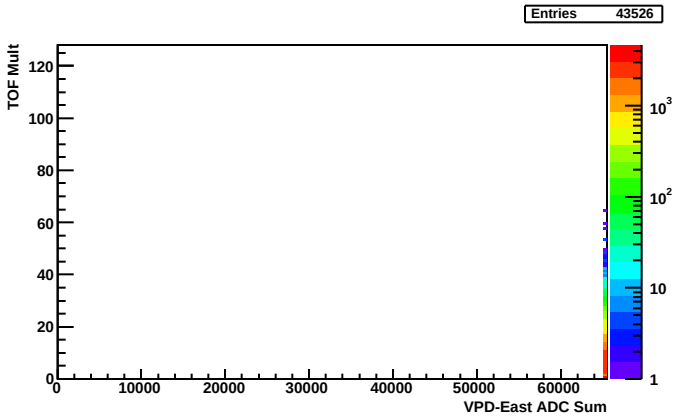

BBQ-VP004 (HI threshold)
Entries 696416
TAC
QT Input Channel

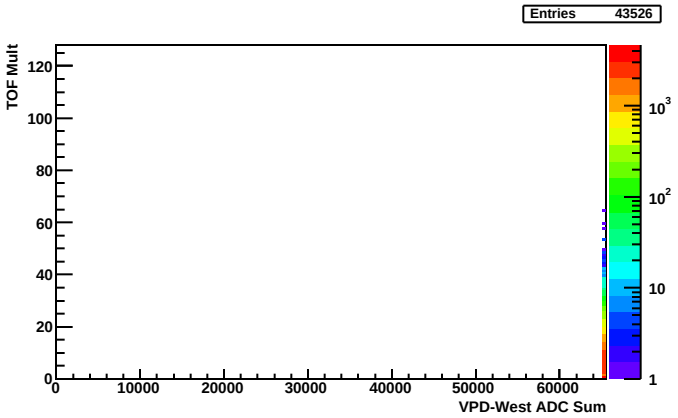

Entries 43526
TOF Mult
BBC-L-East ADC Sum

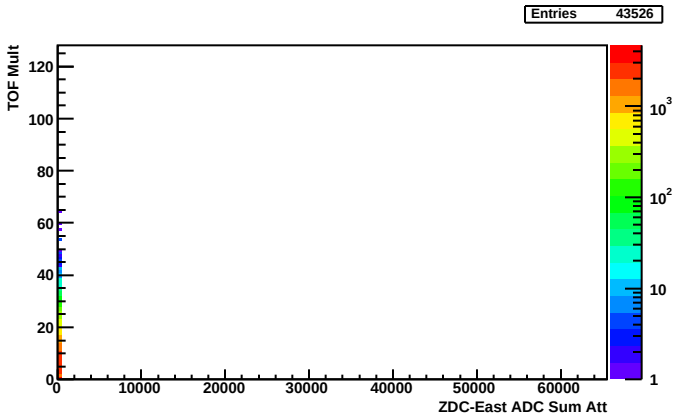

Entries 43526
TOF Mult
BBC-L-West ADC Sum

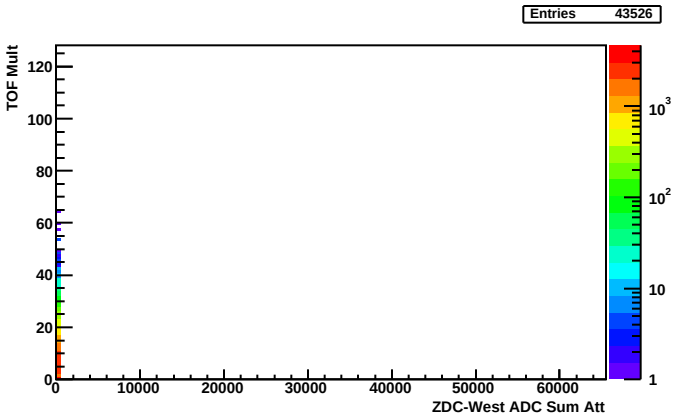

Entries 43526
TOF Mult
BBC-S-East ADC Sum

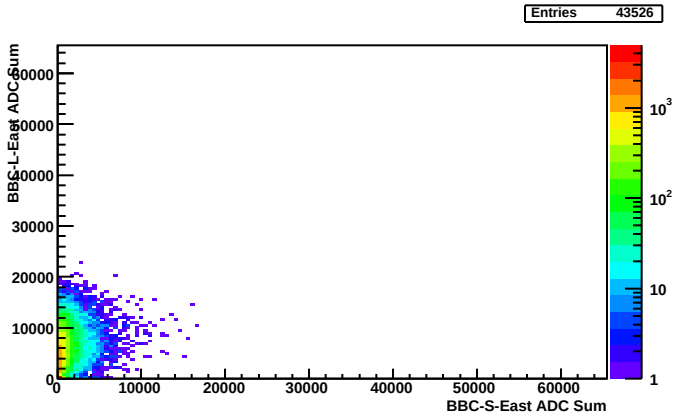

Entries 43526
TOF Mult
BBC-S-West ADC Sum

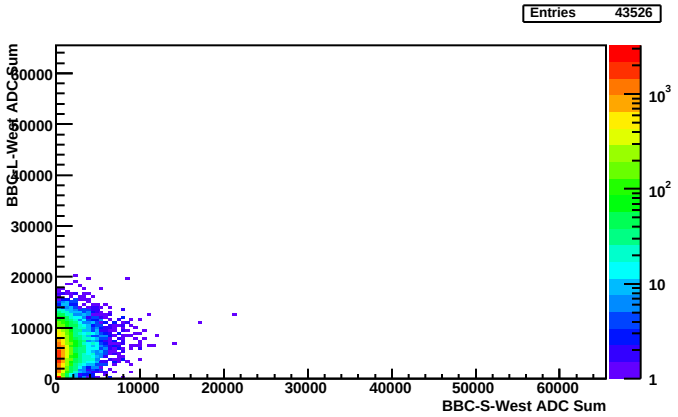

Entries 43526
TOF Mult
VPD-East ADC Sum

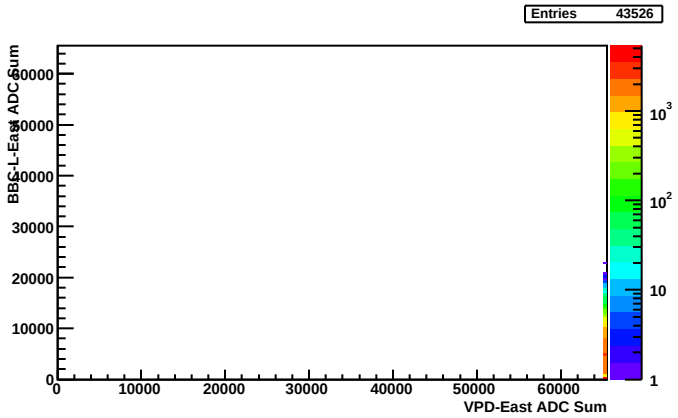

Entries 43526
TOF Mult
VPD-West ADC Sum

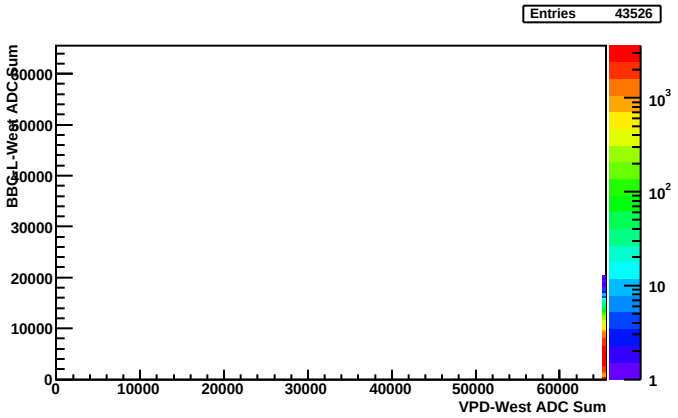

Entries 43526
TOF Mult
ZDC-East ADC Sum Att

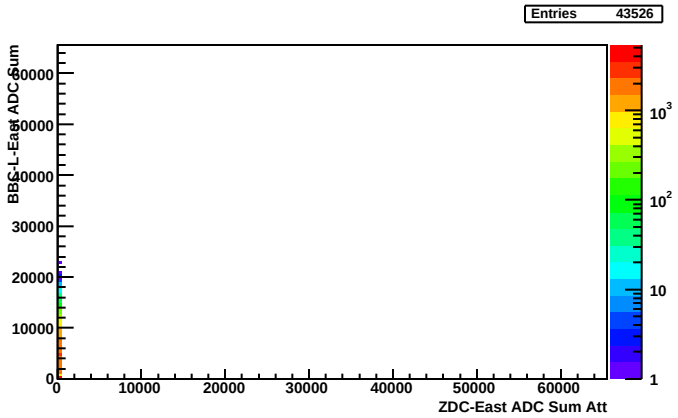

Entries 43526
TOF Mult
ZDC-West ADC Sum Att

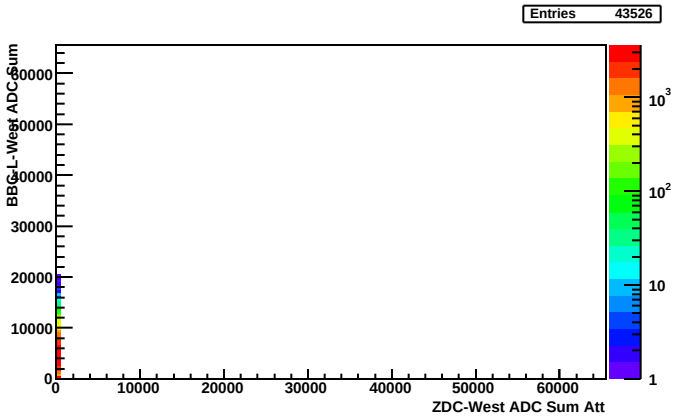

Entries 43526
BBC-L-East ADC Sum
BBC-S-East ADC Sum

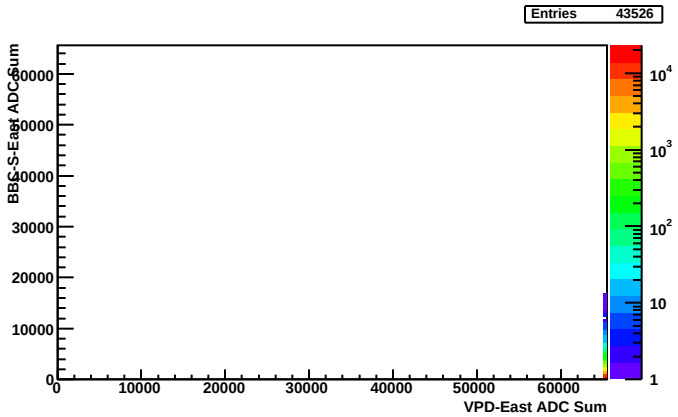

Entries 43526
BBC-L-West ADC Sum
BBC-S-West ADC Sum

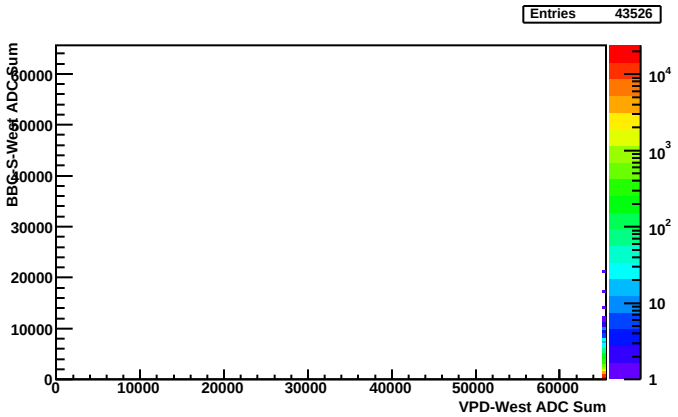

Entries 43526
BBC-L-East ADC Sum
VPD-East ADC Sum

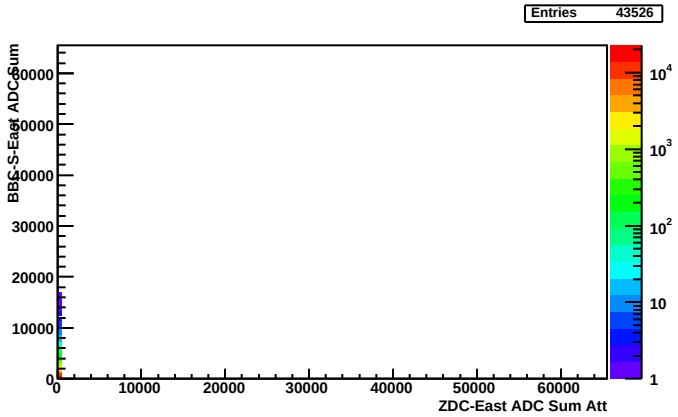

Entries 43526
BBC-L-West ADC Sum
VPD-West ADC Sum

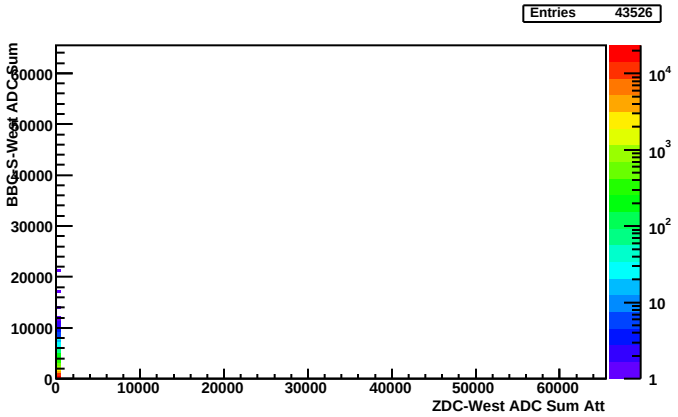

Entries 43526
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

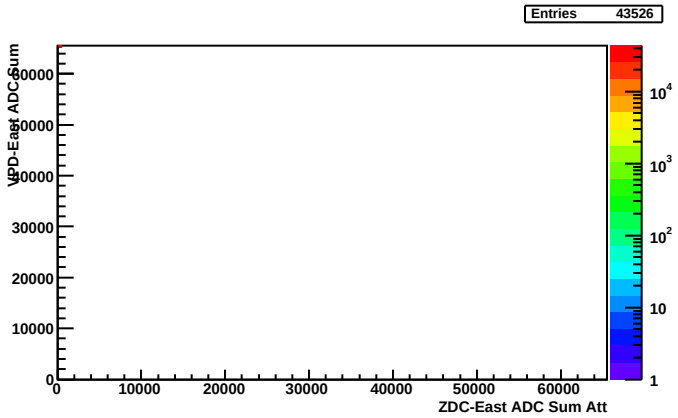

Entries 43526
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

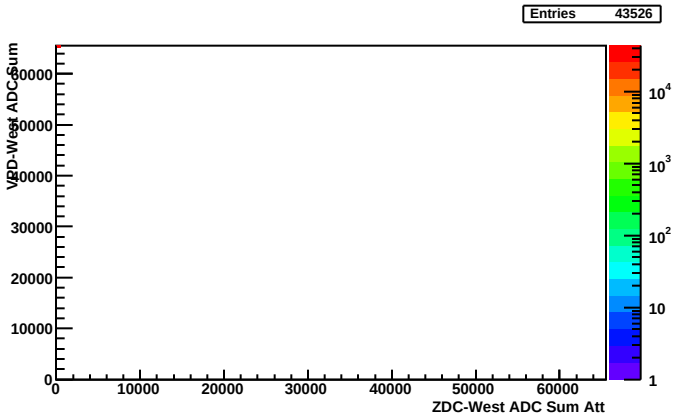

Entries 43526
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 43526
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 43526
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 43526
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 43526
VPD-East ADC Sum
ZDC-East ADC Sum Att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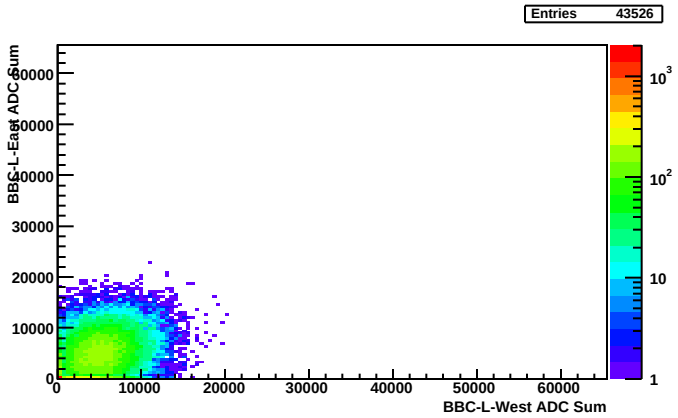
VPD-West ADC Sum
ZDC-West ADC Sum Att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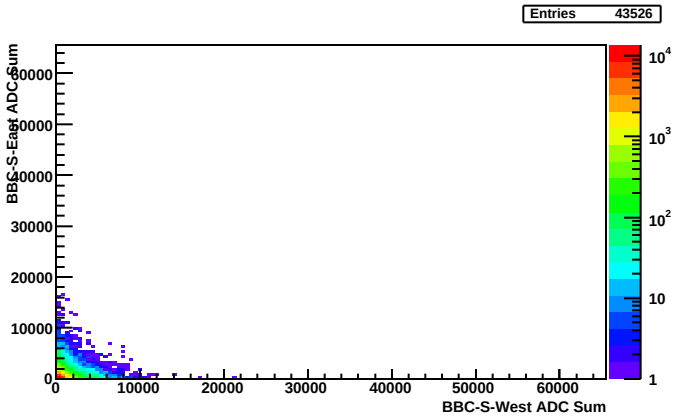
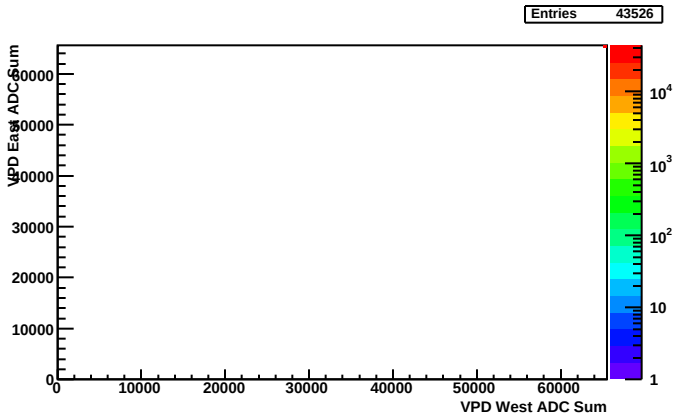
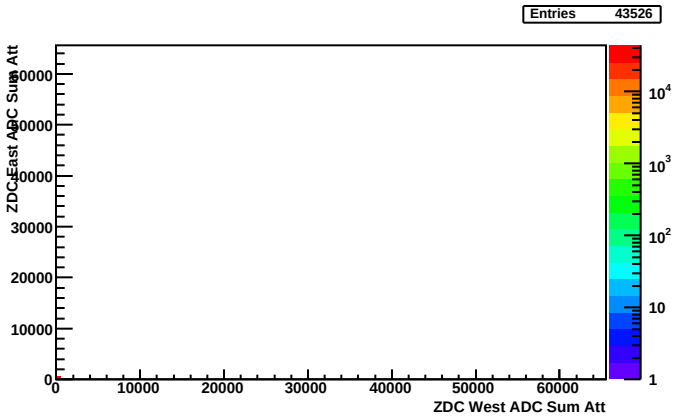
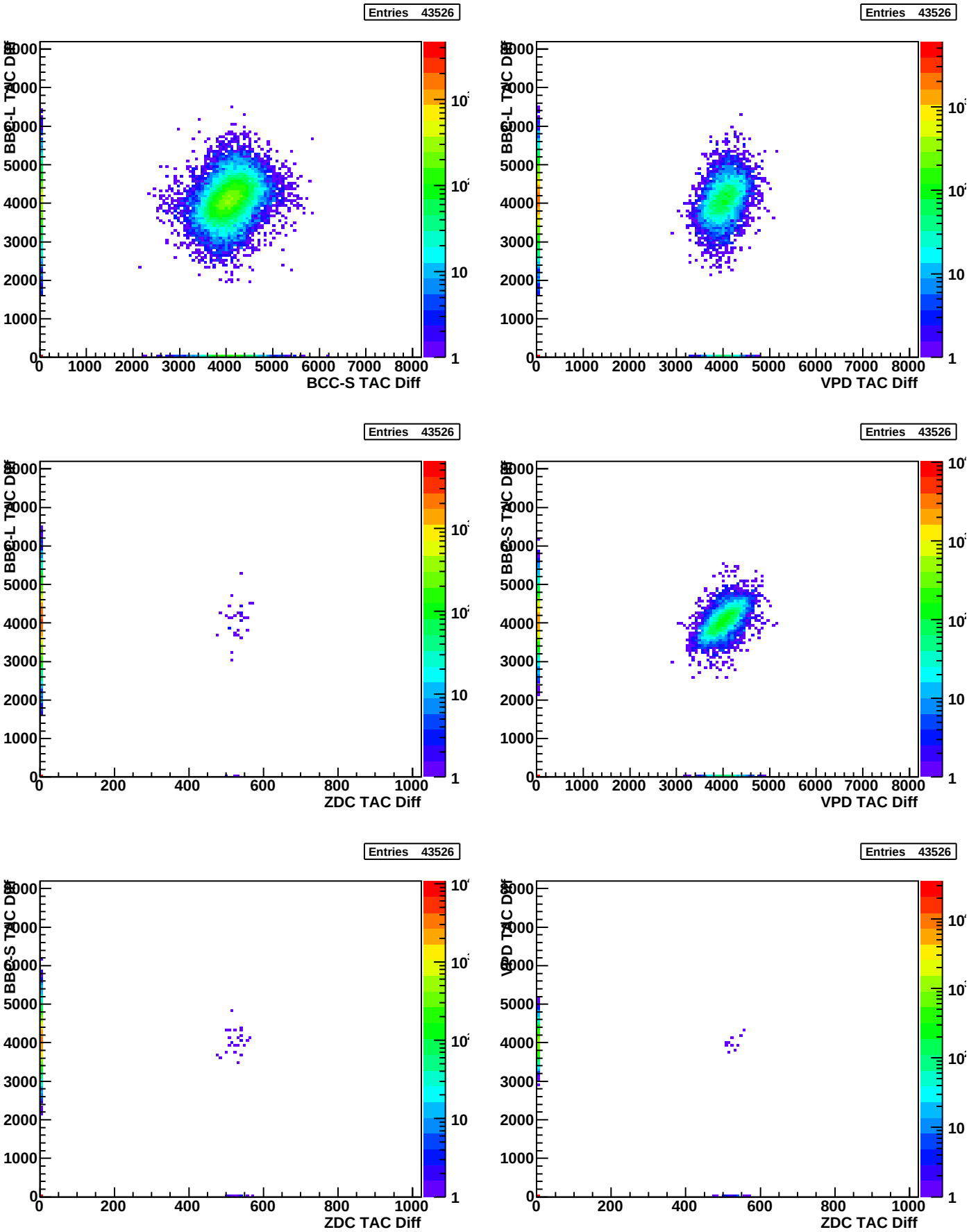
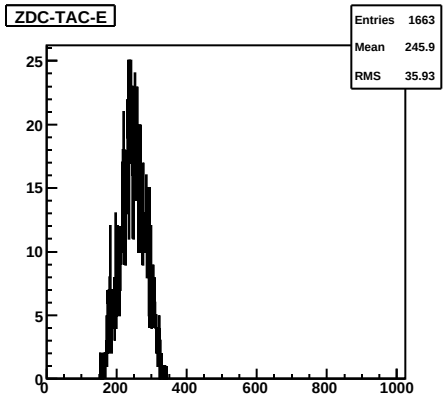
BBC-L-East ADC Sum
BBC-L-West ADC Sum

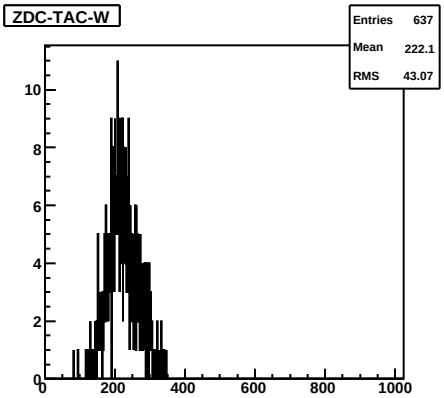

Entries 43526
BBC-S-East ADC Sum
BBC-S-West ADC Sum

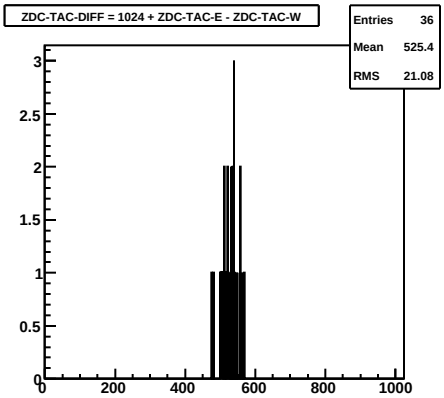

Entries 43526
VPD East ADC Sum
VPD West ADC Sum

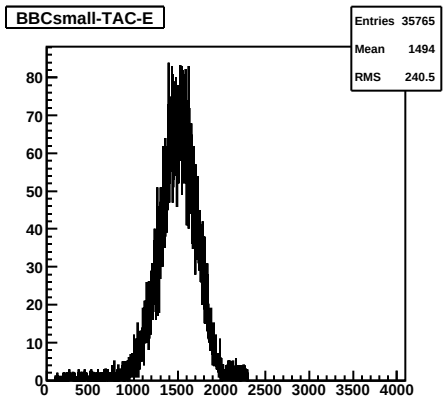

Entries 43526
ZDC East ADC Sum Att
ZDC West ADC Sum Att

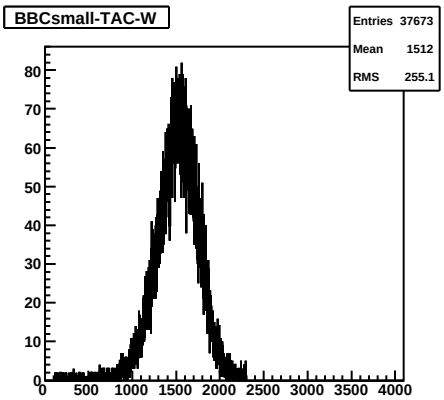

Entries 43526
BBC-L TAC Diff
BCC-S TAC Diff
Entries 43526
BBC-L TAC Diff
VPD TAC Diff
Entries 43526
BBC-L TAC Diff
ZDC TAC Diff
Entries 43526
BBC-S TAC Diff
VPD TAC Diff
Entries 43526
BBC-S TAC Diff
ZDC TAC Diff
Entries 43526
VPD TAC Diff
ZDC TAC Diff

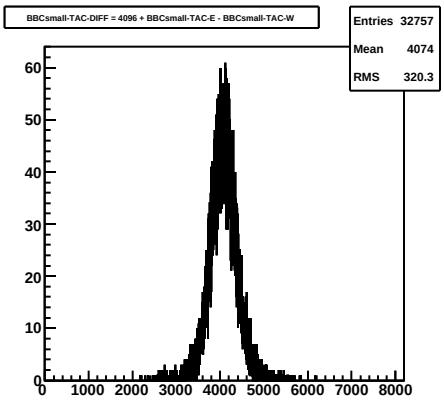

ZDC-TAC-E
Entries 1663
Mean 245.9
RMS 35.93

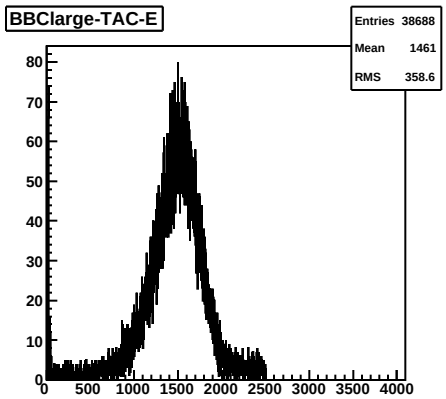

ZDC-TAC-W
Entries 637
Mean 222.1
RMS 43.07

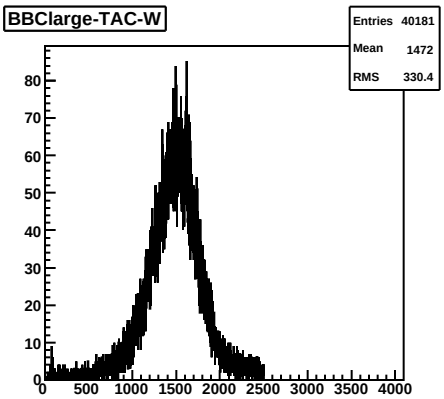

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 36
Mean 525.4
RMS 21.08

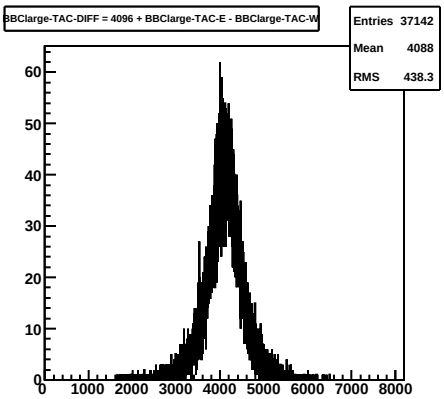

BBCsmall-TAC-E
Entries 35765
Mean 1494
RMS 240.5

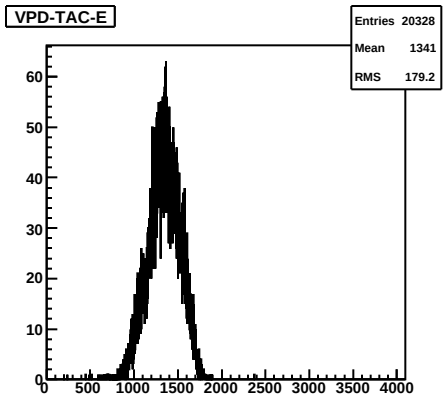

BBCsmall-TAC-W
Entries 37673
Mean 1512
RMS 255.1

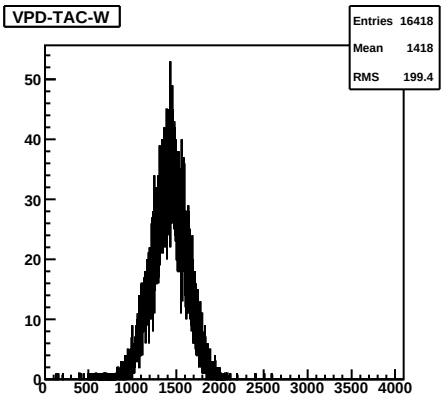

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 32757
Mean 4074
RMS 320.3

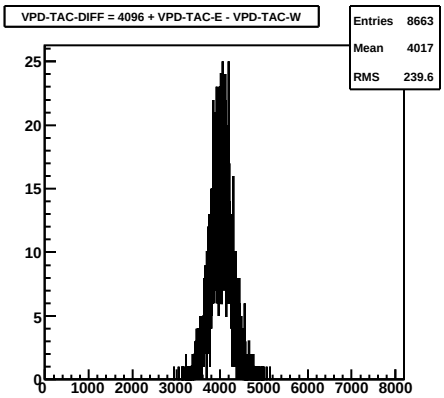

BBClarge-TAC-E
Entries 38688
Mean 1461
RMS 358.6

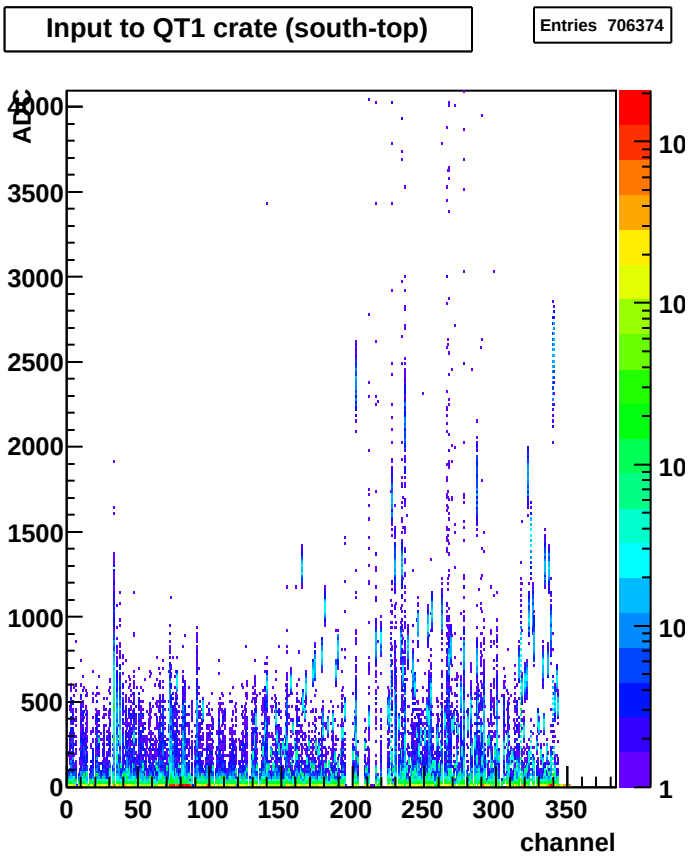

BBClarge-TAC-W
Entries 40181
Mean 1472
RMS 330.4

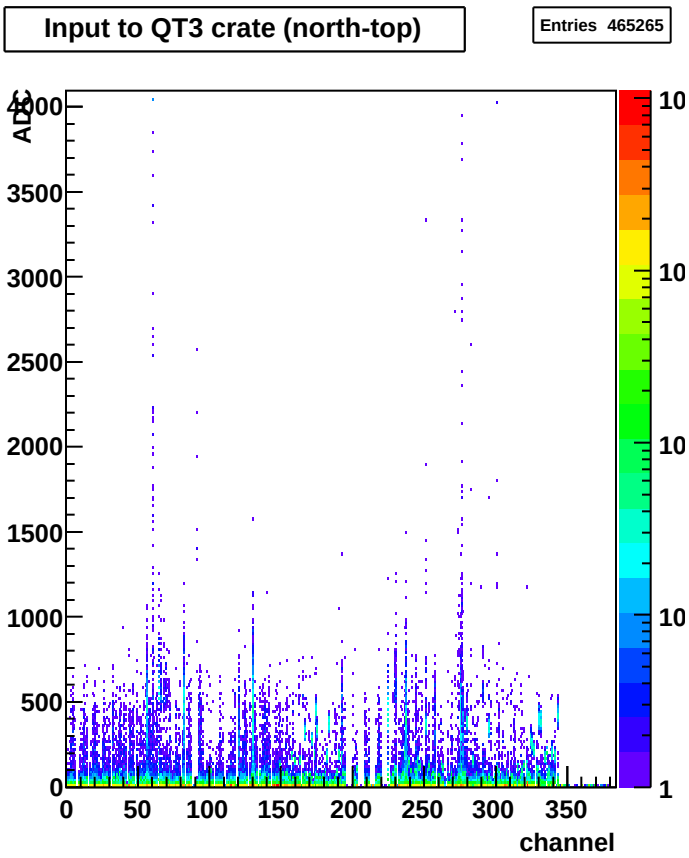

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 37142
Mean 4088
RMS 438.3

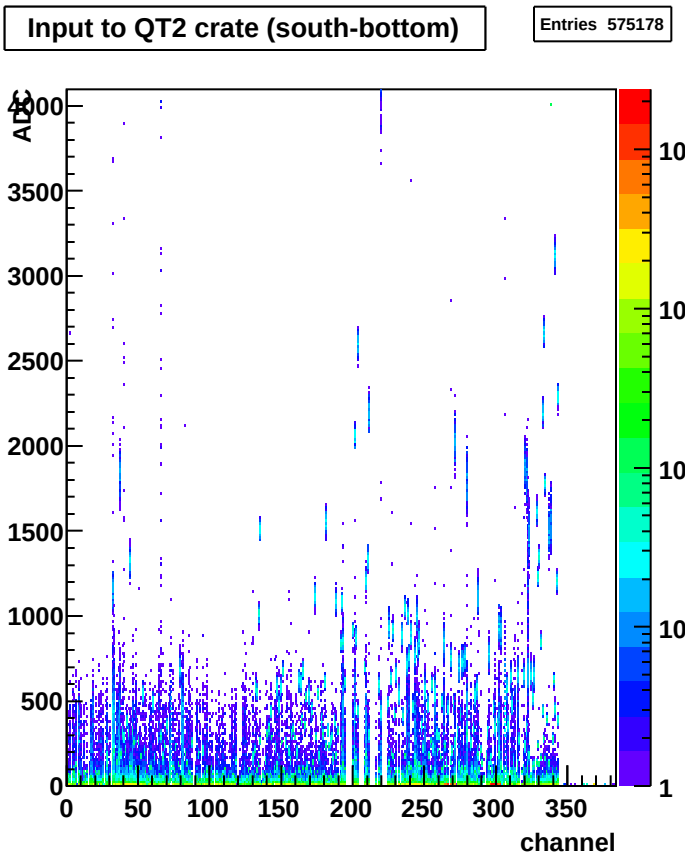

VPD-TAC-E
Entries 20328
Mean 1341
RMS 179.2

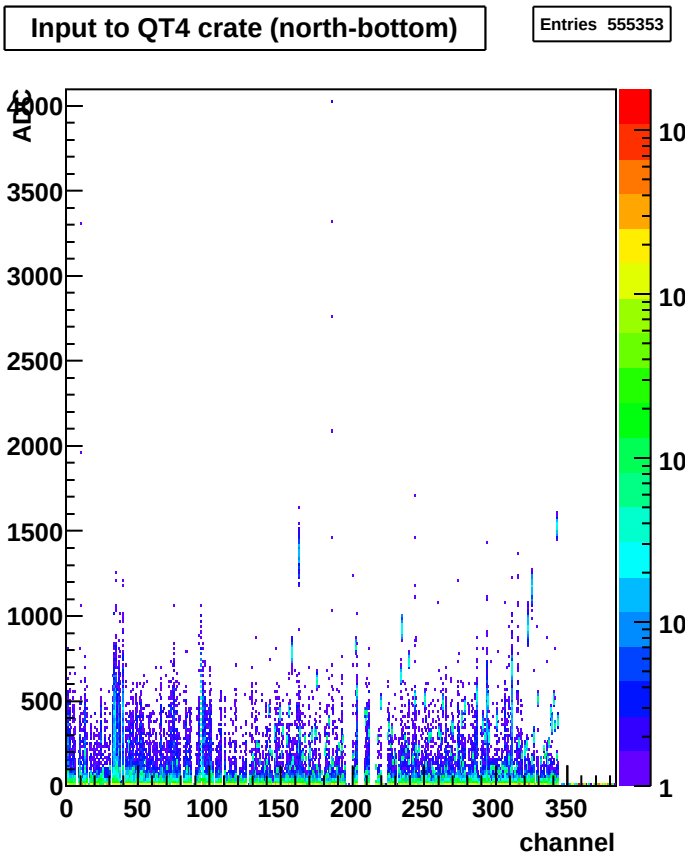

VPD-TAC-W
Entries 16418
Mean 1418
RMS 199.4

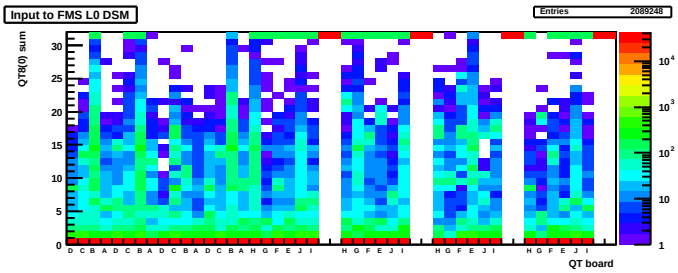

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 8663
Mean 4017
RMS 239.6

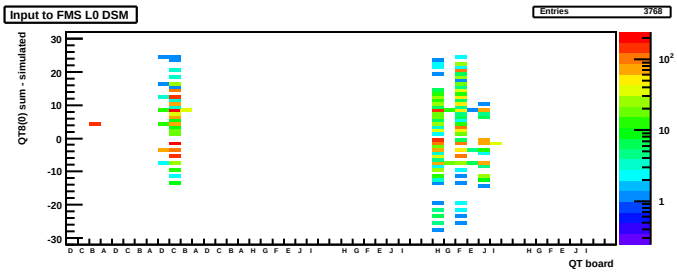

Input to QT1 crate (south-top)
Entries 706374
ADC
channel

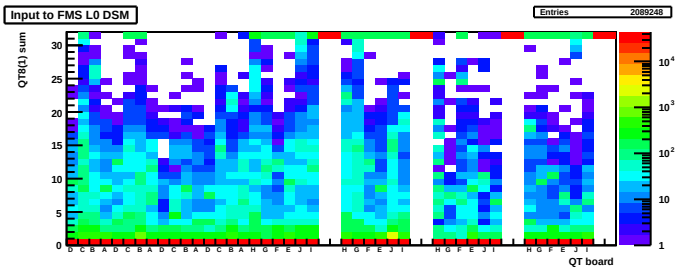

Input to QT3 crate (north-top)
Entries 465265
ADC
channel

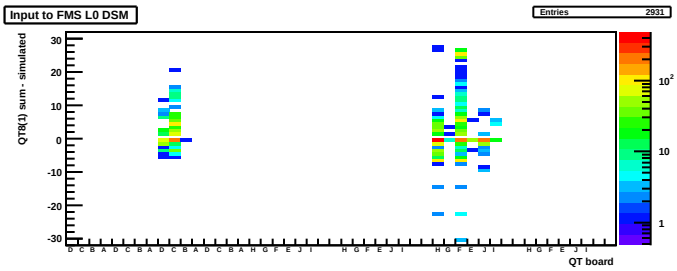

Input to QT2 crate (south-bottom)
Entries 575178
ADC
channel

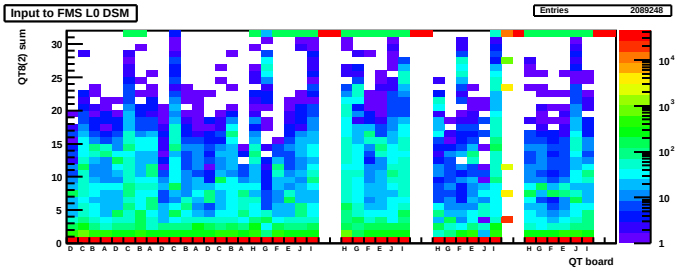

Input to QT4 crate (north-bottom)
Entries 555353
ADC
channel

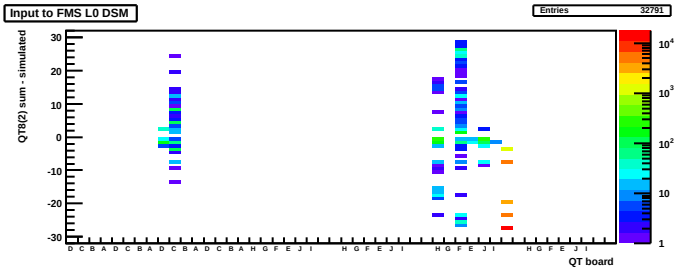

Input to FMS L0 DSM
Entries 2089248
QT8(0) sum
QT board

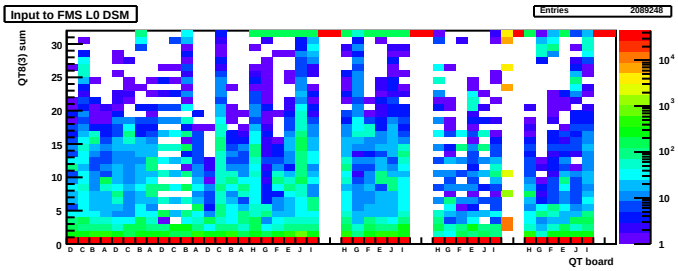

Input to FMS L0 DSM
Entries 3768
QT8(0) sum - simulated
QT board

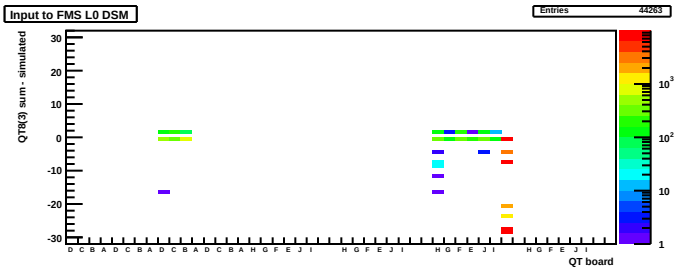

Input to FMS L0 DSM
Entries 2089248
QT8(1) sum
QT board

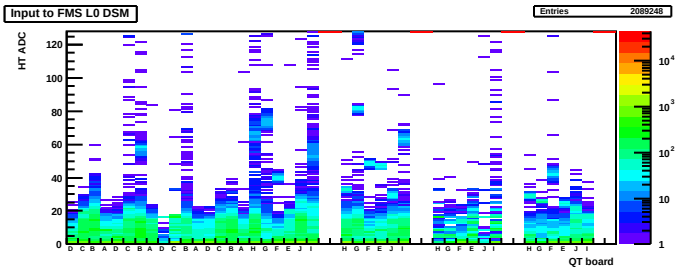

Input to FMS L0 DSM
Entries 2931
QT8(1) sum - simulated
QT board

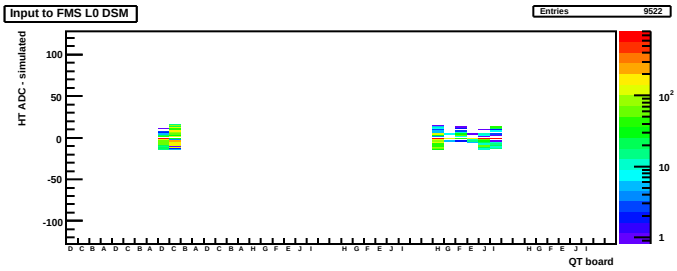

Input to FMS L0 DSM
Entries 2089248
QT8(2) sum
QT board

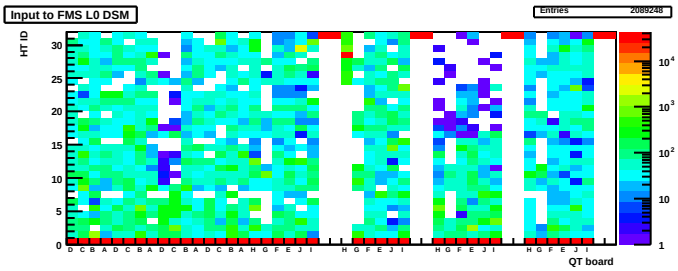

Input to FMS L0 DSM
Entries 32791
QT8(2) sum - simulated
QT board

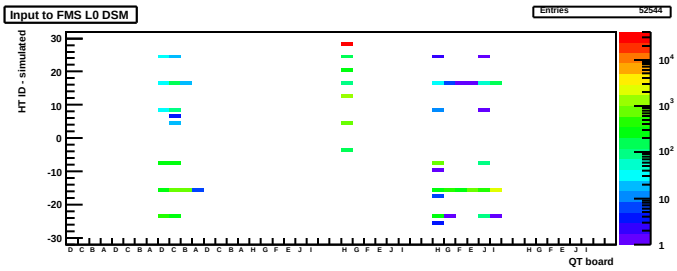

Input to FMS L0 DSM
Entries 2089248
QT8(3) sum
QT board

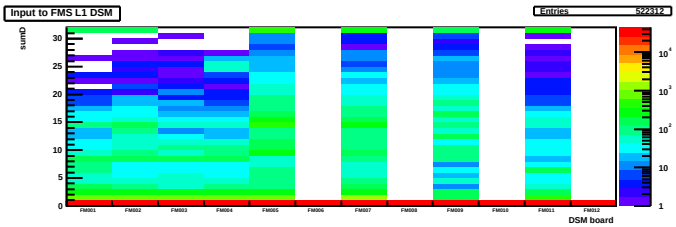

Input to FMS L0 DSM
Entries 44283
QT8(3) sum - simulated
QT board

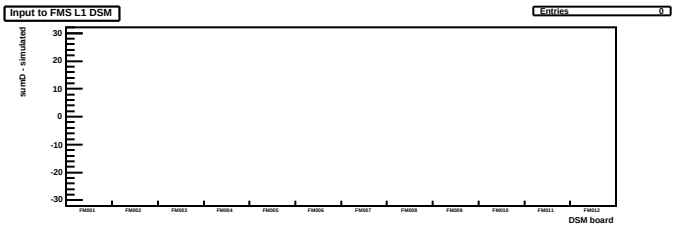

Input to FMS L0 DSM
Entries 2089248
HT ADC
QT board

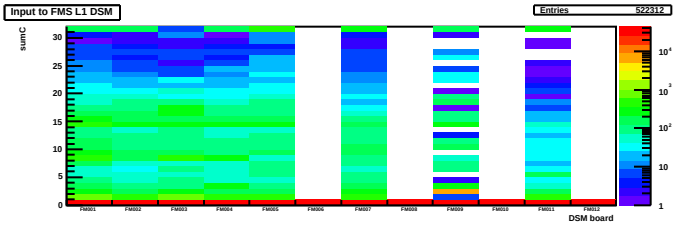

Input to FMS L0 DSM
Entries 9522
HT ADC - simulated
QT board

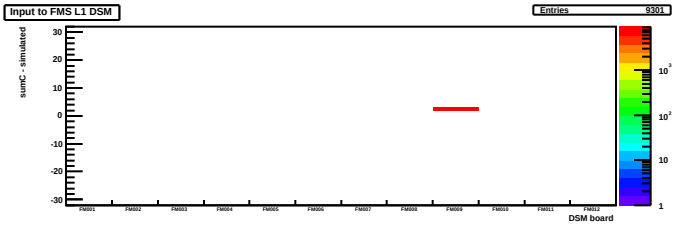

Input to FMS L0 DSM
Entries 2089248
HT ID
QT board

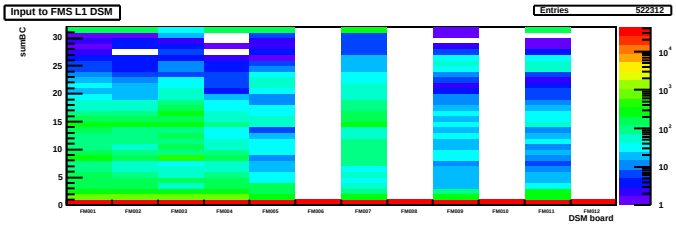

Input to FMS L0 DSM
Entries 52544
HT ID - simulated
QT board

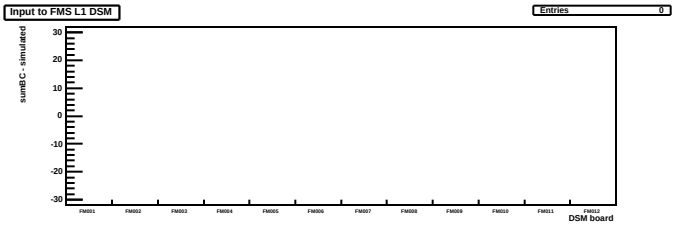

Input to FMS L1 DSM
Entries 522312
sumD
DSM board

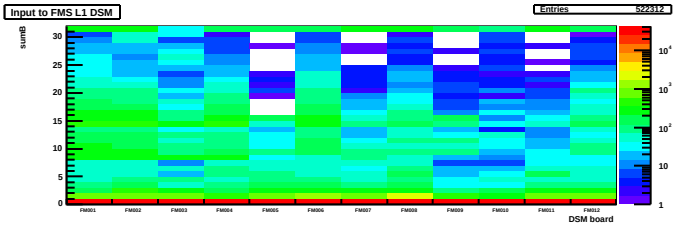

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

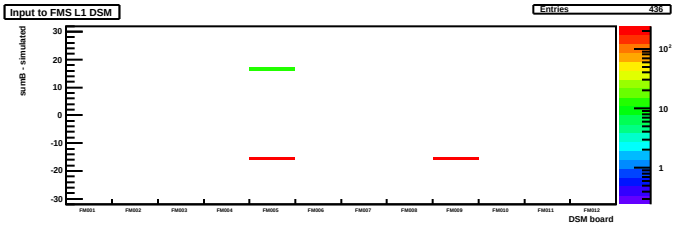

Input to FMS L1 DSM
Entries 522312
sumC
DSM board

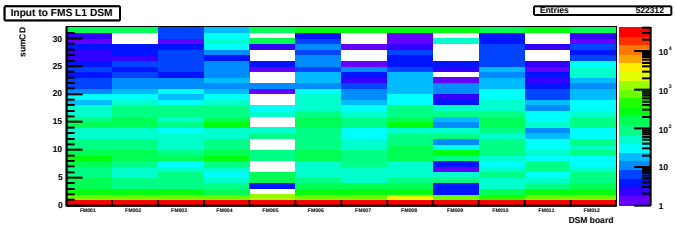

Input to FMS L1 DSM
Entries 9301
sumC - simulated
DSM board

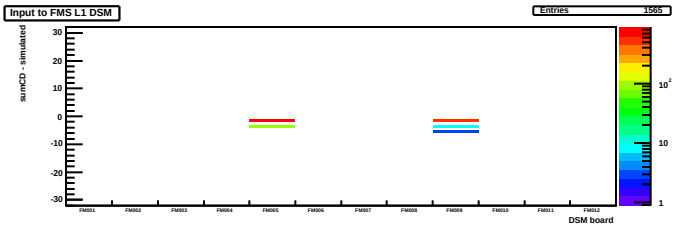

Input to FMS L1 DSM
Entries 522312
sumBC
DSM board

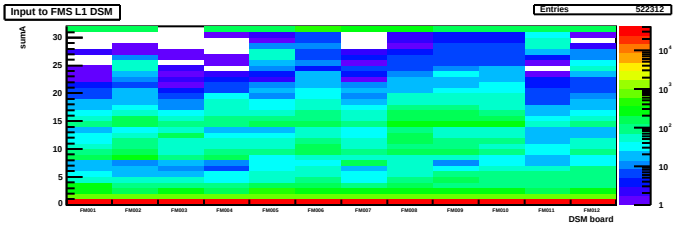

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

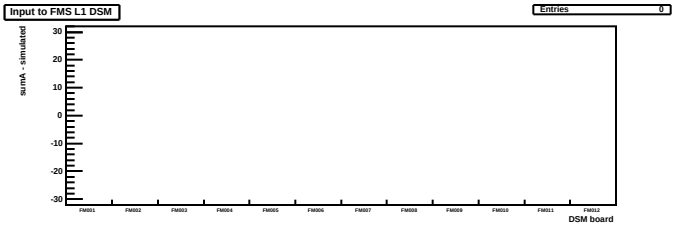

Input to FMS L1 DSM
Entries 522312
sumB
DSM board

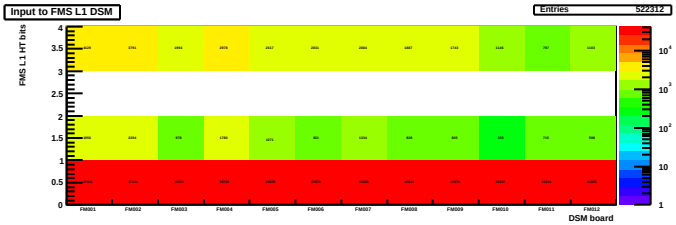

Input to FMS L1 DSM
Entries 436
sumB - simulated
DSM board

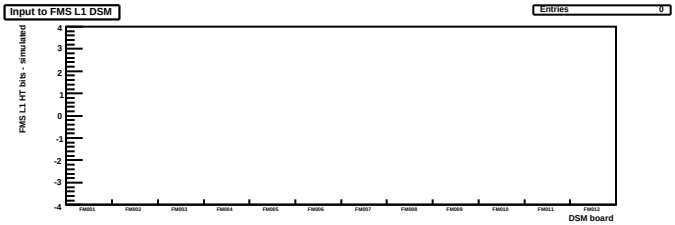

Input to FMS L1 DSM
Entries 522312
sumCD
DSM board

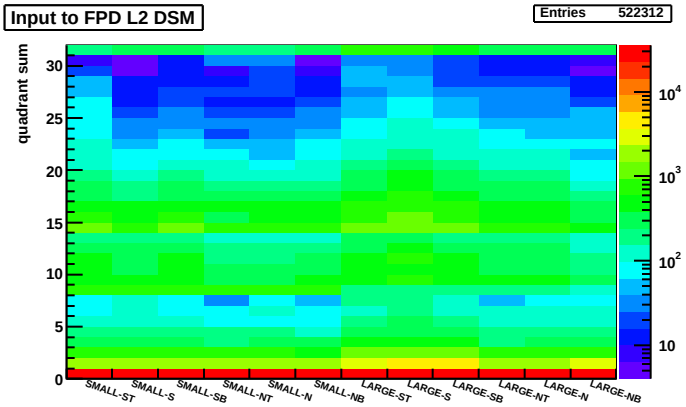

Input to FMS L1 DSM
Entries 1565
sumCD - simulated
DSM board

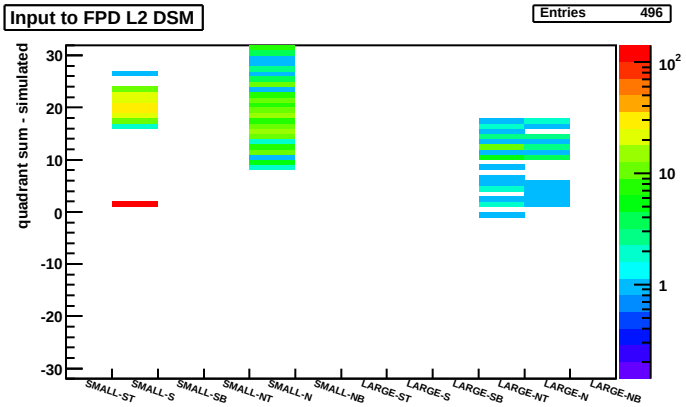

Input to FMS L1 DSM
Entries 522312
sumA
DSM board

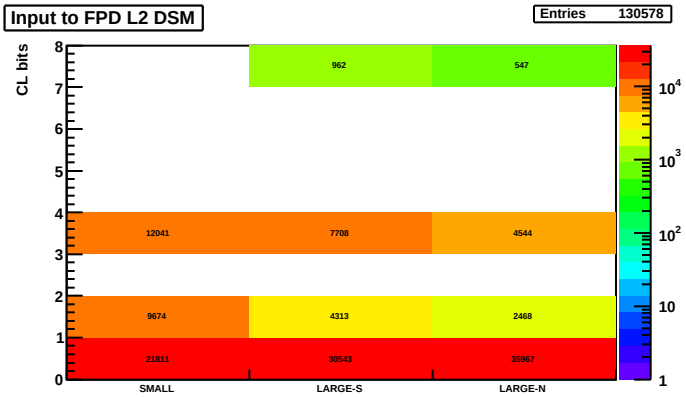

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

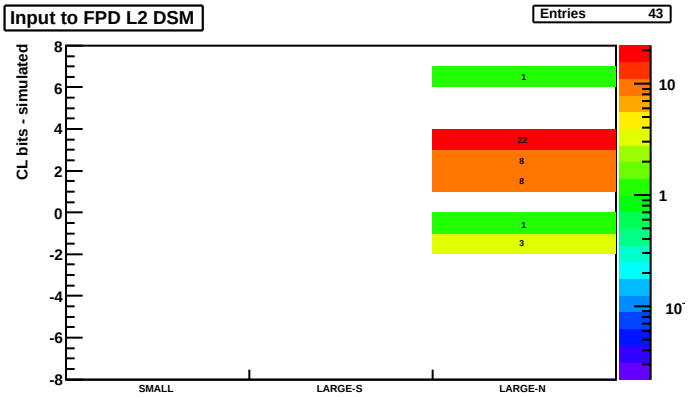

Input to FMS L1 DSM
Entries 522312
FMS L1 HT bits
DSM board

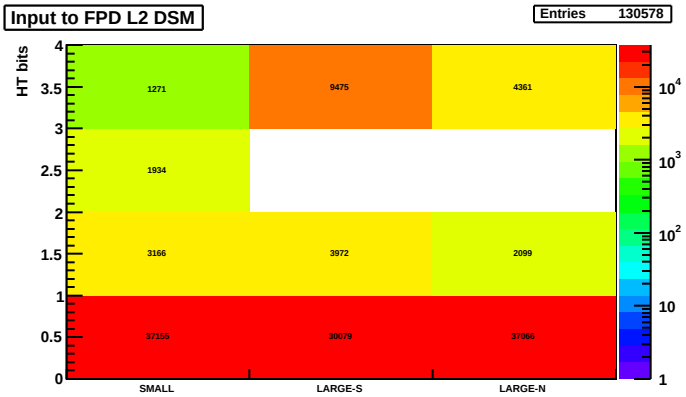

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

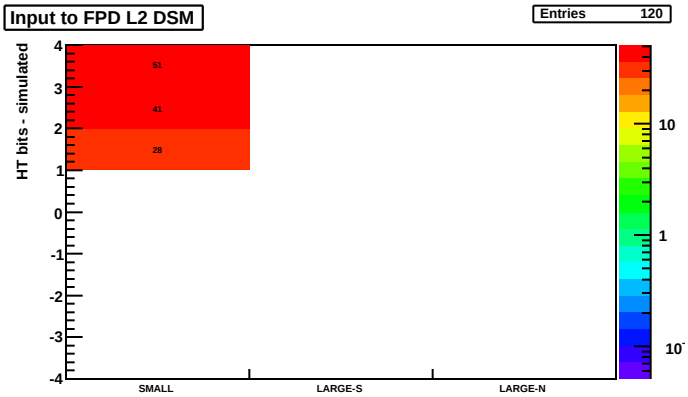

Input to FPD L2 DSM
Entries 522312
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

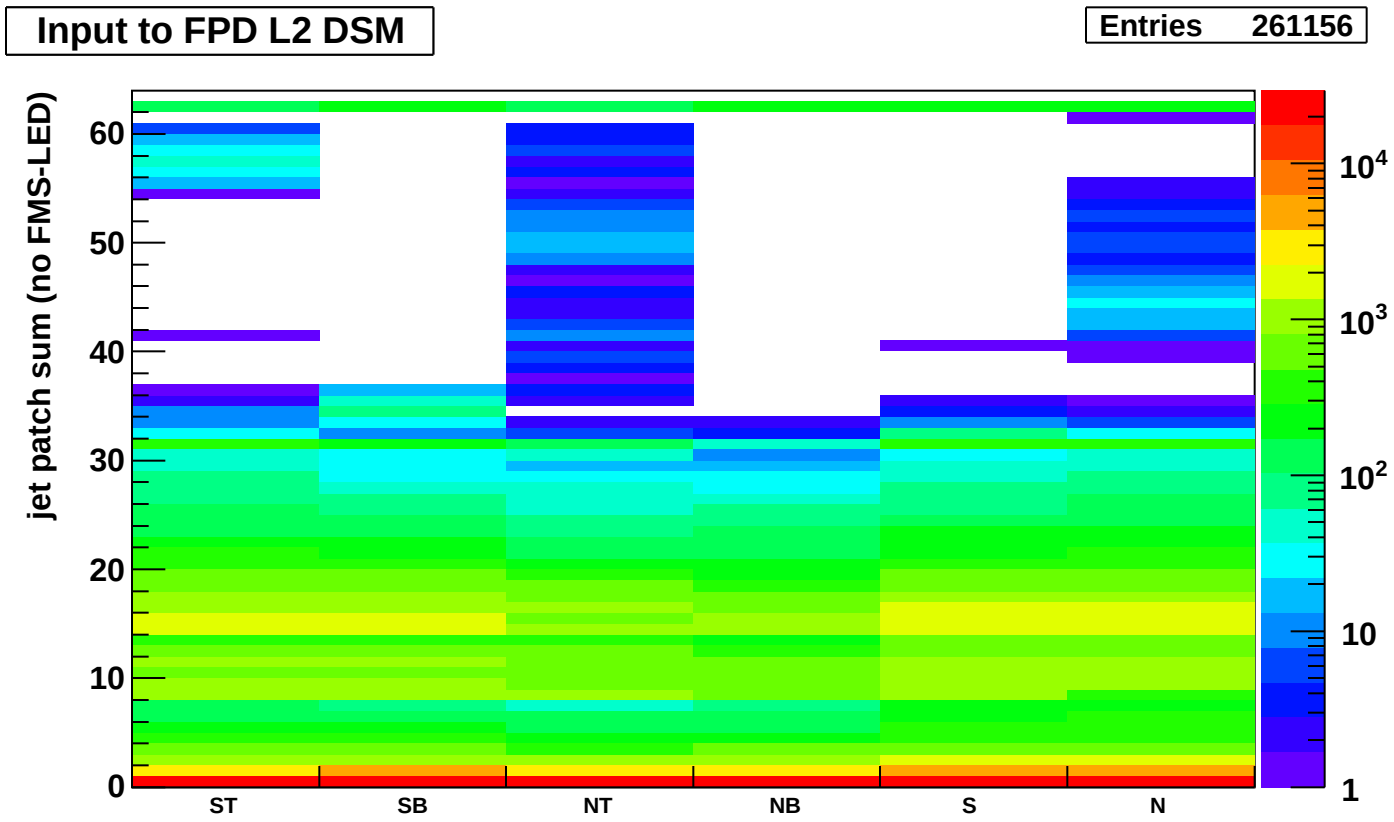

Input to FPD L2 DSM
Entries 496
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

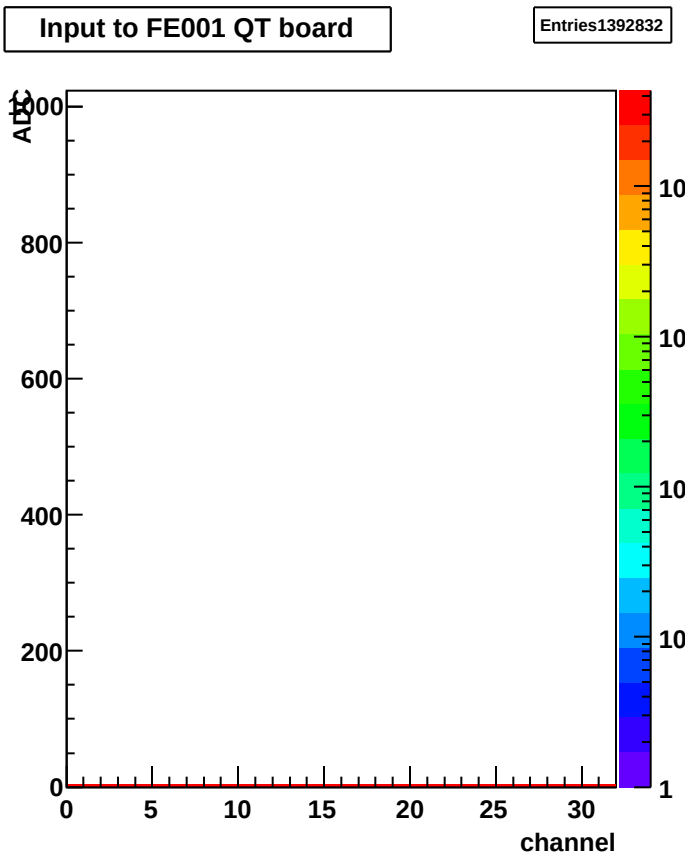

Input to FPD L2 DSM
Entries 130578
CL bits
962
547
12041
7708
4544
9674
4313
2468
21811
30543
35967
SMALL
LARGE-S
LARGE-N

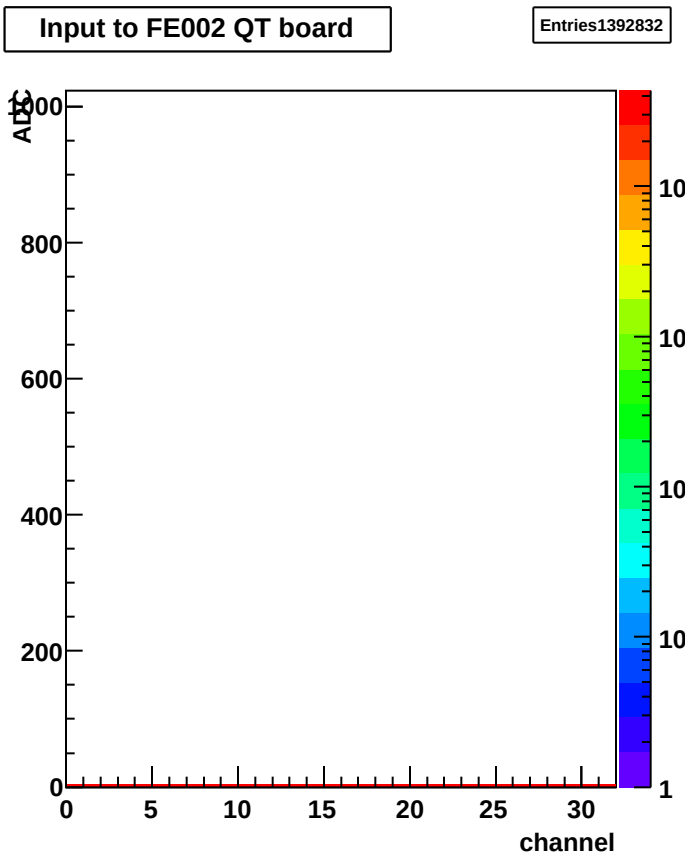

Input to FPD L2 DSM
Entries 43
CL bits - simulated
1
22
8
8
1
3
SMALL
LARGE-S
LARGE-N

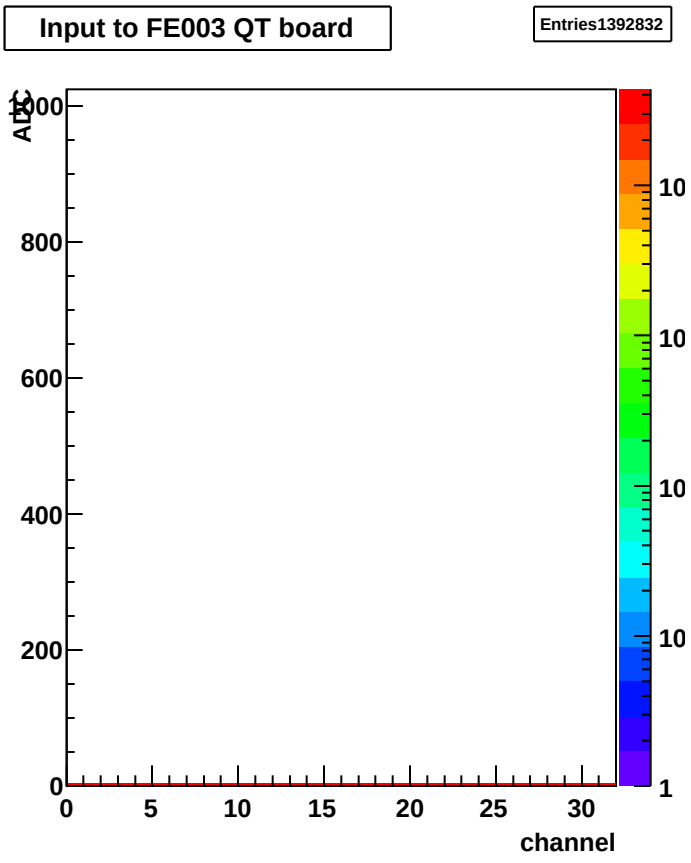

Input to FPD L2 DSM
Entries 130578
HT bits
1271
9475
4361
1934
3166
3972
2099
37155
30079
37066
SMALL
LARGE-S
LARGE-N

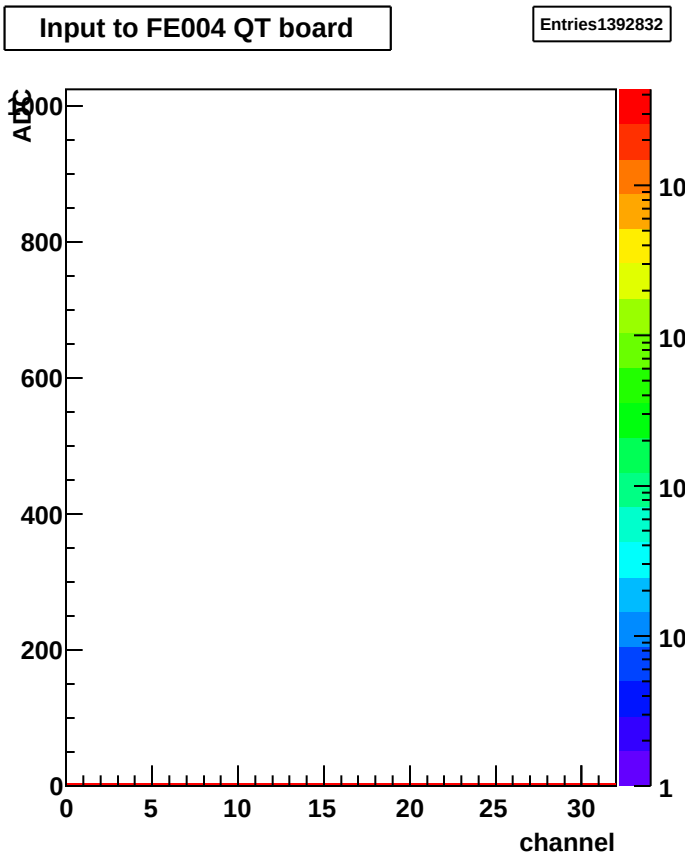

Input to FPD L2 DSM
Entries 120
HT bits - simulated
51
41
28
SMALL
LARGE-S
LARGE-N

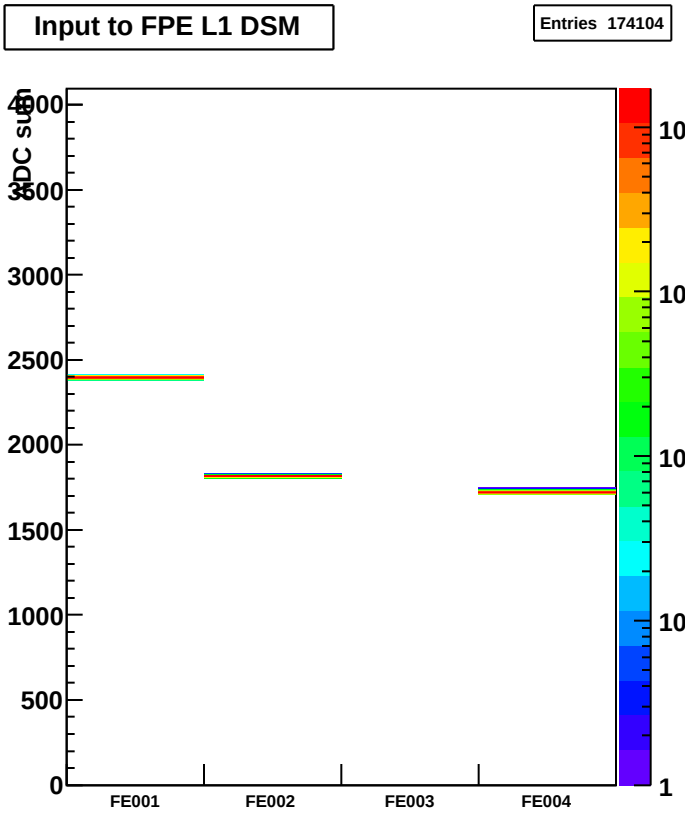

Input to FPD L2 DSM
Entries 261156
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

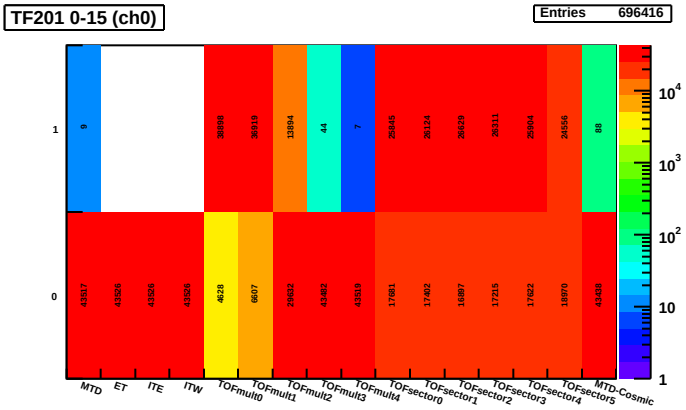

Input to FE001 QT board
Entries1392832
ADC
channel

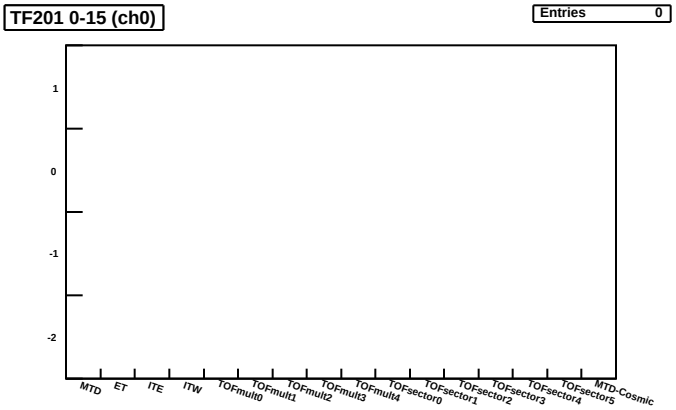

Input to FE002 QT board
Entries1392832
ADC
channel

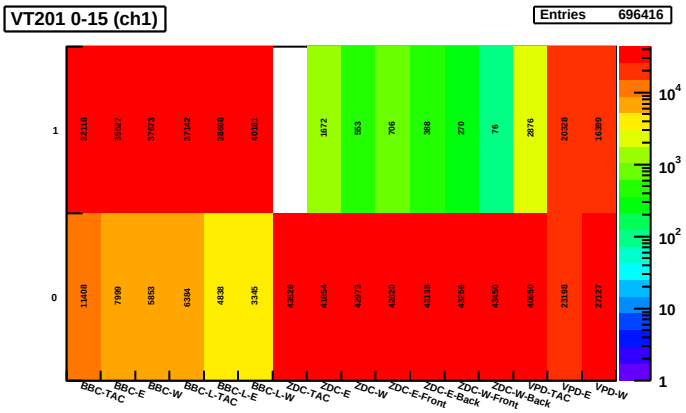

Input to FE003 QT board
Entries1392832
ADC
channel

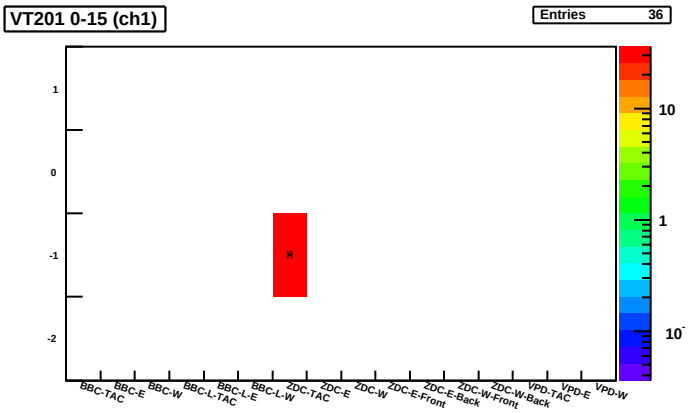

Input to FE004 QT board
Entries1392832
ADC
channel

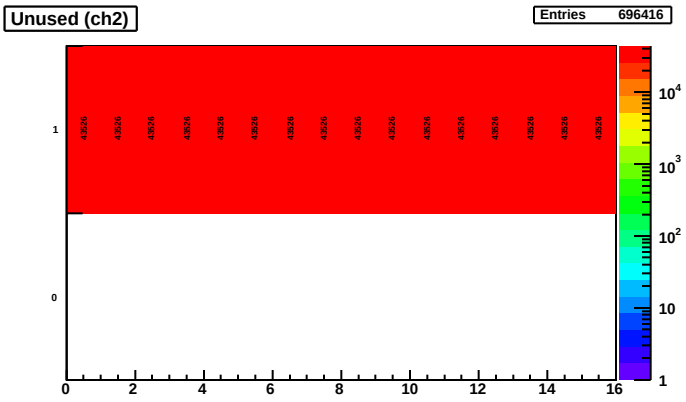

Input to FPE L1 DSM
Entries 174104
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

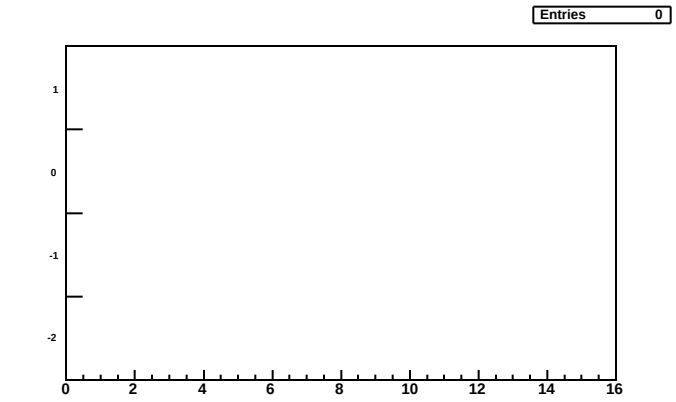

TF201 0-15 (ch0)
Entries 696416
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

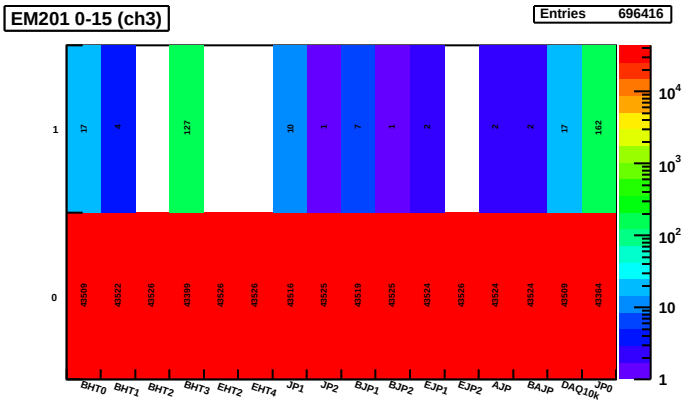

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

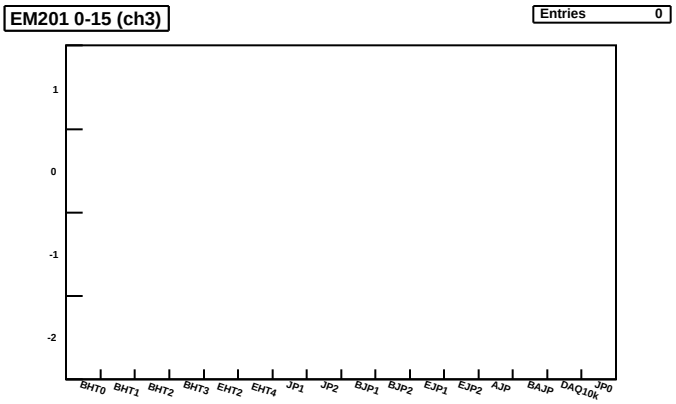

VT201 0-15 (ch1)
Entries 696416
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 36
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 696416


Entries 0


EM201 0-15 (ch3)
Entries 696416
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0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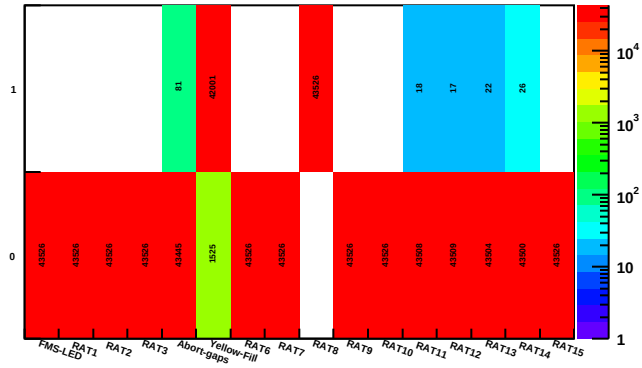

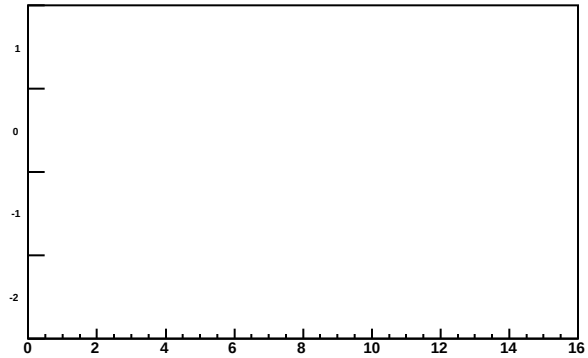

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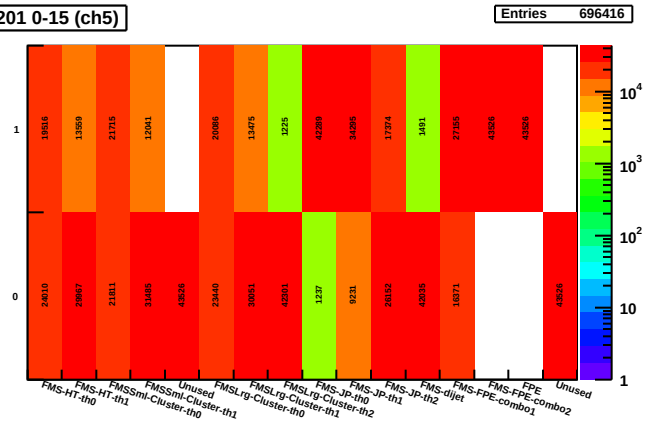

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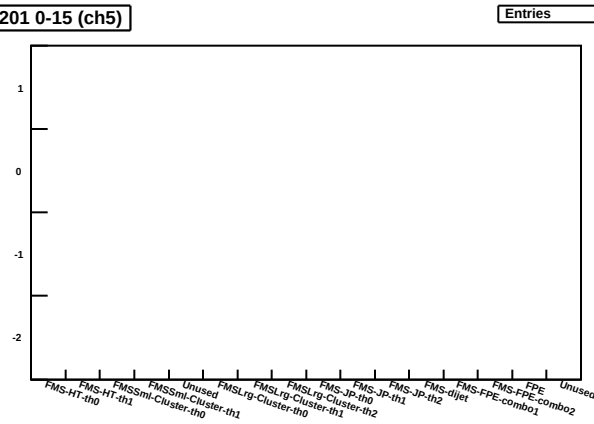
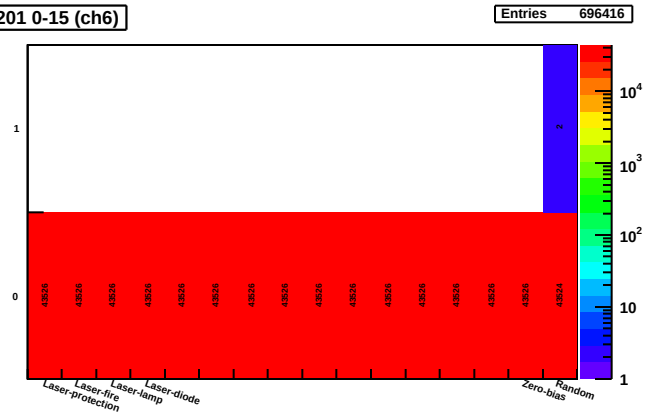
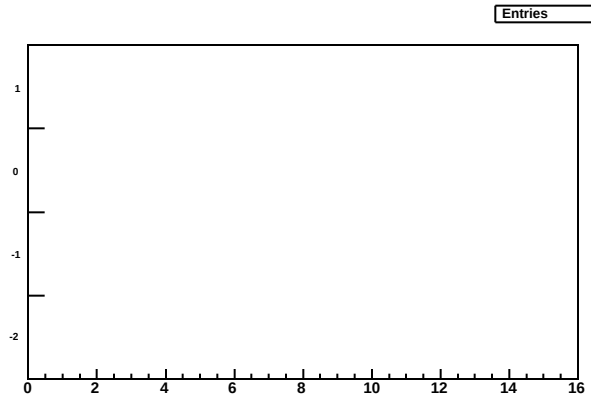
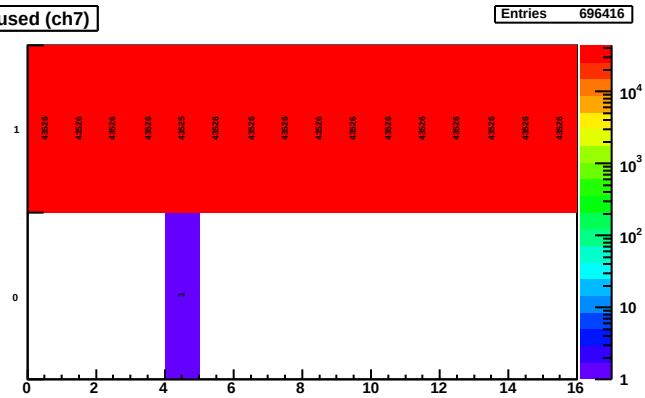
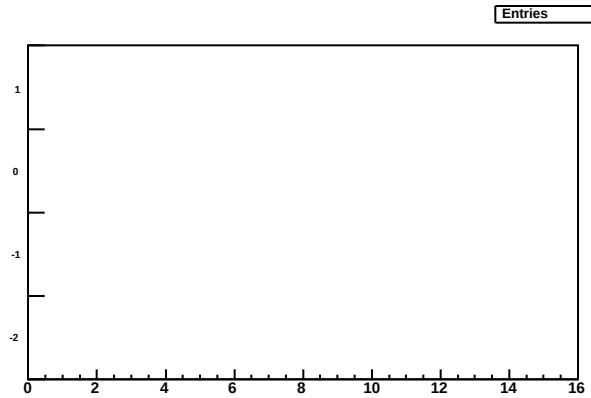


ST201 0-15 (ch6)





Unused (ch7)