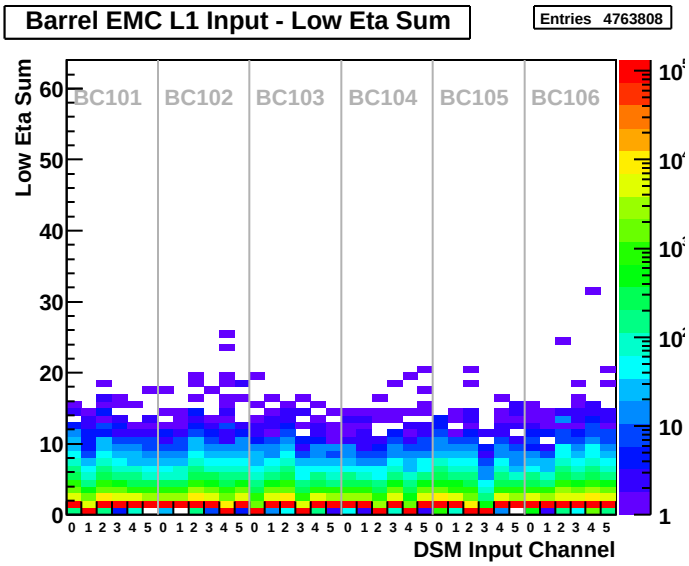
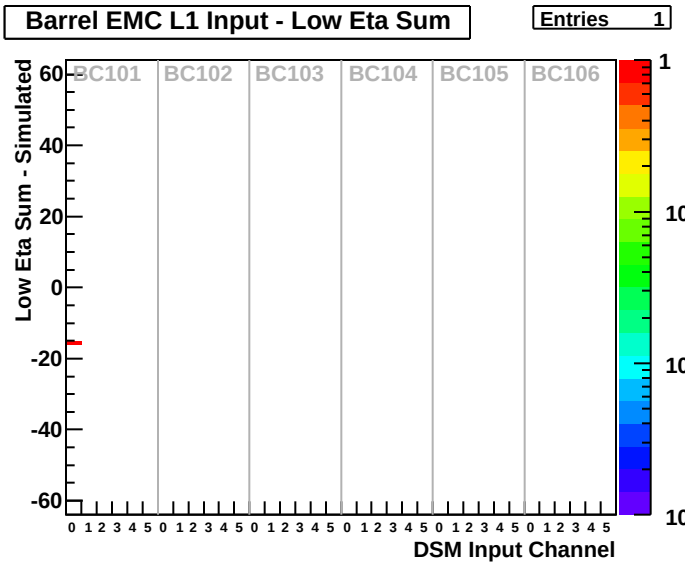
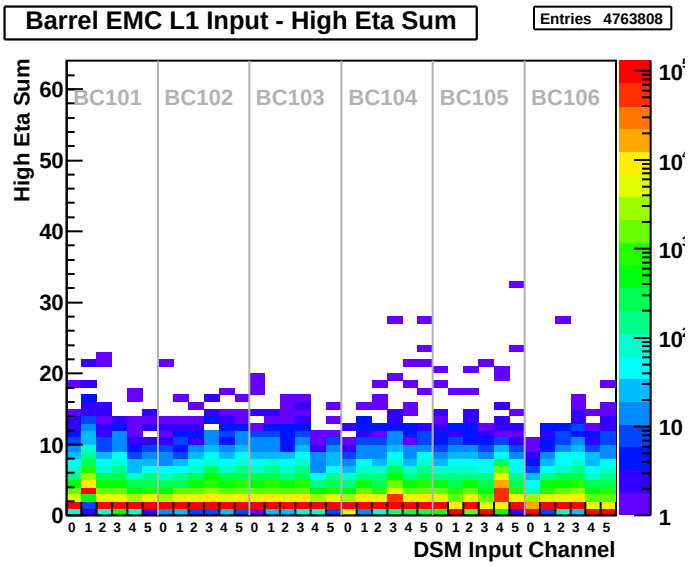
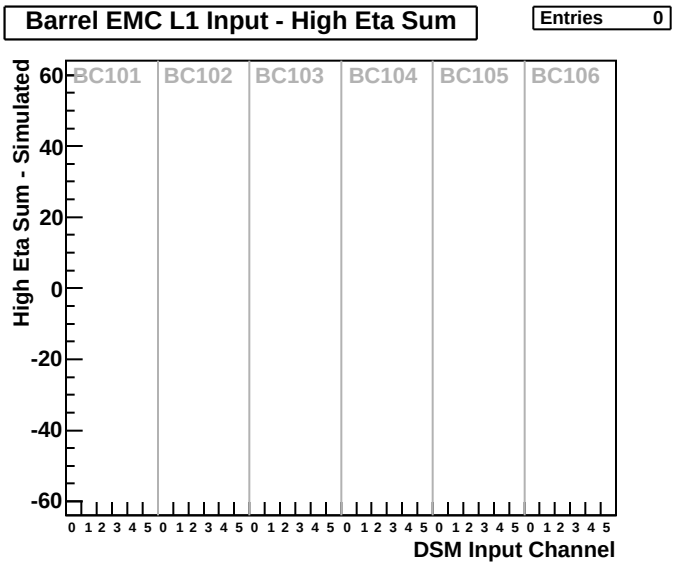
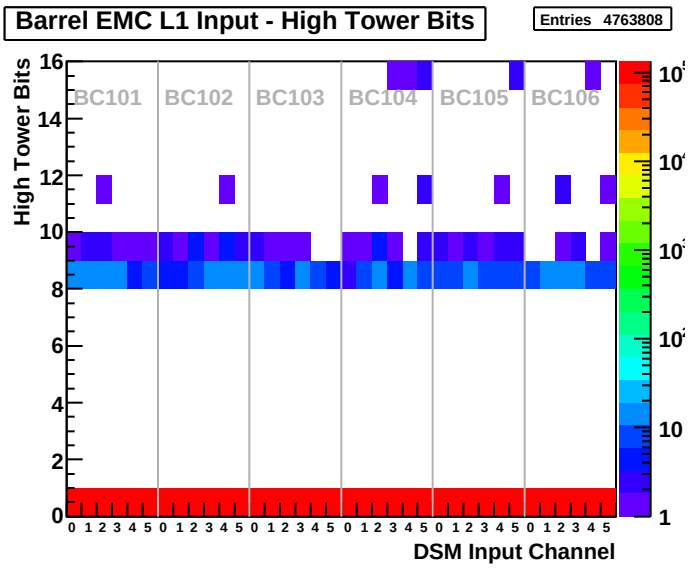
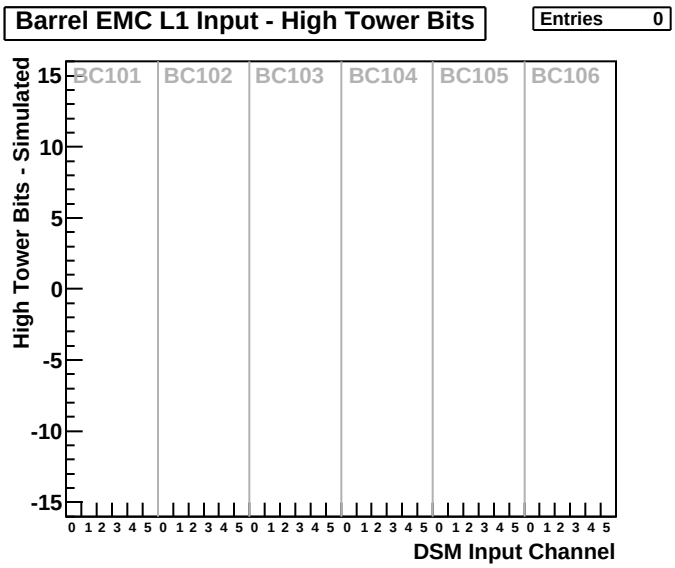

Barrel EMC L1 Input - Low Eta Sum
Entries 4763808
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

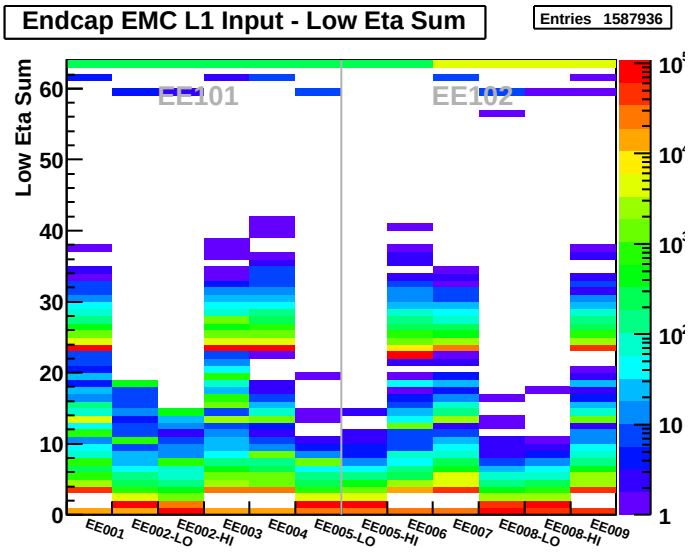

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

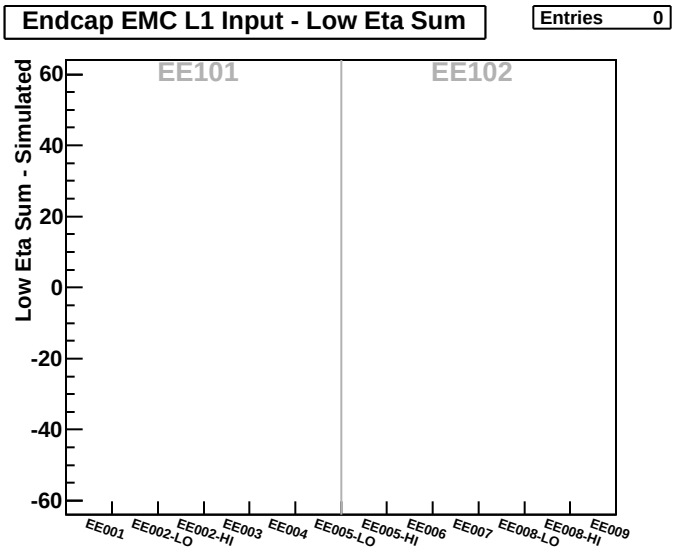

Barrel EMC L1 Input - High Eta Sum
Entries 4763808
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

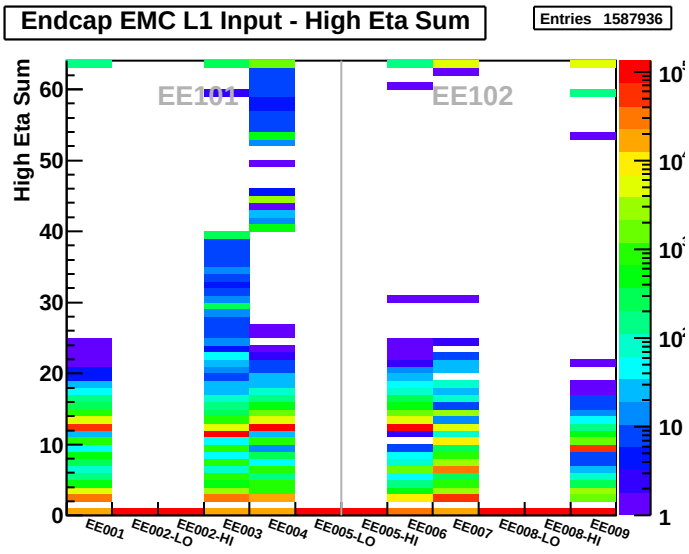

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

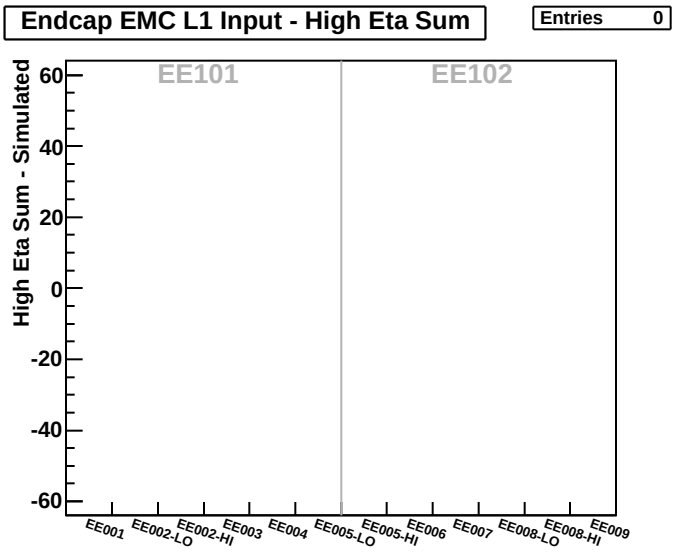

Barrel EMC L1 Input - High Tower Bits
Entries 4763808
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

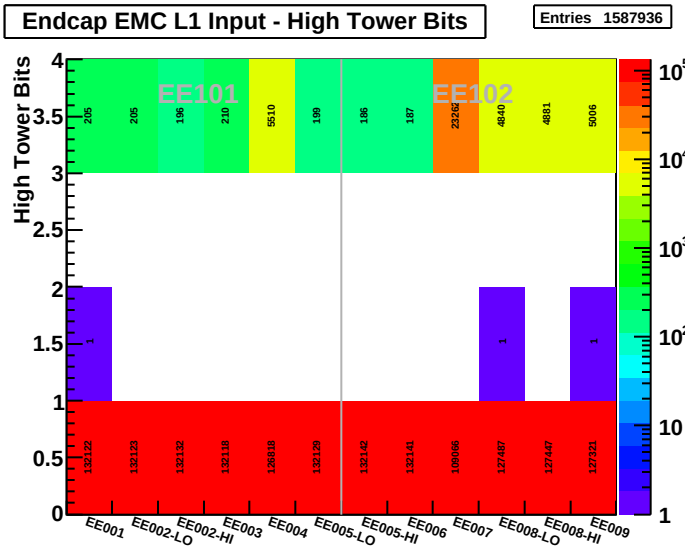

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

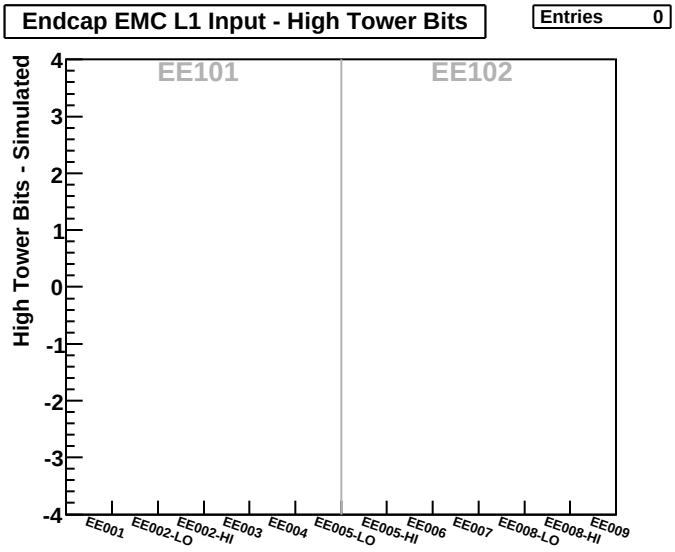

Endcap EMC L1 Input - Low Eta Sum
Entries 1587936
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

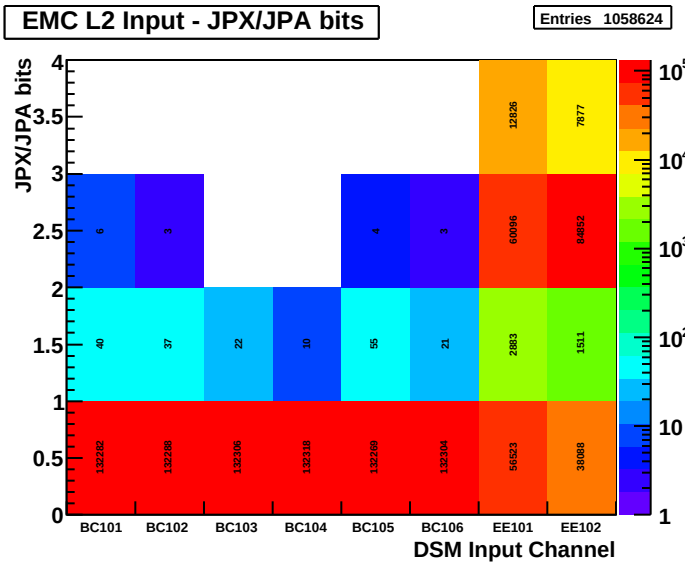

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

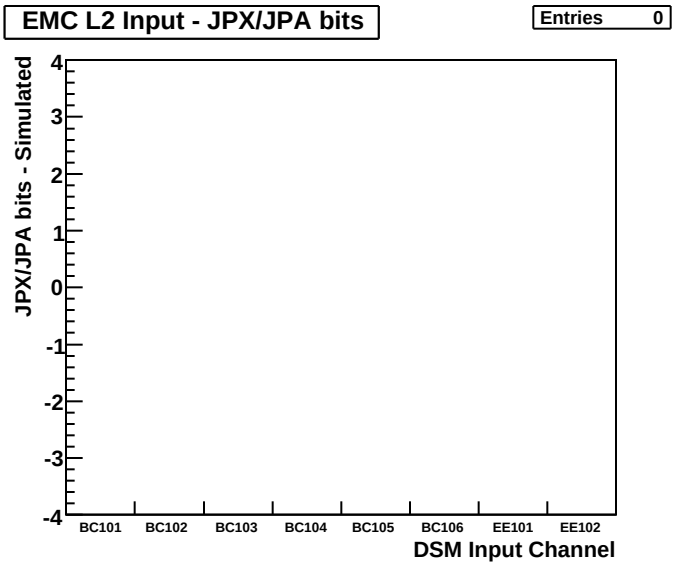

Endcap EMC L1 Input - High Eta Sum
Entries 1587936
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

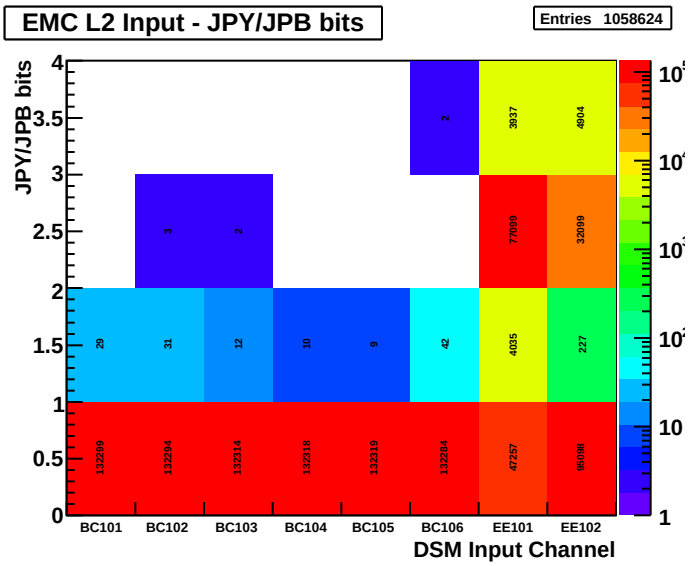

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

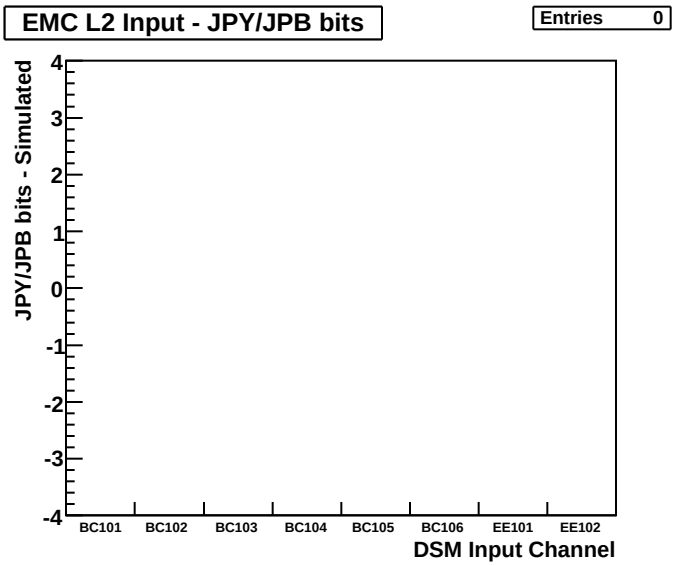

Endcap EMC L1 Input - High Tower Bits
Entries 1587936
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

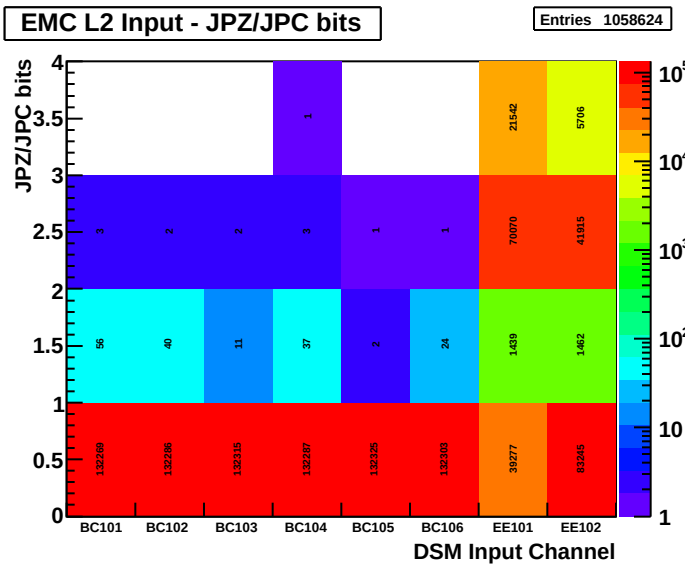

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

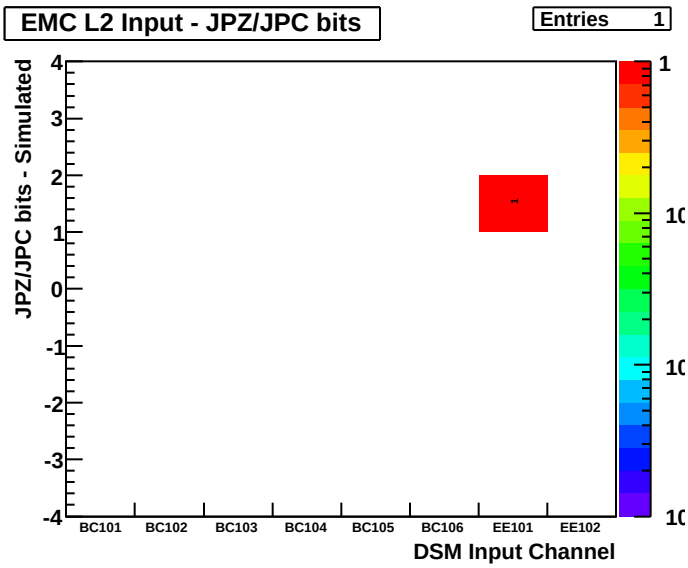

EMC L2 Input - JPX/JPA bits
Entries 1058624
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

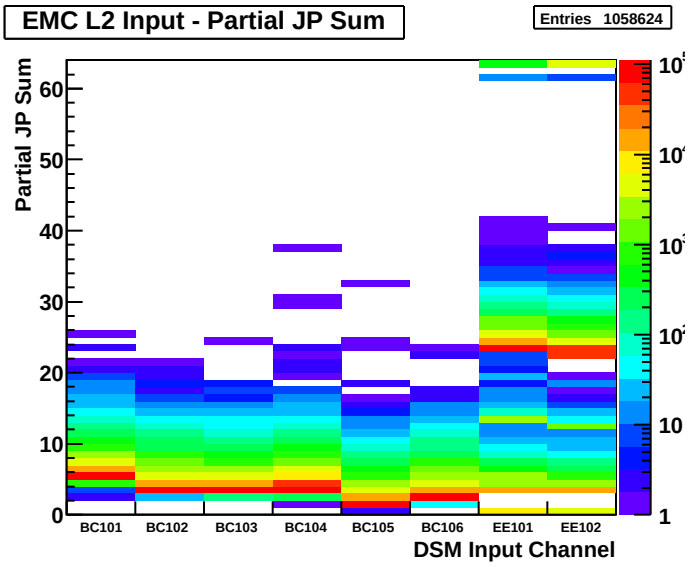

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

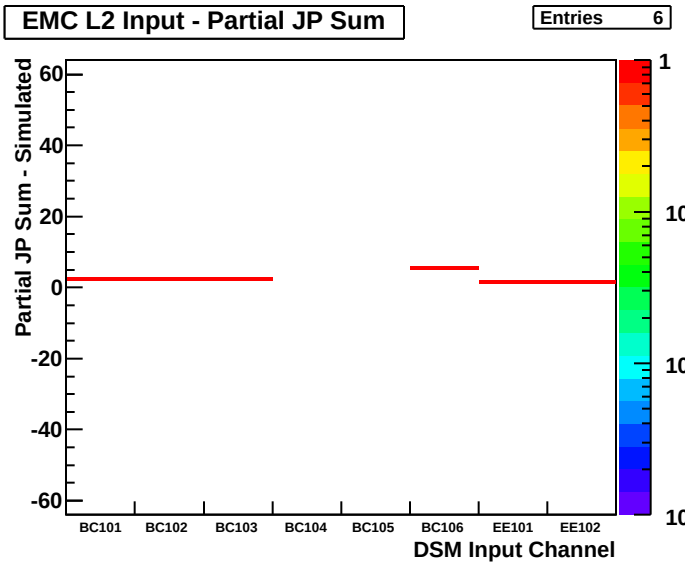

EMC L2 Input - JPY/JPB bits
Entries 1058624
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

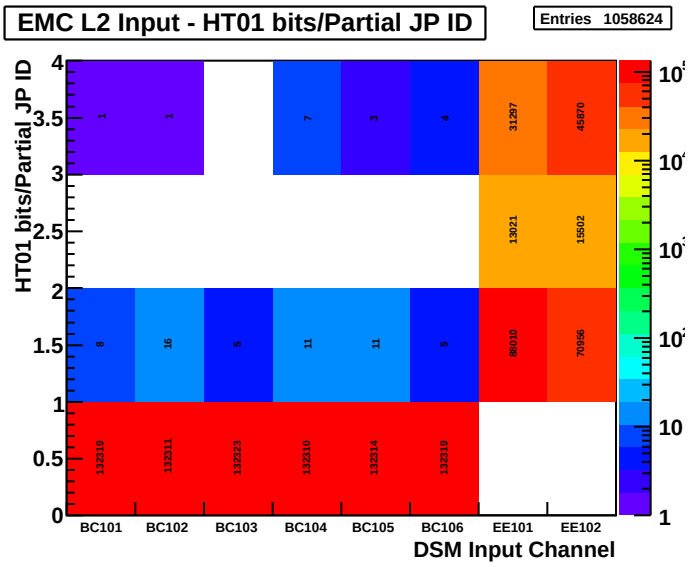

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

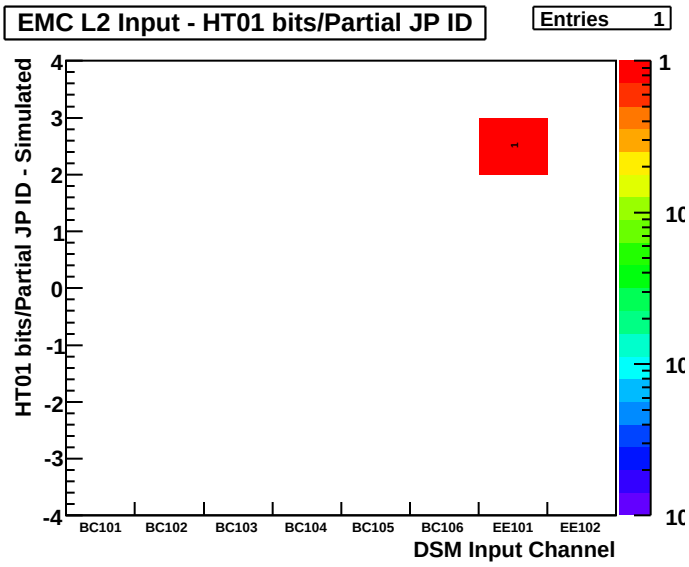

EMC L2 Input - JPZ/JPC bits
Entries 1058624
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

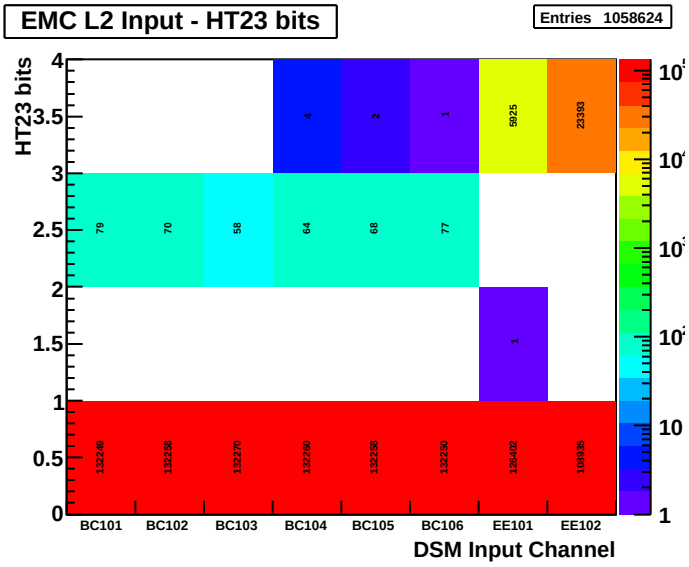

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

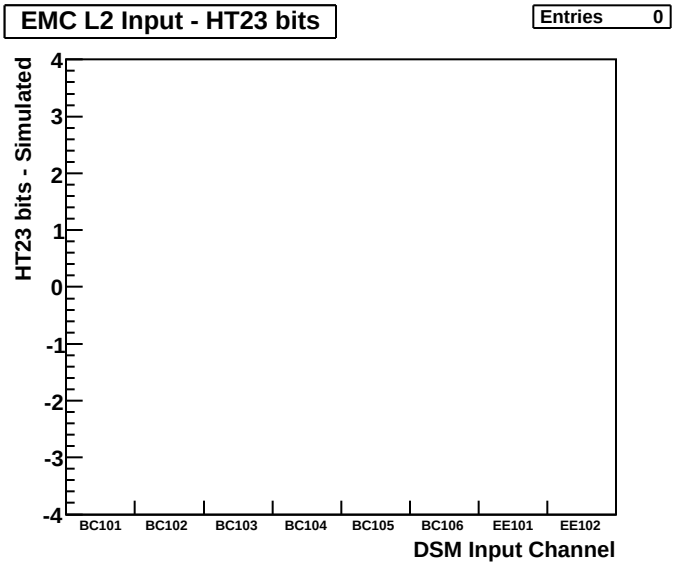

EMC L2 Input - Partial JP Sum
Entries 1058624
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

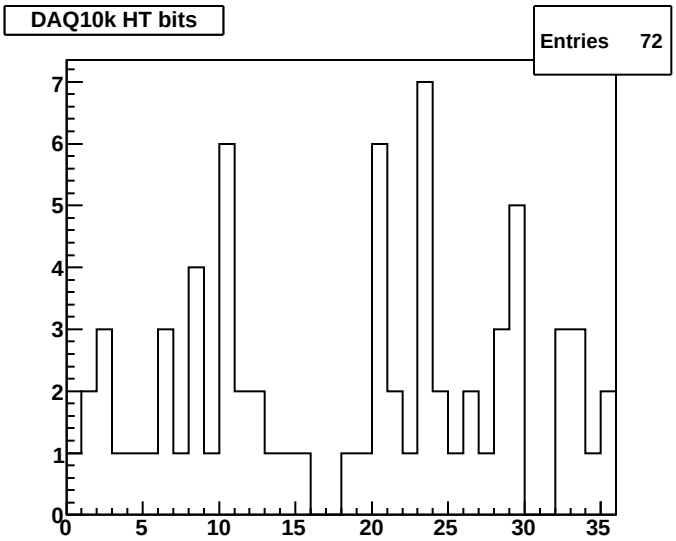

EMC L2 Input - Partial JP Sum
Entries 6
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

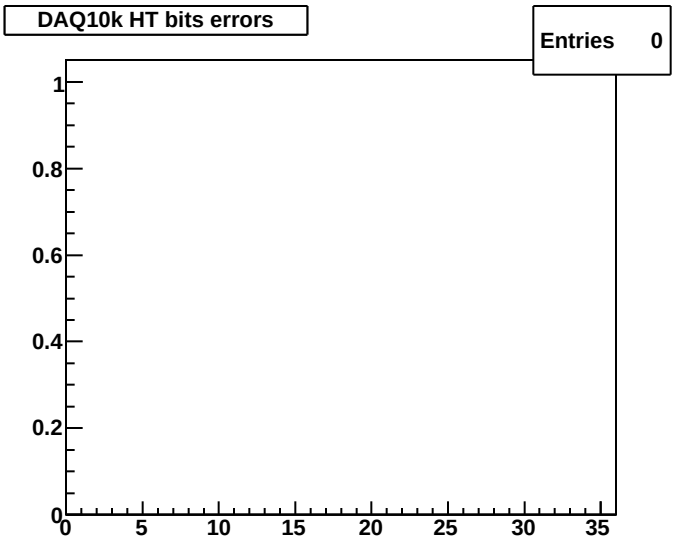

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1058624
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

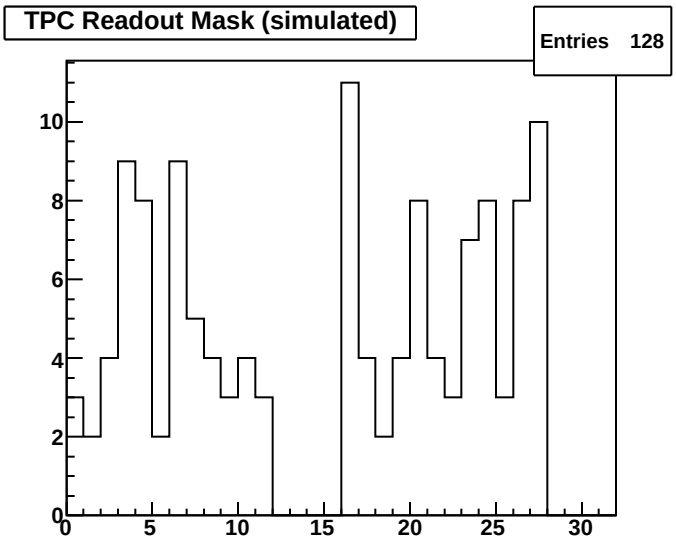

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

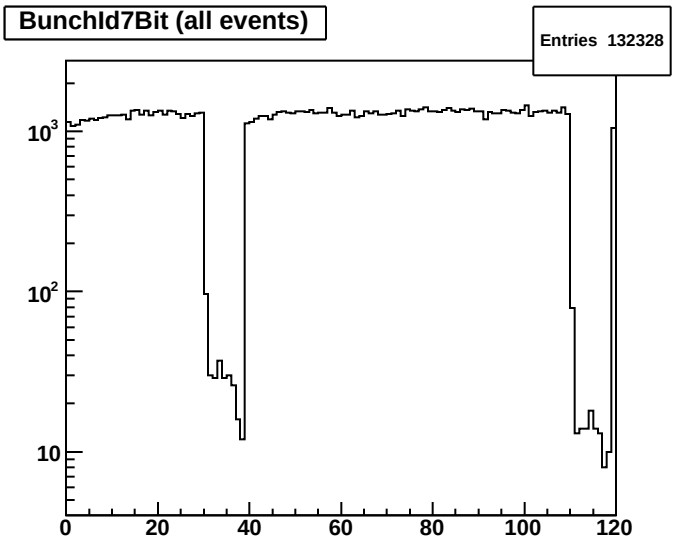

EMC L2 Input - HT23 bits
Entries 1058624
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

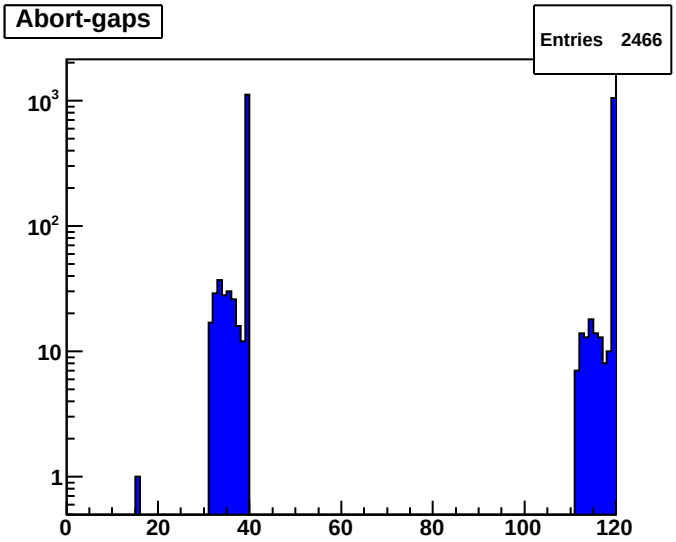

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

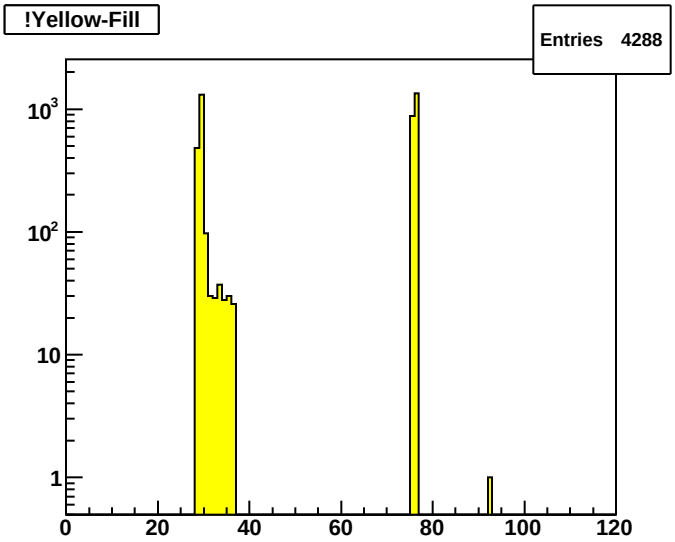

DAQ10k HT bits
Entries 72

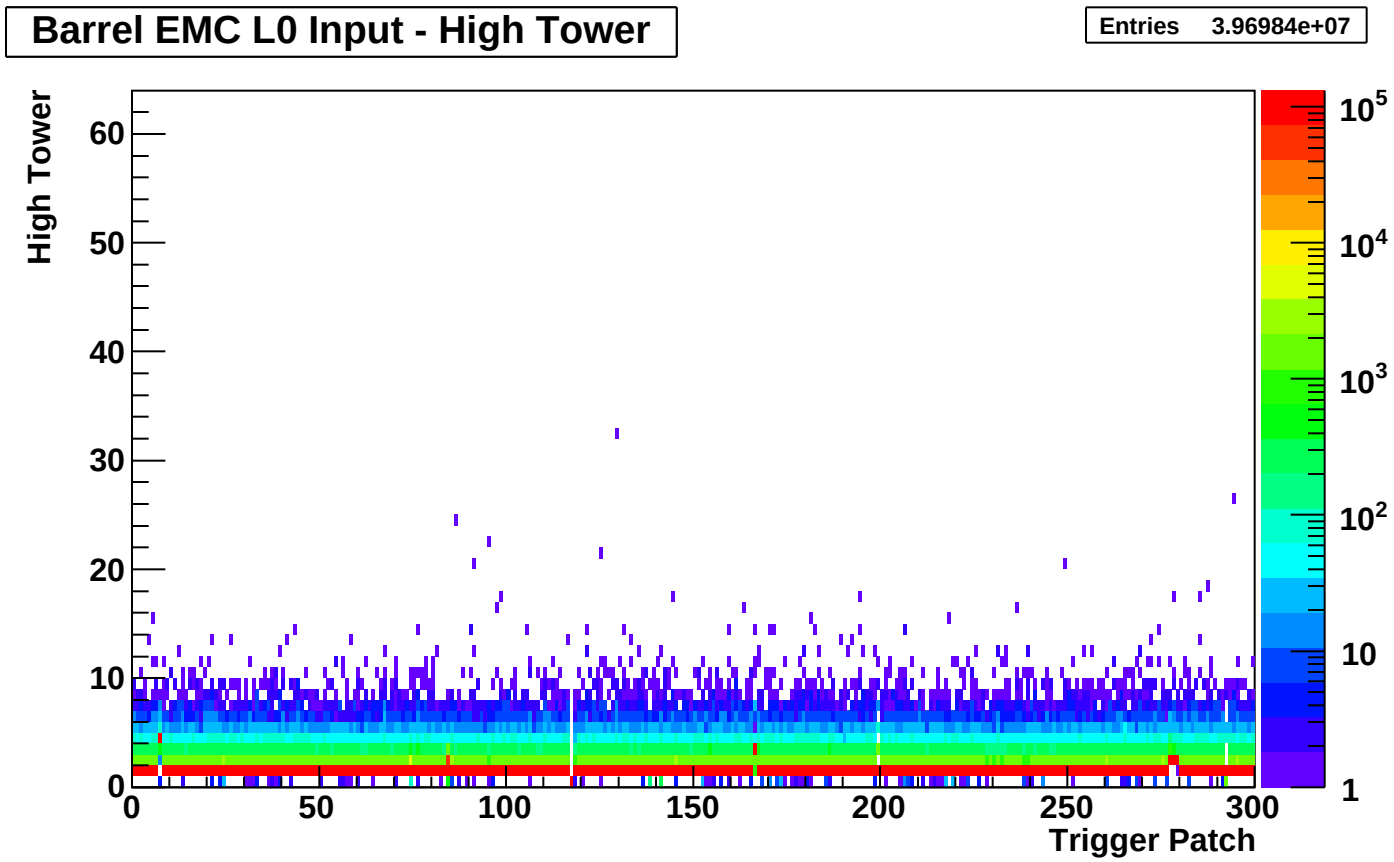

DAQ10k HT bits errors
Entries 0

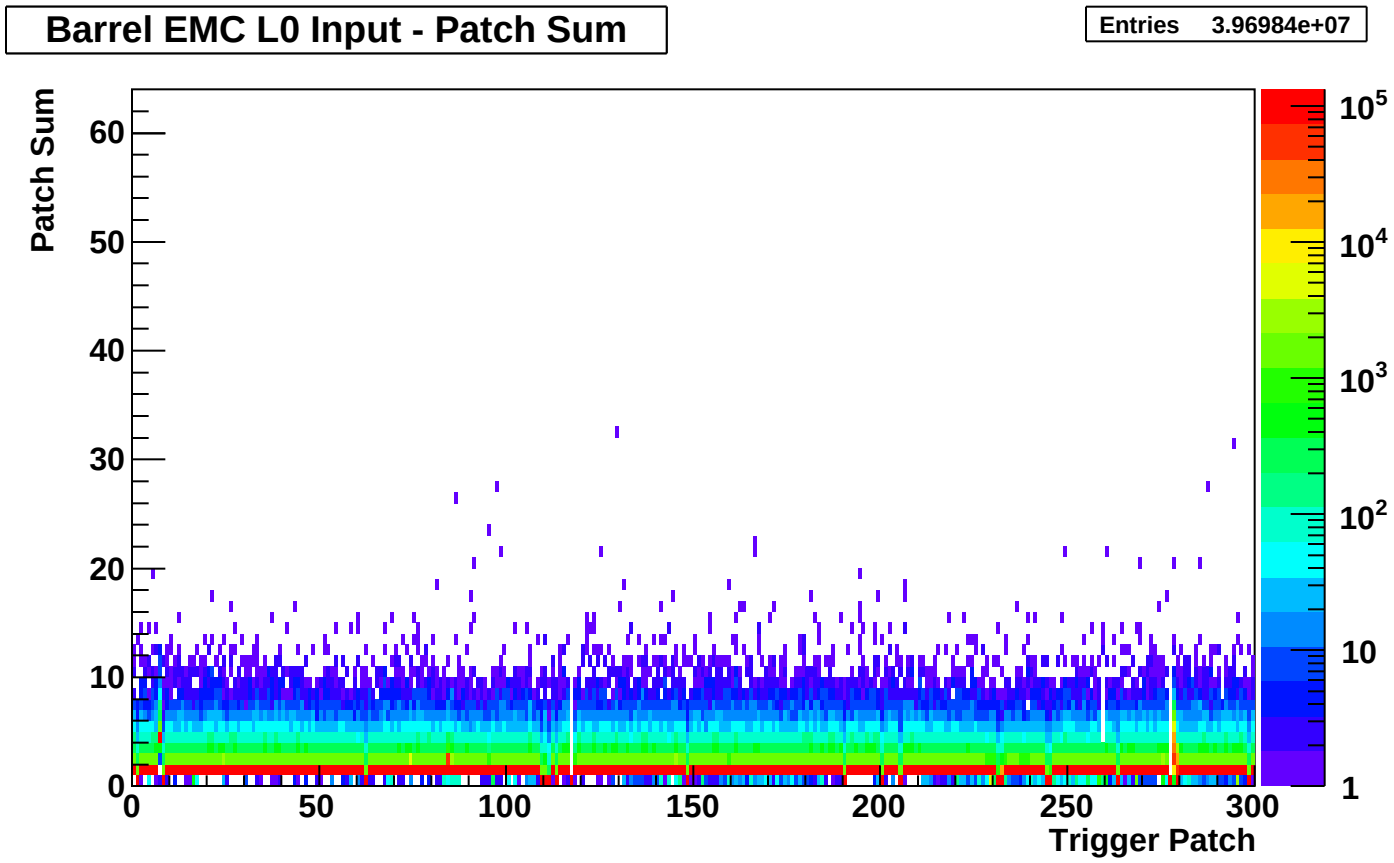

TPC Readout Mask (simulated)
Entries 128

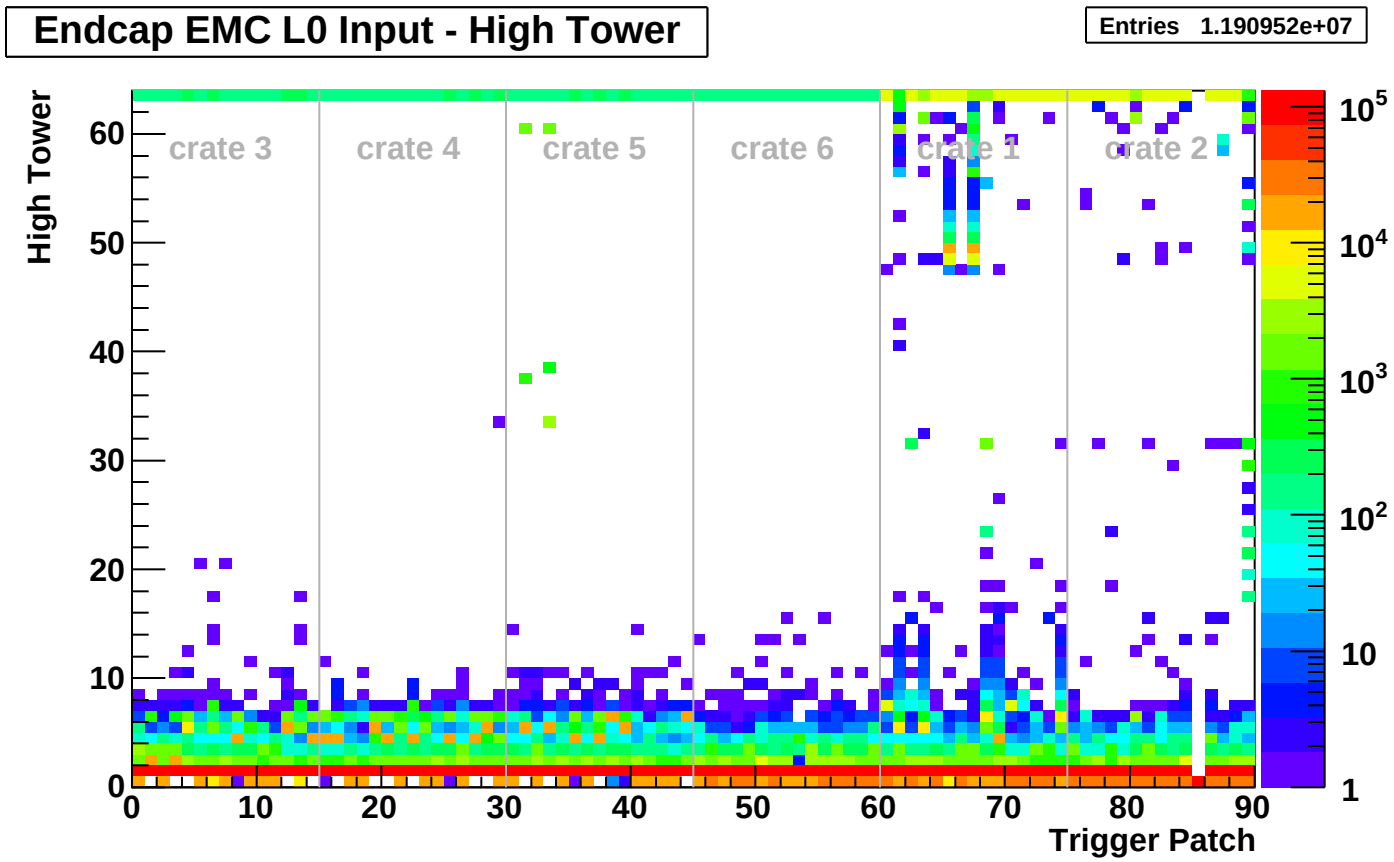

BunchId7Bit (all events)
Entries 132328

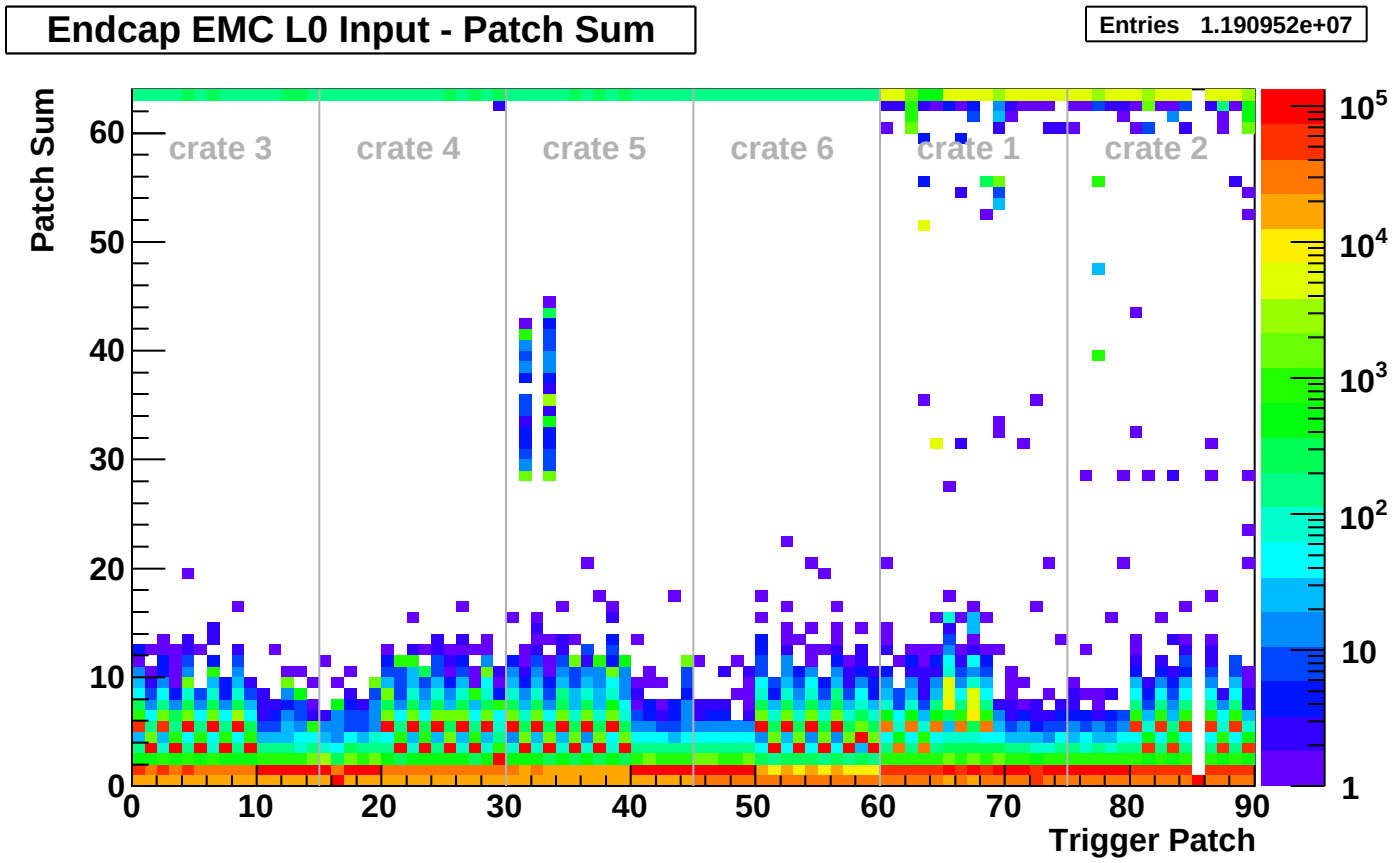

Abort-gaps
Entries 2466

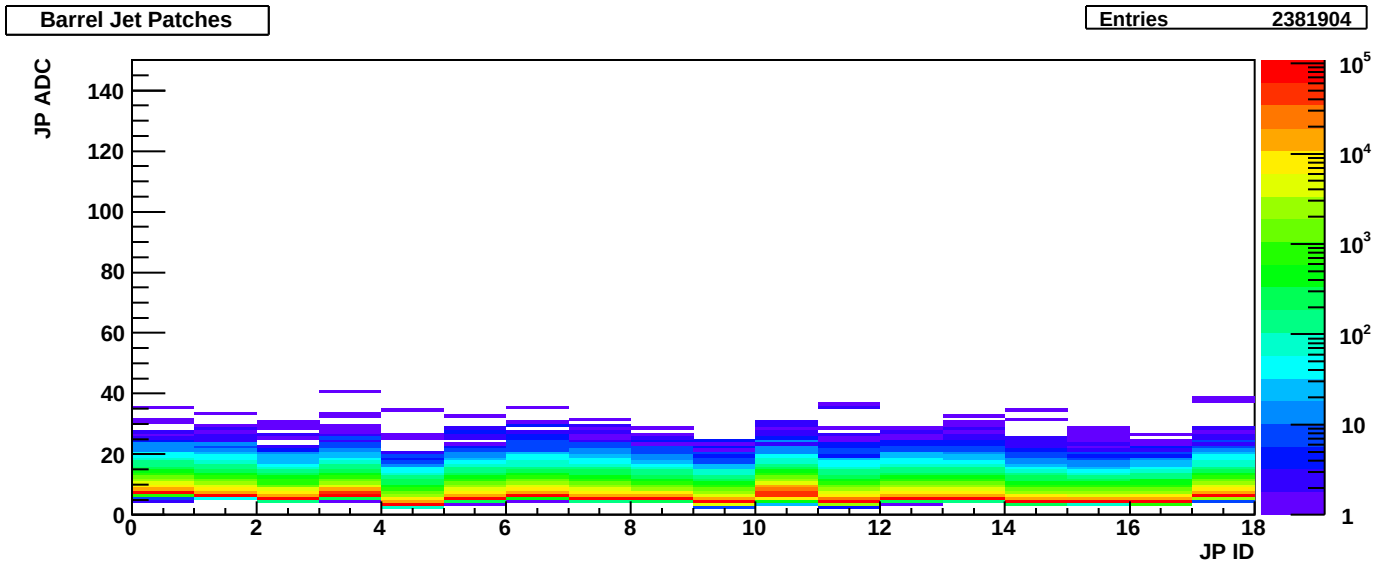

!Yellow-Fill
Entries 4288

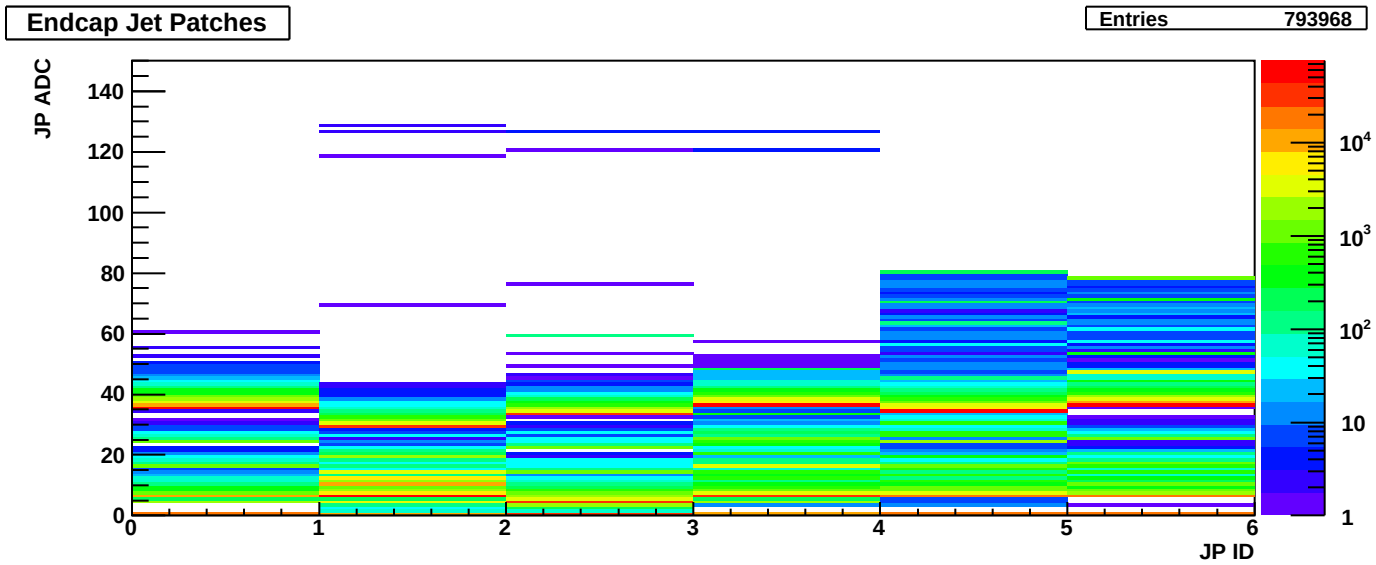

Barrel EMC L0 Input - High Tower
Entries 3.96984e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

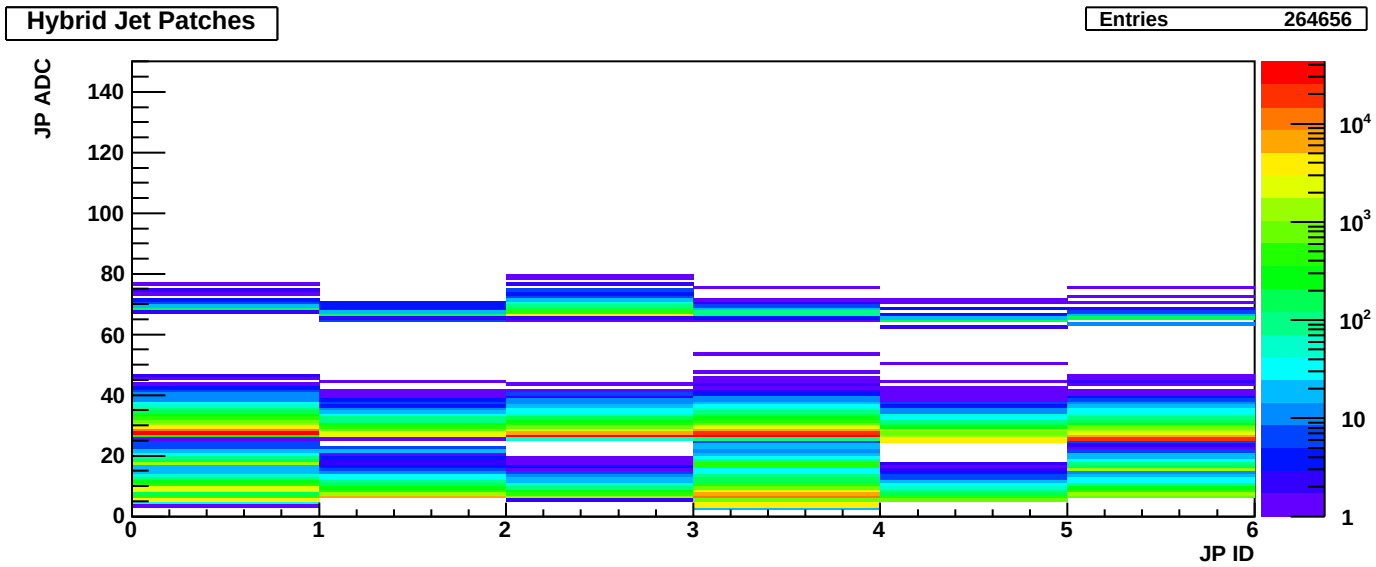

Barrel EMC L0 Input - Patch Sum
Entries 3.96984e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

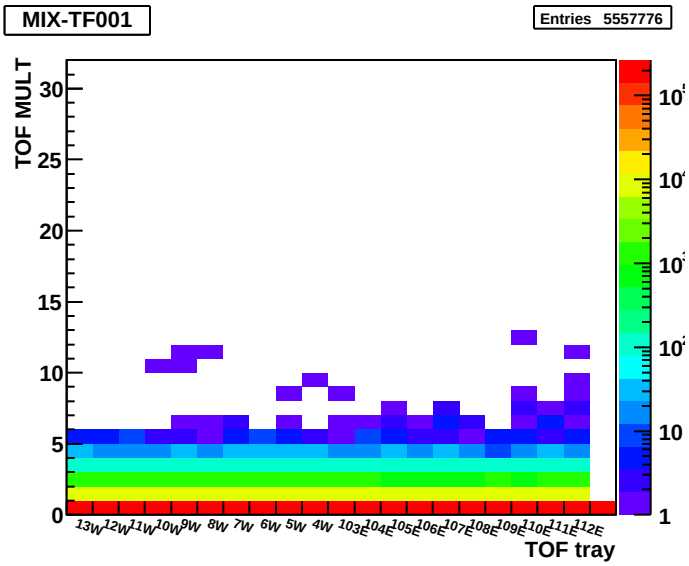

Endcap EMC L0 Input - High Tower
Entries 1.190952e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

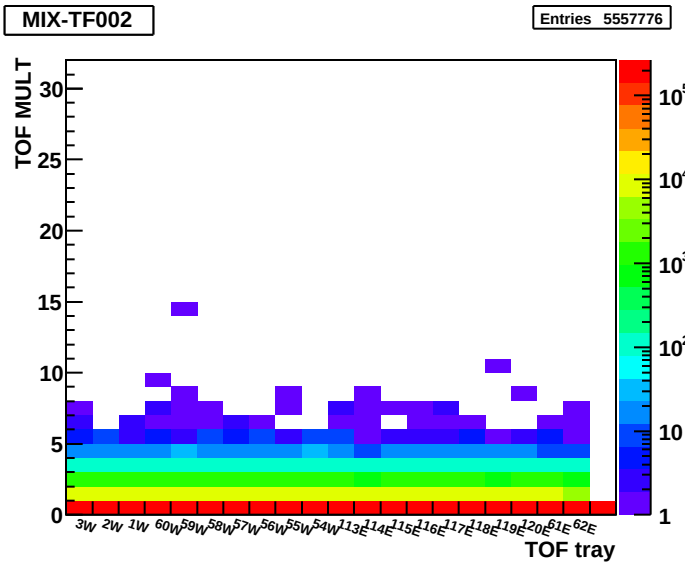

Endcap EMC L0 Input - Patch Sum
Entries 1.190952e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

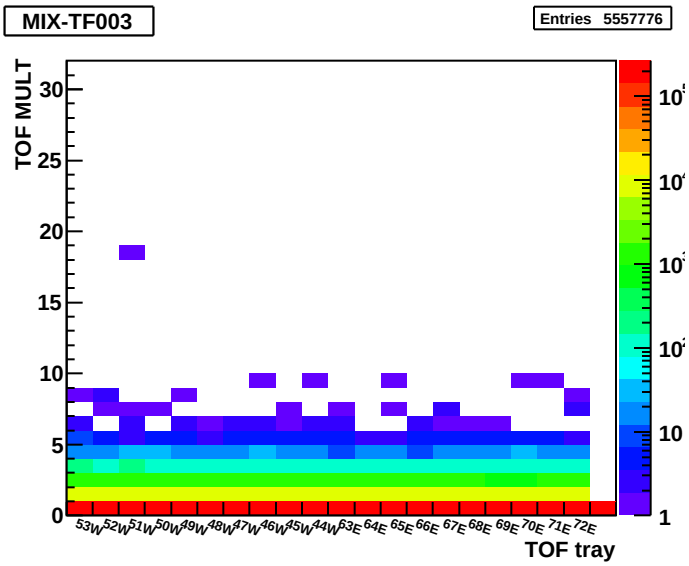

Barrel Jet Patches
Entries 2381904
JP ADC
JP ID

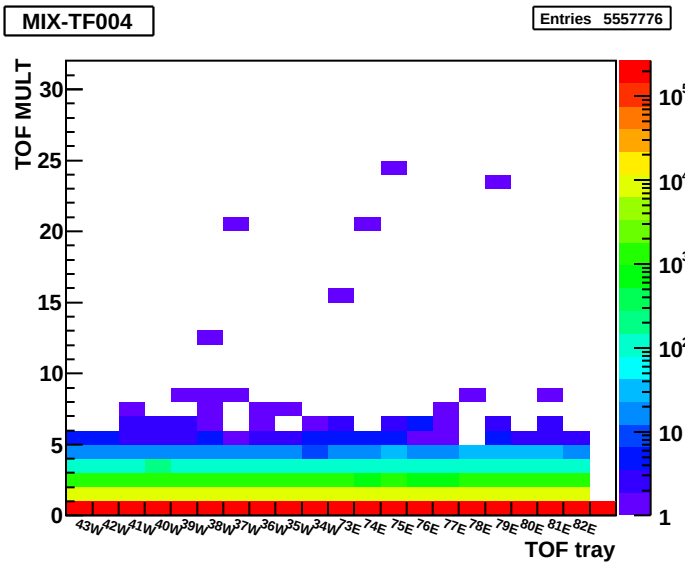

Endcap Jet Patches
Entries 793968
JP ADC
JP ID

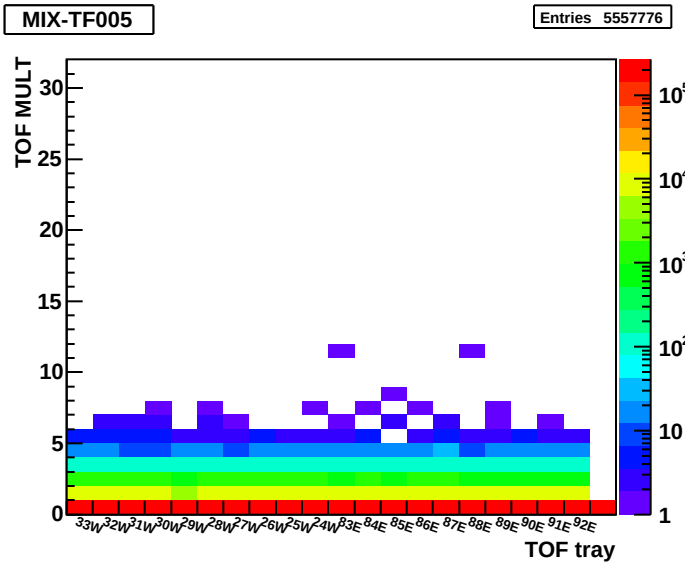

Hybrid Jet Patches
Entries 264656
JP ADC
JP ID

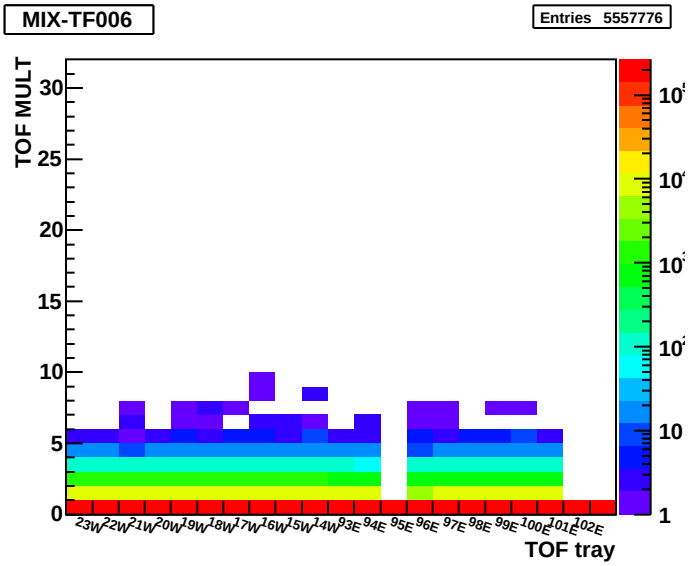

MIX-TF001
Entries 5557776
TOF MULT
TOF tray

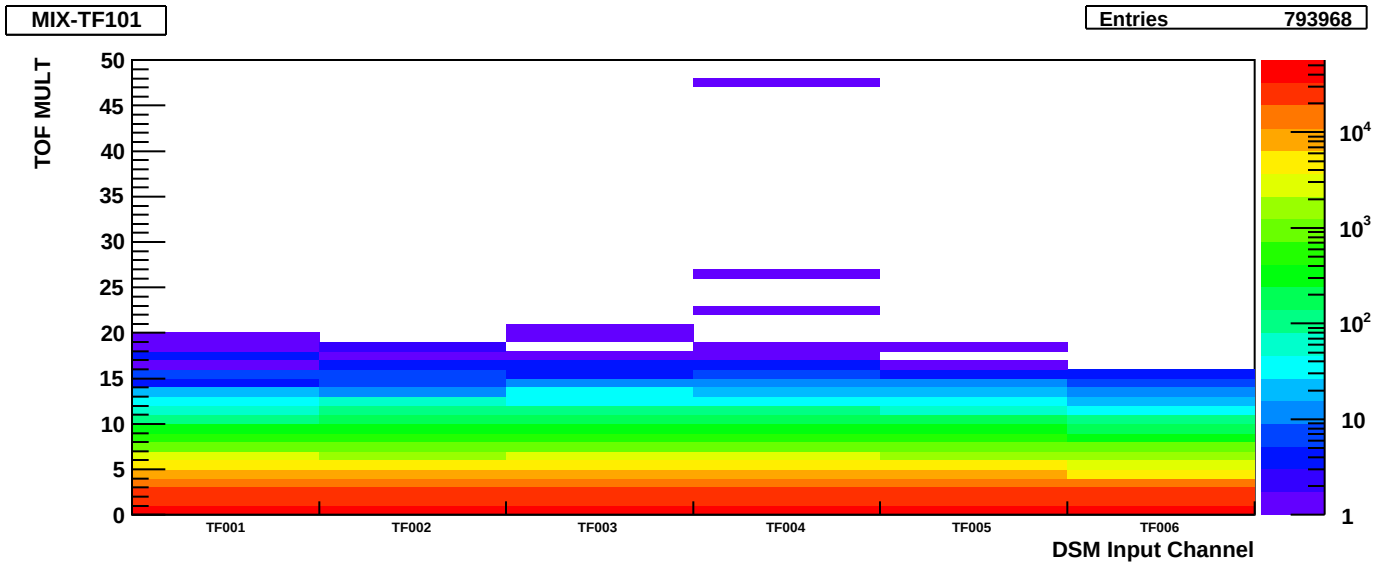

MIX-TF002
Entries 5557776
TOF MULT
TOF tray

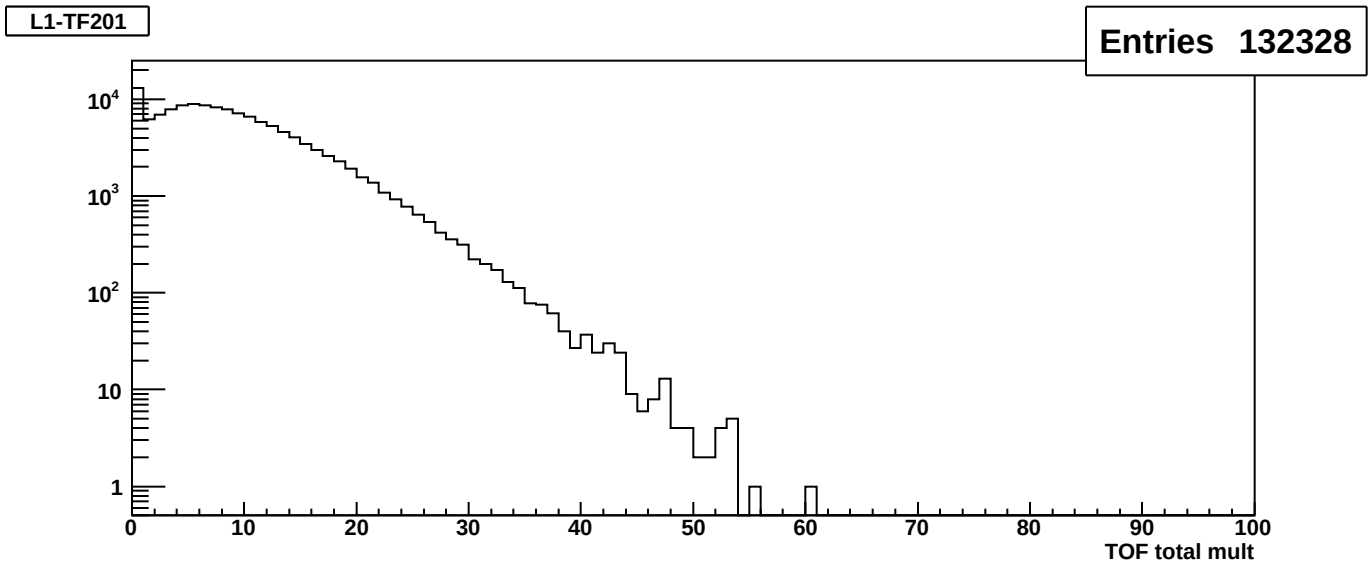

MIX-TF003
Entries 5557776
TOF MULT
TOF tray

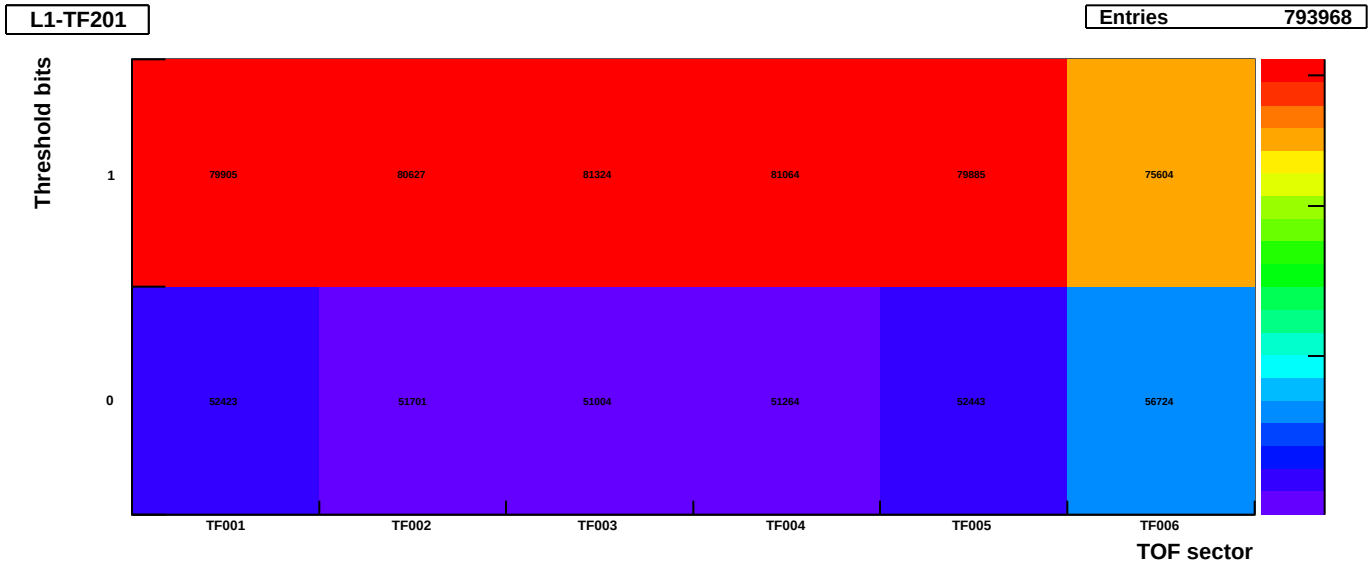

MIX-TF004
Entries 5557776
TOF MULT
TOF tray

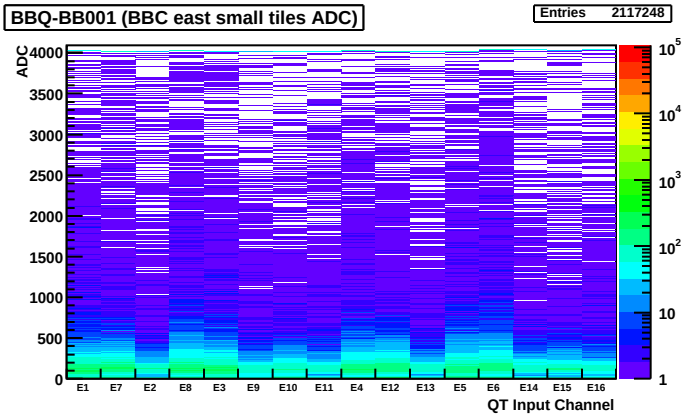

MIX-TF005
Entries 5557776
TOF MULT
TOF tray

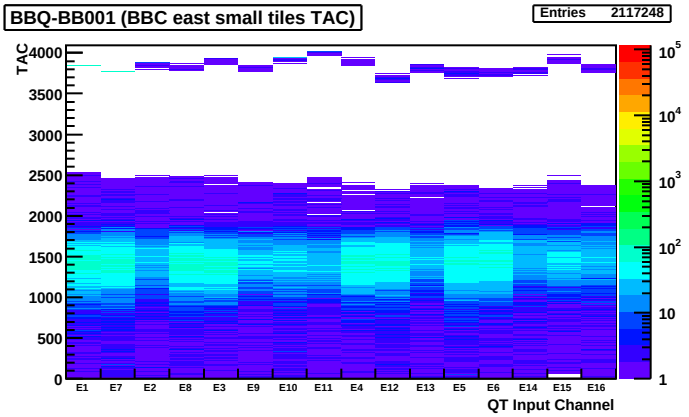

MIX-TF006
Entries 5557776
TOF MULT
TOF tray

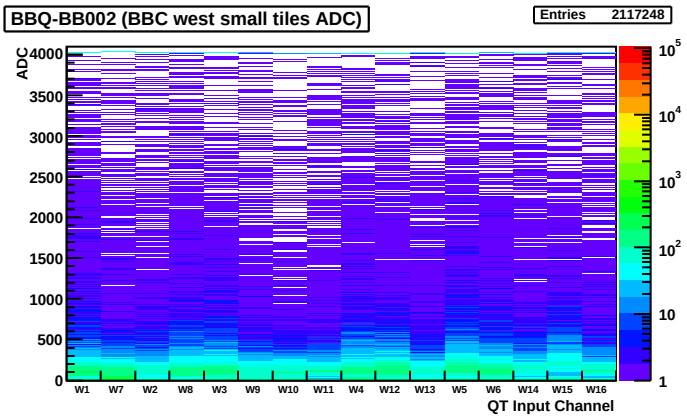

MIX-TF101
Entries 793968
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

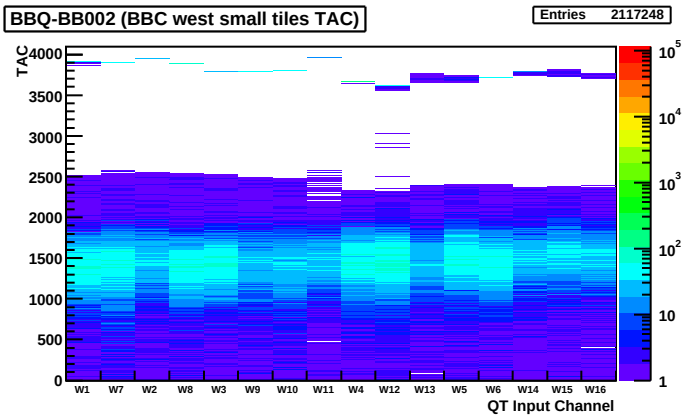

L1-TF201
Entries 132328
TOF total mult

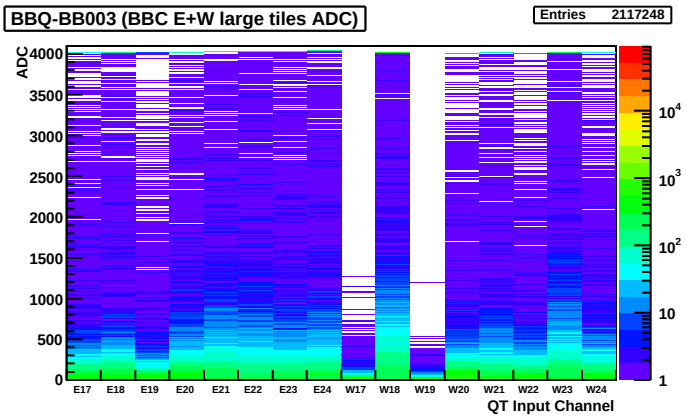

L1-TF201
Entries 793968
Threshold bits
79905
80627
81324
81064
79885
75604
52423
51701
51004
51264
52443
56724
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

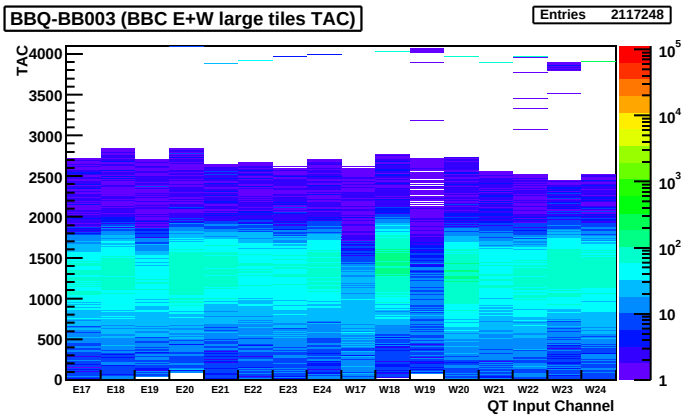

BBQ-BB001 (BBC east small tiles ADC)
Entries 2117248
ADC
QT Input Channel

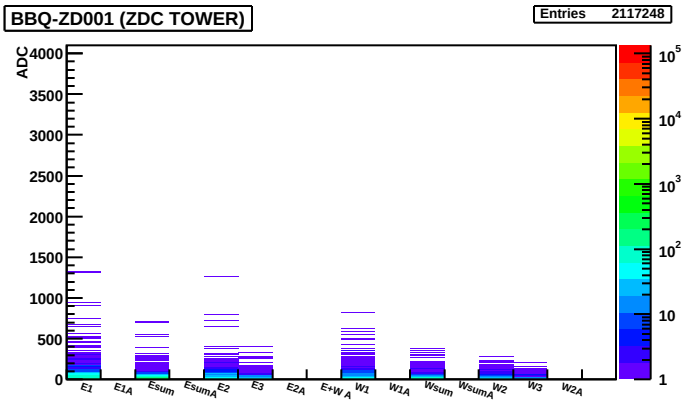

BBQ-BB001 (BBC east small tiles TAC)
Entries 2117248
TAC
QT Input Channel

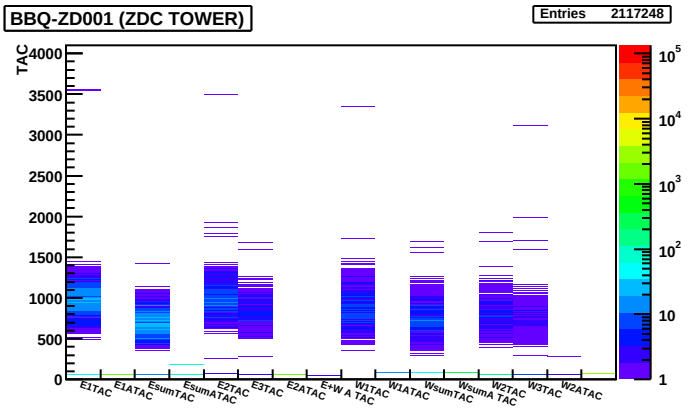

BBQ-BB002 (BBC west small tiles ADC)
Entries 2117248
ADC
QT Input Channel

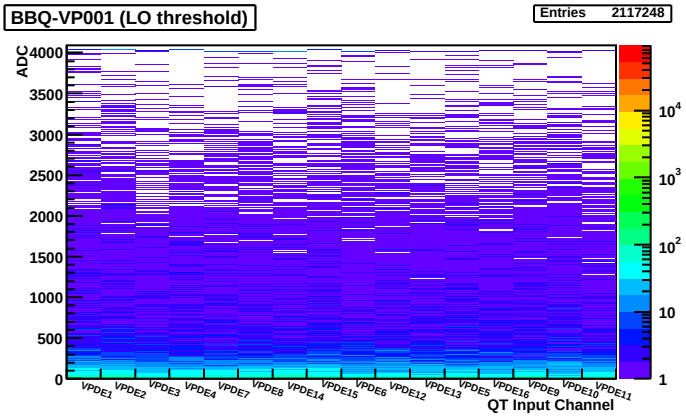

BBQ-BB002 (BBC west small tiles TAC)
Entries 2117248
TAC
QT Input Channel

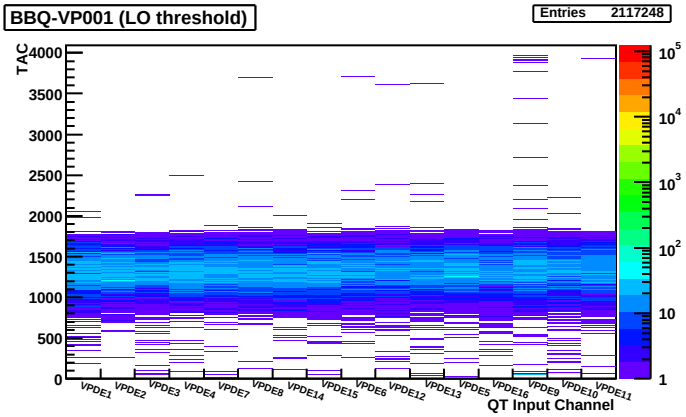

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2117248
ADC
QT Input Channel

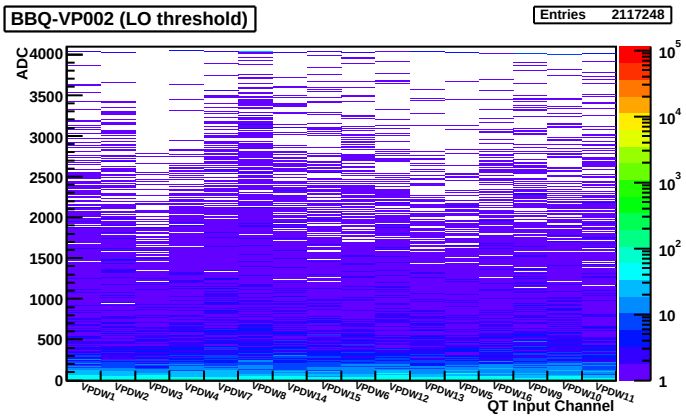

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2117248
TAC
QT Input Channel

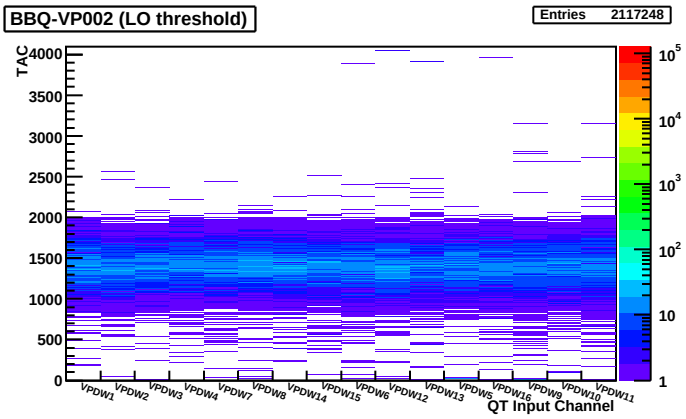

BBQ-ZD001 (ZDC TOWER)
Entries 2117248
ADC

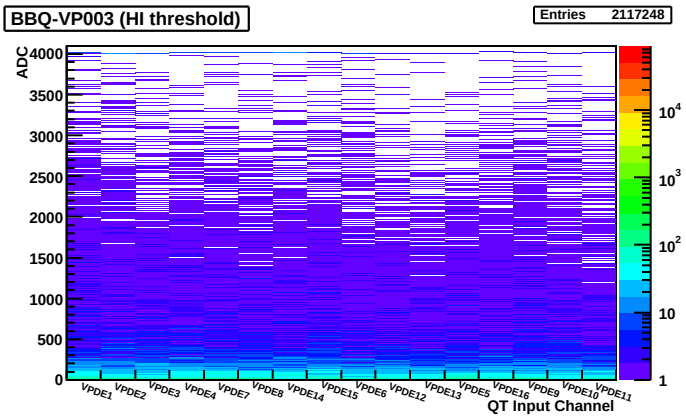

BBQ-ZD001 (ZDC TOWER)
Entries 2117248
TAC

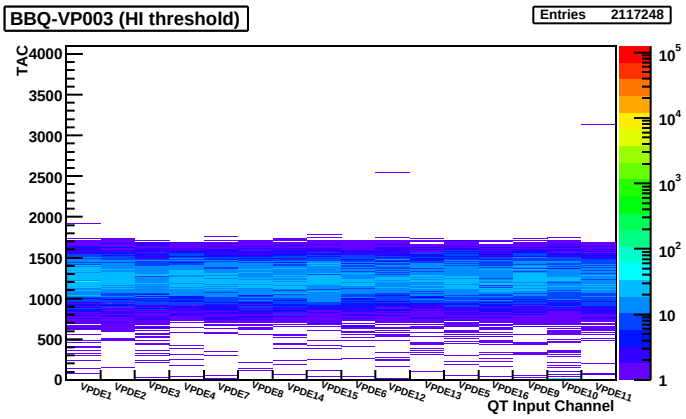

BBQ-VP001 (LO threshold)
Entries 2117248
ADC
QT Input Channel

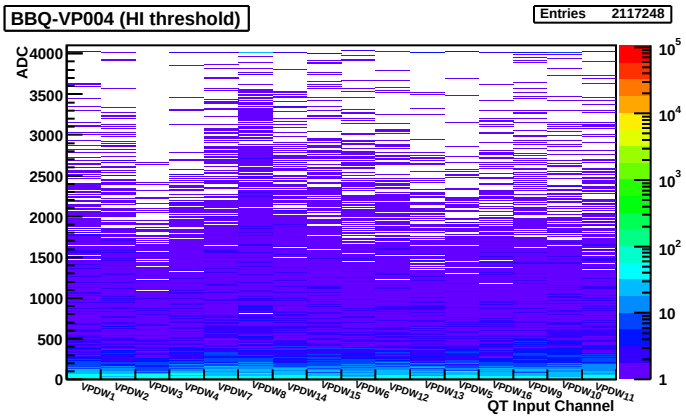

BBQ-VP001 (LO threshold)
Entries 2117248
TAC
QT Input Channel

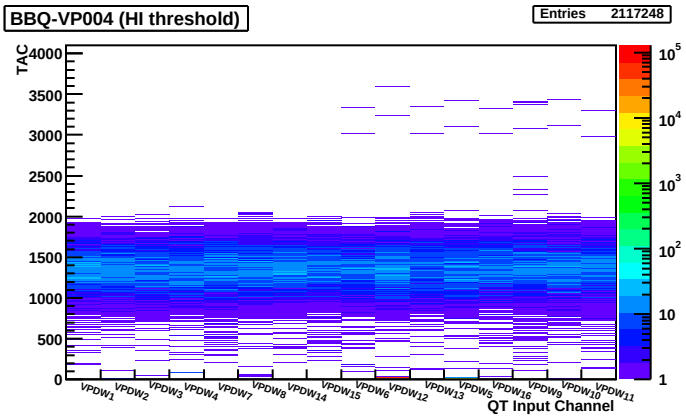

BBQ-VP002 (LO threshold)
Entries 2117248
ADC
QT Input Channel

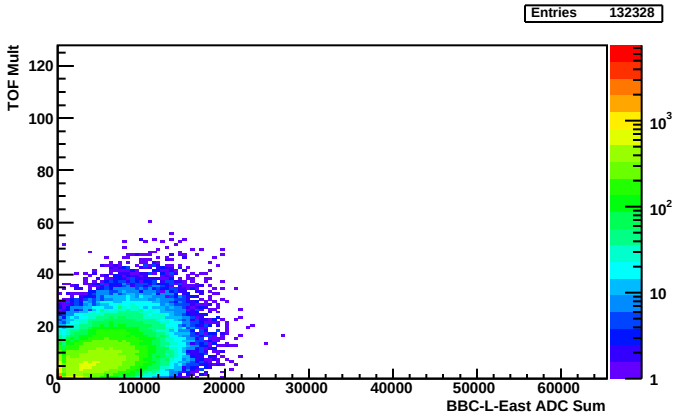

BBQ-VP002 (LO threshold)
Entries 2117248
TAC
QT Input Channel

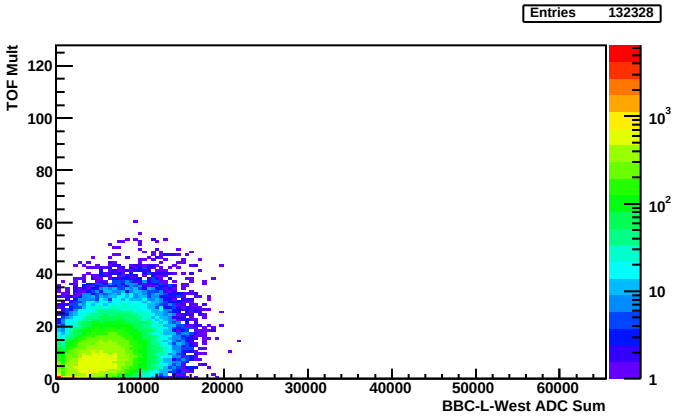

BBQ-VP003 (HI threshold)
Entries 2117248
ADC
QT Input Channel

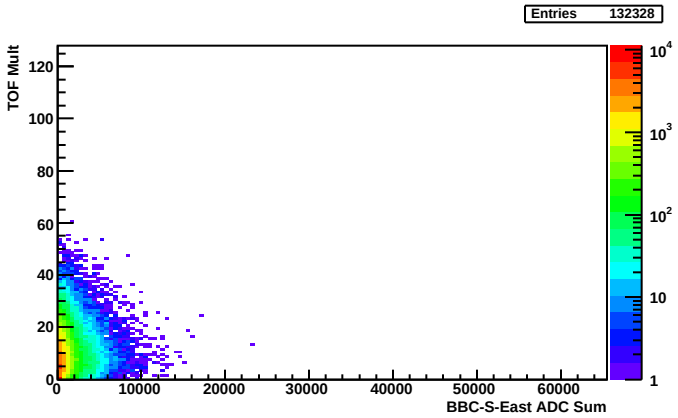

BBQ-VP003 (HI threshold)
Entries 2117248
TAC
QT Input Channel

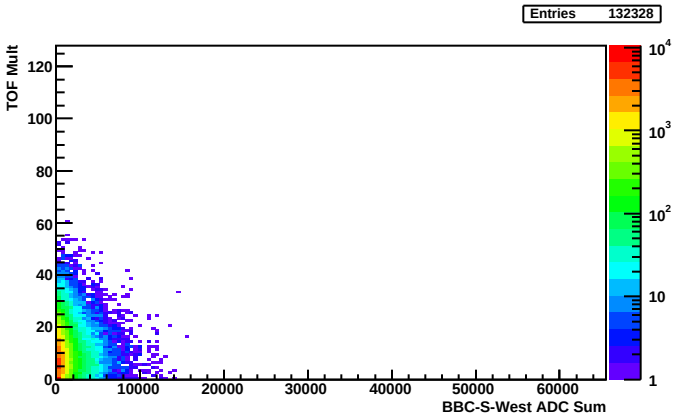

BBQ-VP004 (HI threshold)
Entries 2117248
ADC
QT Input Channel

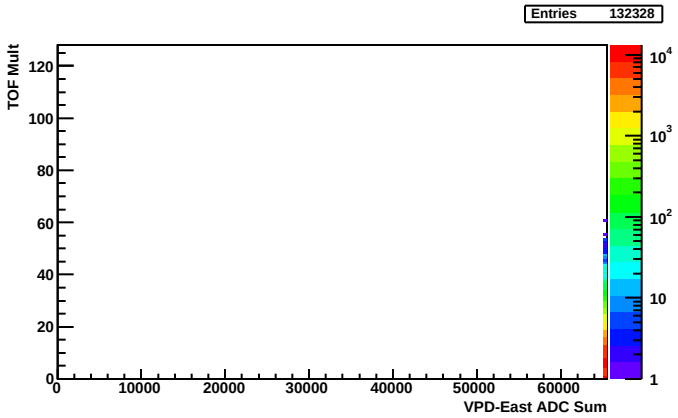

BBQ-VP004 (HI threshold)
Entries 2117248
TAC
QT Input Channel

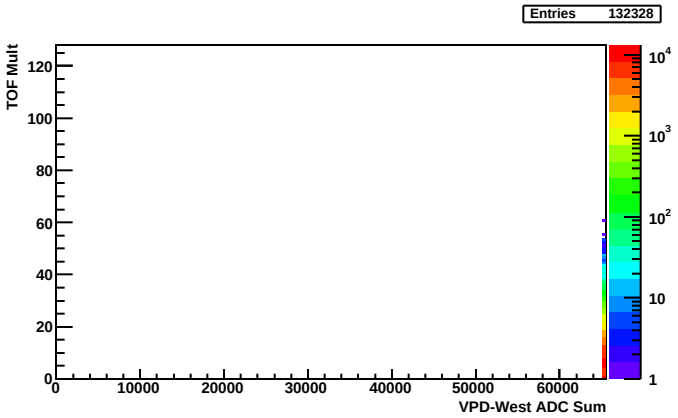

Entries 132328
TOF Mult
BBC-L-East ADC Sum

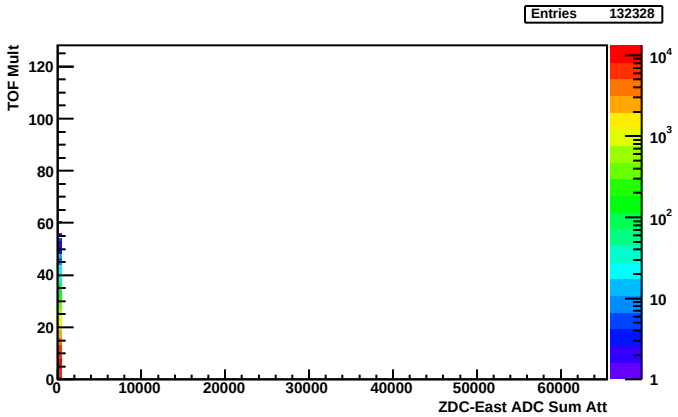

Entries 132328
TOF Mult
BBC-L-West ADC Sum

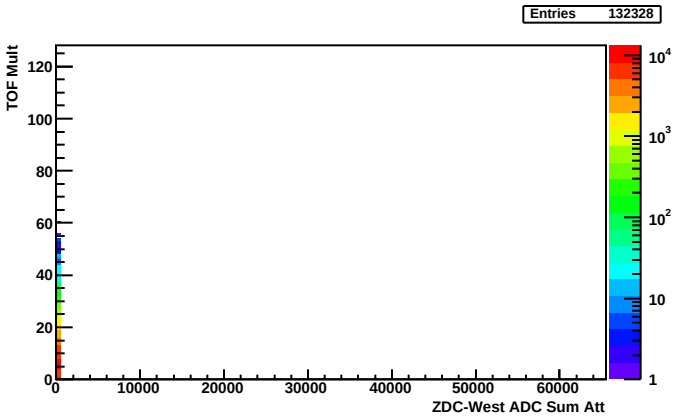

Entries 132328
TOF Mult
BBC-S-East ADC Sum

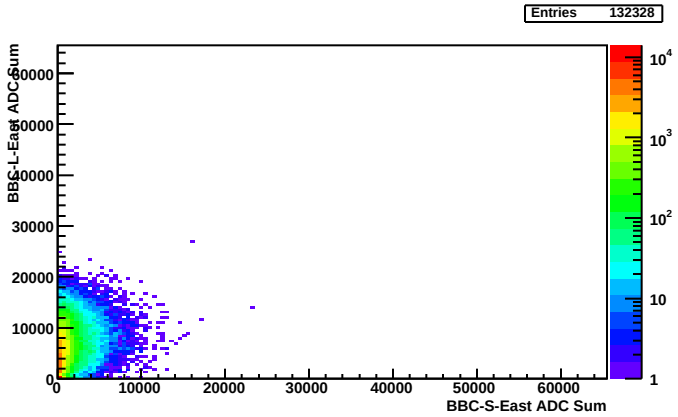

Entries 132328
TOF Mult
BBC-S-West ADC Sum

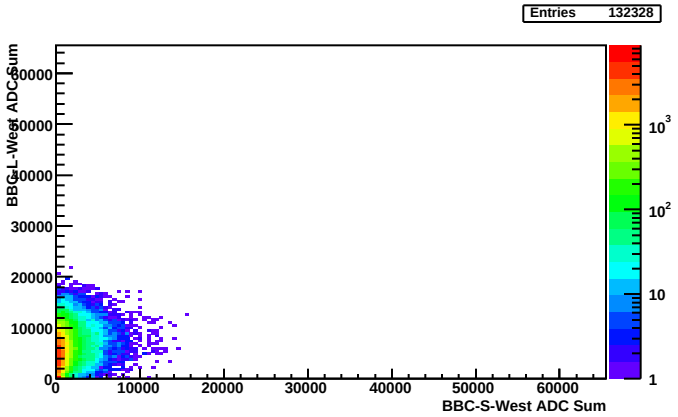

Entries 132328
TOF Mult
VPD-East ADC Sum

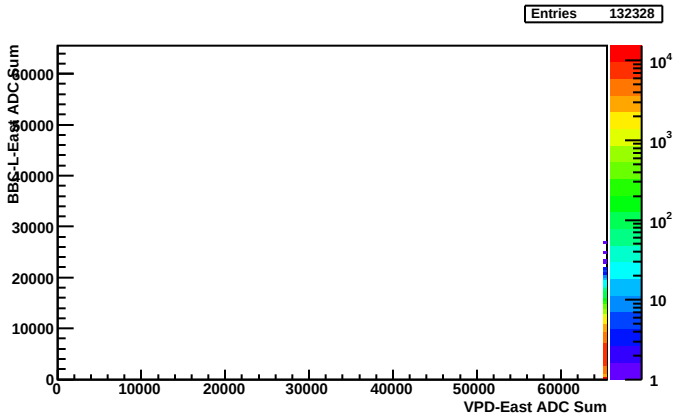

Entries 132328
TOF Mult
VPD-West ADC Sum

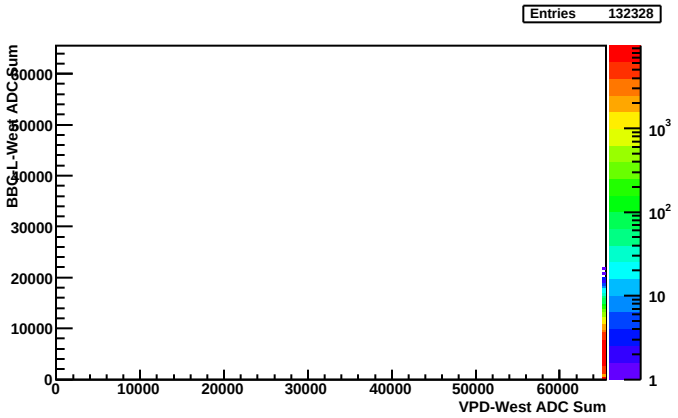

Entries 132328
TOF Mult
ZDC-East ADC Sum Att

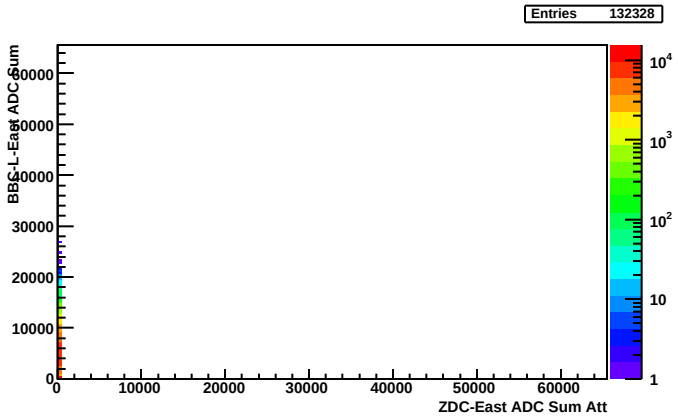

Entries 132328
TOF Mult
ZDC-West ADC Sum Att

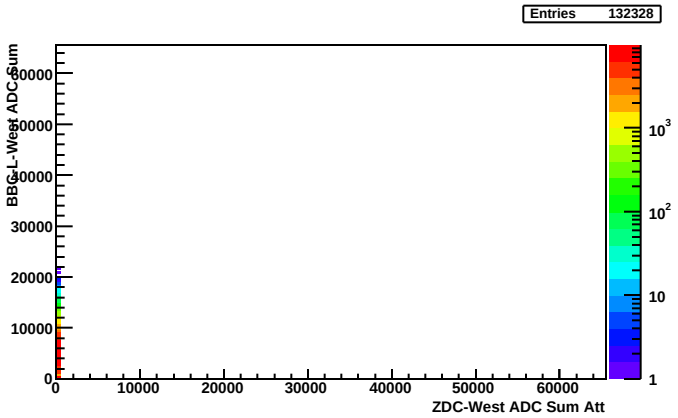

Entries 132328
BBC-L-East ADC Sum
BBC-S-East ADC Sum

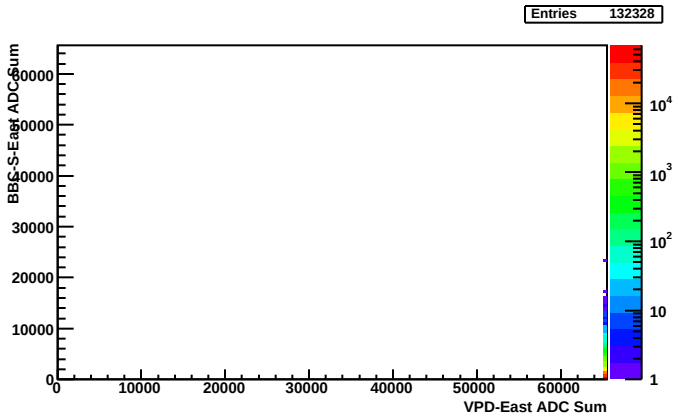

Entries 132328
BBC-L-West ADC Sum
BBC-S-West ADC Sum

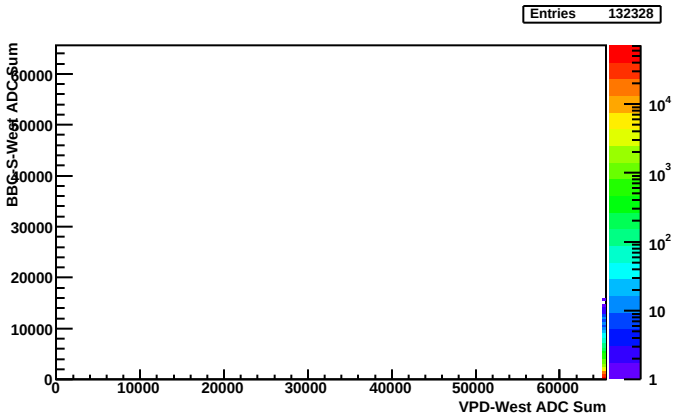

Entries 132328
BBC-L-East ADC Sum
VPD-East ADC Sum

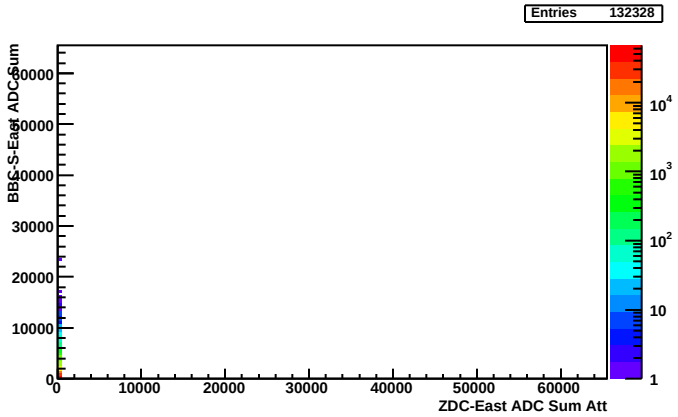

Entries 132328
BBC-L-West ADC Sum
VPD-West ADC Sum

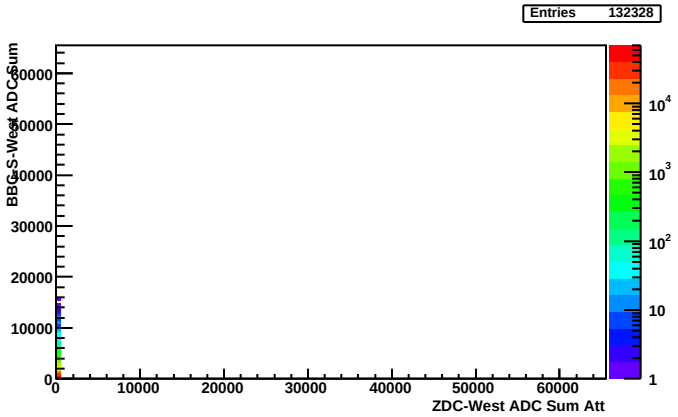

Entries 132328
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

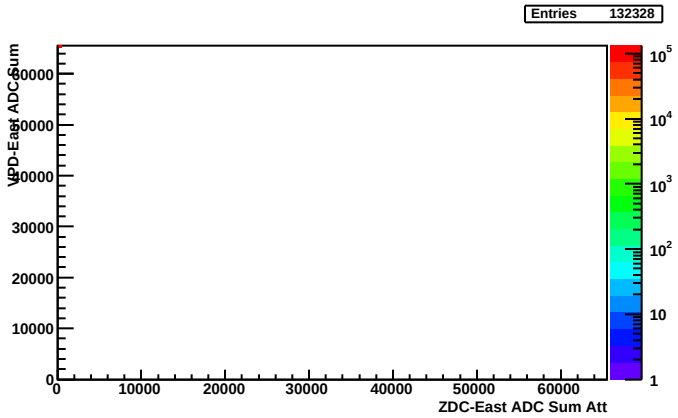

Entries 132328
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

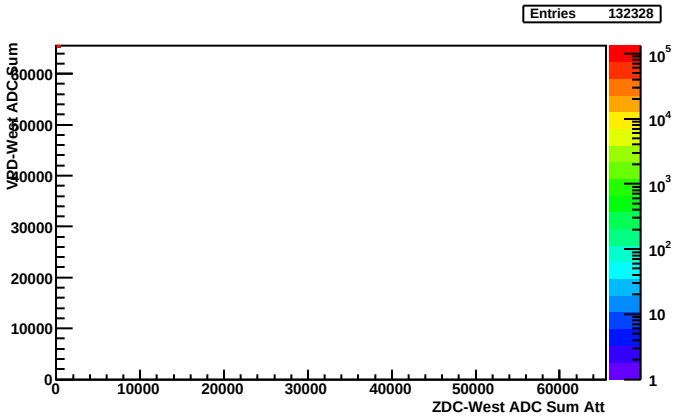

Entries 132328
BBC-S-East ADC Sum
VPD-East ADC Sum

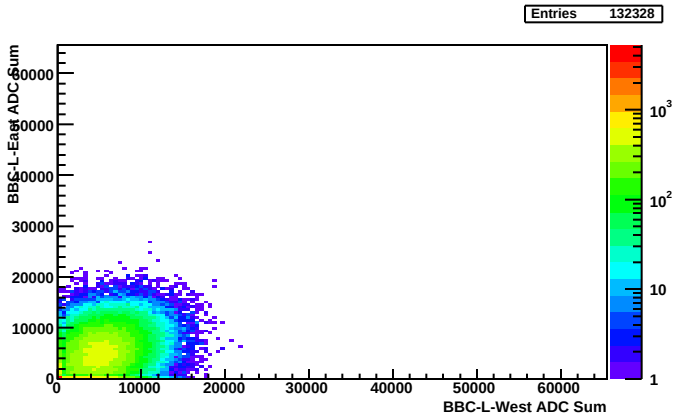

Entries 132328
BBC-S-West ADC Sum
VPD-West ADC Sum

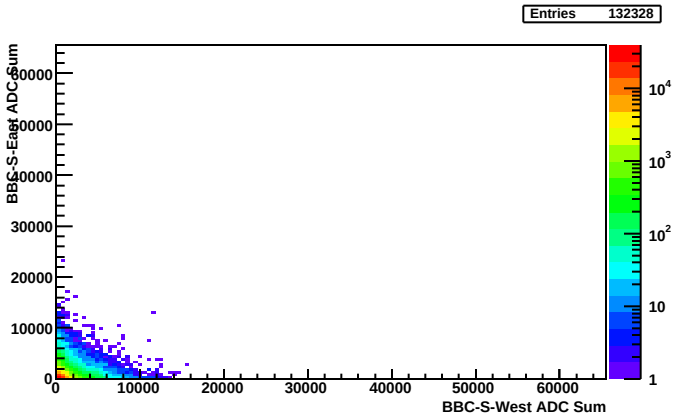

Entries 132328
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

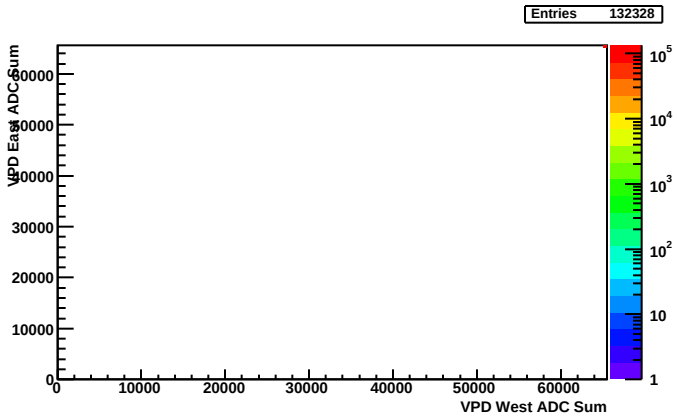

Entries 132328
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

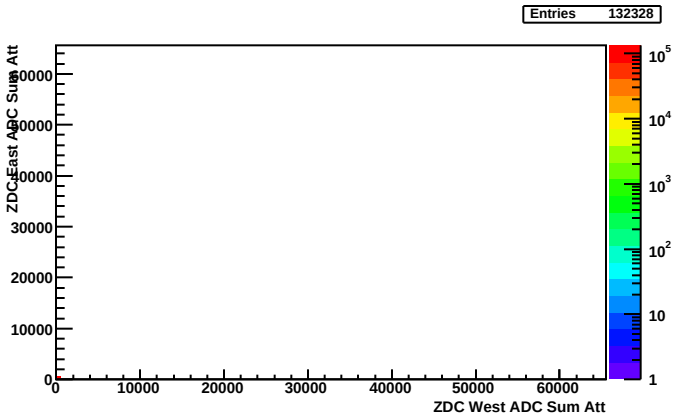

Entries 132328
VPD-East ADC Sum
ZDC-East ADC Sum Att

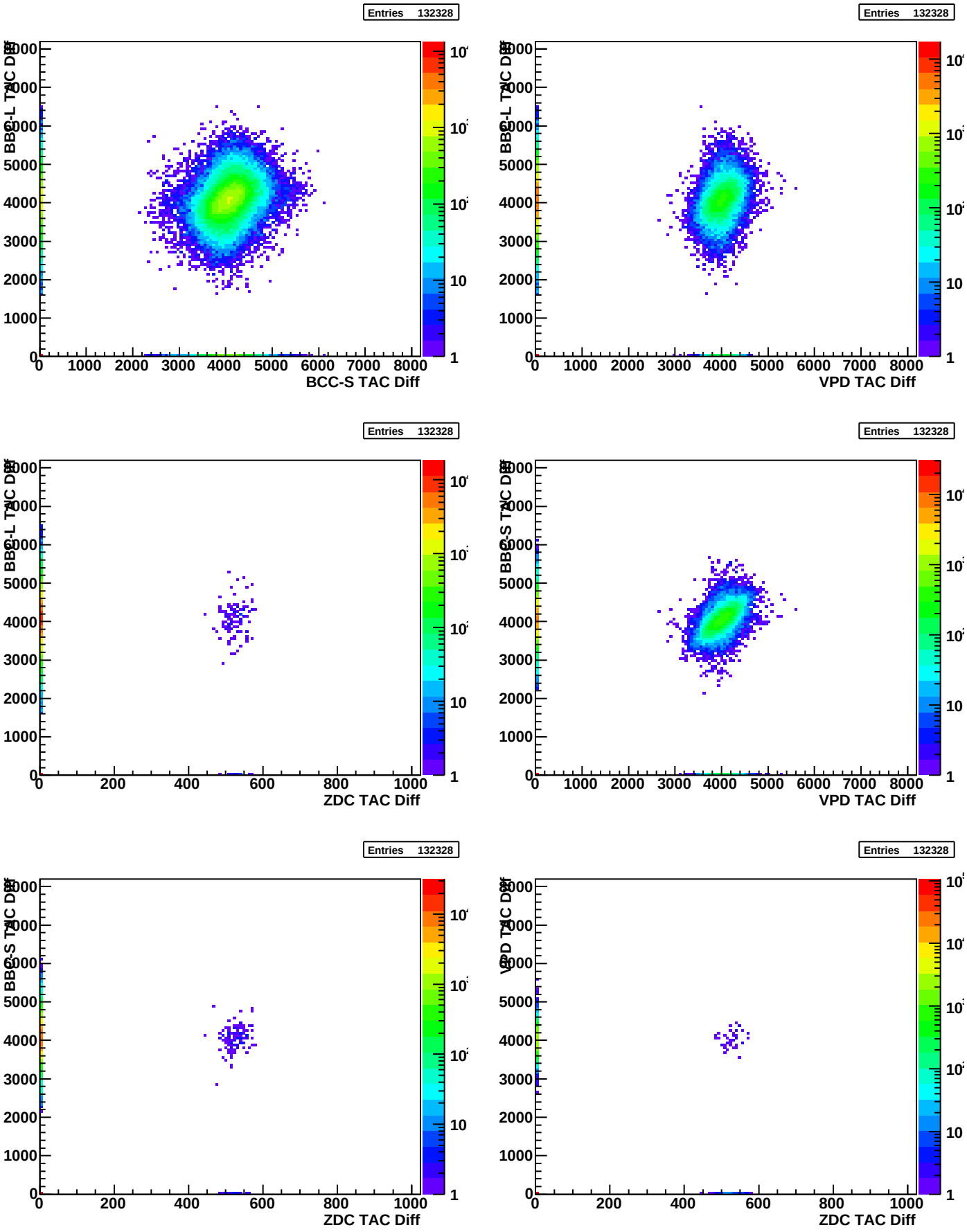

Entries 132328
VPD-West ADC Sum
ZDC-West ADC Sum Att

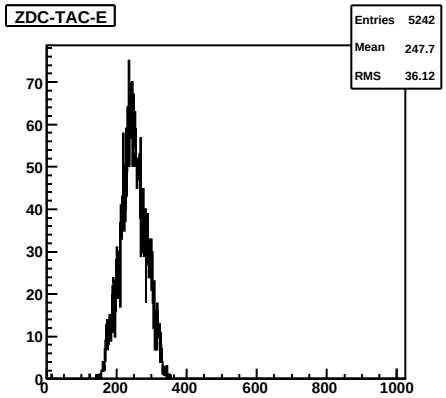

Entries 132328
BBC-L-East ADC Sum
BBC-L-West ADC Sum

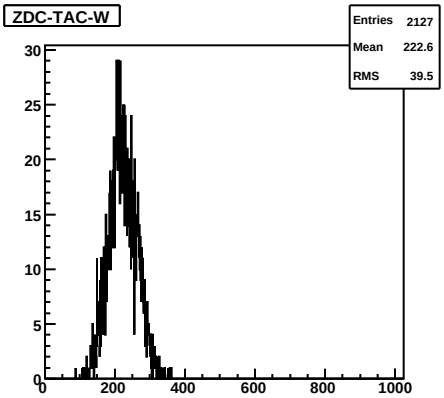

Entries 132328
BBC-S-East ADC Sum
BBC-S-West ADC Sum

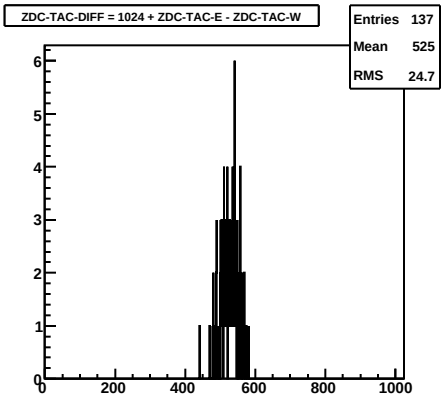

Entries 132328
VPD East ADC Sum
VPD West ADC Sum

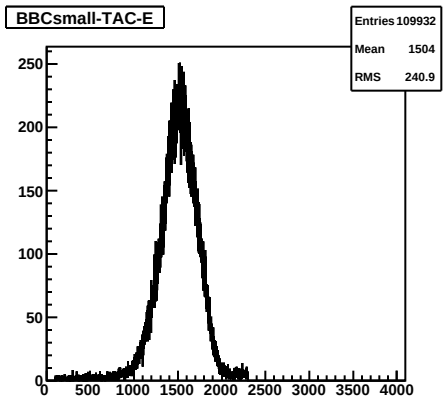

Entries 132328
ZDC East ADC Sum Att
ZDC West ADC Sum Att

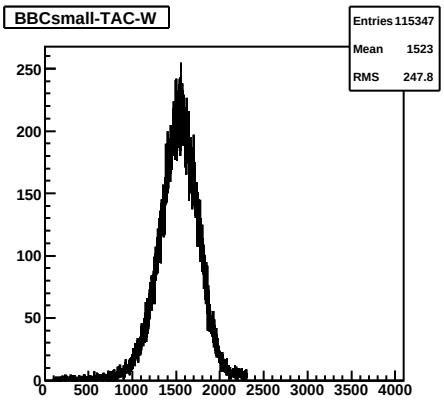

Entries 132328
BBC-L TAC Diff
BCC-S TAC Diff
Entries 132328
BBC-L TAC Diff
VPD TAC Diff
Entries 132328
BBC-L TAC Diff
ZDC TAC Diff
Entries 132328
BBC-S TAC Diff
VPD TAC Diff
Entries 132328
BBC-S TAC Diff
ZDC TAC Diff
Entries 132328
VPD TAC Diff
ZDC TAC Diff

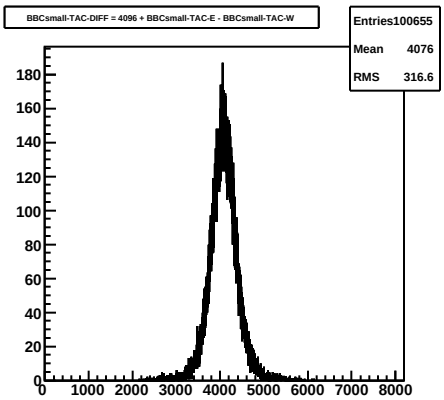

ZDC-TAC-E
Entries 5242
Mean 247.7
RMS 36.12

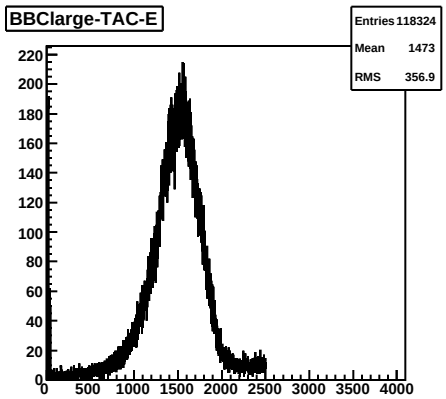

ZDC-TAC-W
Entries 2127
Mean 222.6
RMS 39.5

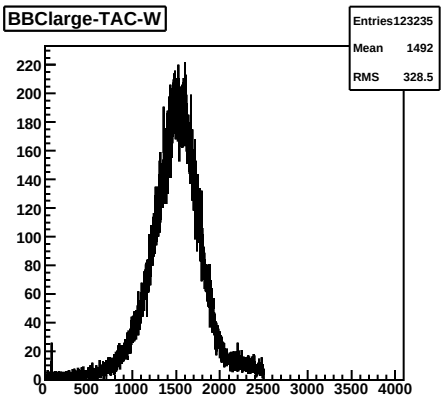

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 137
Mean 525
RMS 24.7

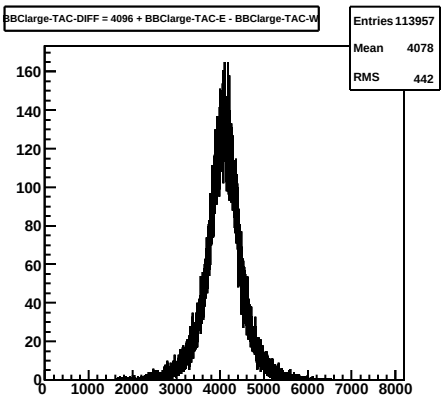

BBCsmall-TAC-E
Entries 109932
Mean 1504
RMS 240.9

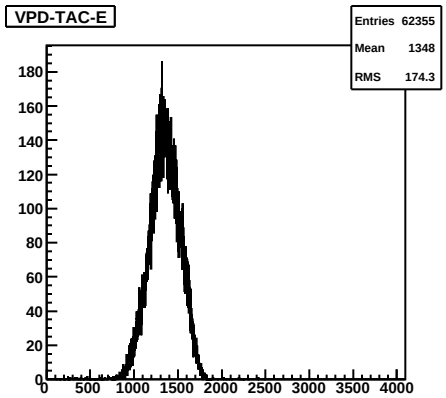

BBCsmall-TAC-W
Entries 115347
Mean 1523
RMS 247.8

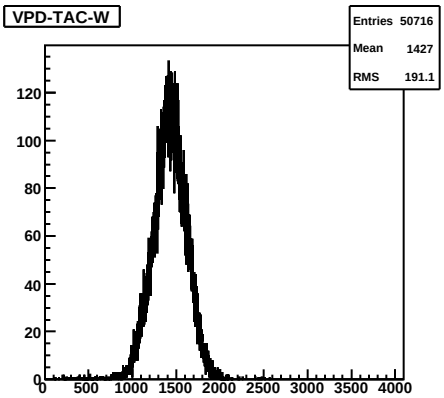

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 100655
Mean 4076
RMS 316.6

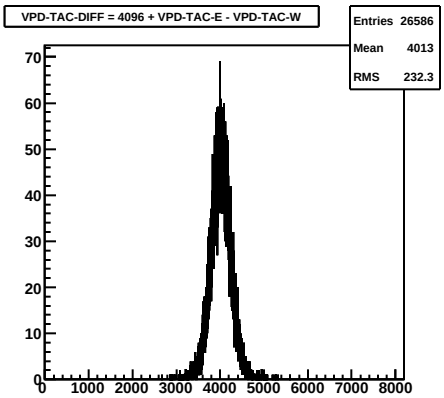

BBClarge-TAC-E
Entries 118324
Mean 1473
RMS 356.9

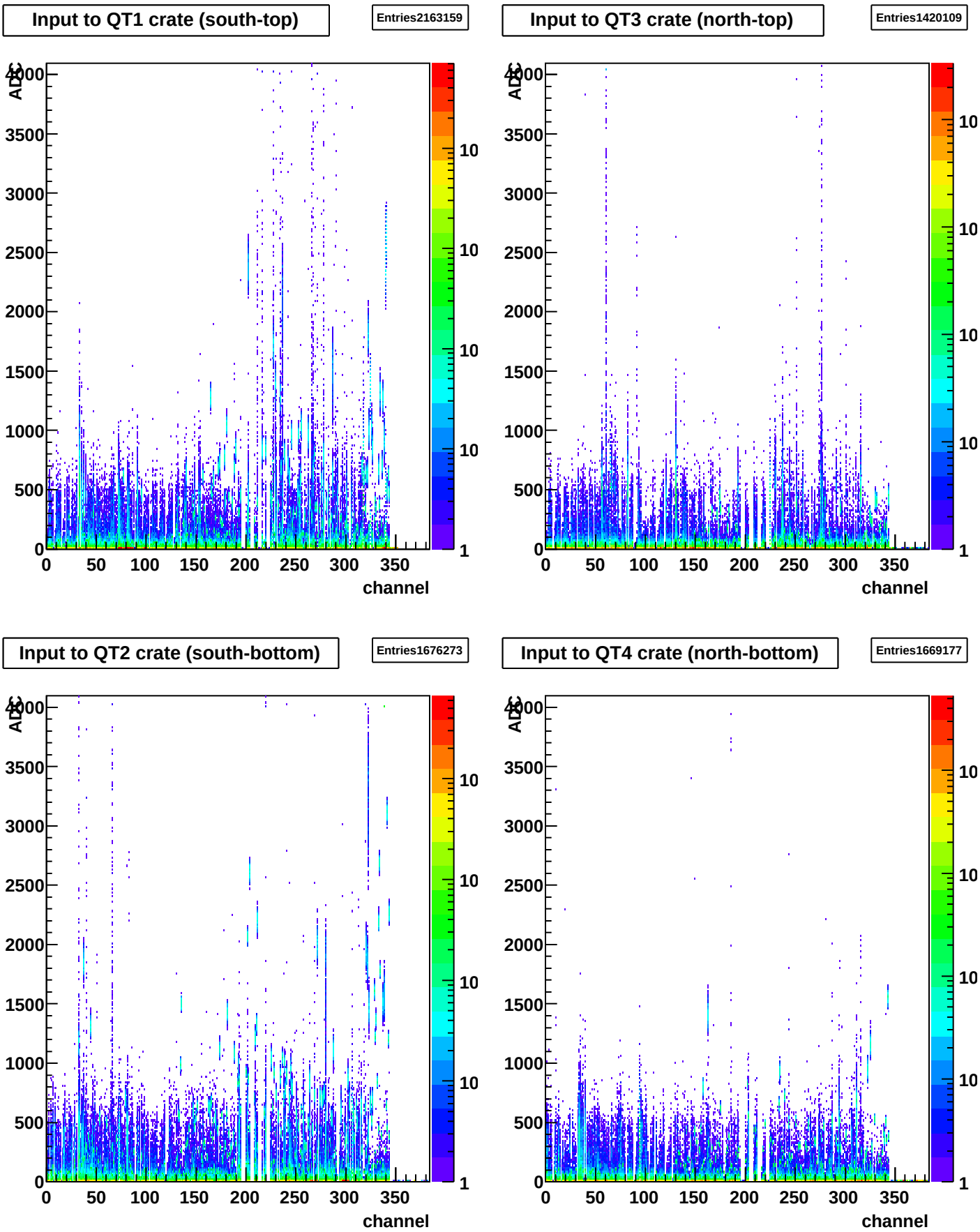

BBClarge-TAC-W
Entries 123235
Mean 1492
RMS 328.5

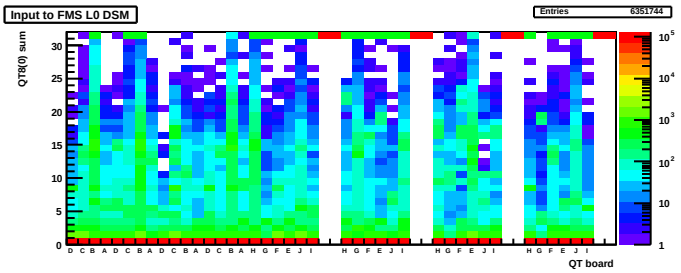

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 113957
Mean 4078
RMS 442

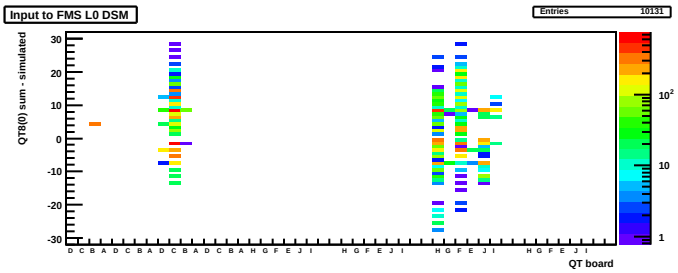

VPD-TAC-E
Entries 62355
Mean 1348
RMS 174.3

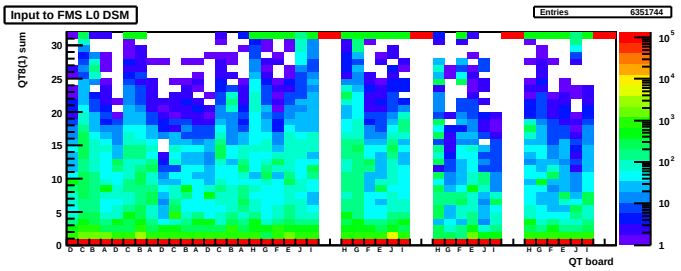

VPD-TAC-W
Entries 50716
Mean 1427
RMS 191.1

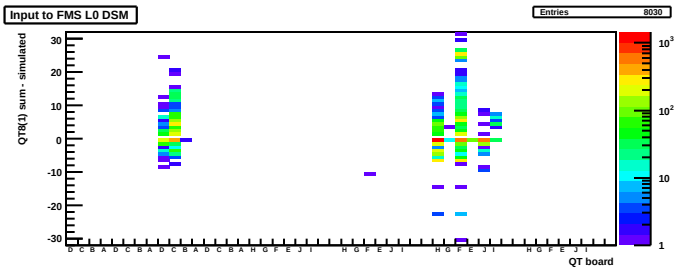

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 26586
Mean 4013
RMS 232.3

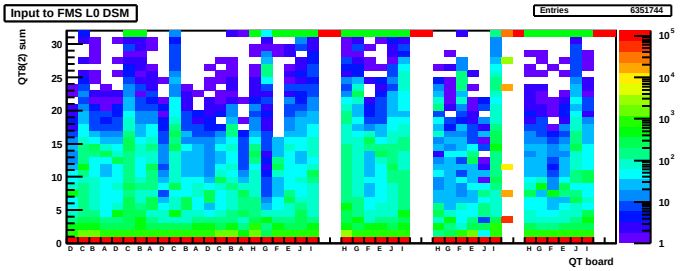

Input to QT1 crate (south-top)
Entries2163159
Input to QT3 crate (north-top)
Entries1420109
Input to QT2 crate (south-bottom)
Entries1676273
Input to QT4 crate (north-bottom)
Entries1669177
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

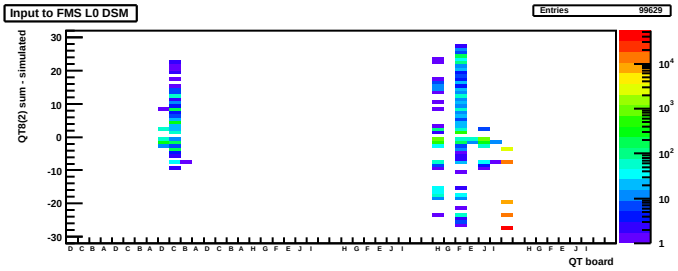

Input to FMS L0 DSM
Entries 6351744
QT8(0) sum
QT board

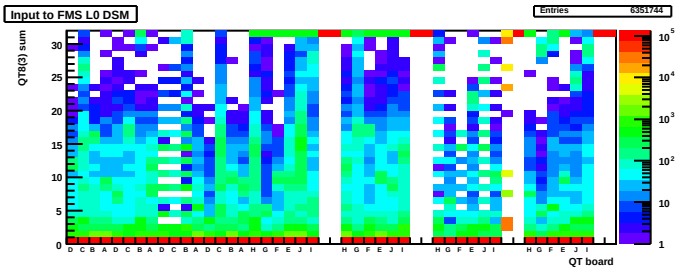

Input to FMS L0 DSM
Entries 10131
QT8(0) sum - simulated
QT board

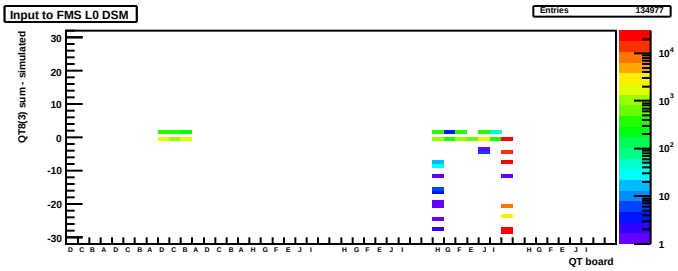

Input to FMS L0 DSM
Entries 6351744
QT8(1) sum
QT board

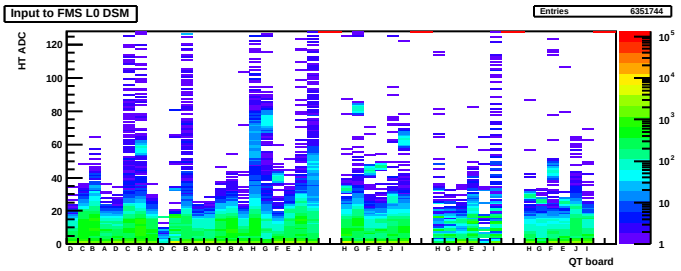

Input to FMS L0 DSM
Entries 8030
QT8(1) sum - simulated
QT board

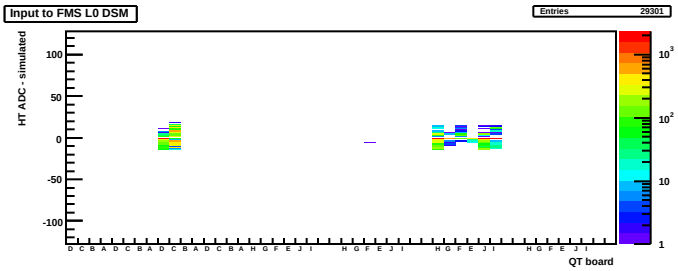

Input to FMS L0 DSM
Entries 6351744
QT8(2) sum
QT board

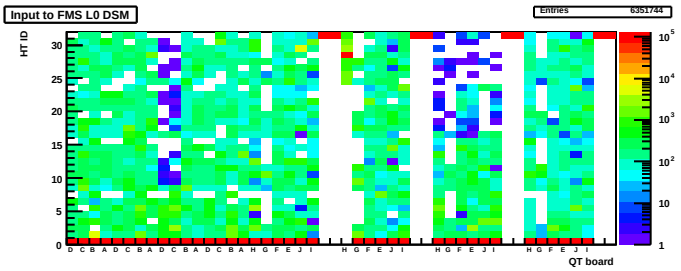

Input to FMS L0 DSM
Entries 99629
QT8(2) sum - simulated
QT board

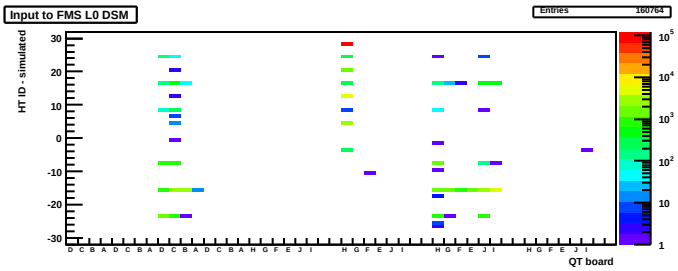

Input to FMS L0 DSM
Entries 6351744
QT8(3) sum
QT board

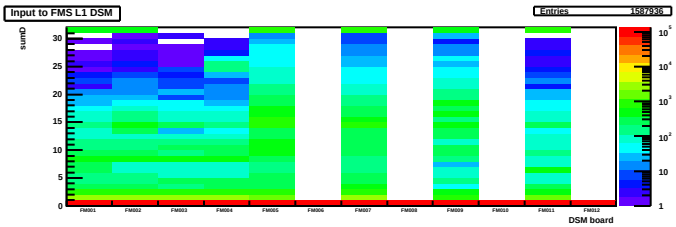

Input to FMS L0 DSM
Entries 134977
QT8(3) sum - simulated
QT board

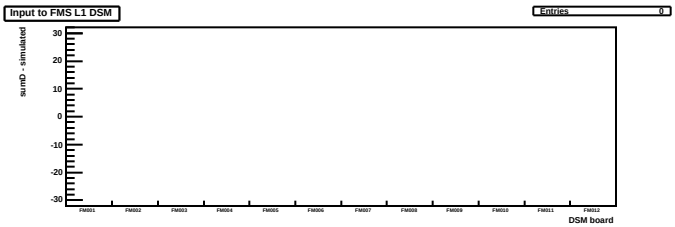

Input to FMS L0 DSM
Entries 6351744
HT ADC
QT board

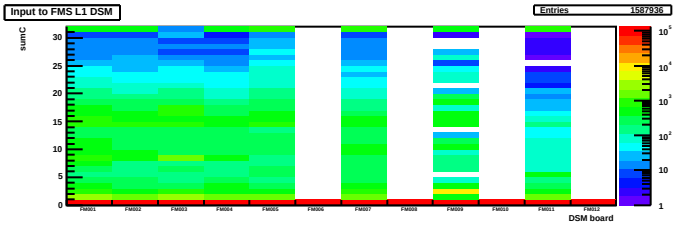

Input to FMS L0 DSM
Entries 29301
HT ADC - simulated
QT board

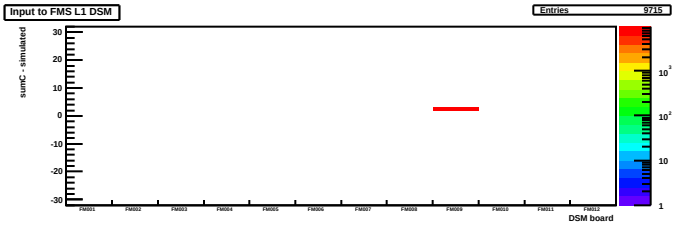

Input to FMS L0 DSM
Entries 6351744
HT ID
QT board

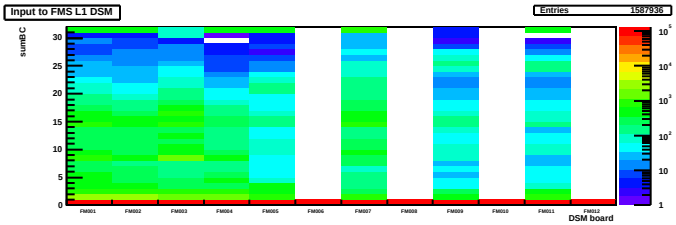

Input to FMS L0 DSM
Entries 160764
HT ID - simulated
QT board

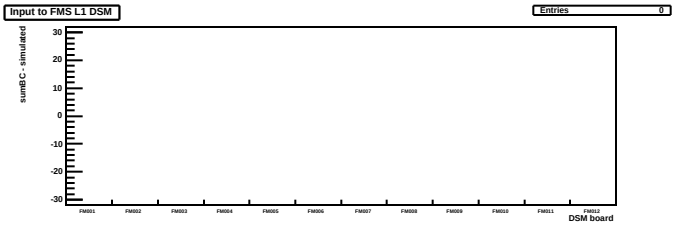

Input to FMS L1 DSM
Entries 1587936
sumD
DSM board

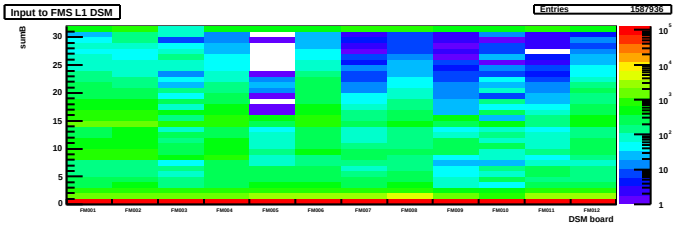

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

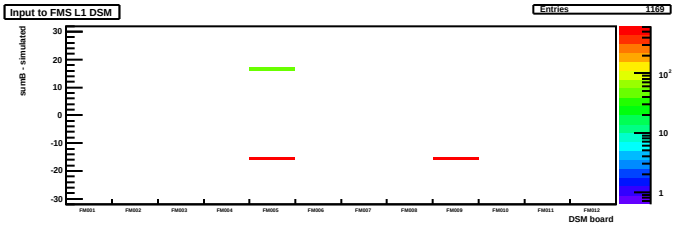

Input to FMS L1 DSM
Entries 1587936
sumC
DSM board

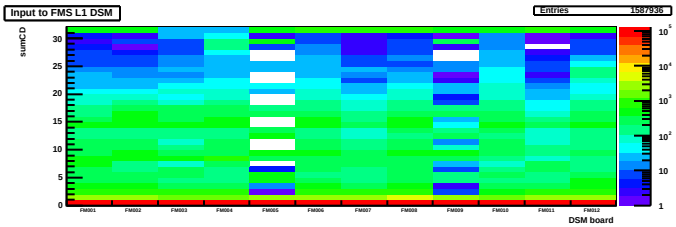

Input to FMS L1 DSM
Entries 9715
sumC - simulated
DSM board

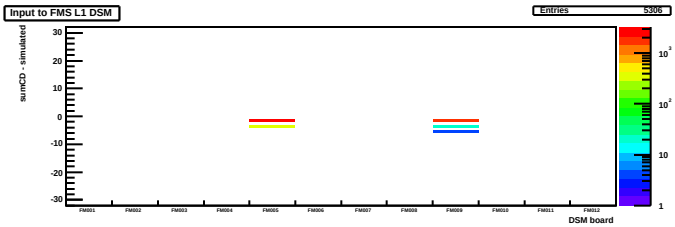

Input to FMS L1 DSM
Entries 1587936
sumBC
DSM board

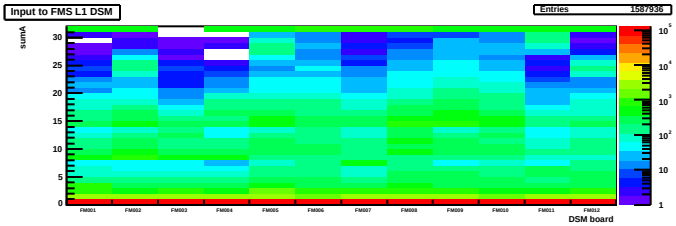

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

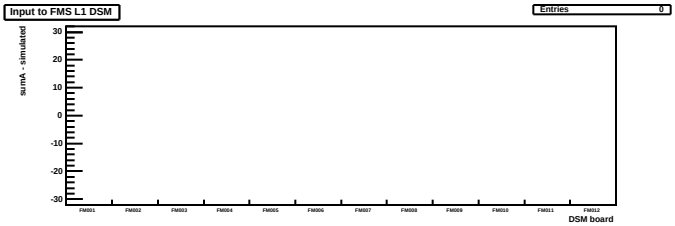

Input to FMS L1 DSM
Entries 1587936
sumB
DSM board

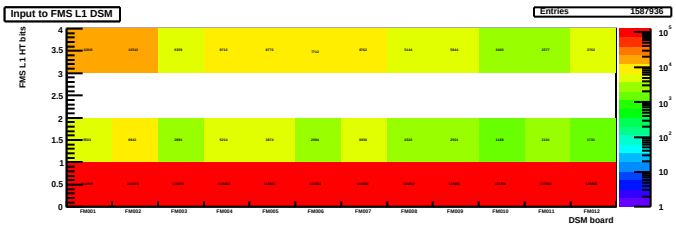

Input to FMS L1 DSM
Entries 1169
sumB - simulated
DSM board

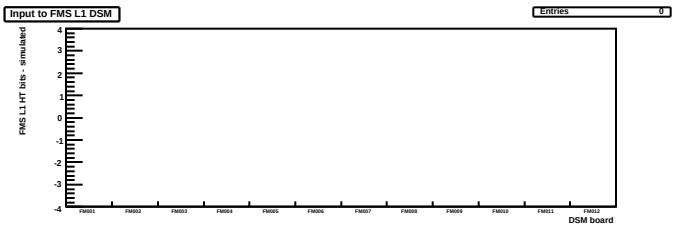

Input to FMS L1 DSM
Entries 1587936
sumCD
DSM board

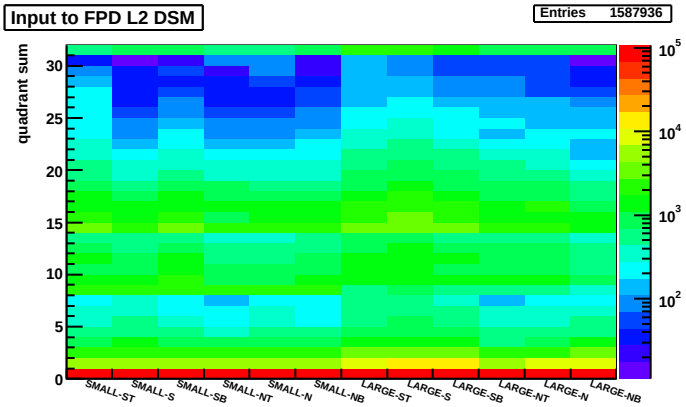

Input to FMS L1 DSM
Entries 5306
sumCD - simulated
DSM board

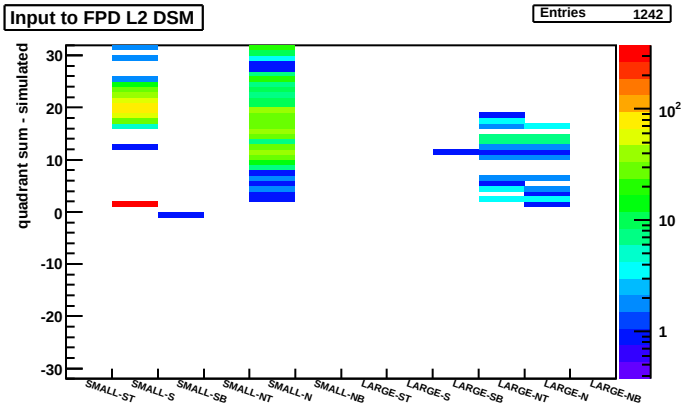

Input to FMS L1 DSM
Entries 1587936
sumA
DSM board

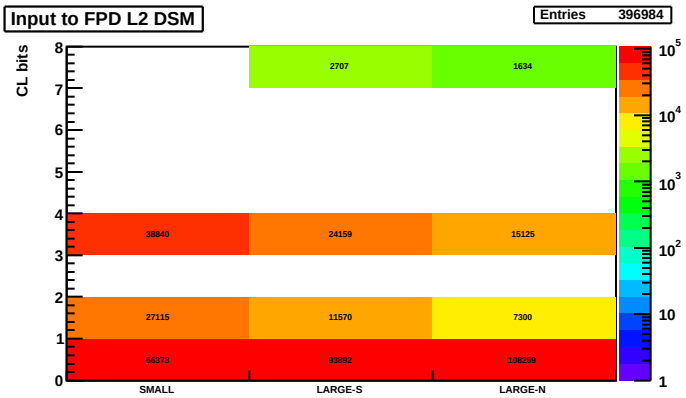

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

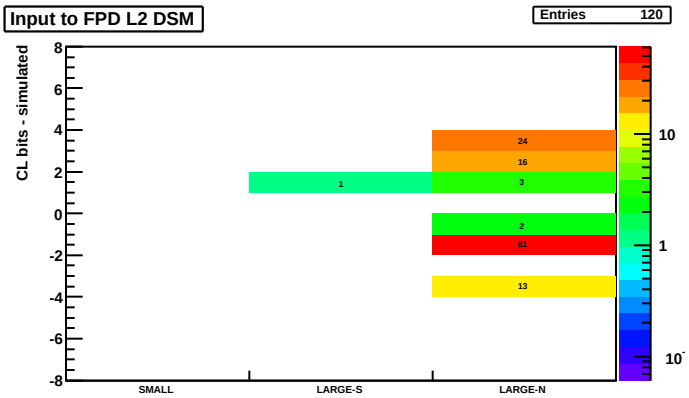

Input to FMS L1 DSM
Entries 1587936
FMS L1 HT bits
DSM board

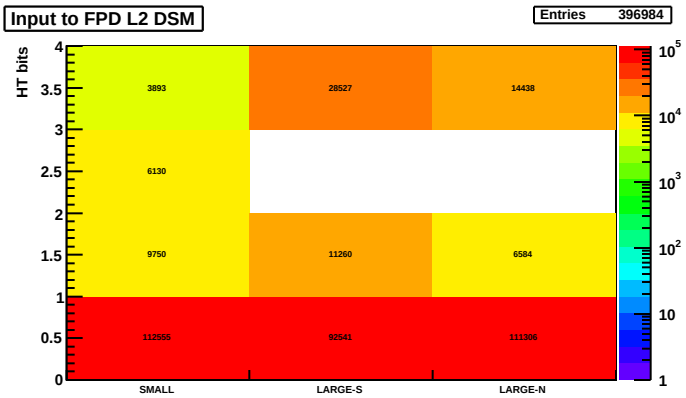

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

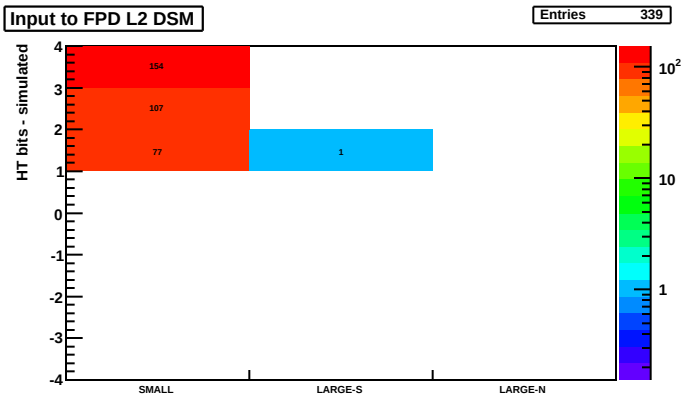

Input to FPD L2 DSM
Entries 1587936
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

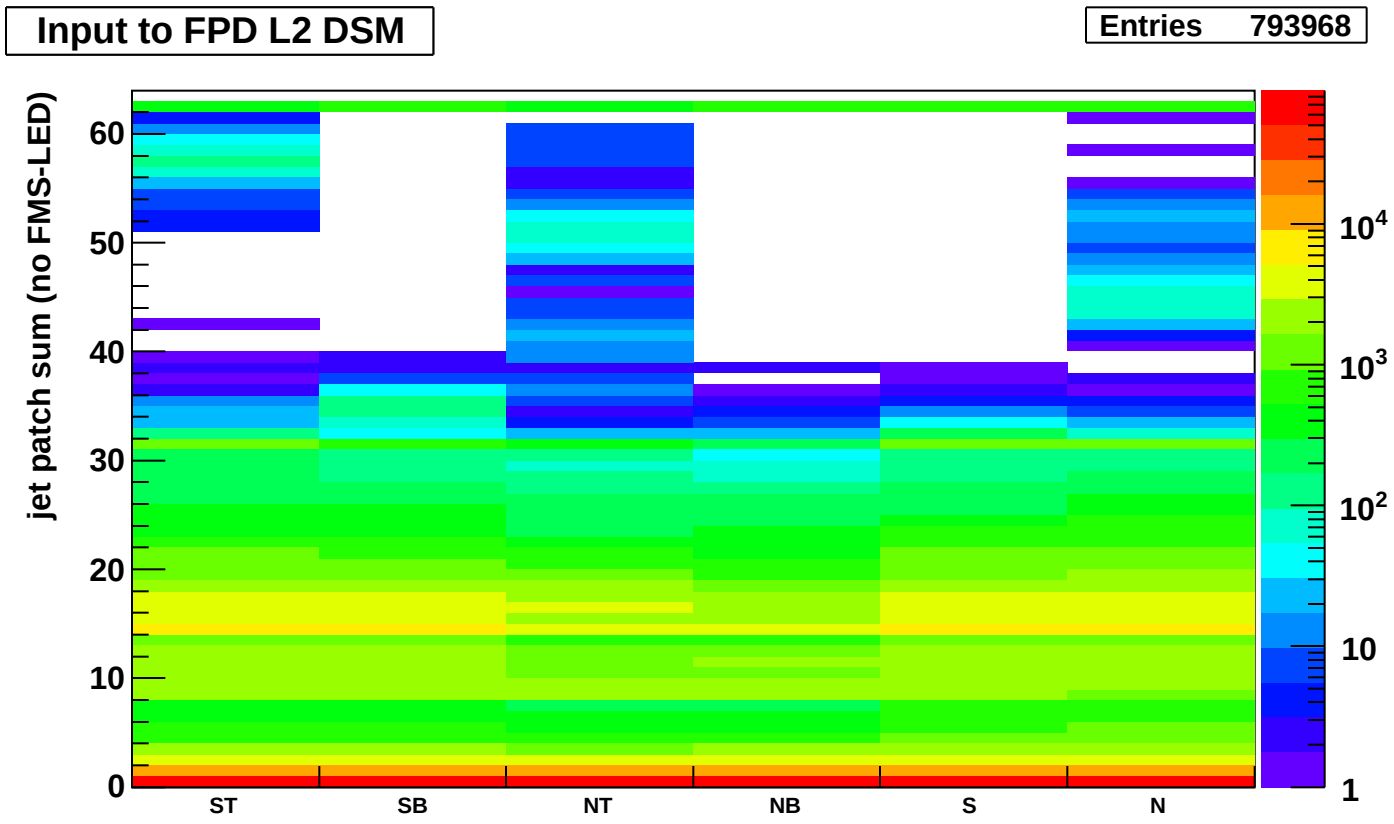

Input to FPD L2 DSM
Entries 1242
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

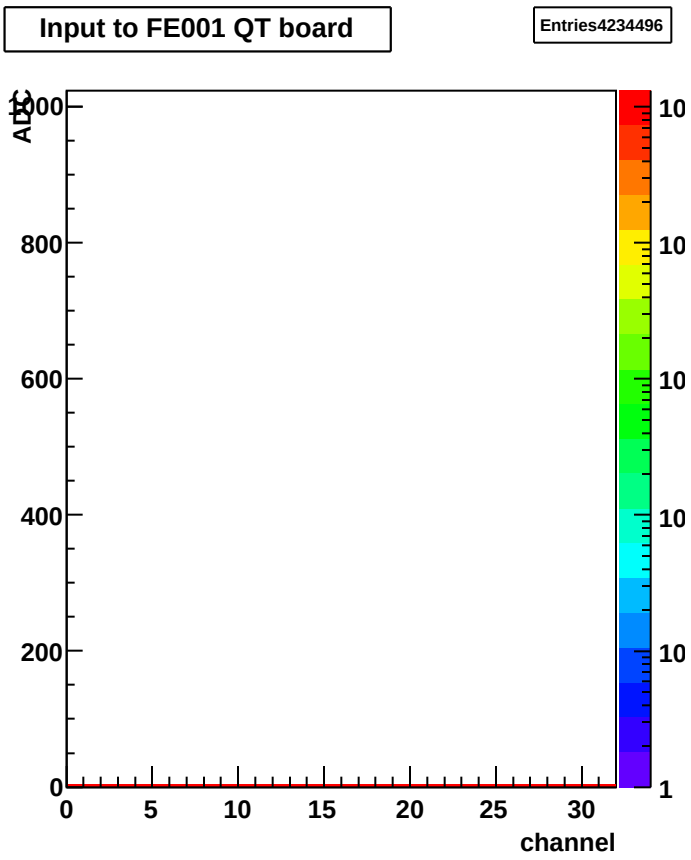

Input to FPD L2 DSM
Entries 396984
CL bits
2707
1634
38840
24159
15125
27115
11570
7300
66373
93892
108269
SMALL
LARGE-S
LARGE-N

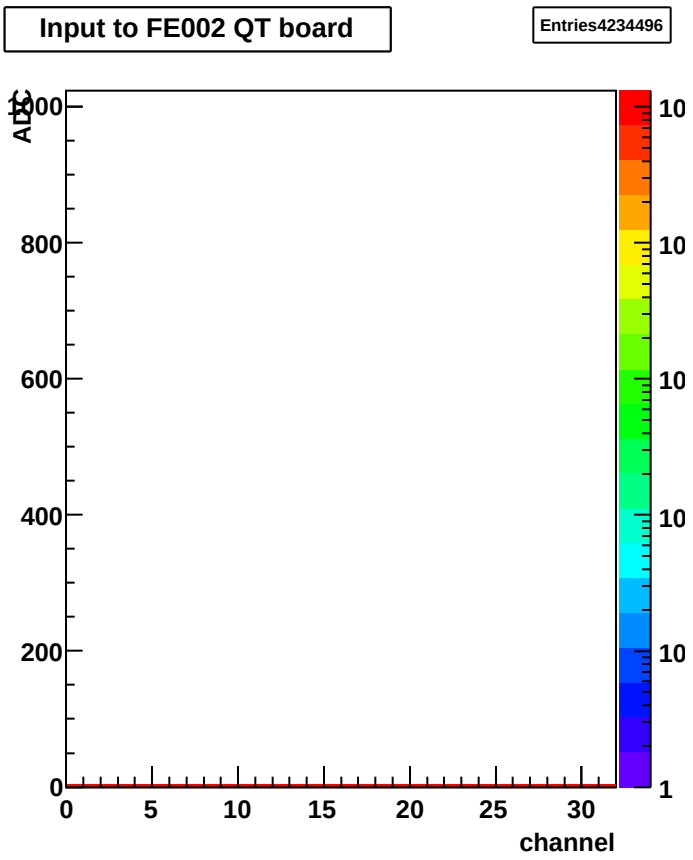

Input to FPD L2 DSM
Entries 120
CL bits - simulated
24
16
1
3
2
61
13
SMALL
LARGE-S
LARGE-N

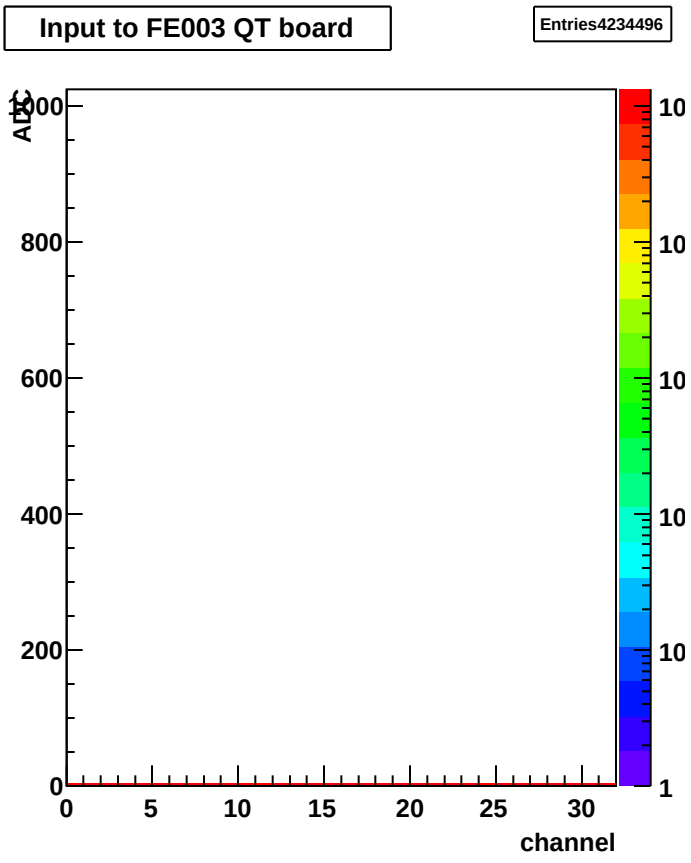

Input to FPD L2 DSM
Entries 396984
HT bits
3893
28527
14438
6130
9750
11260
6584
112555
92541
111306
SMALL
LARGE-S
LARGE-N

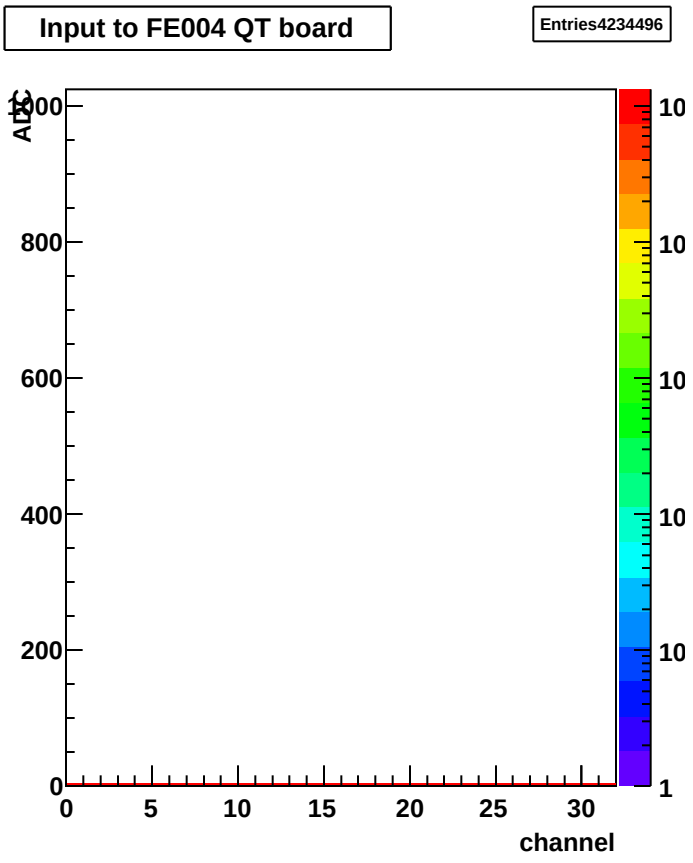

Input to FPD L2 DSM
Entries 339
HT bits - simulated
154
107
77
1
SMALL
LARGE-S
LARGE-N

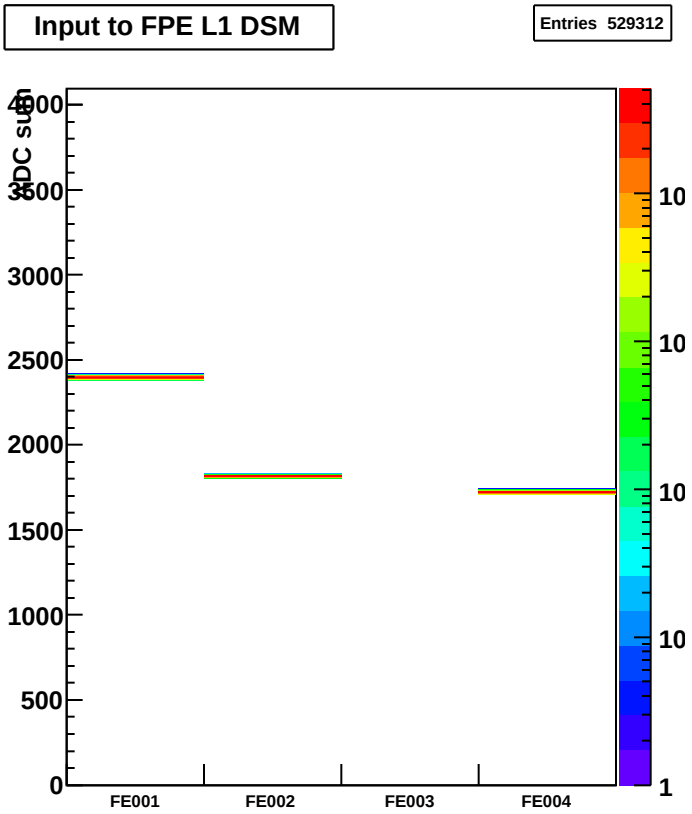

Input to FPD L2 DSM
Entries 793968
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

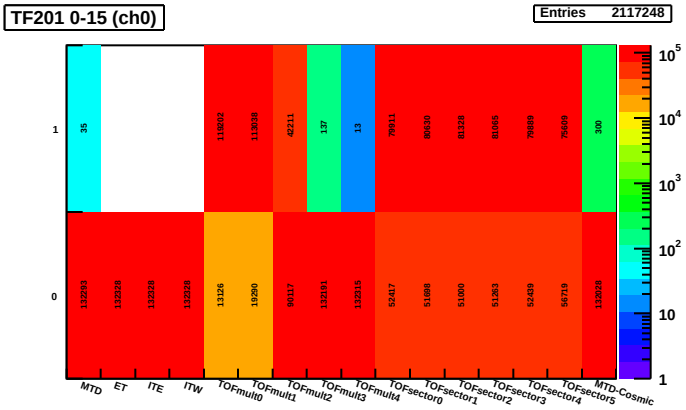

Input to FE001 QT board
Entries4234496
ADC
channel

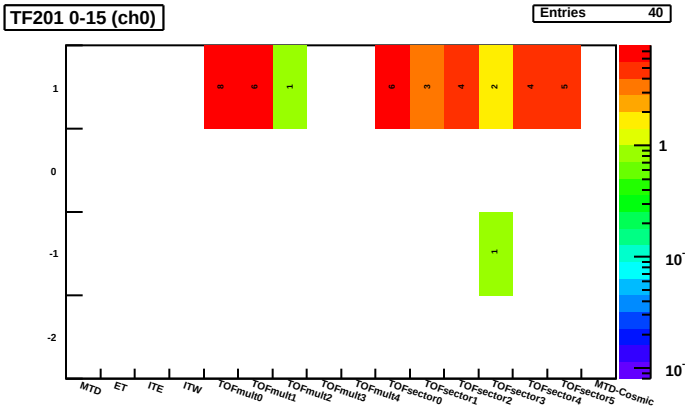

Input to FE002 QT board
Entries4234496
ADC
channel

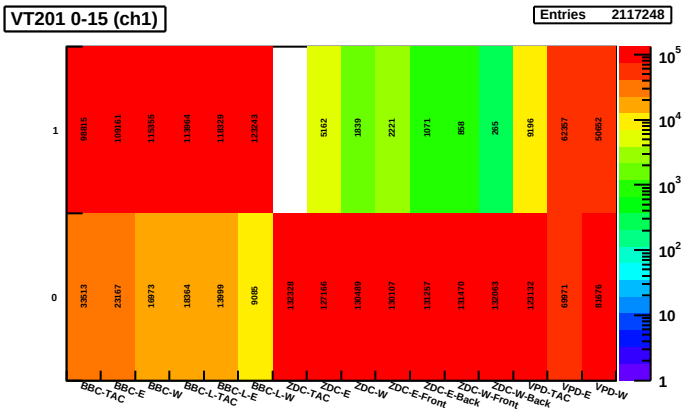

Input to FE003 QT board
Entries4234496
ADC
channel

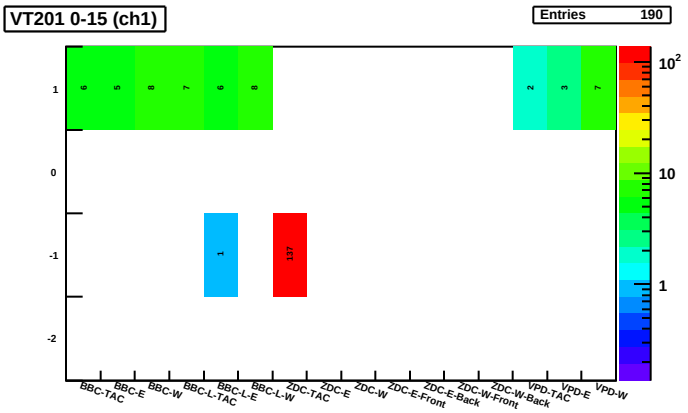

Input to FE004 QT board
Entries4234496
ADC
channel

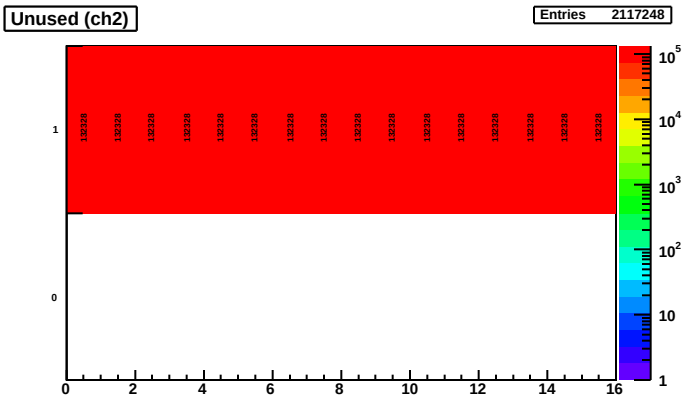

Input to FPE L1 DSM
Entries 529312
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

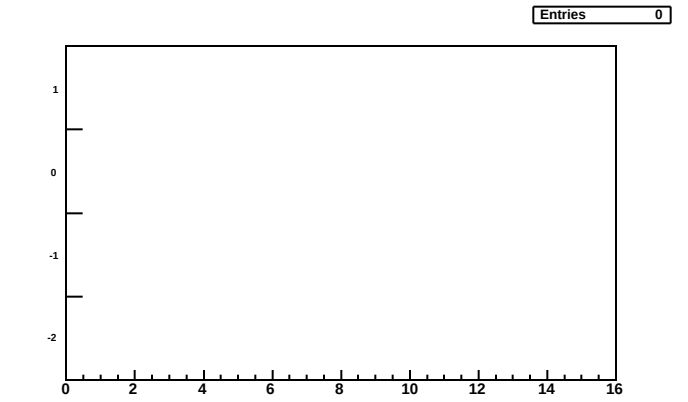

TF201 0-15 (ch0)
Entries 2117248
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

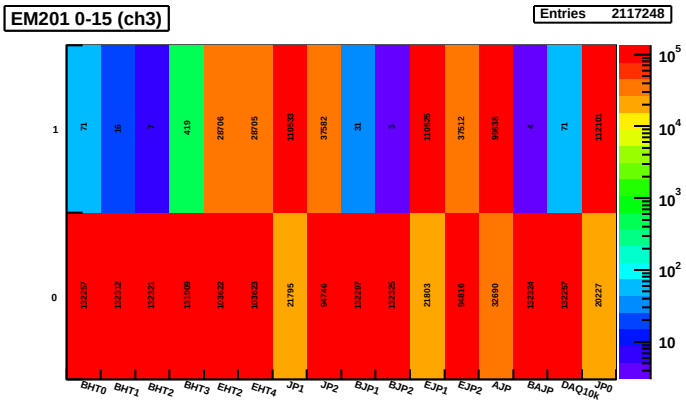

TF201 0-15 (ch0)
Entries 40
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

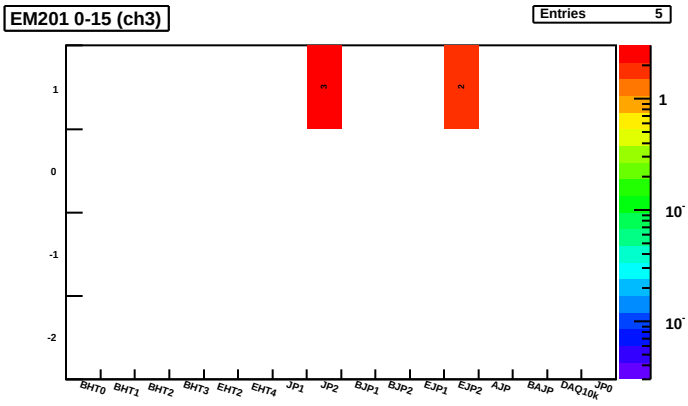

VT201 0-15 (ch1)
Entries 2117248
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 190
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

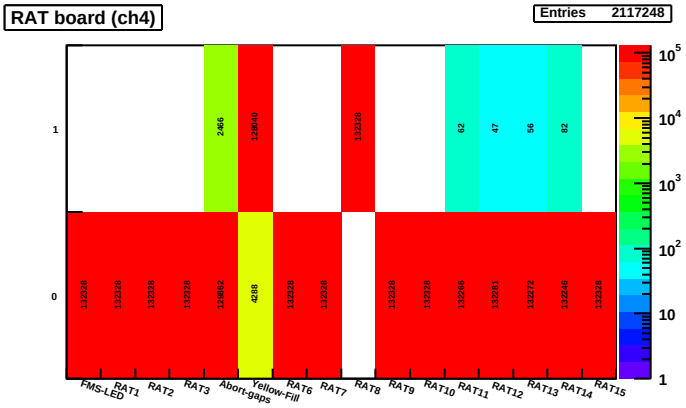

Unused (ch2)
Entries 2117248

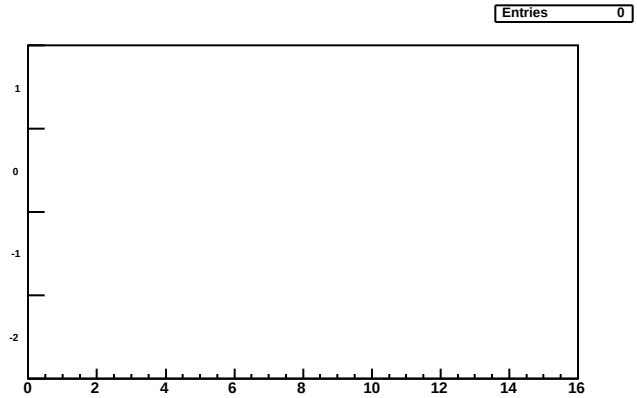

Entries 0


EM201 0-15 (ch3)
Entries 2117248
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

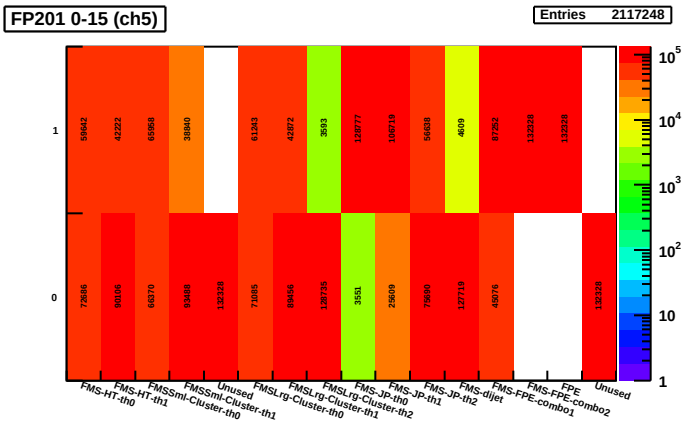

EM201 0-15 (ch3)
Entries 5
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

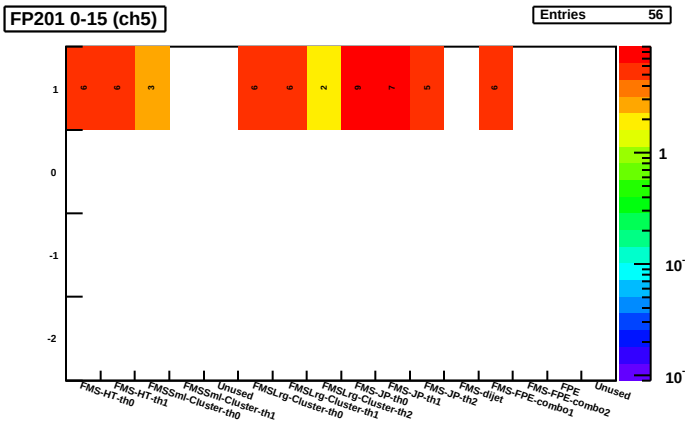




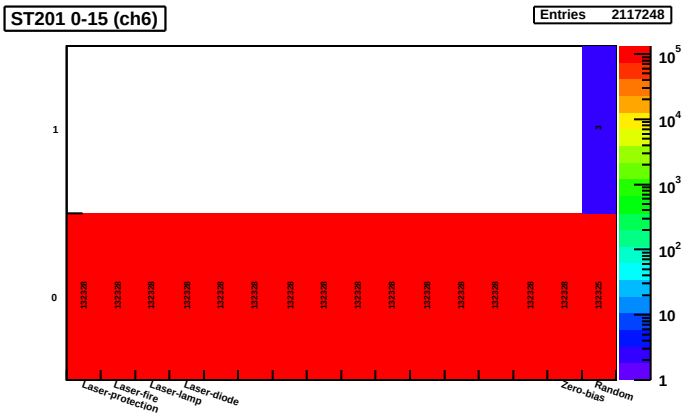
FP201 0-15 (ch5)

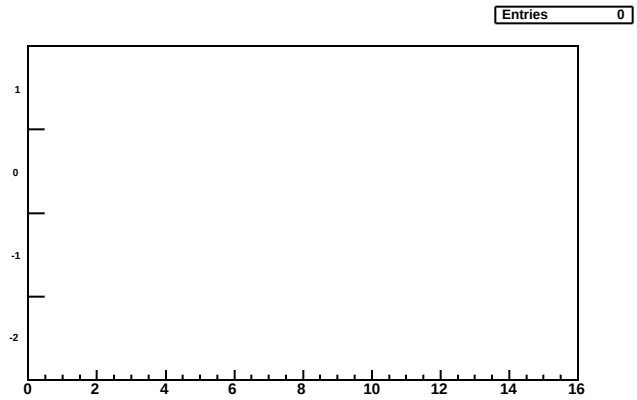
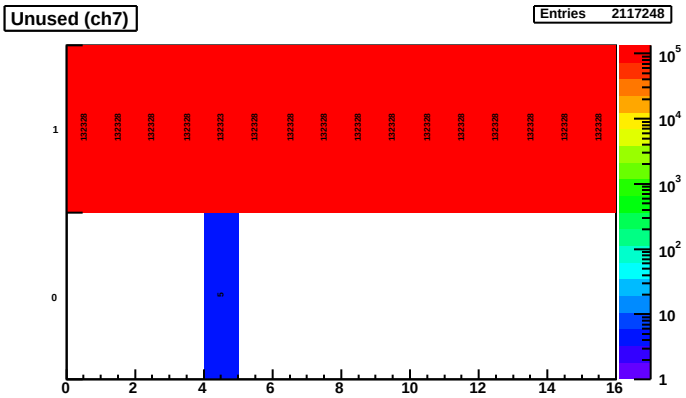
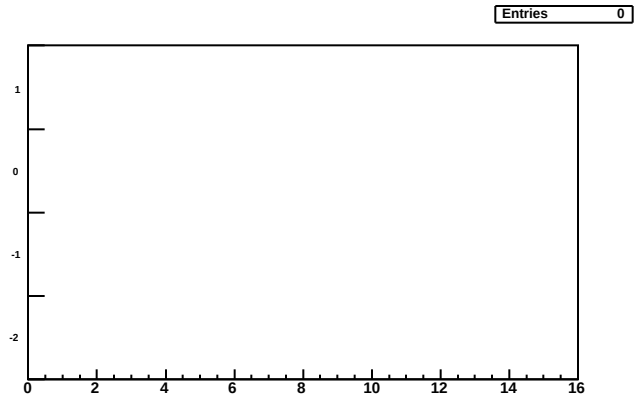


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)