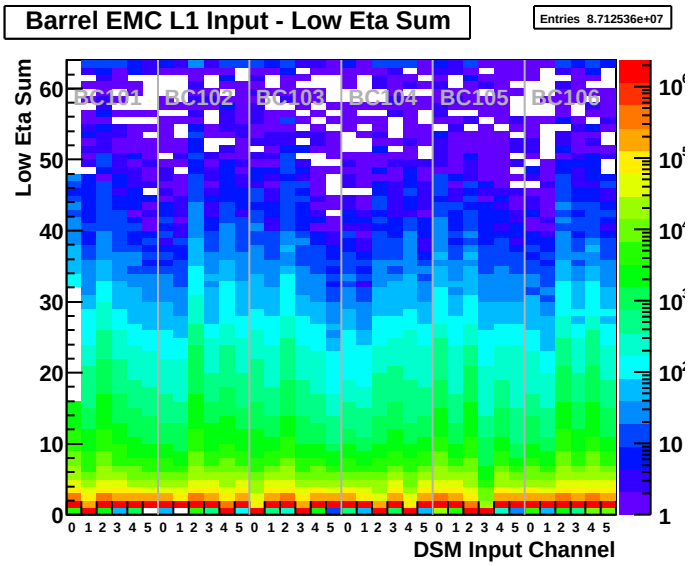
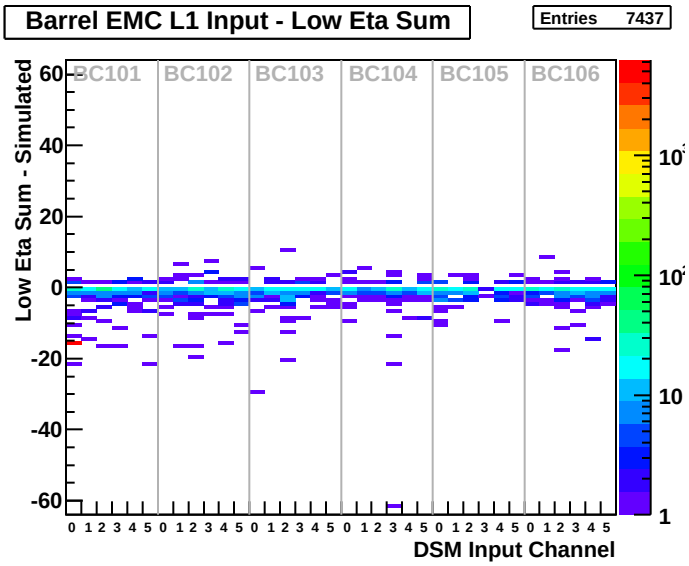
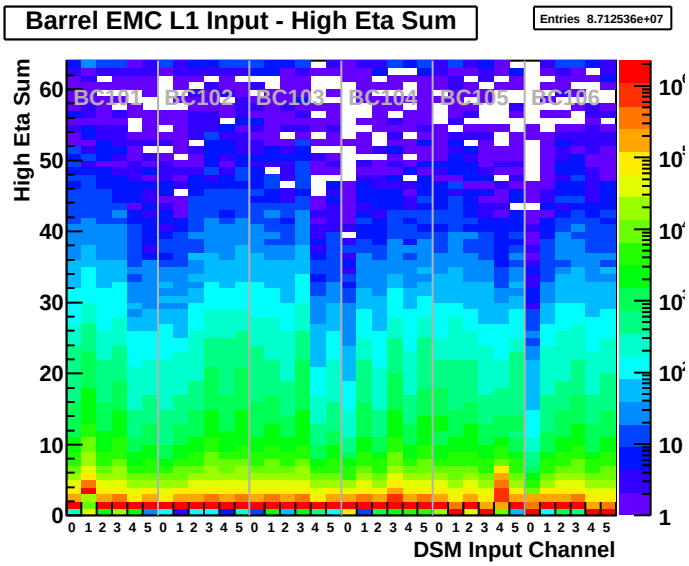
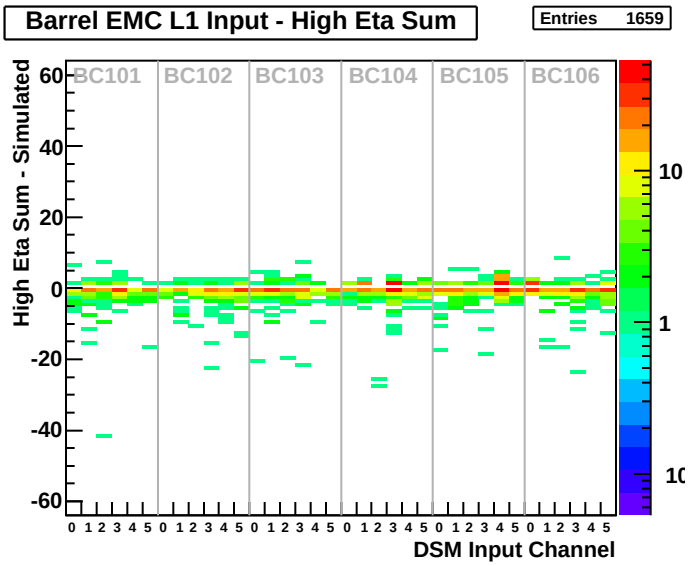
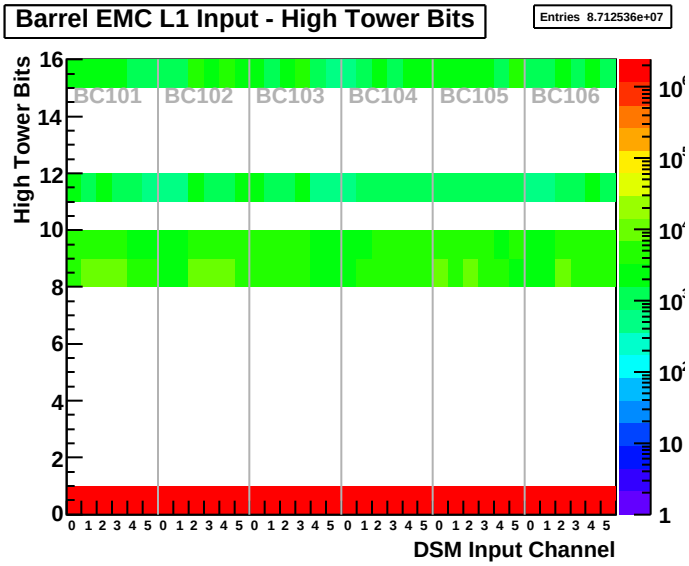
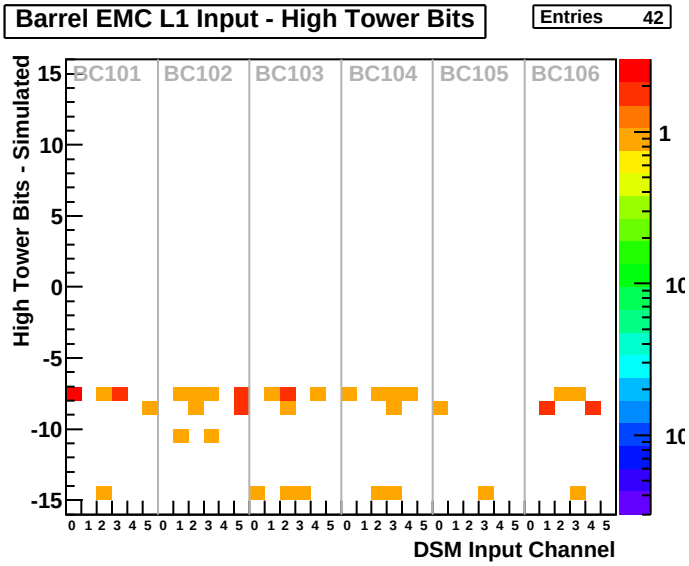

Barrel EMC L1 Input - Low Eta Sum
Entries 8.712536e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

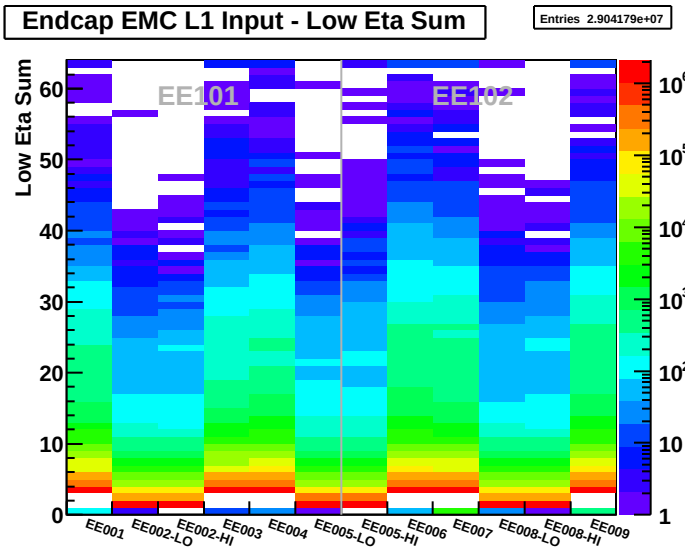

Barrel EMC L1 Input - Low Eta Sum
Entries 7437
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

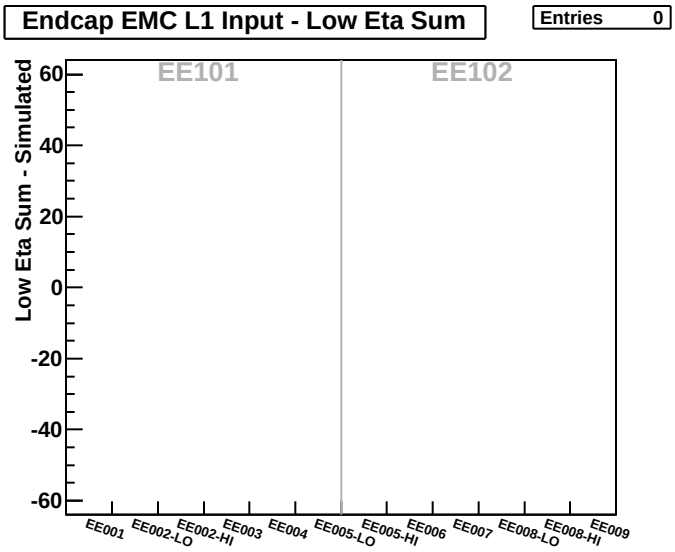

Barrel EMC L1 Input - High Eta Sum
Entries 8.712536e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

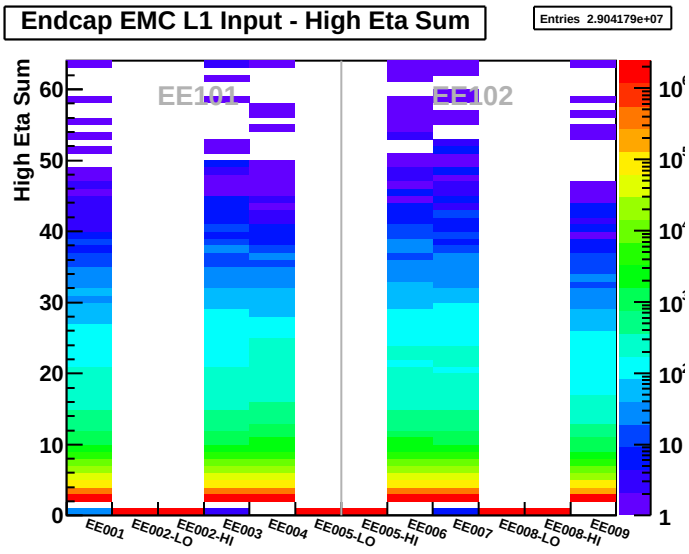

Barrel EMC L1 Input - High Eta Sum
Entries 1659
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

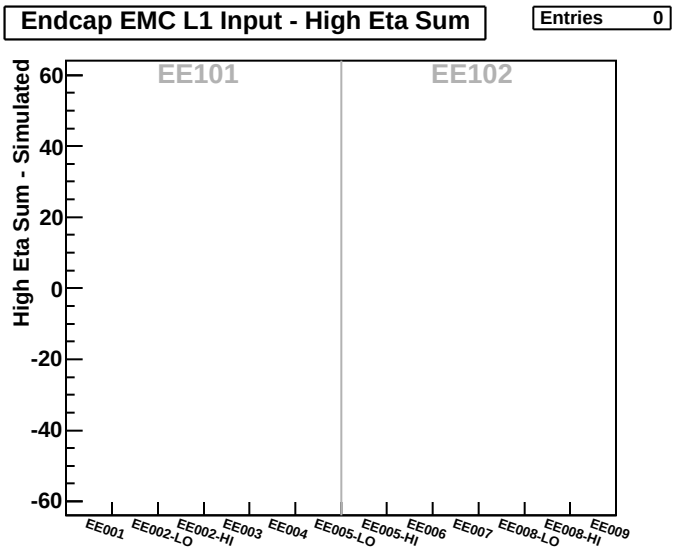

Barrel EMC L1 Input - High Tower Bits
Entries 8.712536e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

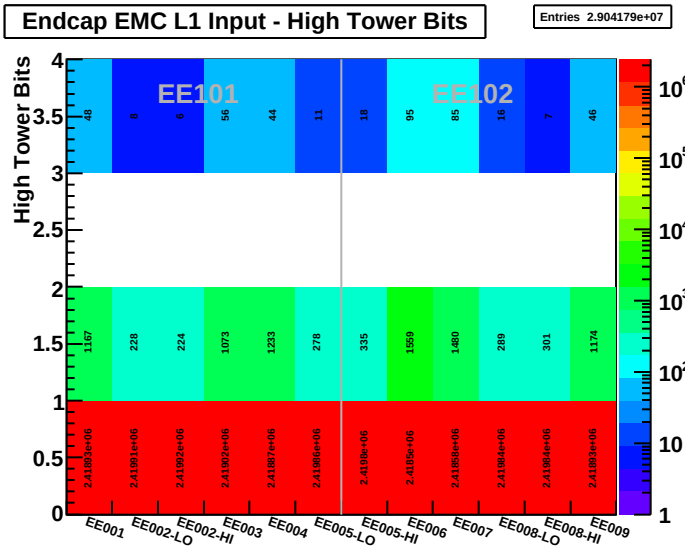

Barrel EMC L1 Input - High Tower Bits
Entries 42
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

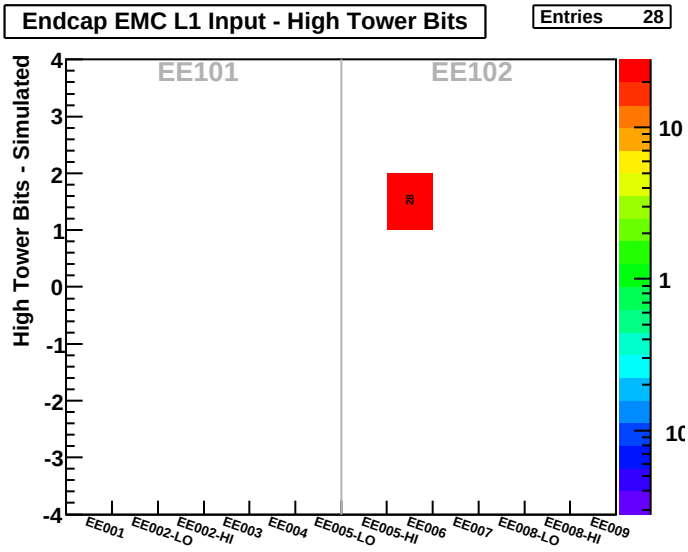

Endcap EMC L1 Input - Low Eta Sum
Entries 2.904179e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

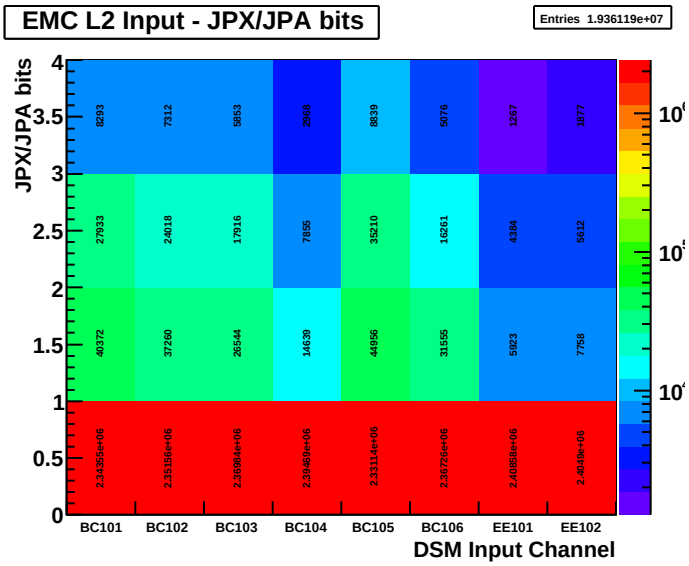

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

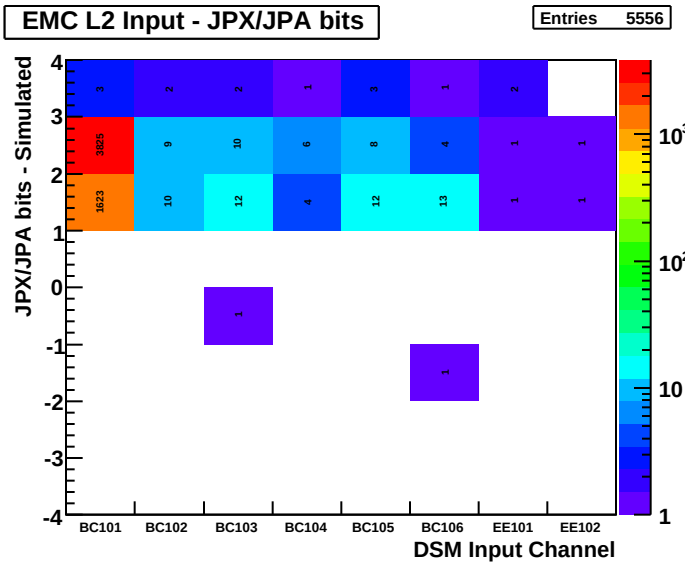

Endcap EMC L1 Input - High Eta Sum
Entries 2.904179e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

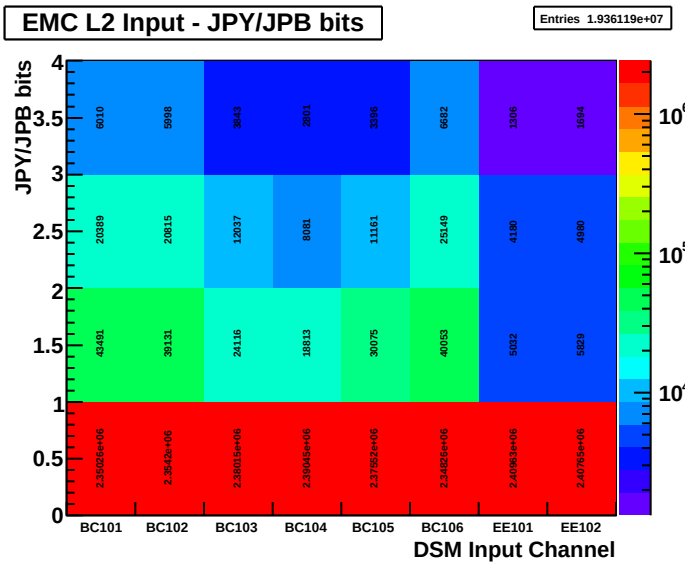

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

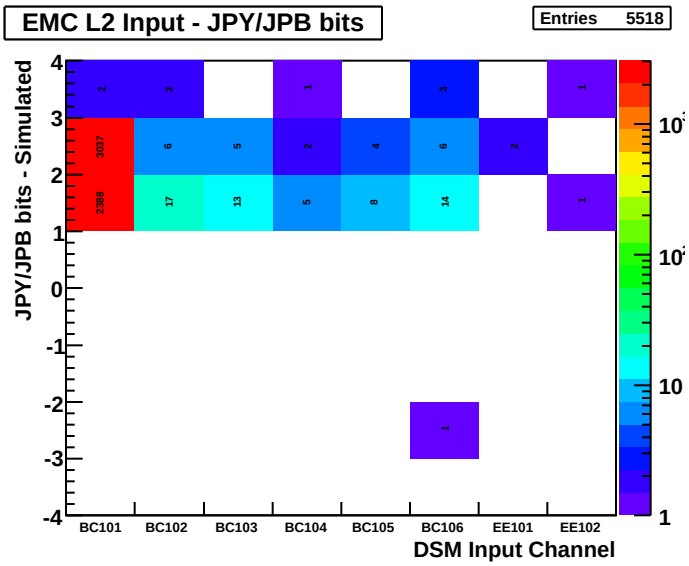

Endcap EMC L1 Input - High Tower Bits
Entries 2.904179e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

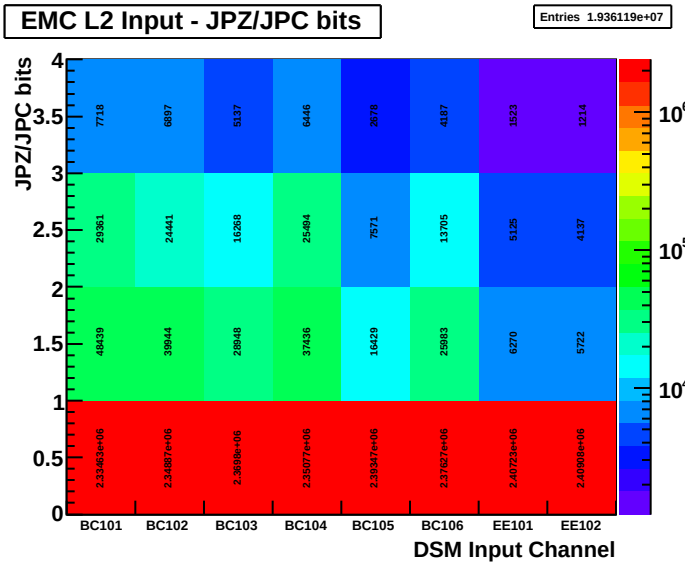

Endcap EMC L1 Input - High Tower Bits
Entries 28
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

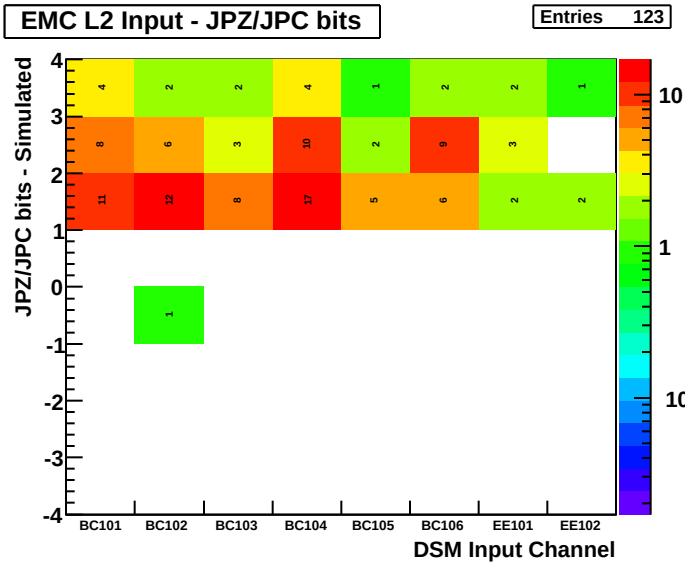

EMC L2 Input - JPX/JPA bits
Entries 1.936119e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

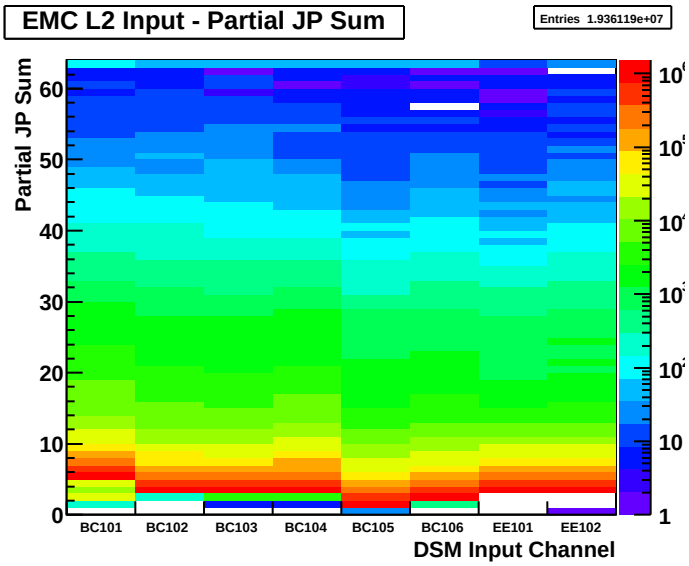

EMC L2 Input - JPX/JPA bits
Entries 5556
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

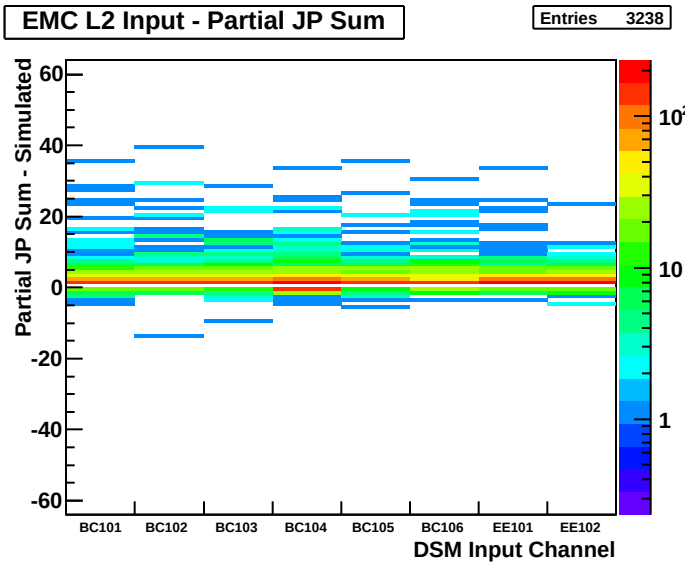

EMC L2 Input - JPY/JPB bits
Entries 1.936119e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

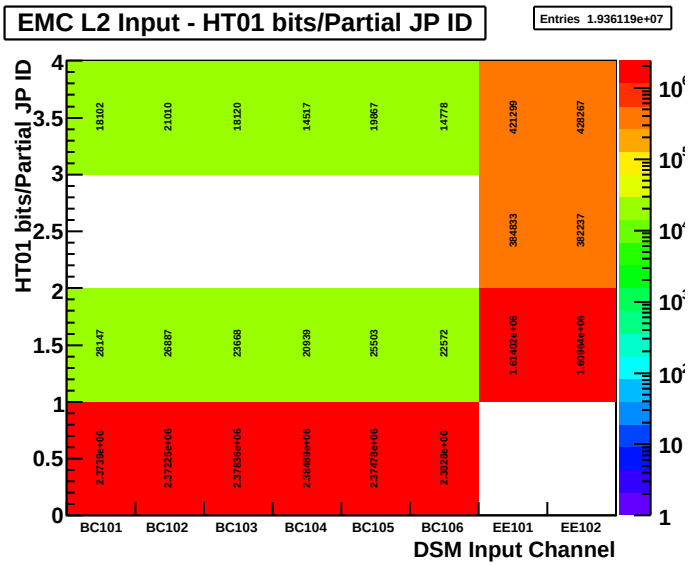

EMC L2 Input - JPY/JPB bits
Entries 5518
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

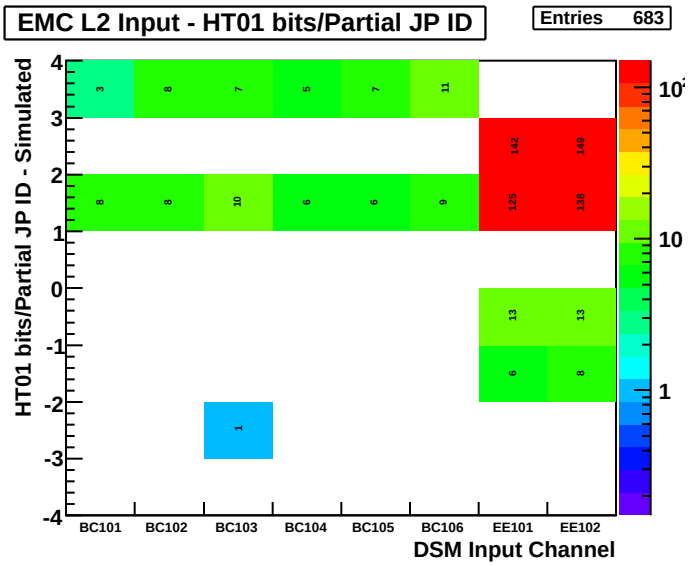

EMC L2 Input - JPZ/JPC bits
Entries 1.936119e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

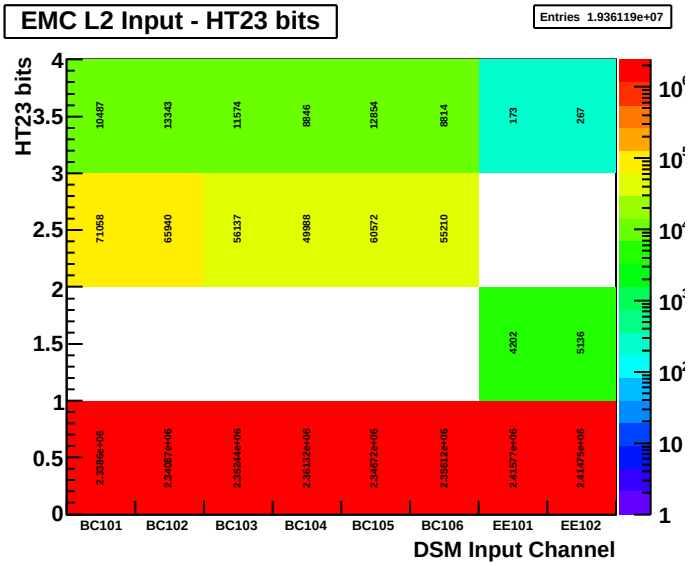

EMC L2 Input - JPZ/JPC bits
Entries 123
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

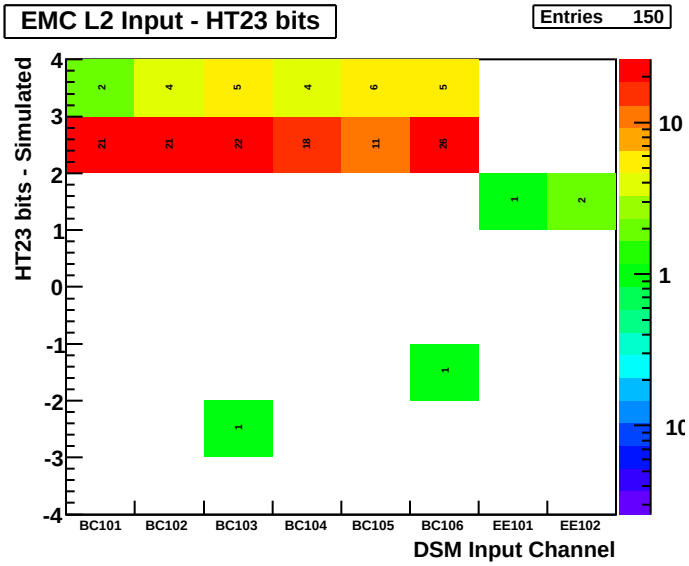

EMC L2 Input - Partial JP Sum
Entries 1.936119e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

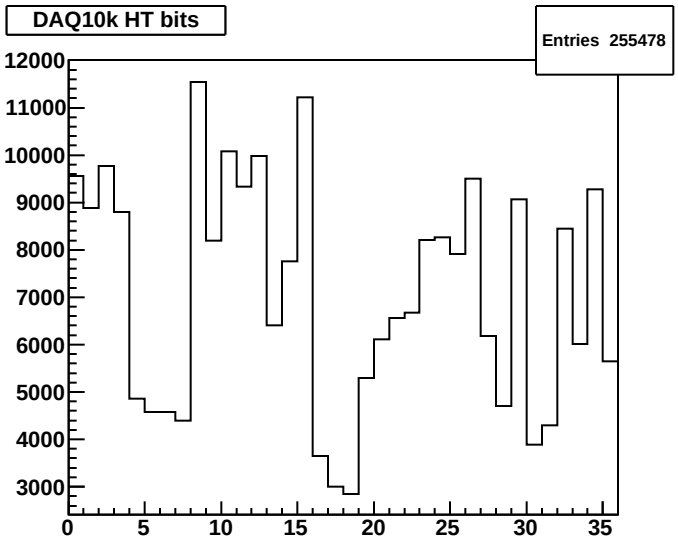

EMC L2 Input - Partial JP Sum
Entries 3238
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

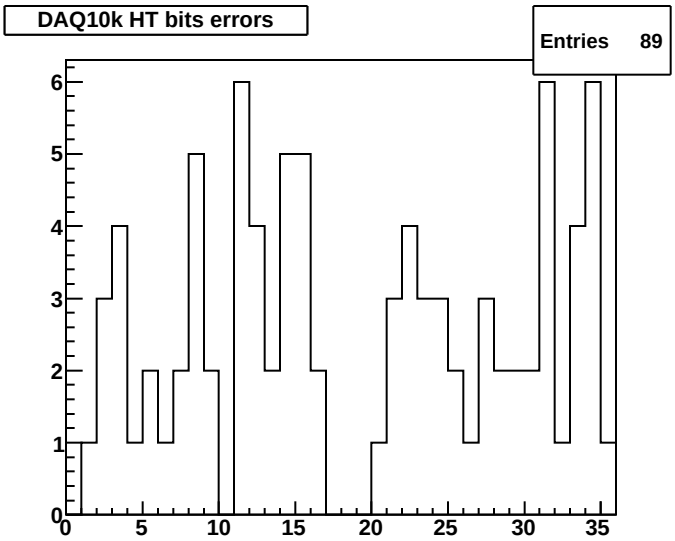

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.936119e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

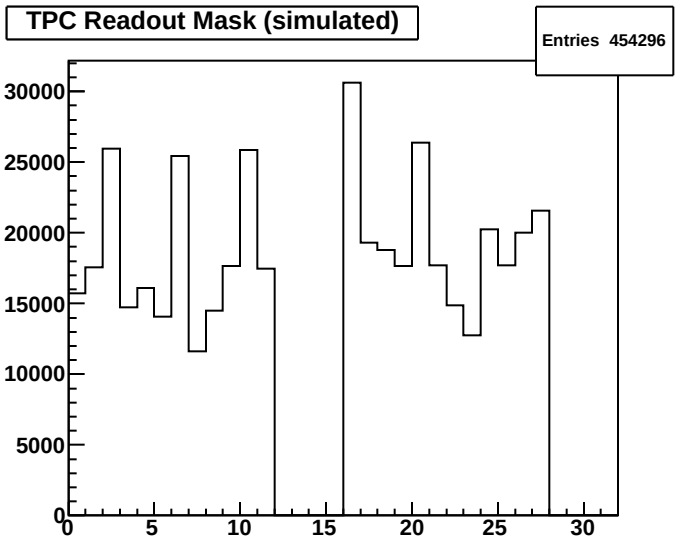

EMC L2 Input - HT01 bits/Partial JP ID
Entries 683
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

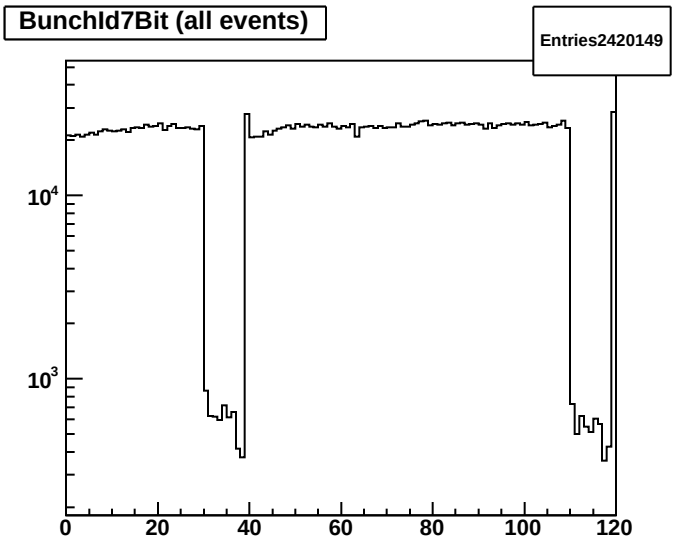

EMC L2 Input - HT23 bits
Entries 1.936119e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

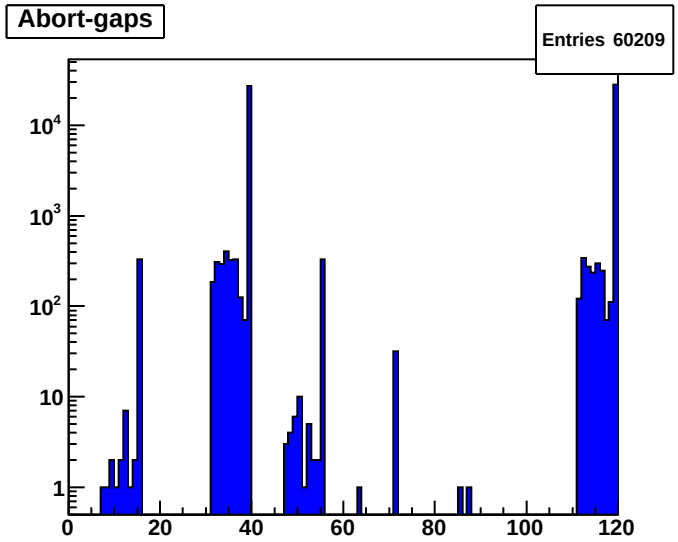

EMC L2 Input - HT23 bits
Entries 150
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

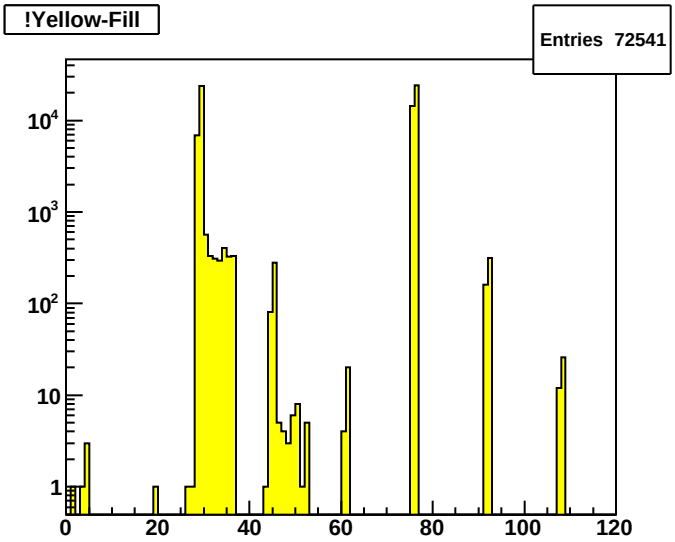

DAQ10k HT bits
Entries 255478

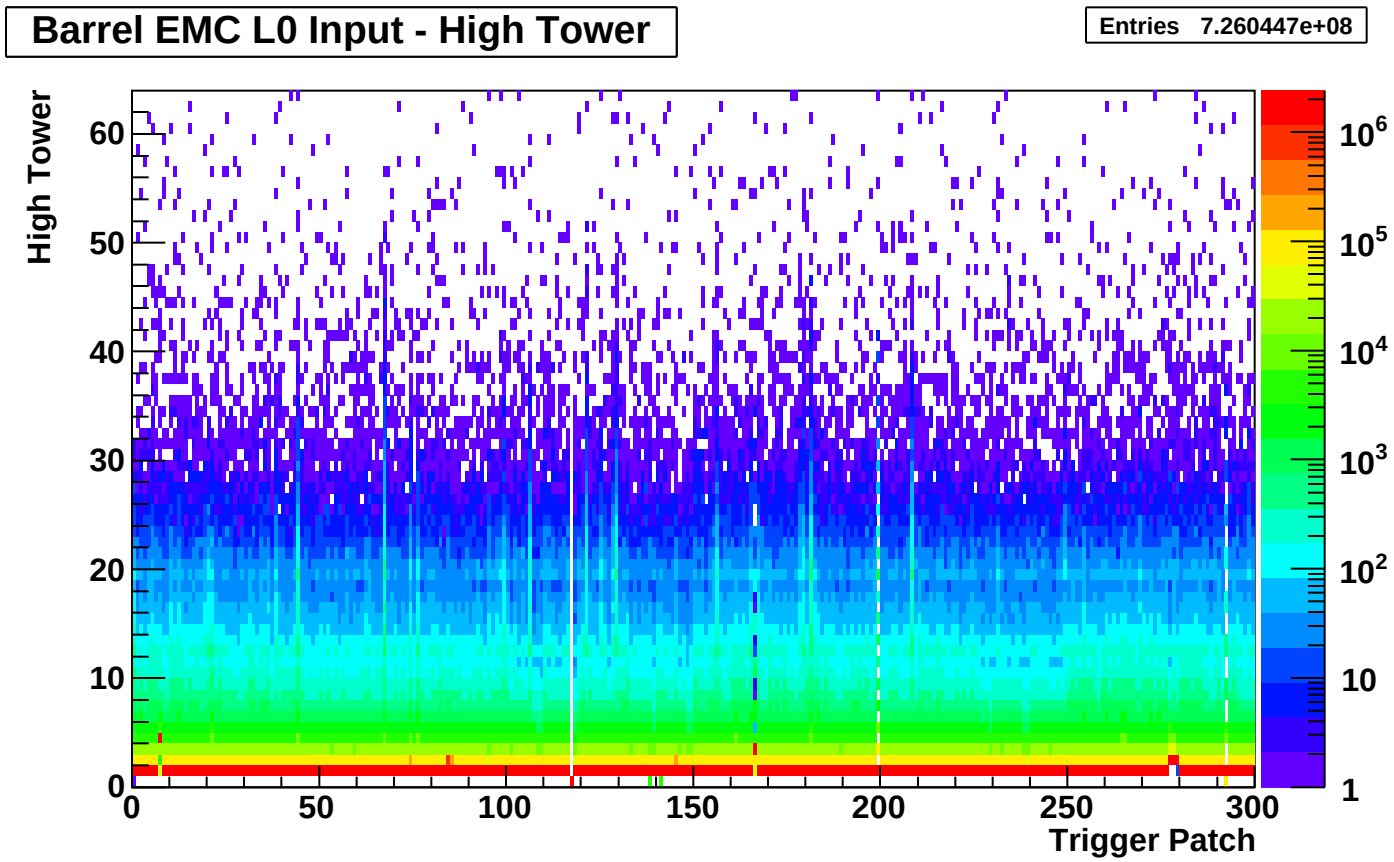

DAQ10k HT bits errors
Entries 89

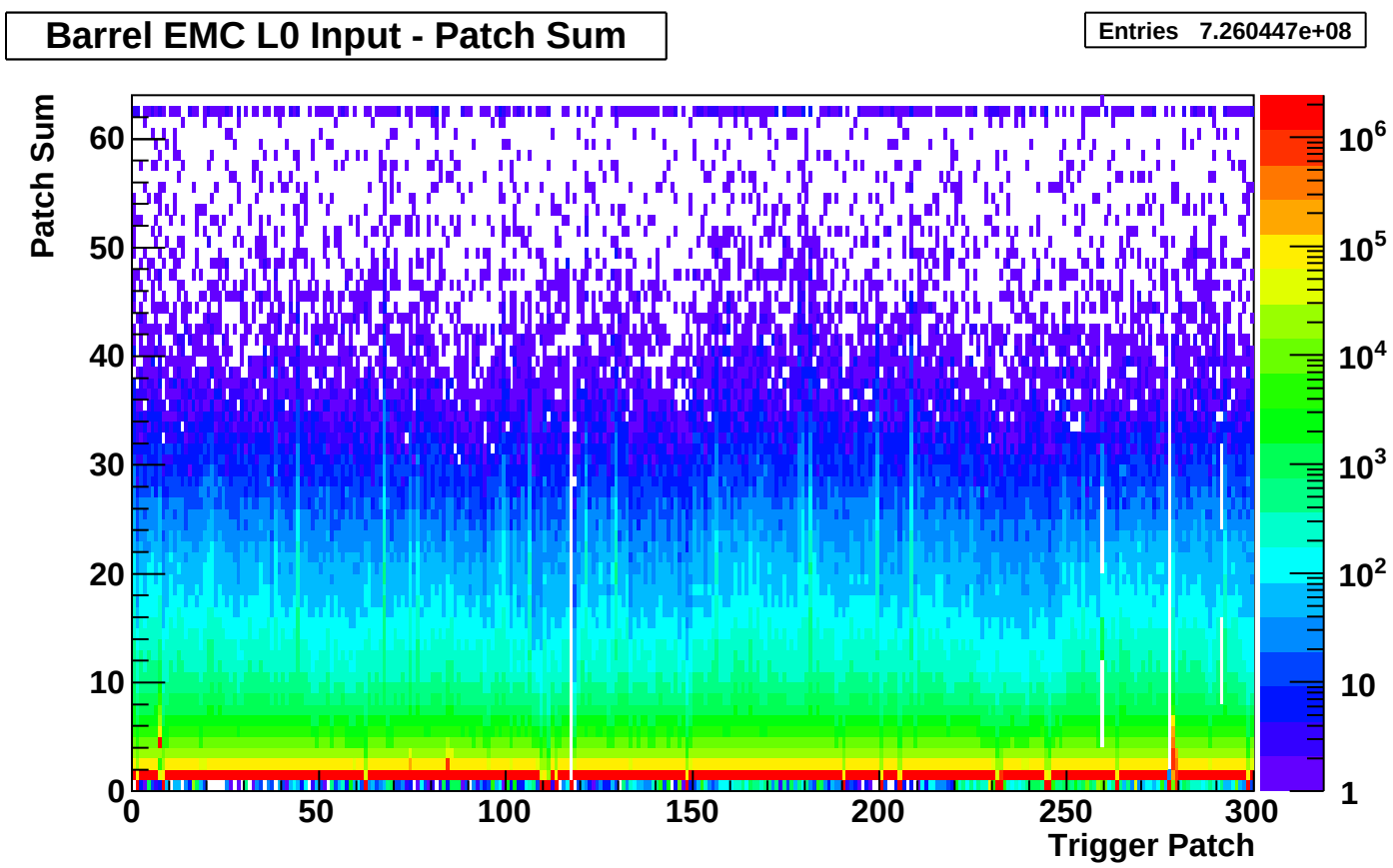

TPC Readout Mask (simulated)
Entries 454296

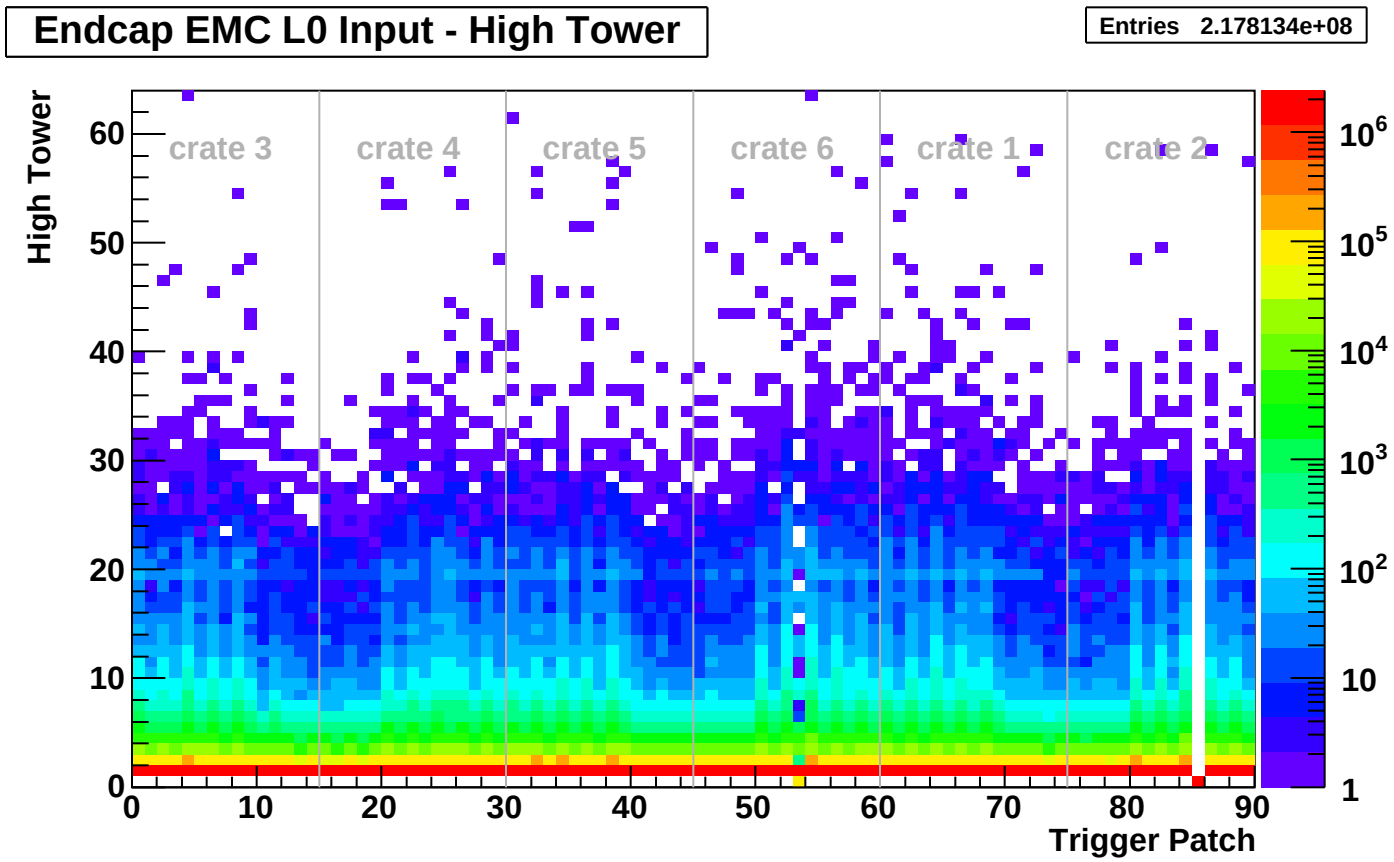

BunchId7Bit (all events)
Entries2420149

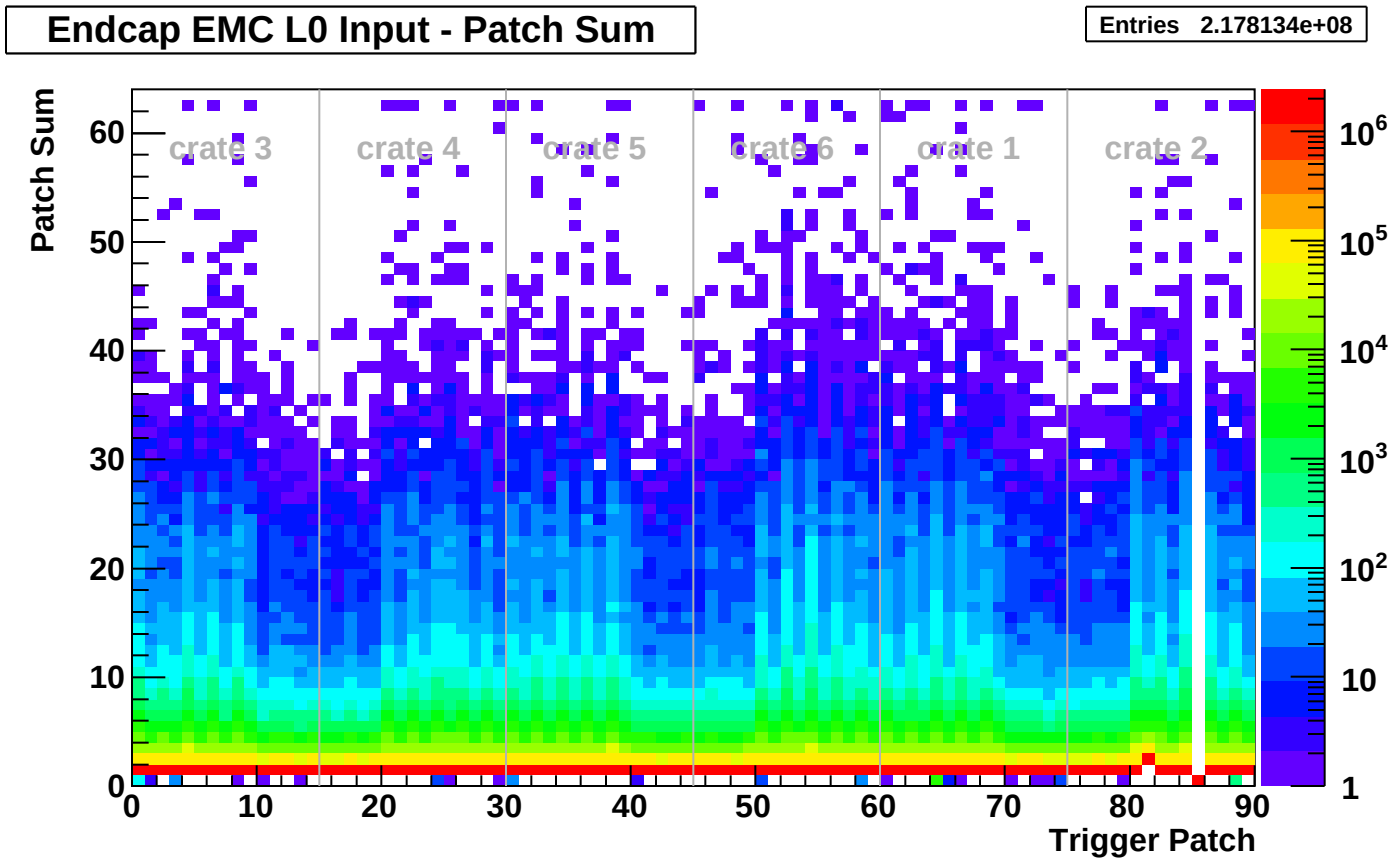

Abort-gaps
Entries 60209

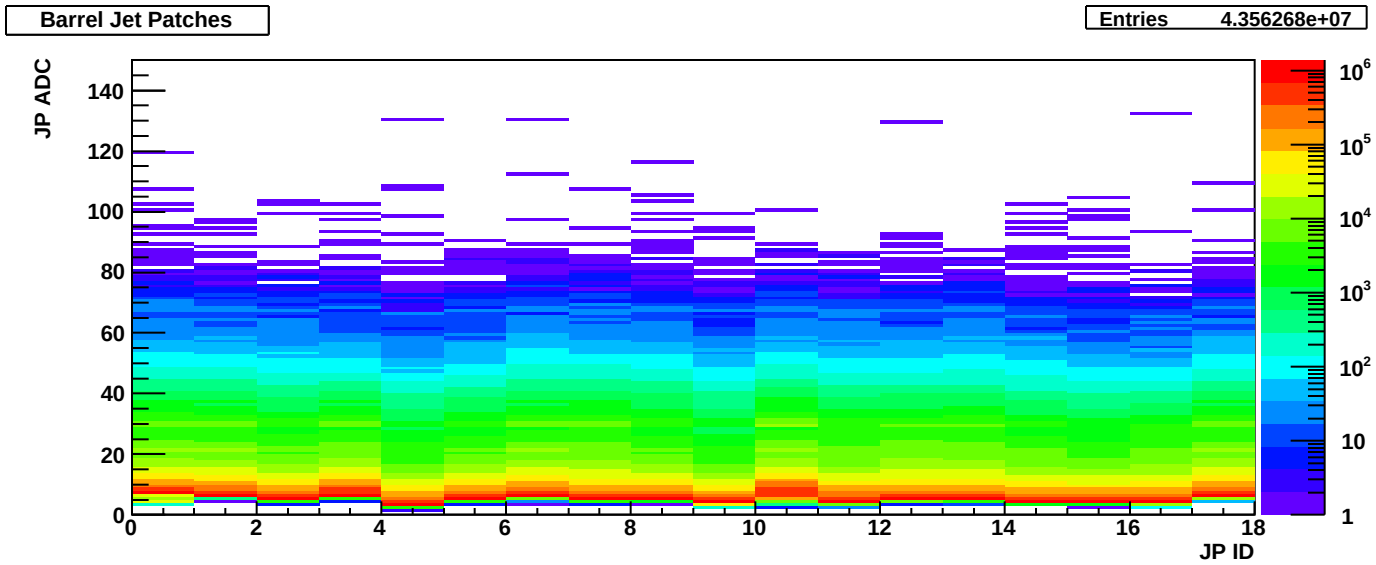

!Yellow-Fill
Entries 72541

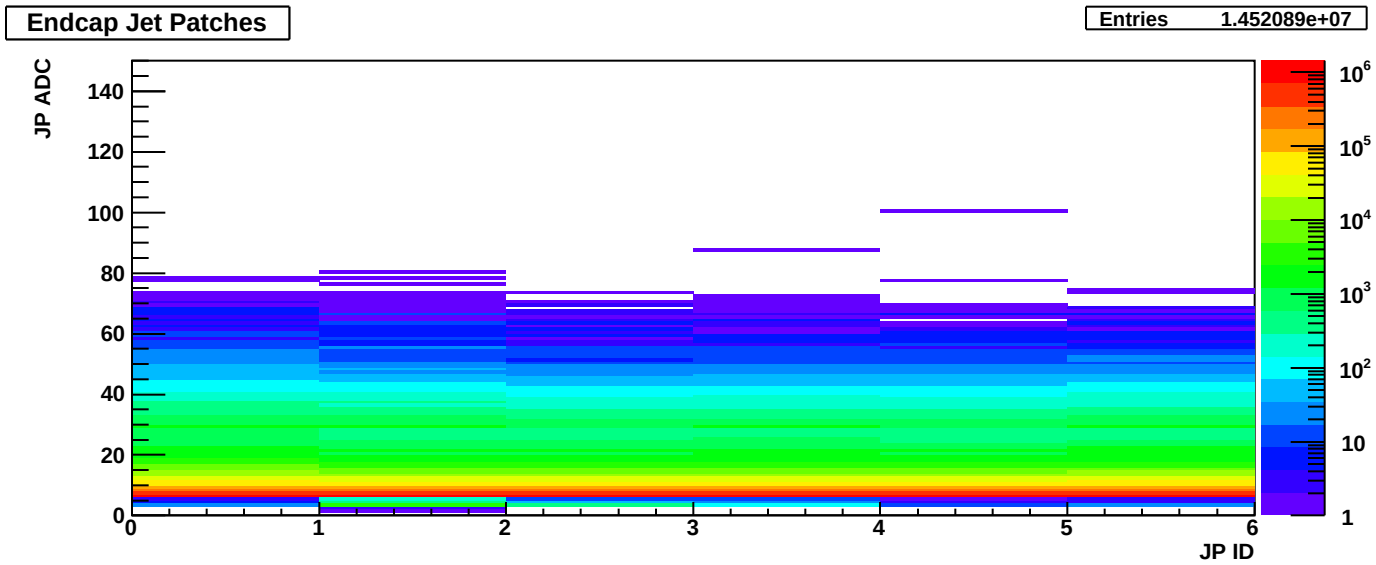

Barrel EMC L0 Input - High Tower
Entries 7.260447e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

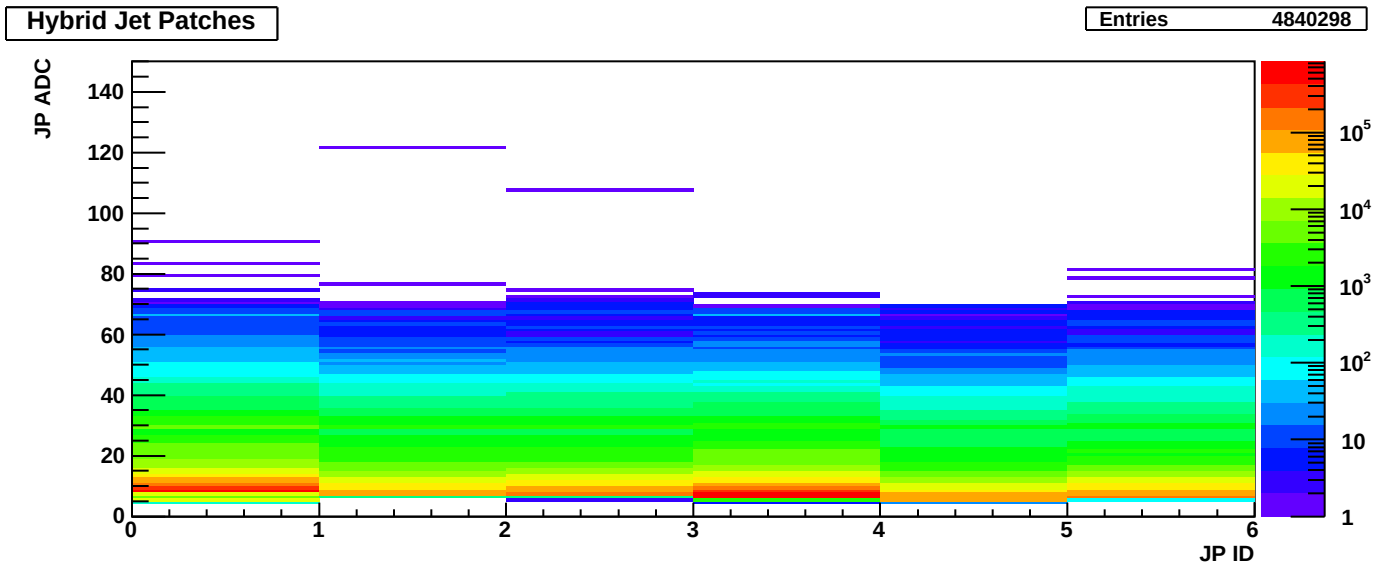

Barrel EMC L0 Input - Patch Sum
Entries 7.260447e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

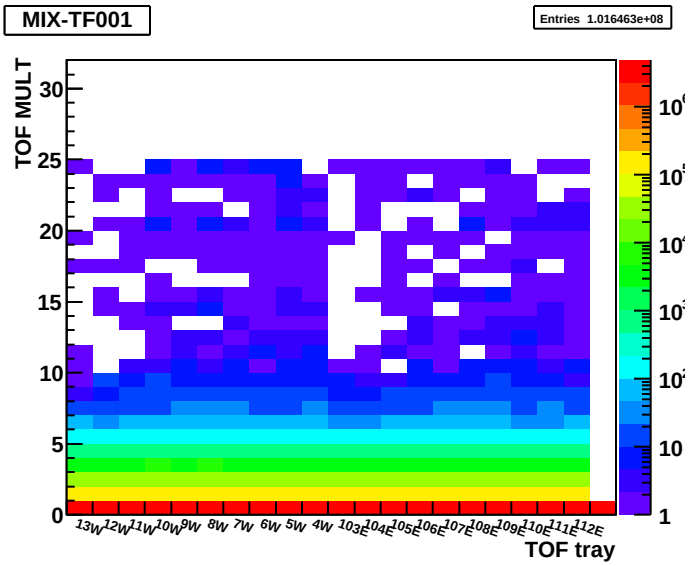

Endcap EMC L0 Input - High Tower
Entries 2.178134e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

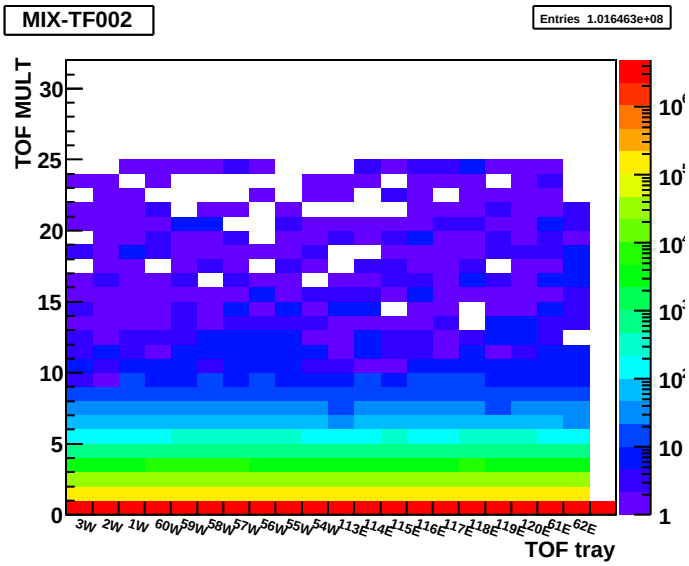

Endcap EMC L0 Input - Patch Sum
Entries 2.178134e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

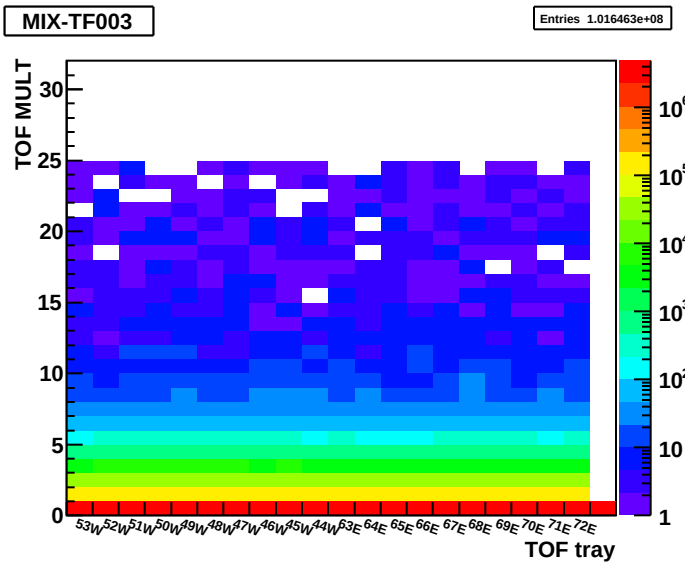

Barrel Jet Patches
Entries 4.356268e+07
JP ADC
JP ID

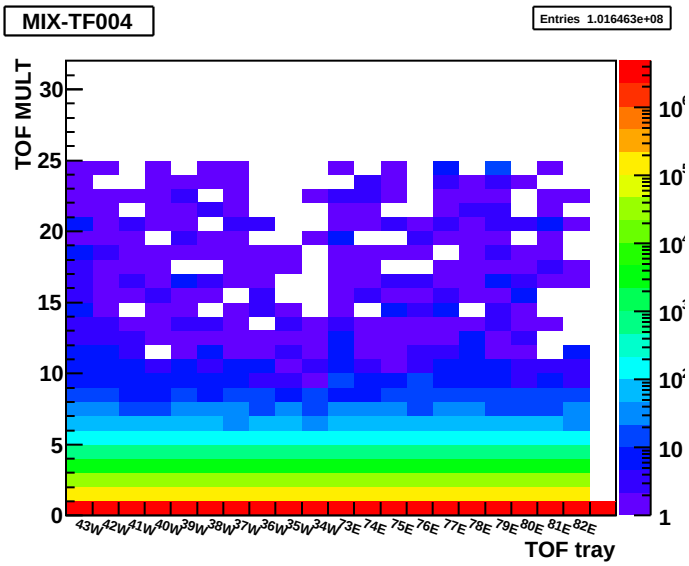

Endcap Jet Patches
Entries 1.452089e+07
JP ADC
JP ID

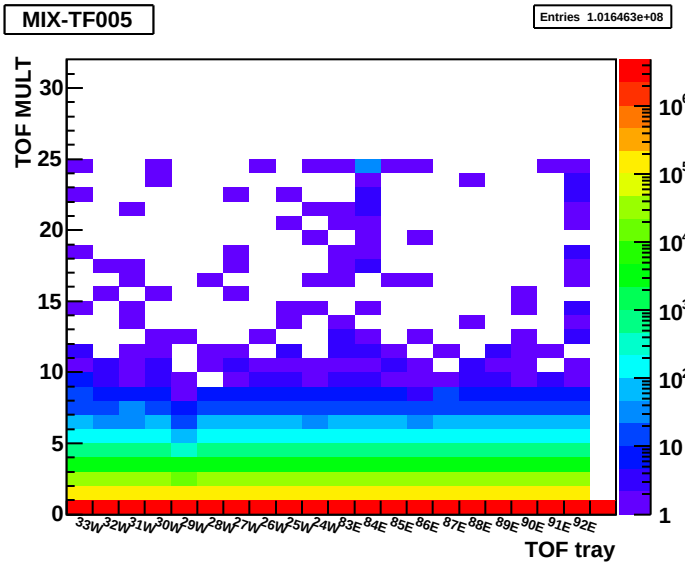

Hybrid Jet Patches
Entries 4840298
JP ADC
JP ID

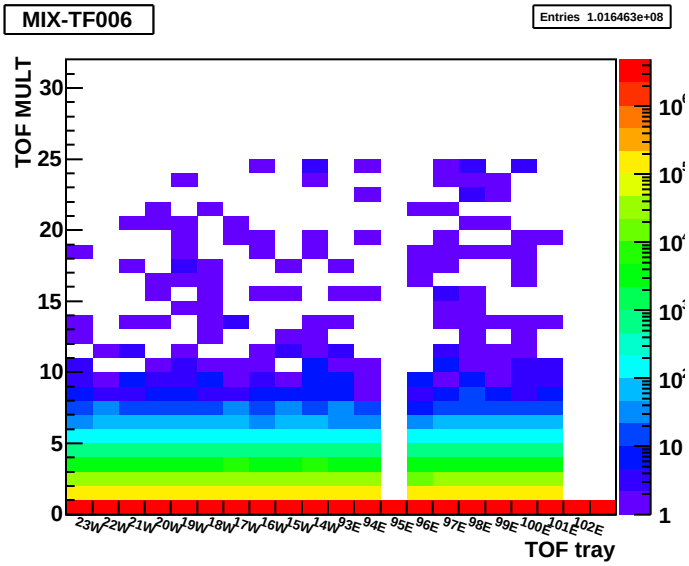

MIX-TF001
Entries 1.016463e+08
TOF MULT
TOF tray

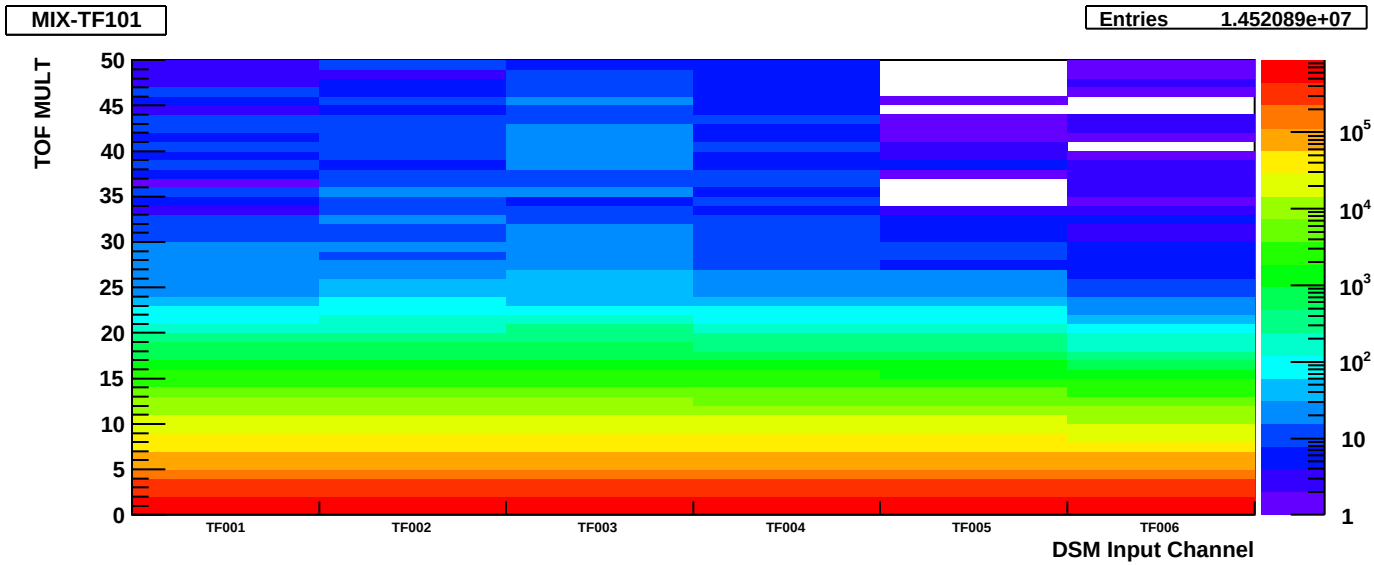

MIX-TF002
Entries 1.016463e+08
TOF MULT
TOF tray

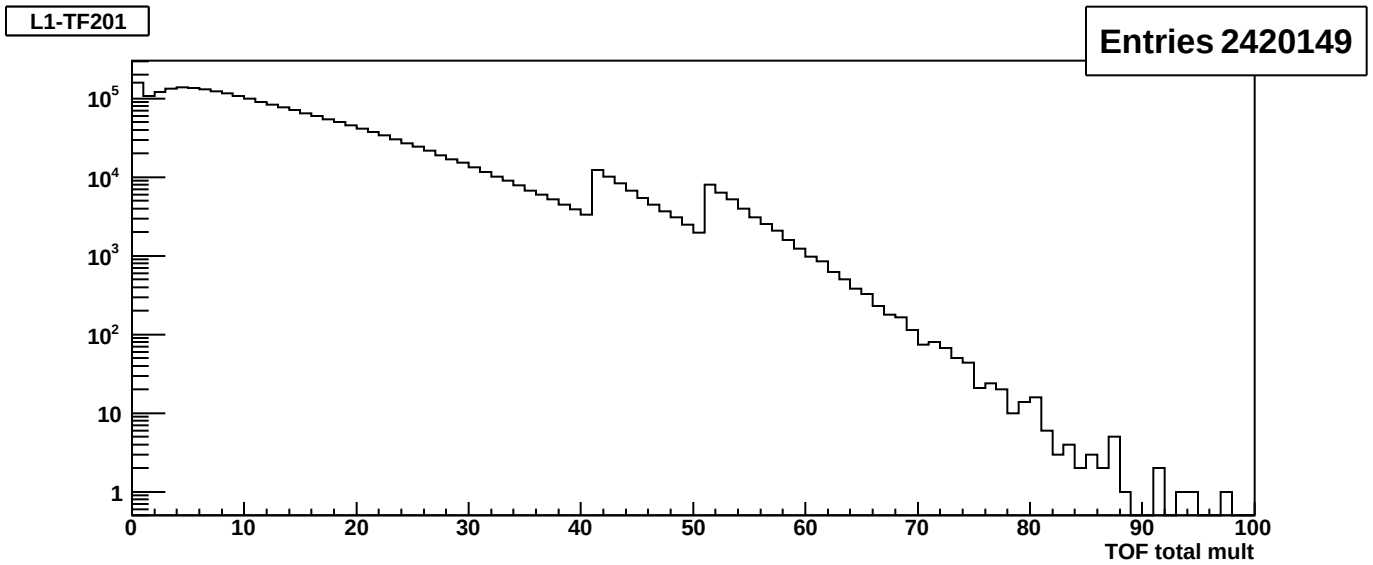

MIX-TF003
Entries 1.016463e+08
TOF MULT
TOF tray

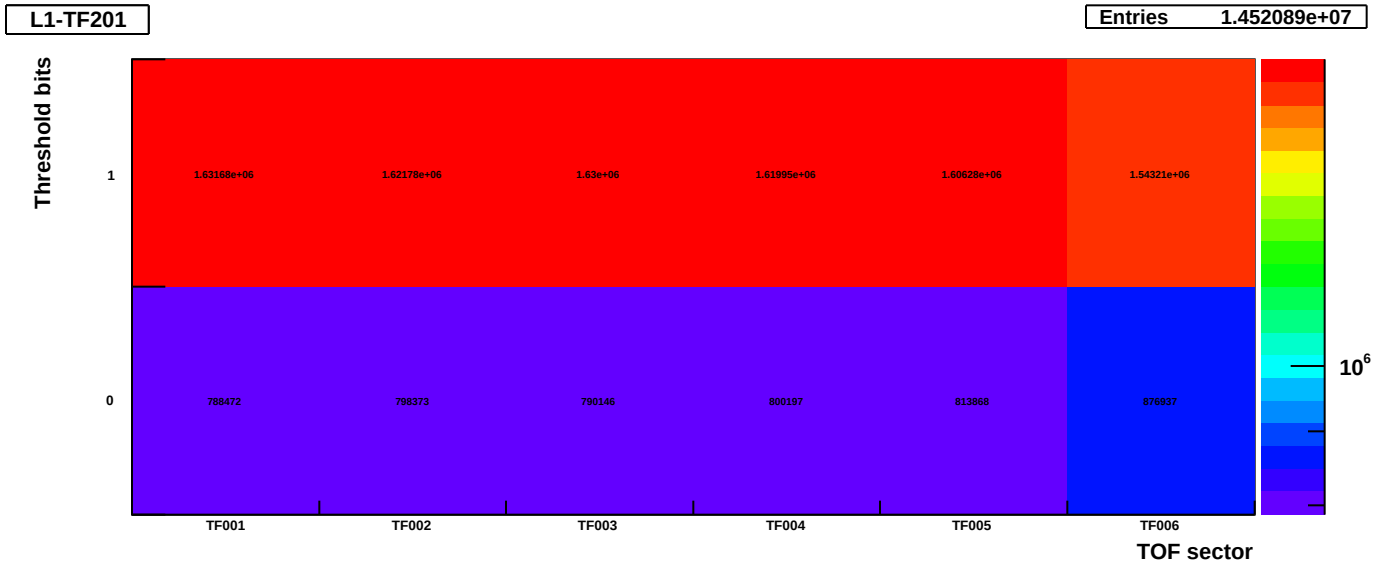

MIX-TF004
Entries 1.016463e+08
TOF MULT
TOF tray

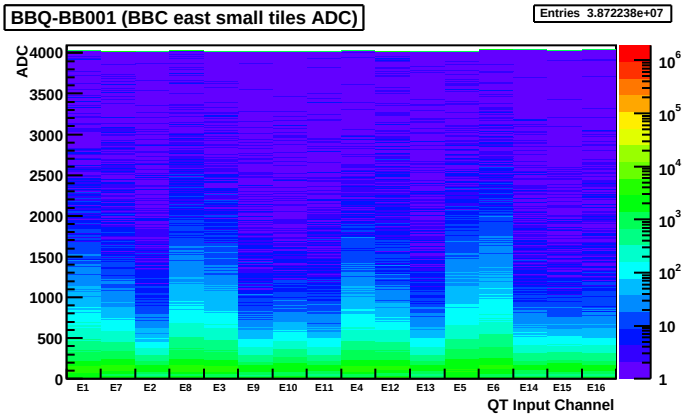

MIX-TF005
Entries 1.016463e+08
TOF MULT
TOF tray

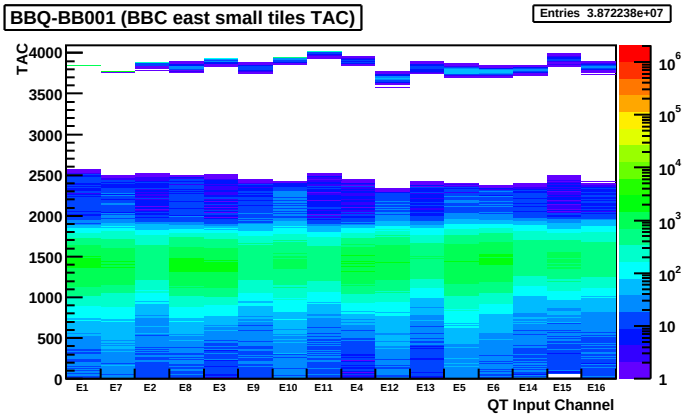

MIX-TF006
Entries 1.016463e+08
TOF MULT
TOF tray

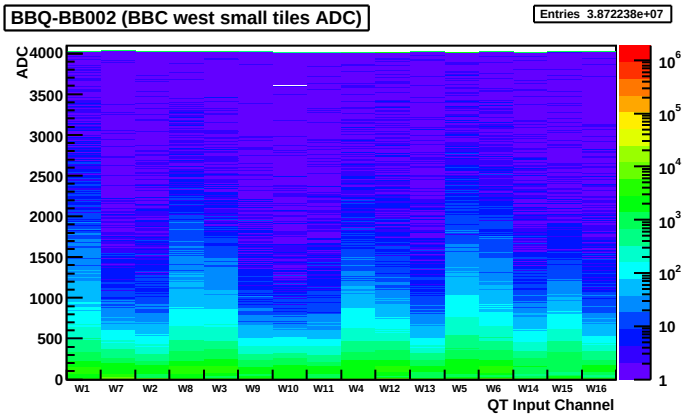

MIX-TF101
Entries 1.452089e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

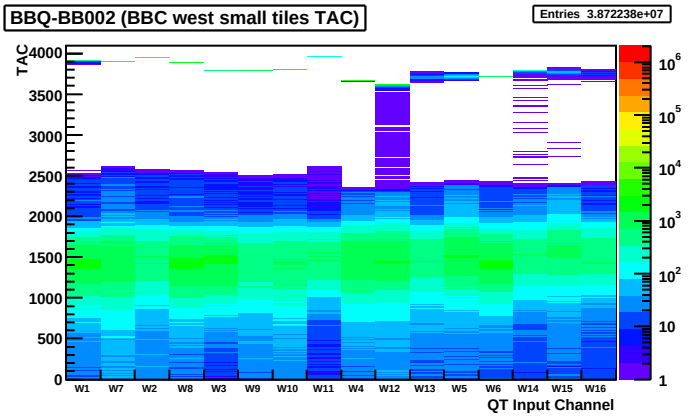

L1-TF201
Entries 2420149
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

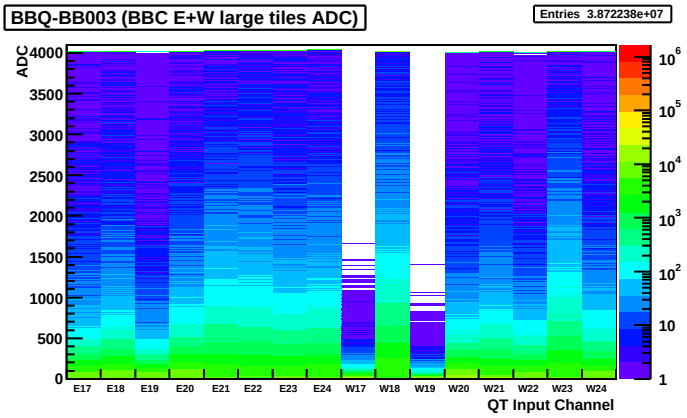

L1-TF201
Entries 1.452089e+07
Threshold bits
1
0
1.63168e+06
1.62178e+06
1.63e+06
1.61995e+06
1.60628e+06
1.54321e+06
788472
798373
790146
800197
813868
876937
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

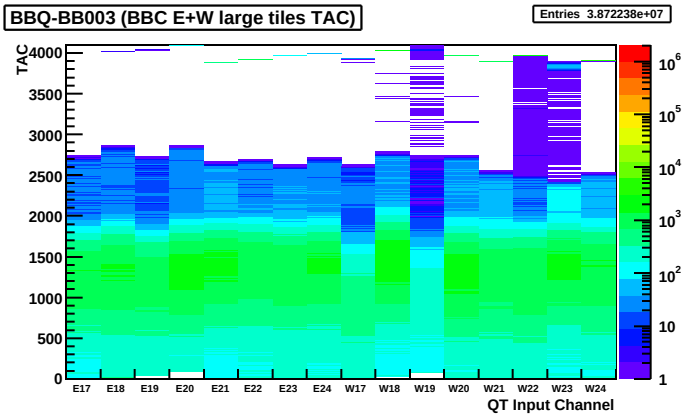

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.872238e+07
ADC
QT Input Channel

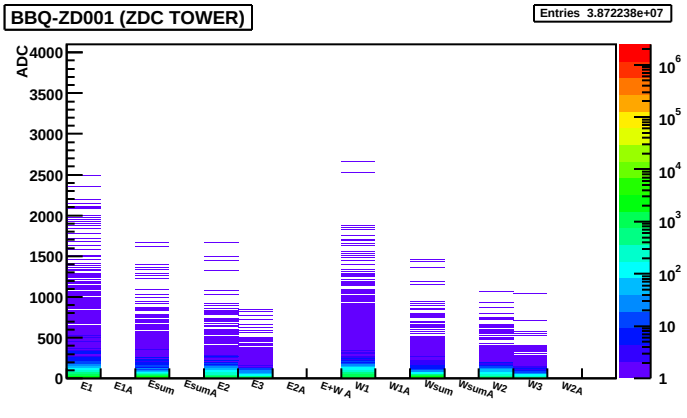

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.872238e+07
TAC
QT Input Channel

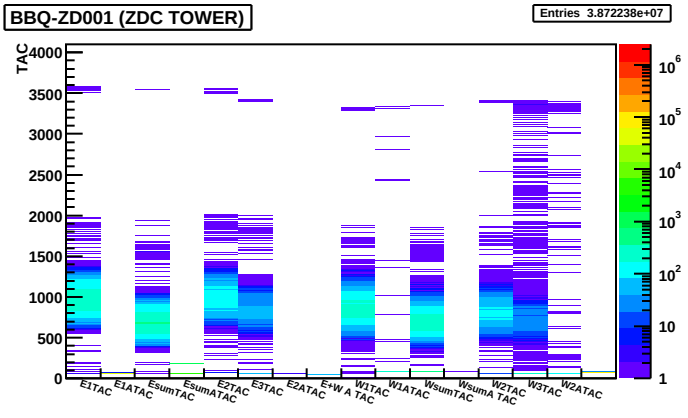

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.872238e+07
ADC
QT Input Channel

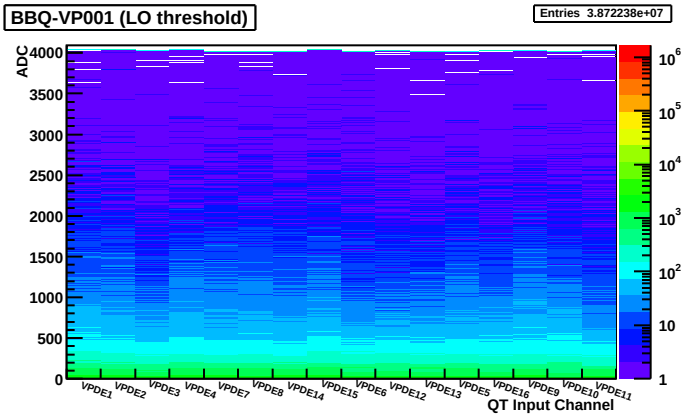

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.872238e+07
TAC
QT Input Channel

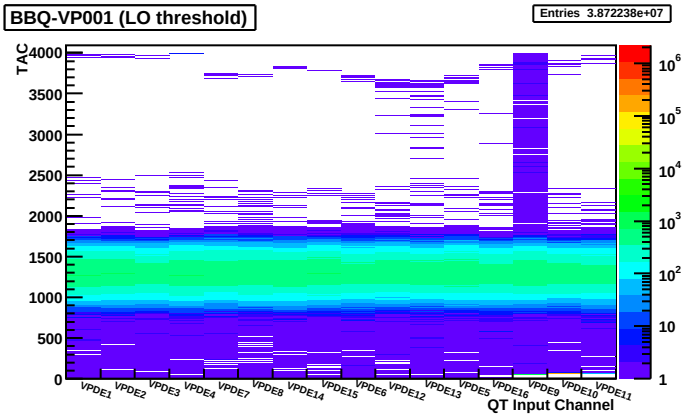

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.872238e+07
ADC
QT Input Channel

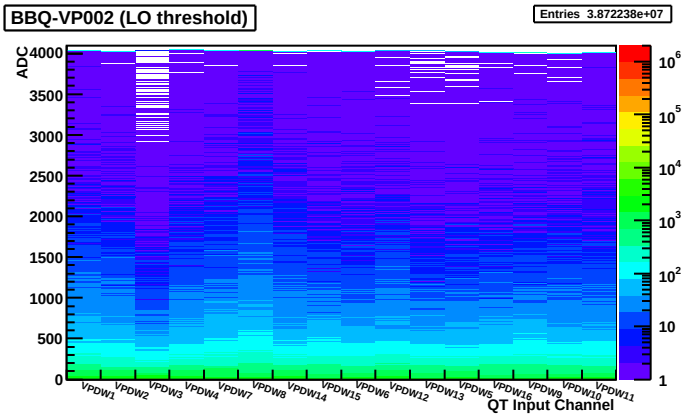

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.872238e+07
TAC
QT Input Channel

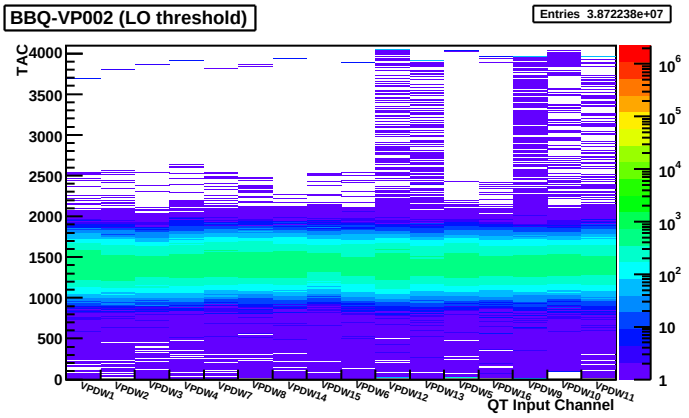

BBQ-ZD001 (ZDC TOWER)
Entries 3.872238e+07
ADC

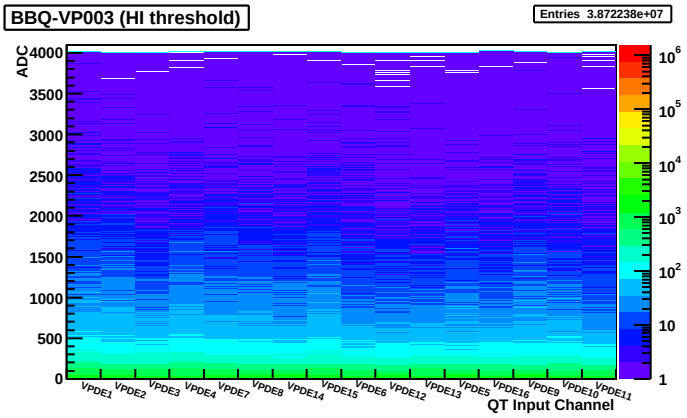

BBQ-ZD001 (ZDC TOWER)
Entries 3.872238e+07
TAC

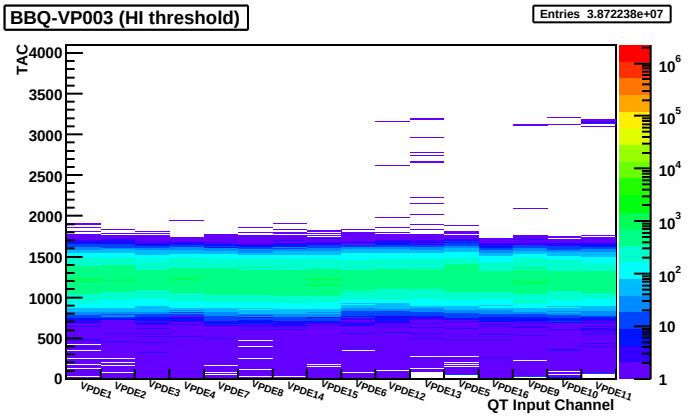

BBQ-VP001 (LO threshold)
Entries 3.872238e+07
ADC
QT Input Channel

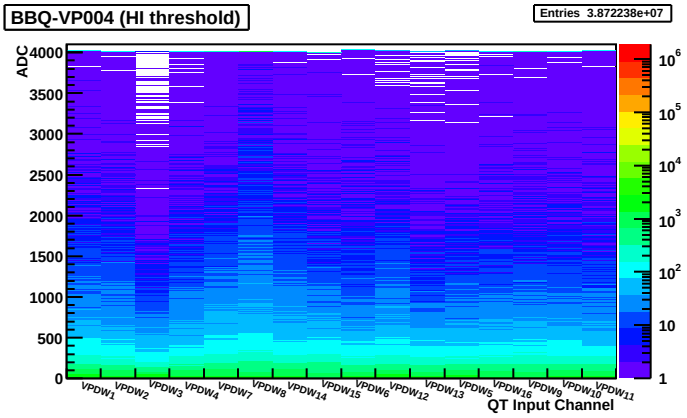

BBQ-VP001 (LO threshold)
Entries 3.872238e+07
TAC
QT Input Channel

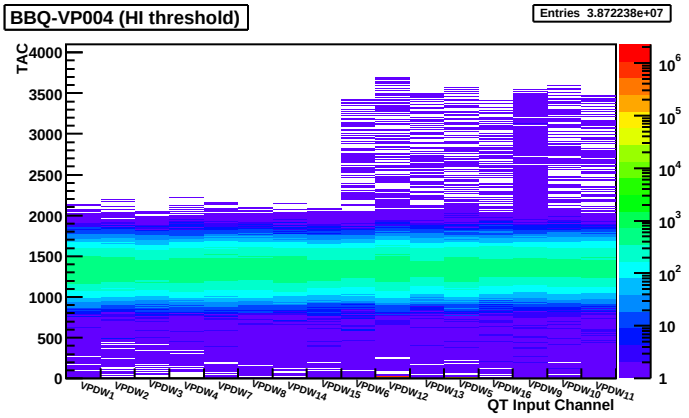

BBQ-VP002 (LO threshold)
Entries 3.872238e+07
ADC
QT Input Channel

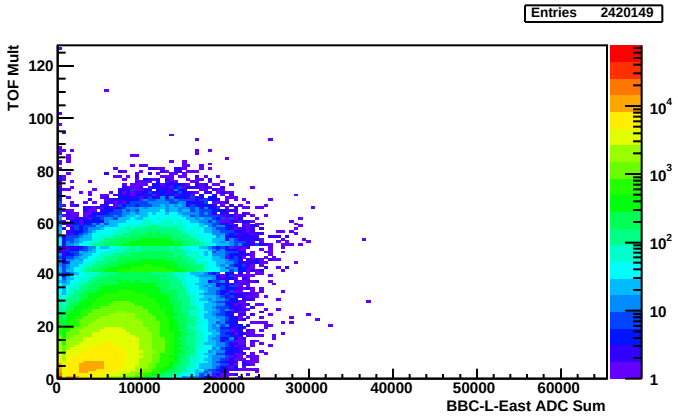

BBQ-VP002 (LO threshold)
Entries 3.872238e+07
TAC
QT Input Channel

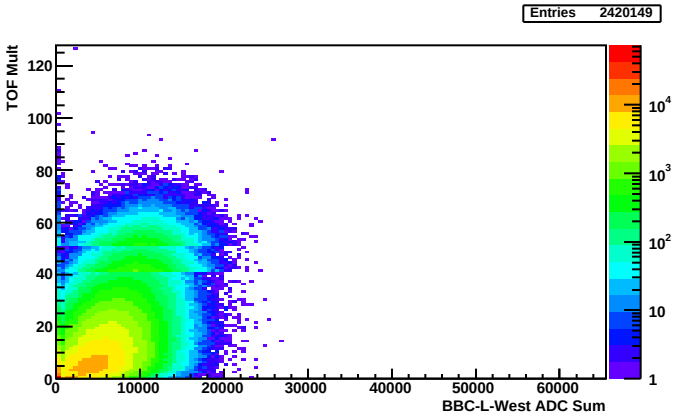

BBQ-VP003 (HI threshold)
Entries 3.872238e+07
ADC
QT Input Channel

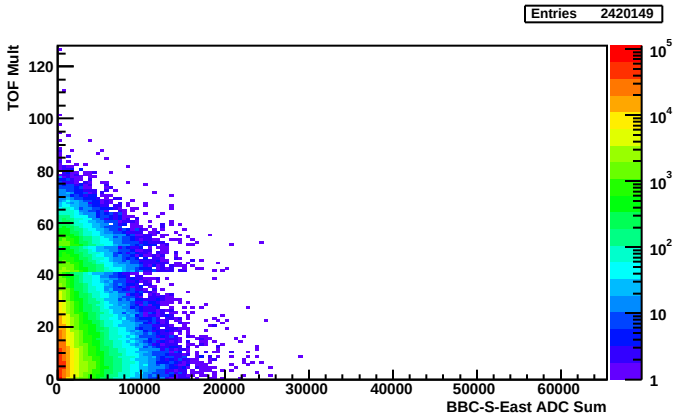

BBQ-VP003 (HI threshold)
Entries 3.872238e+07
TAC
QT Input Channel

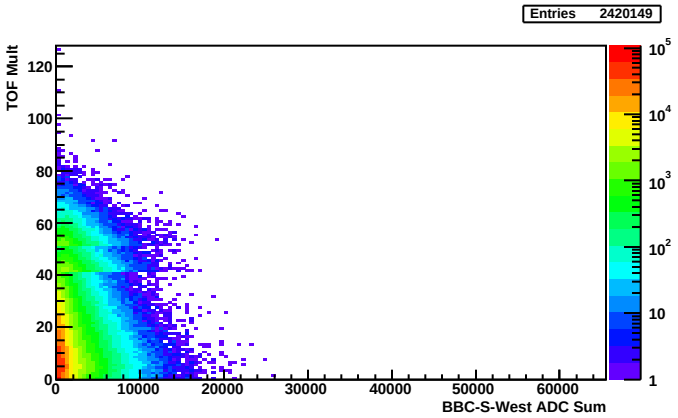

BBQ-VP004 (HI threshold)
Entries 3.872238e+07
ADC
QT Input Channel

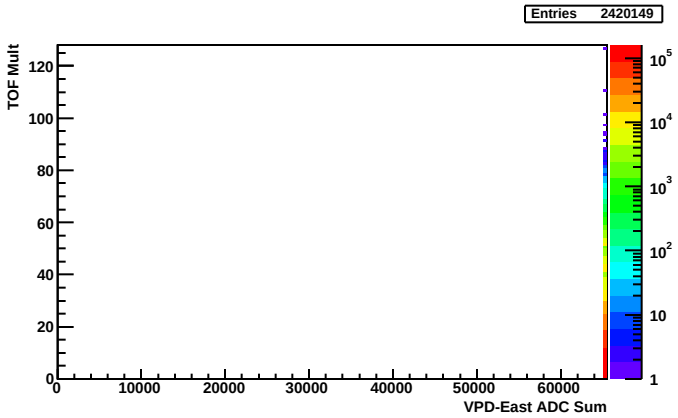

BBQ-VP004 (HI threshold)
Entries 3.872238e+07
TAC
QT Input Channel

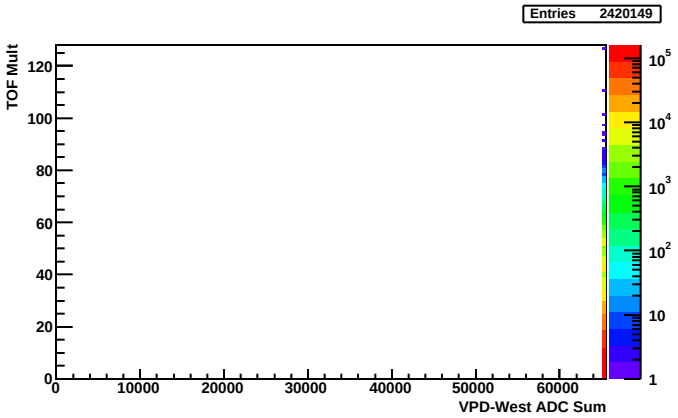

Entries 2420149
TOF Mult
BBC-L-East ADC Sum

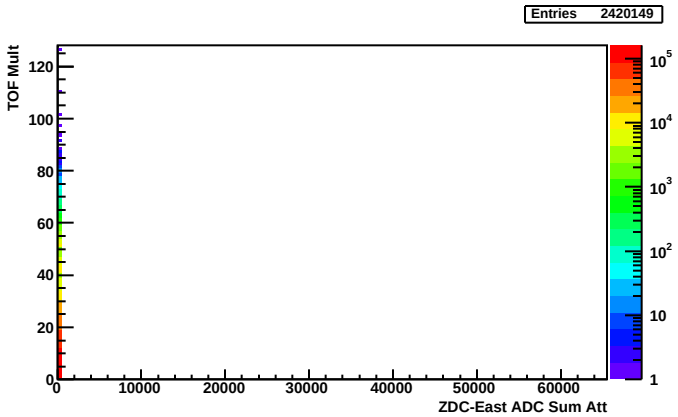

Entries 2420149
TOF Mult
BBC-L-West ADC Sum

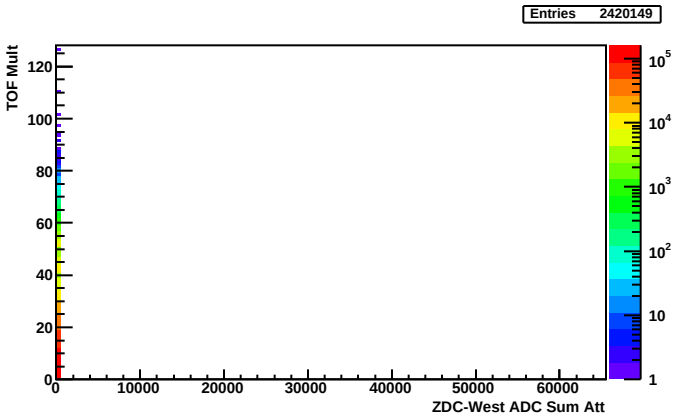

Entries 2420149
TOF Mult
BBC-S-East ADC Sum

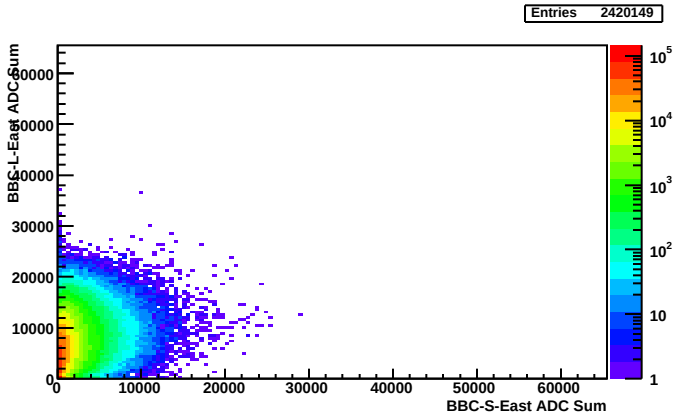

Entries 2420149
TOF Mult
BBC-S-West ADC Sum

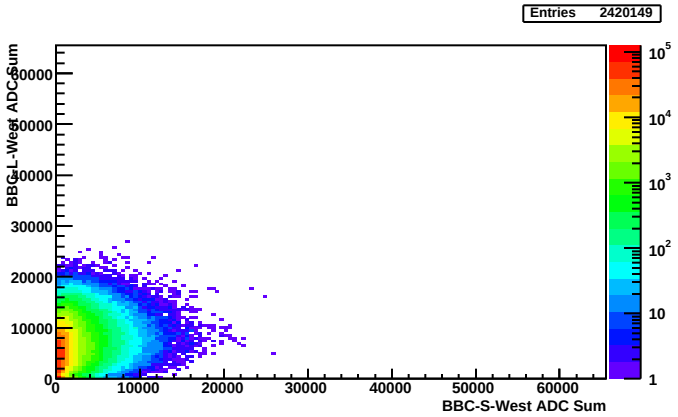

Entries 2420149
TOF Mult
VPD-East ADC Sum

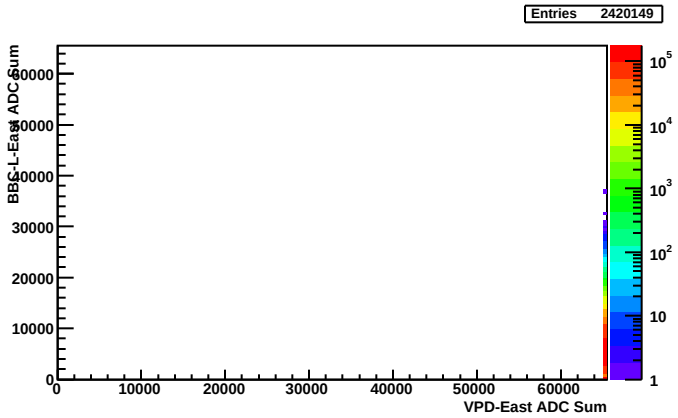

Entries 2420149
TOF Mult
VPD-West ADC Sum

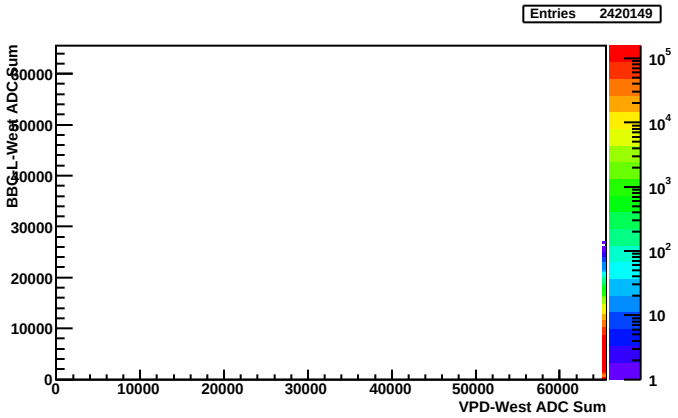

Entries 2420149
TOF Mult
ZDC-East ADC Sum Att

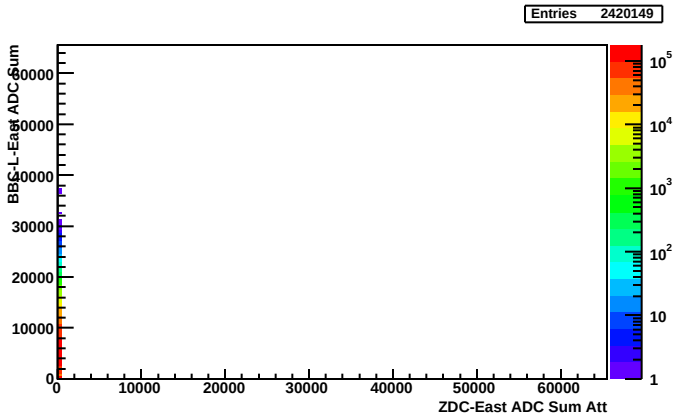

Entries 2420149
TOF Mult
ZDC-West ADC Sum Att

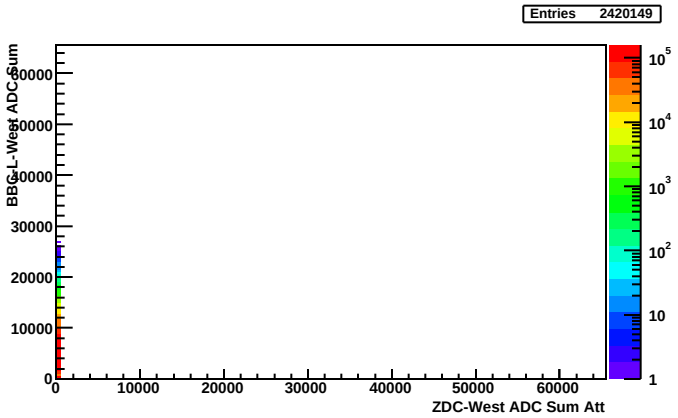

Entries 2420149
BBC-L-East ADC Sum
BBC-S-East ADC Sum

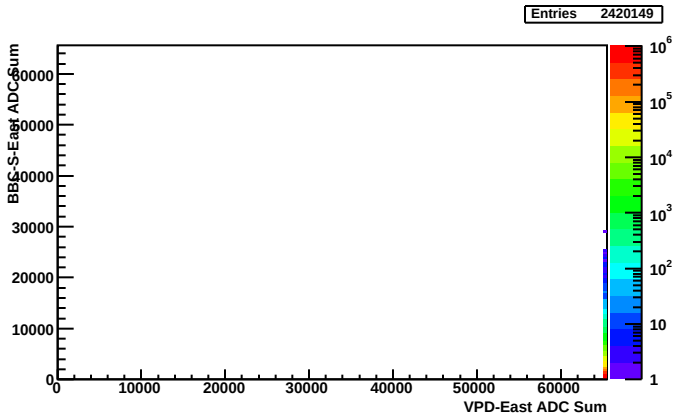

Entries 2420149
BBC-L-West ADC Sum
BBC-S-West ADC Sum

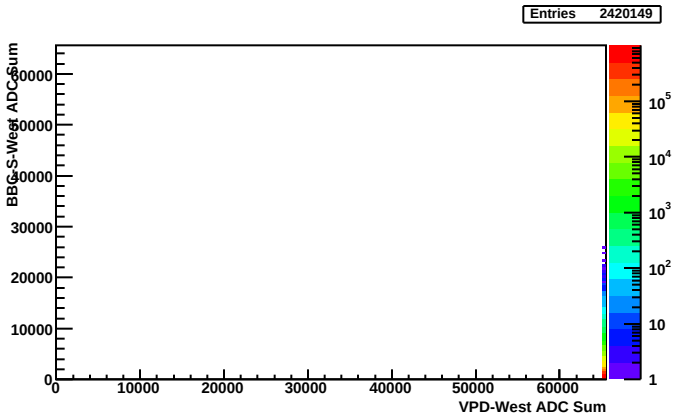

Entries 2420149
BBC-L-East ADC Sum
VPD-East ADC Sum

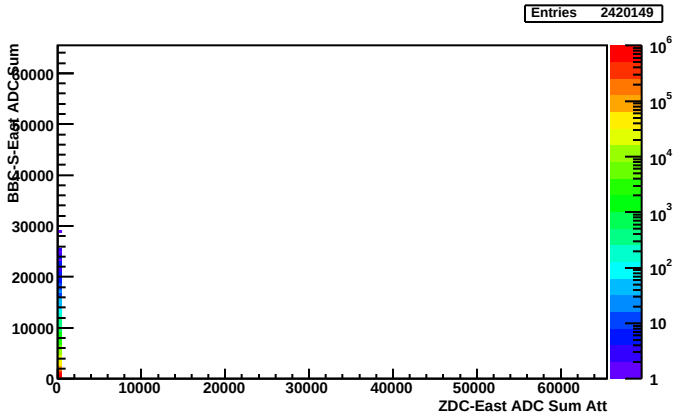

Entries 2420149
BBC-L-West ADC Sum
VPD-West ADC Sum

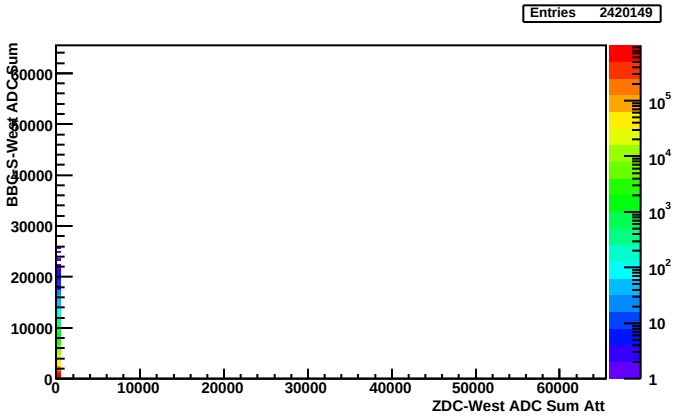

Entries 2420149
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

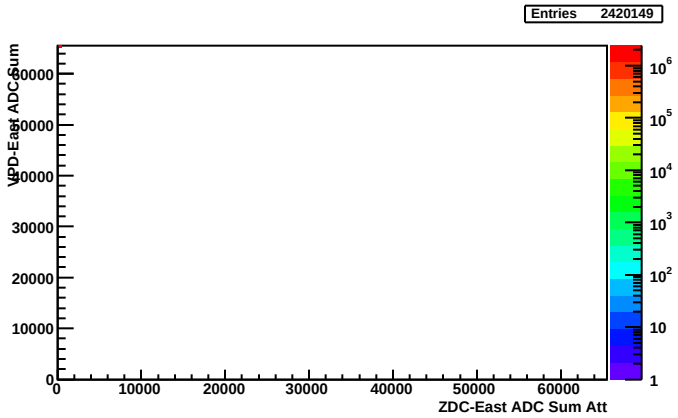

Entries 2420149
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

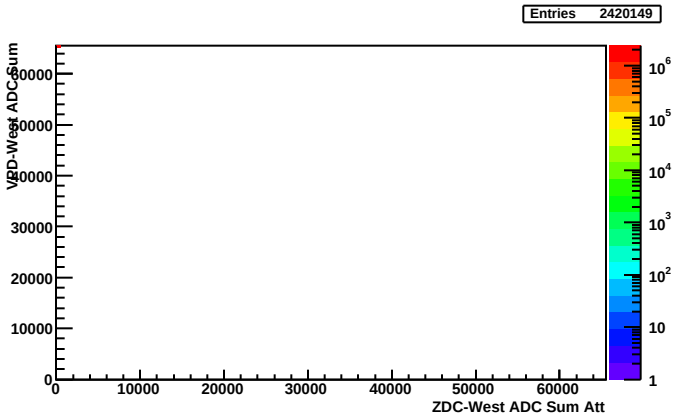

Entries 2420149
BBC-S-East ADC Sum
VPD-East ADC Sum

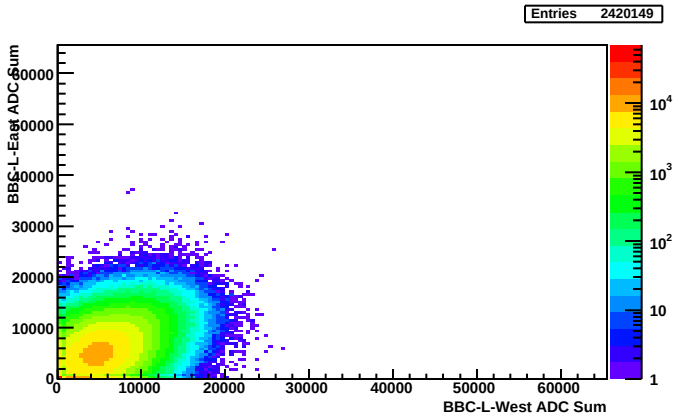

Entries 2420149
BBC-S-West ADC Sum
VPD-West ADC Sum

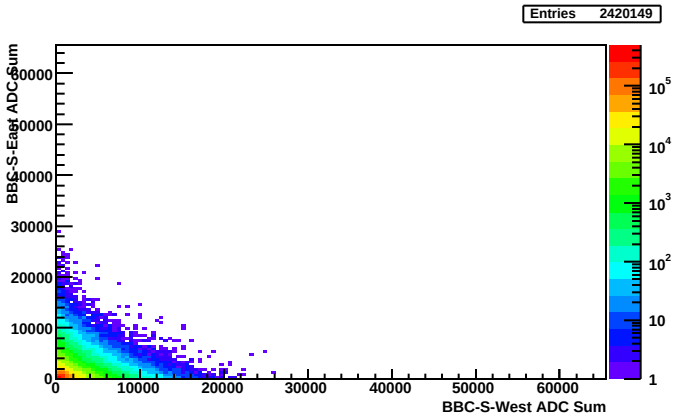

Entries 2420149
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

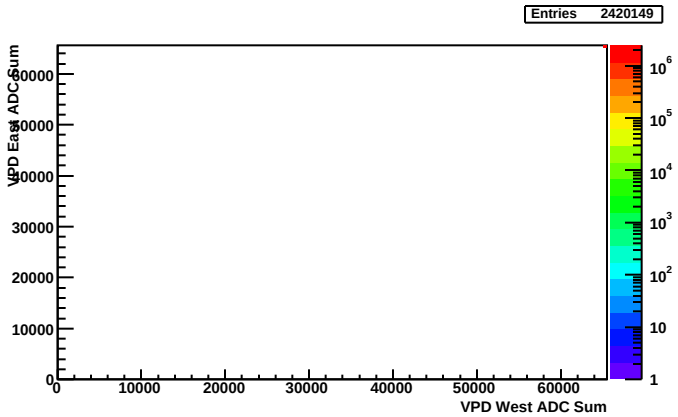

Entries 2420149
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

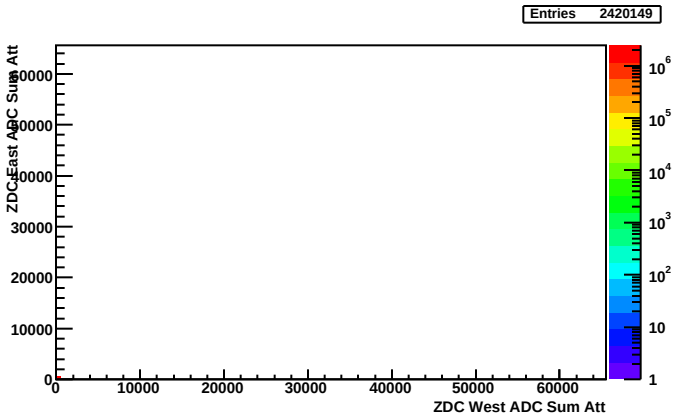

Entries 2420149
VPD-East ADC Sum
ZDC-East ADC Sum Att

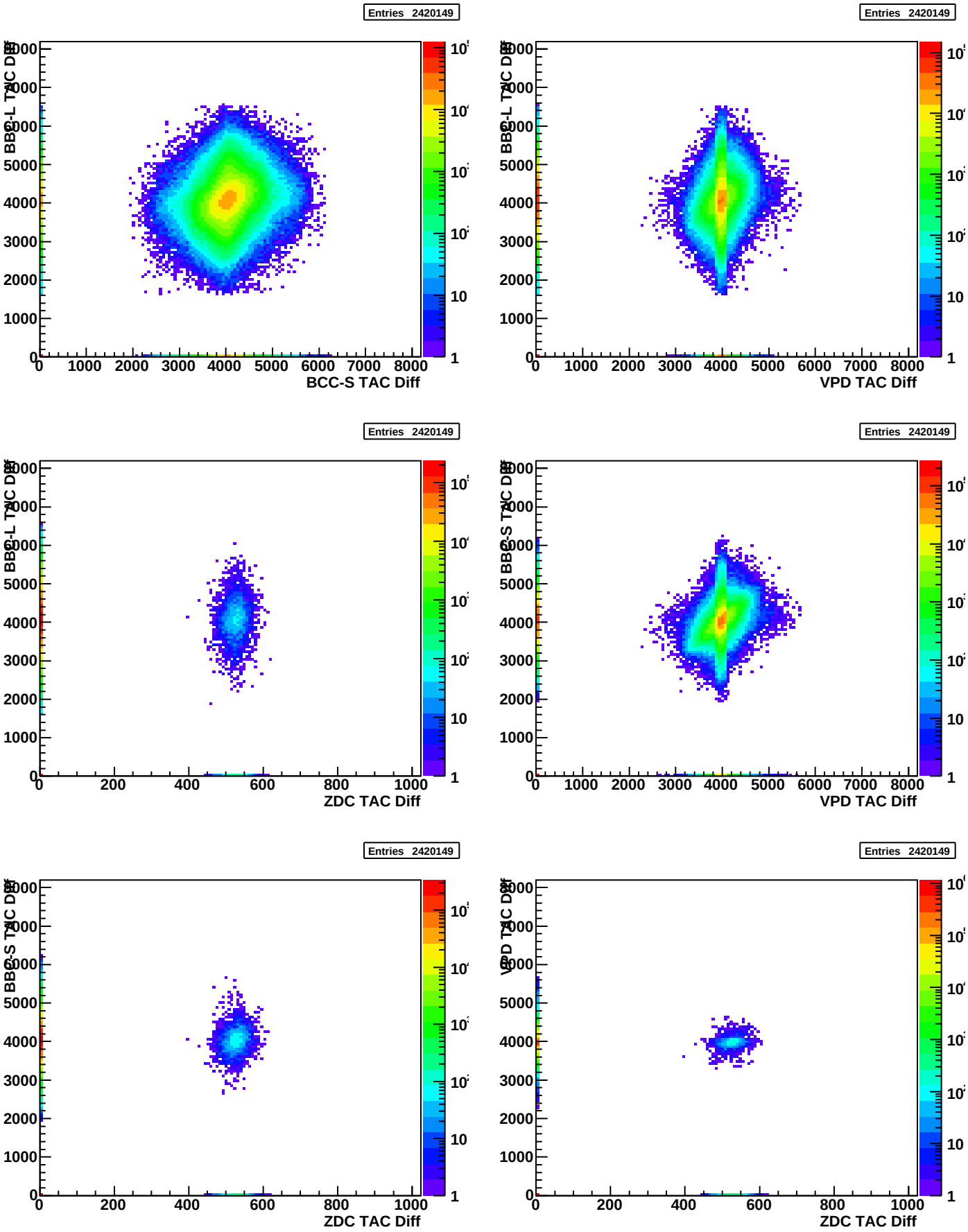

Entries 2420149
VPD-West ADC Sum
ZDC-West ADC Sum Att

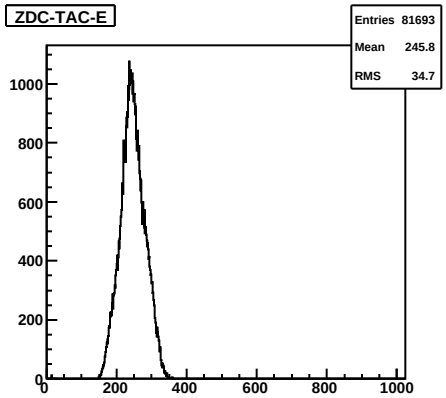

Entries 2420149
BBC-L-East ADC Sum
BBC-L-West ADC Sum

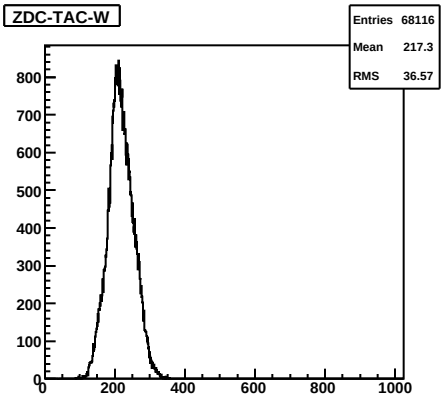

Entries 2420149
BBC-S-East ADC Sum
BBC-S-West ADC Sum

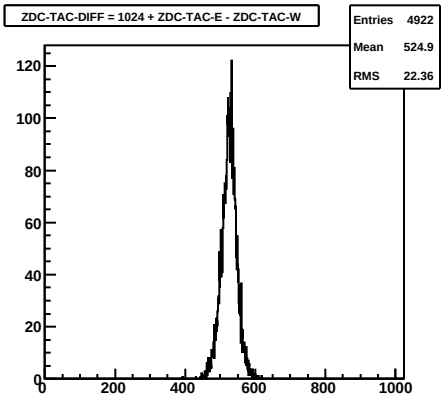

Entries 2420149
VPD East ADC Sum
VPD West ADC Sum

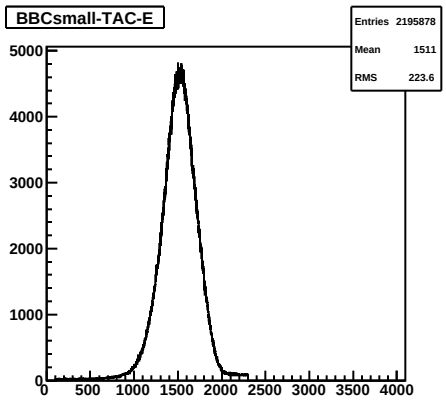

Entries 2420149
ZDC East ADC Sum Att
ZDC West ADC Sum Att

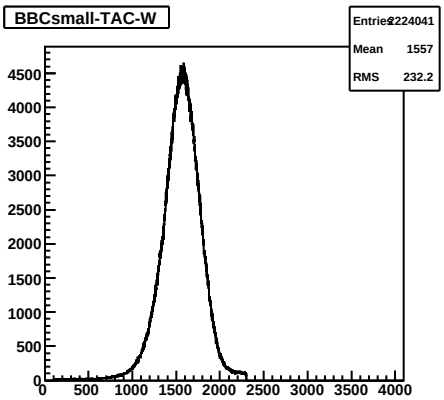

Entries 2420149
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2420149
BBC-L TAC Diff
VPD TAC Diff
Entries 2420149
BBC-L TAC Diff
ZDC TAC Diff
Entries 2420149
BBC-S TAC Diff
VPD TAC Diff
Entries 2420149
BBC-S TAC Diff
ZDC TAC Diff
Entries 2420149
VPD TAC Diff
ZDC TAC Diff

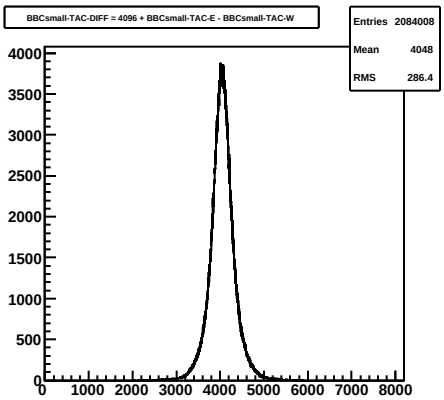

ZDC-TAC-E
Entries 81693
Mean 245.8
RMS 34.7

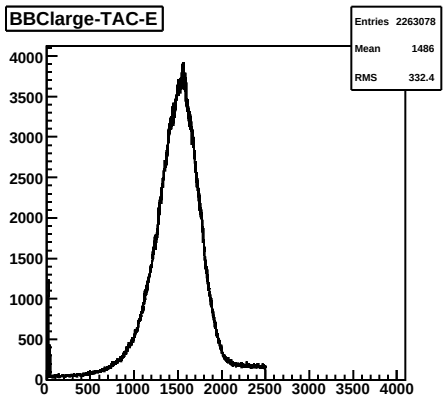

ZDC-TAC-W
Entries 68116
Mean 217.3
RMS 36.57

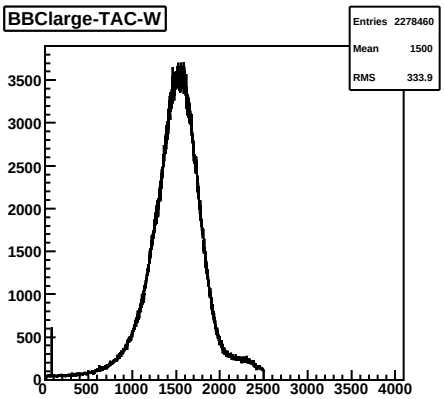

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4922
Mean 524.9
RMS 22.36

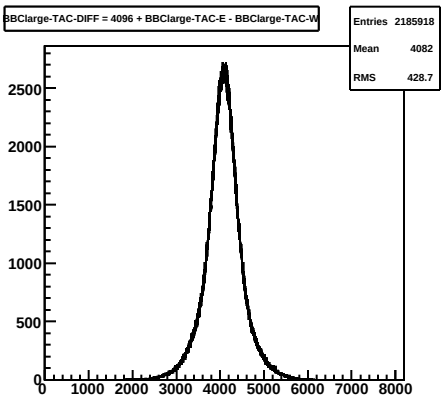

BBCsmall-TAC-E
Entries 2195878
Mean 1511
RMS 223.6

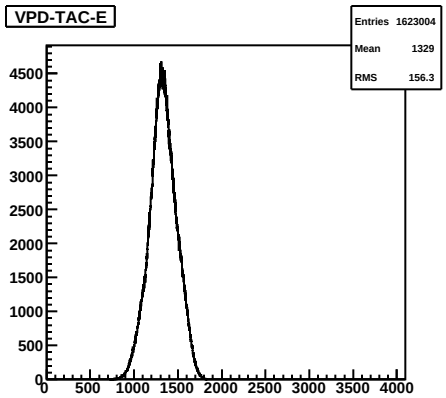

BBCsmall-TAC-W
Entries 2224041
Mean 1557
RMS 232.2

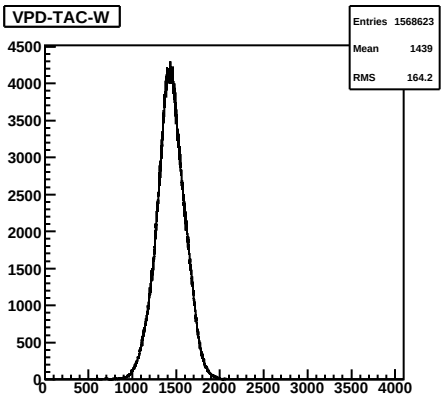

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2084008
Mean 4048
RMS 286.4

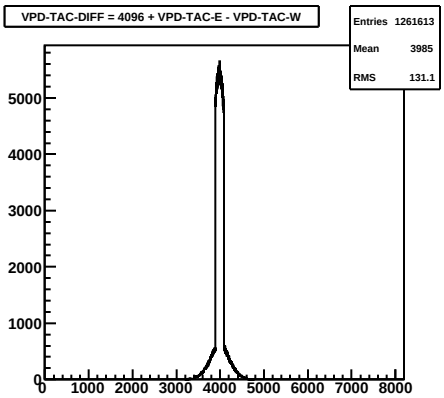

BBClarge-TAC-E
Entries 2263078
Mean 1486
RMS 332.4

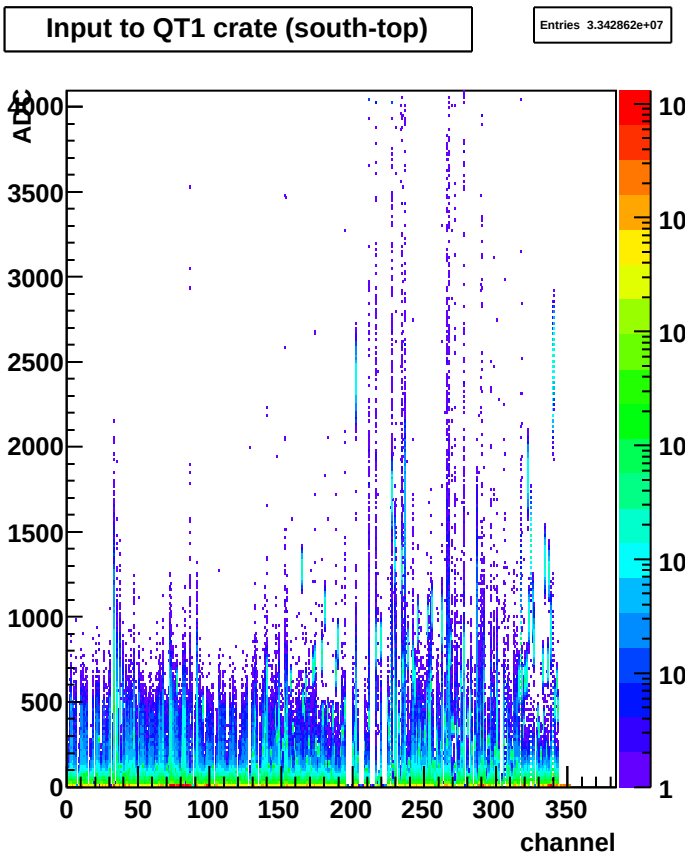

BBClarge-TAC-W
Entries 2278460
Mean 1500
RMS 333.9

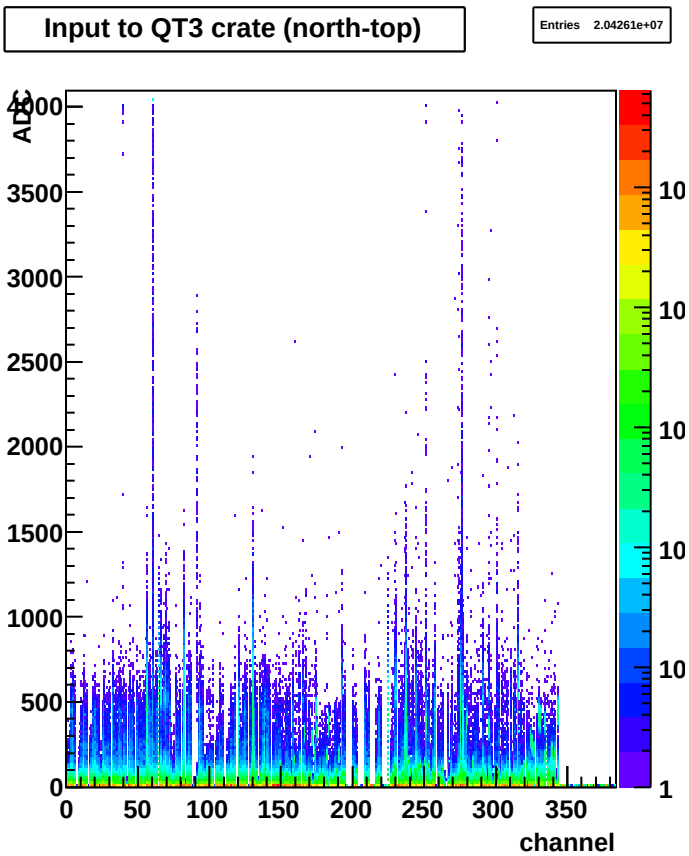

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2185918
Mean 4082
RMS 428.7

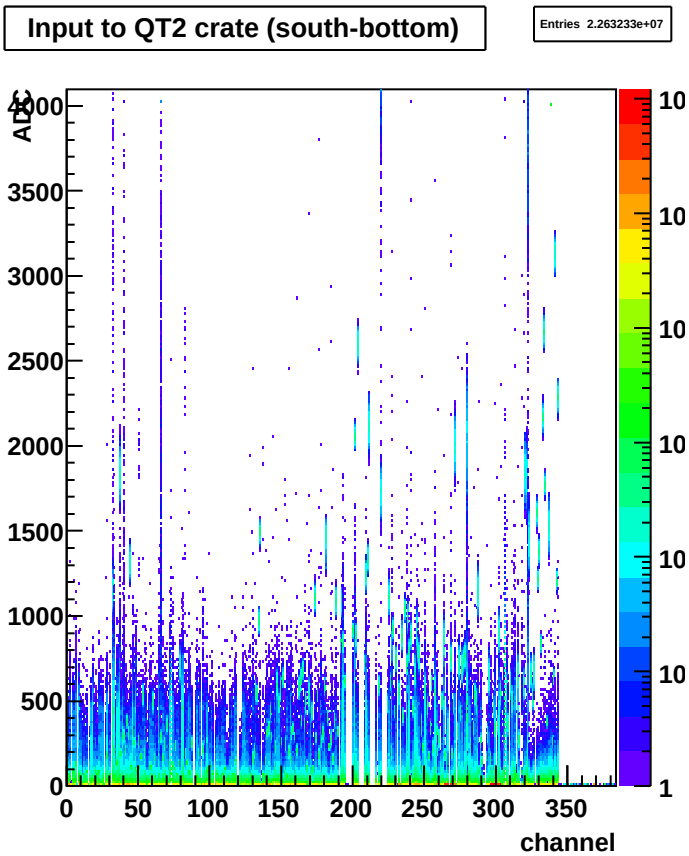

VPD-TAC-E
Entries 1623004
Mean 1329
RMS 156.3

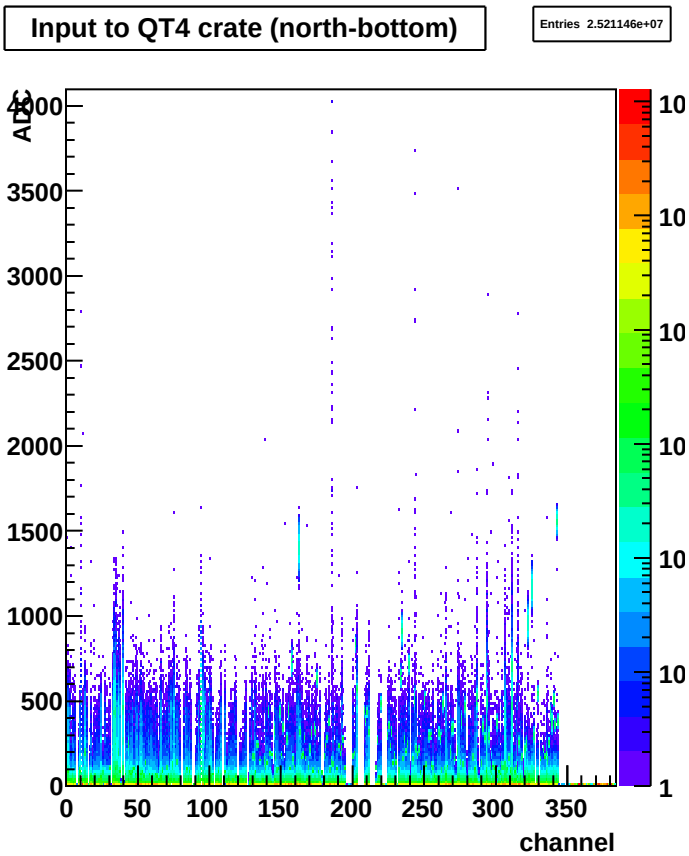

VPD-TAC-W
Entries 1568623
Mean 1439
RMS 164.2

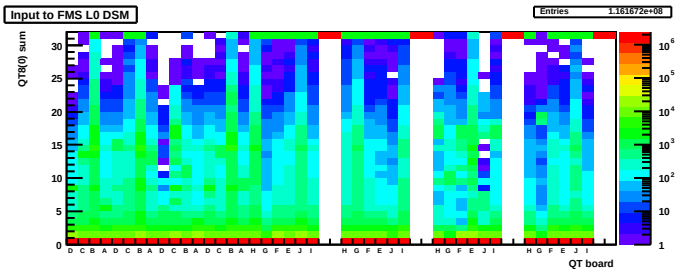

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1261613
Mean 3985
RMS 131.1

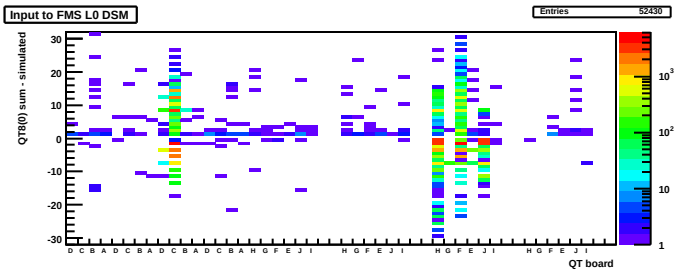

Input to QT1 crate (south-top)
Entries 3.342862e+07
ADC
channel

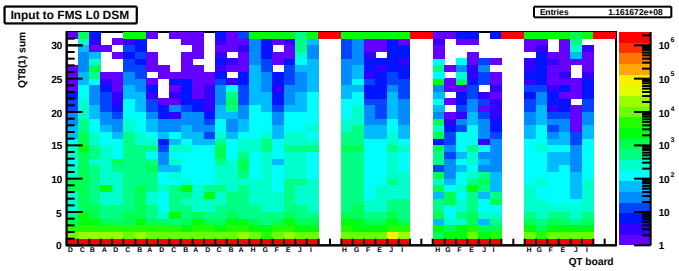

Input to QT3 crate (north-top)
Entries 2.04261e+07
ADC
channel

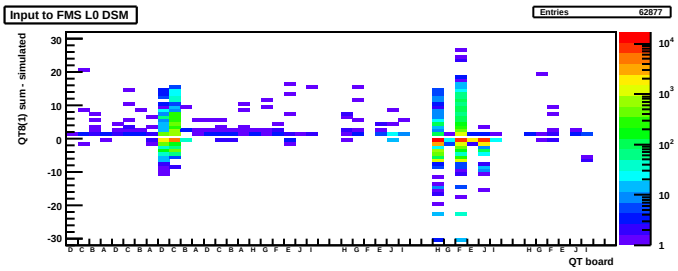

Input to QT2 crate (south-bottom)
Entries 2.263233e+07
ADC
channel

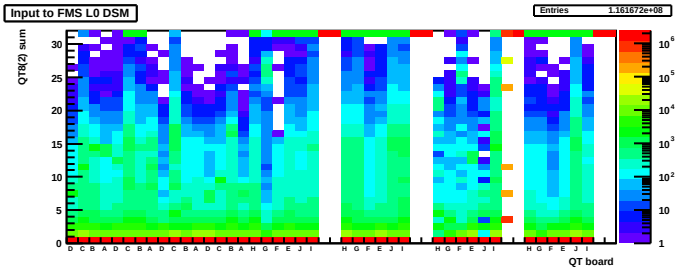

Input to QT4 crate (north-bottom)
Entries 2.521146e+07
ADC
channel

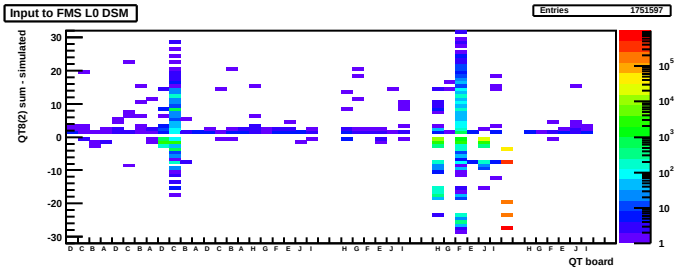

Input to FMS L0 DSM
Entries 1.161672e+08
QT8(0) sum
QT board

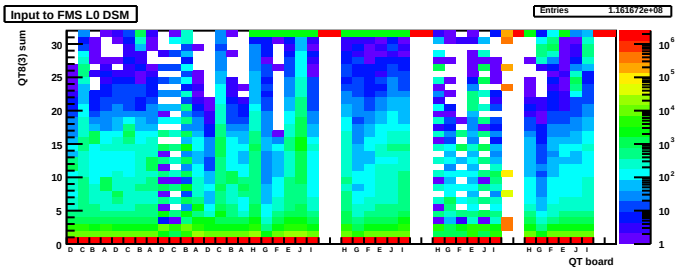

Input to FMS L0 DSM
Entries 52430
QT8(0) sum - simulated
QT board

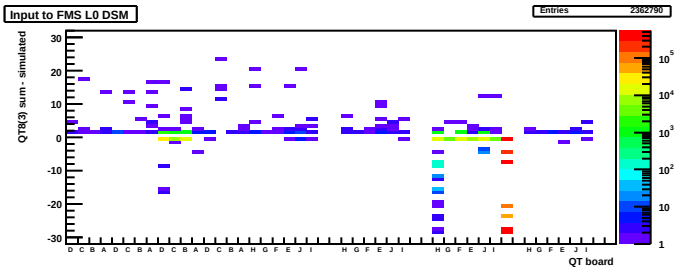

Input to FMS L0 DSM
Entries 1.161672e+08
QT8(1) sum
QT board

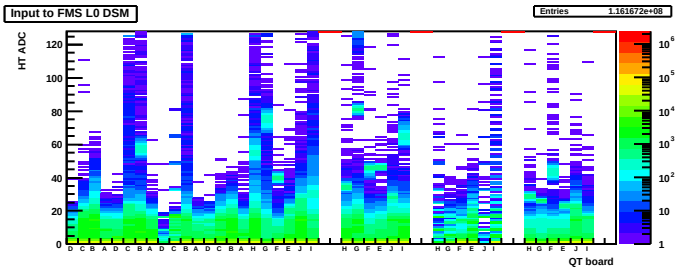

Input to FMS L0 DSM
Entries 62877
QT8(1) sum - simulated
QT board

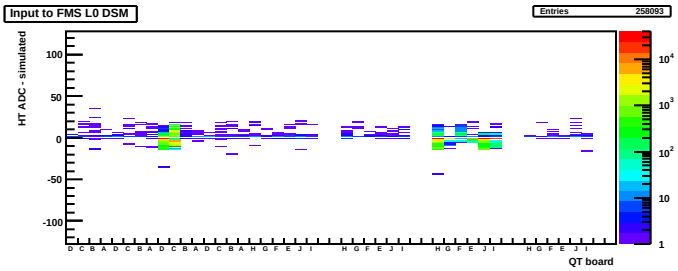

Input to FMS L0 DSM
Entries 1.161672e+08
QT8(2) sum
QT board

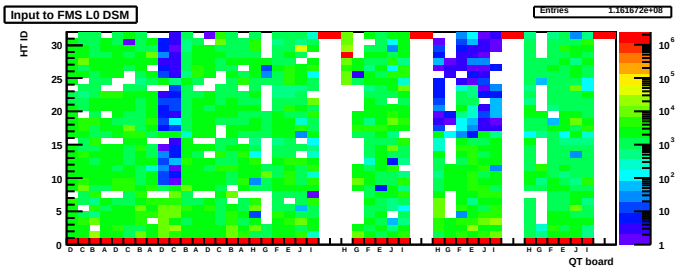

Input to FMS L0 DSM
Entries 1751597
QT8(2) sum - simulated
QT board

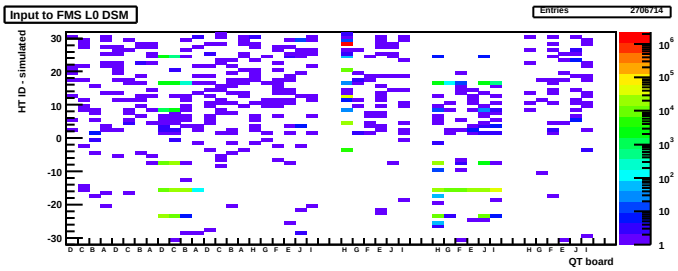

Input to FMS L0 DSM
Entries 1.161672e+08
QT8(3) sum
QT board

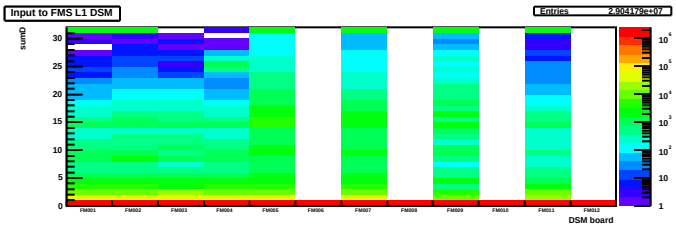

Input to FMS L0 DSM
Entries 2362790
QT8(3) sum - simulated
QT board

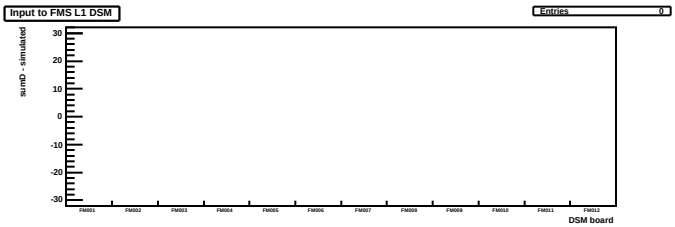

Input to FMS L0 DSM
Entries 1.161672e+08
HT ADC
QT board

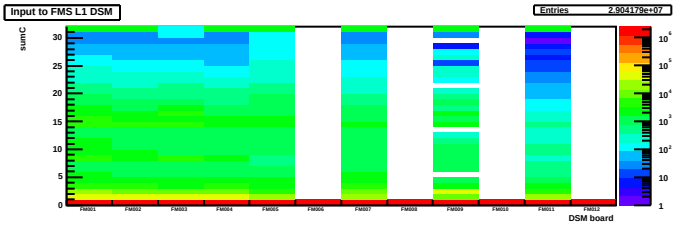

Input to FMS L0 DSM
Entries 258093
HT ADC - simulated
QT board

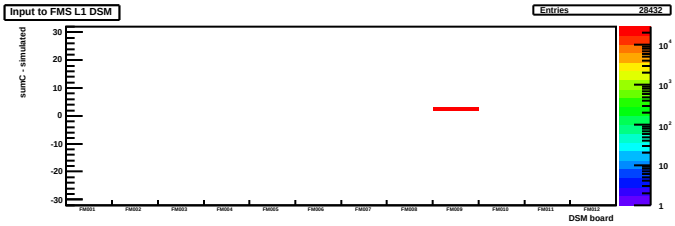

Input to FMS L0 DSM
Entries 1.161672e+08
HT ID
QT board

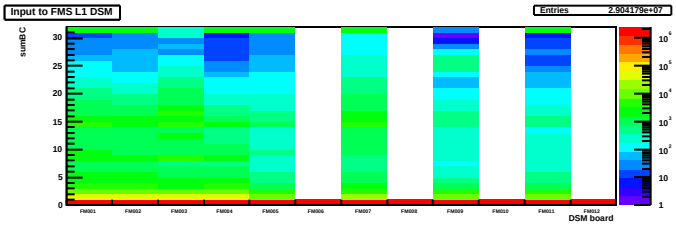

Input to FMS L0 DSM
Entries 2706714
HT ID - simulated
QT board

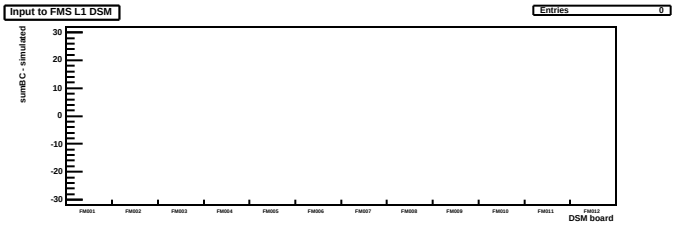

Input to FMS L1 DSM
Entries 2.904179e+07
sumD
DSM board

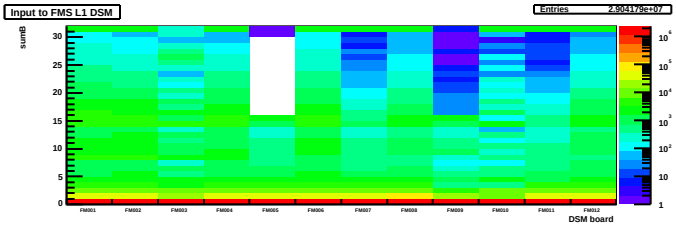

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

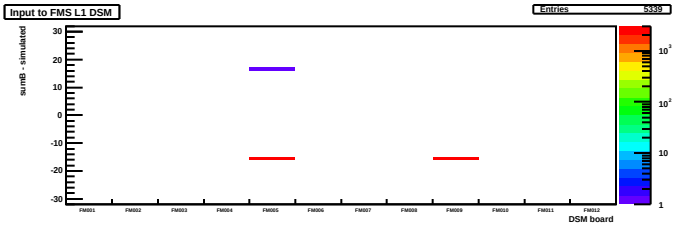

Input to FMS L1 DSM
Entries 2.904179e+07
sumC
DSM board

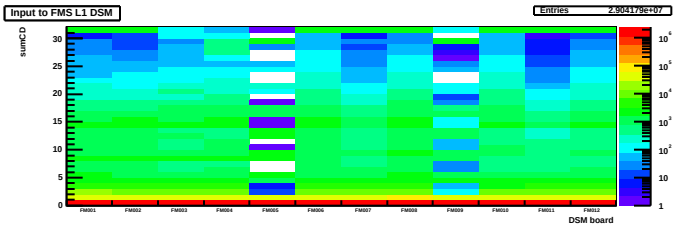

Input to FMS L1 DSM
Entries 28432
sumC - simulated
DSM board

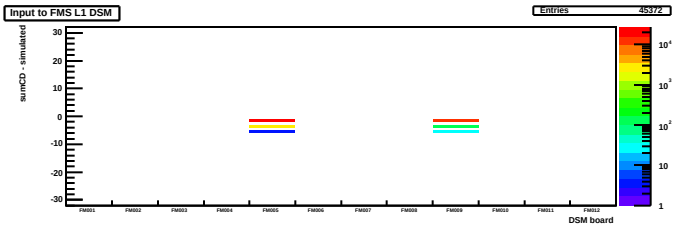

Input to FMS L1 DSM
Entries 2.904179e+07
sumBC
DSM board

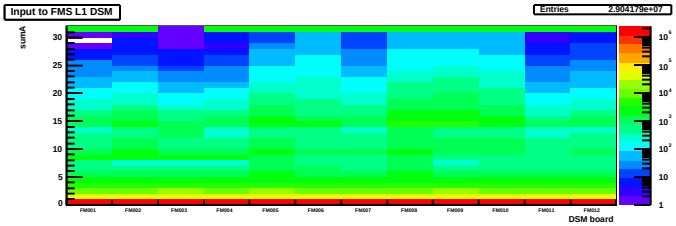

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

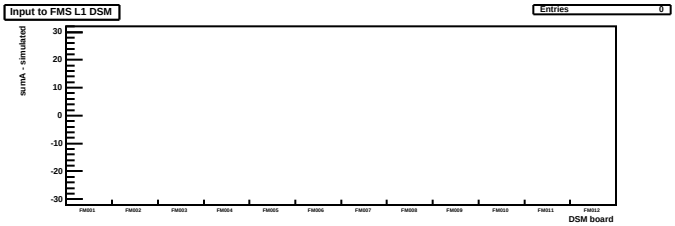

Input to FMS L1 DSM
Entries 2.904179e+07
sumB
DSM board

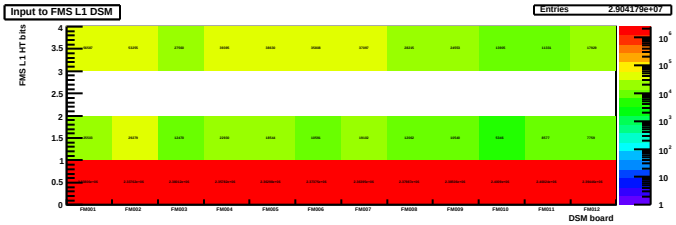

Input to FMS L1 DSM
Entries 5339
sumB - simulated
DSM board

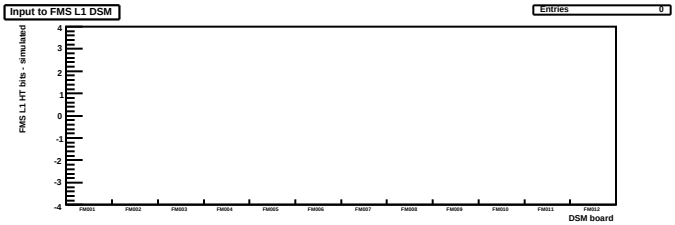

Input to FMS L1 DSM
Entries 2.904179e+07
sumCD
DSM board

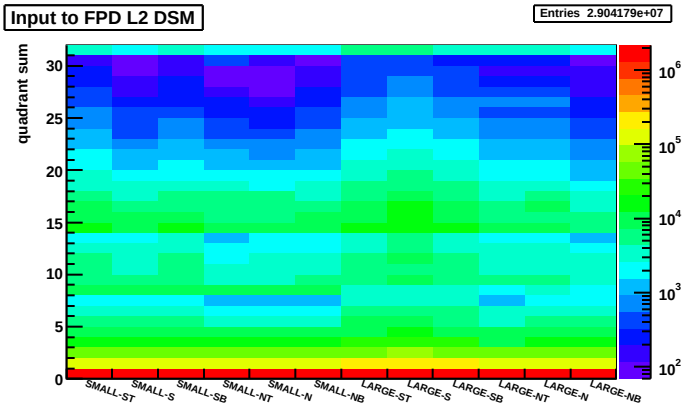

Input to FMS L1 DSM
Entries 45372
sumCD - simulated
DSM board

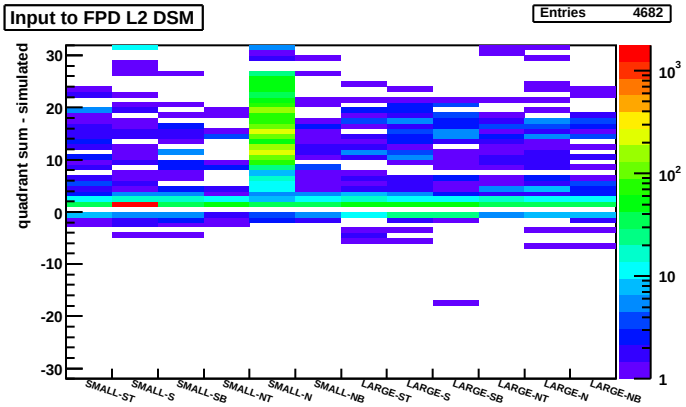

Input to FMS L1 DSM
Entries 2.904179e+07
sumA
DSM board

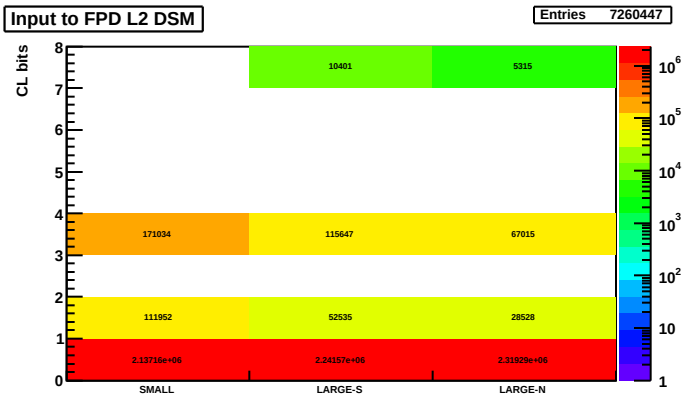

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

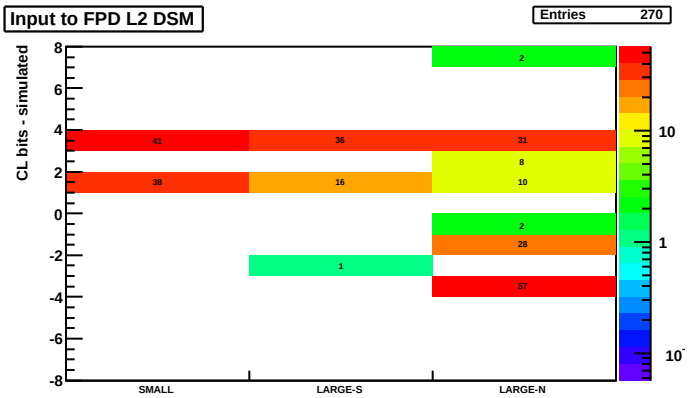

Input to FMS L1 DSM
Entries 2.904179e+07
FMS L1 HT bits
DSM board

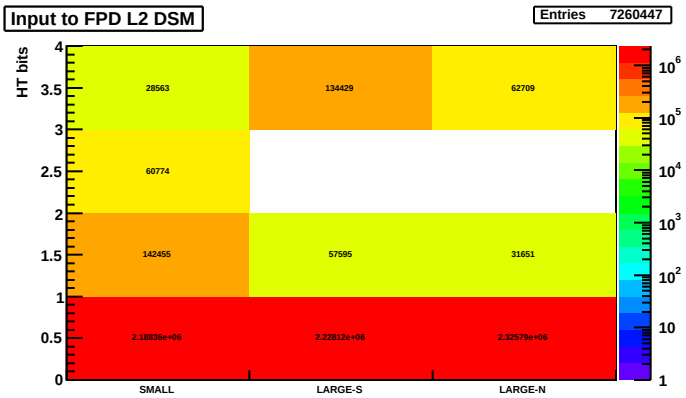

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

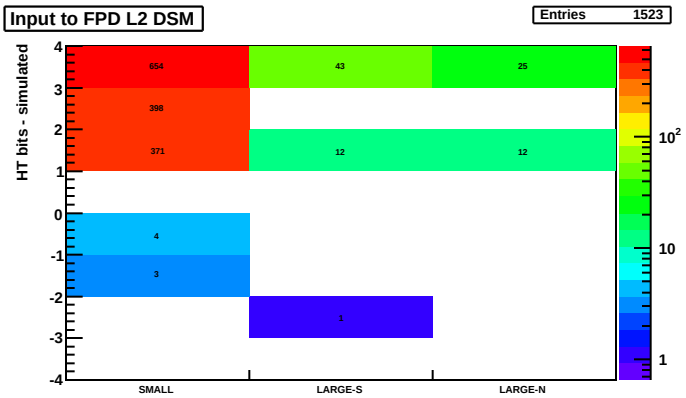

Input to FPD L2 DSM
Entries 2.904179e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

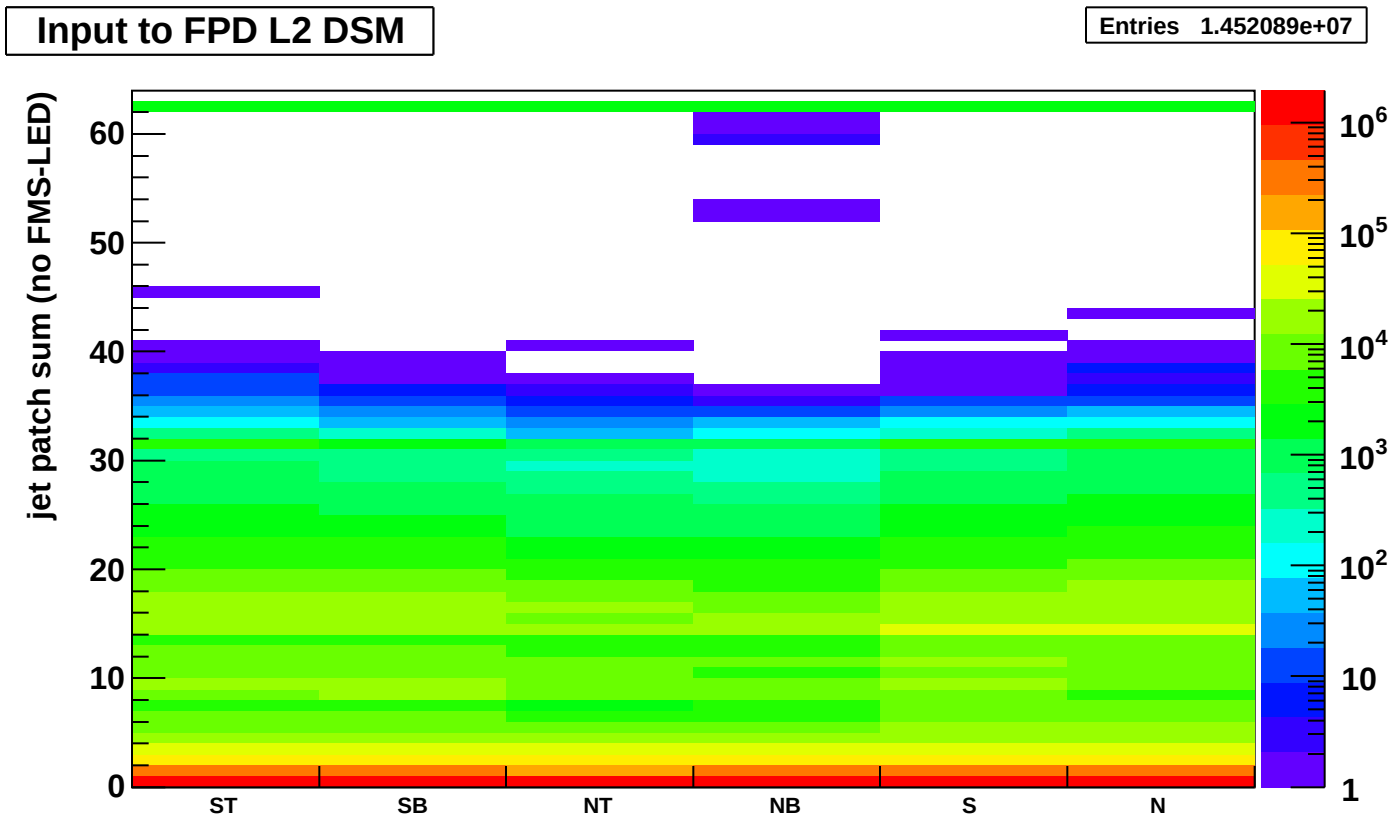

Input to FPD L2 DSM
Entries 4682
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

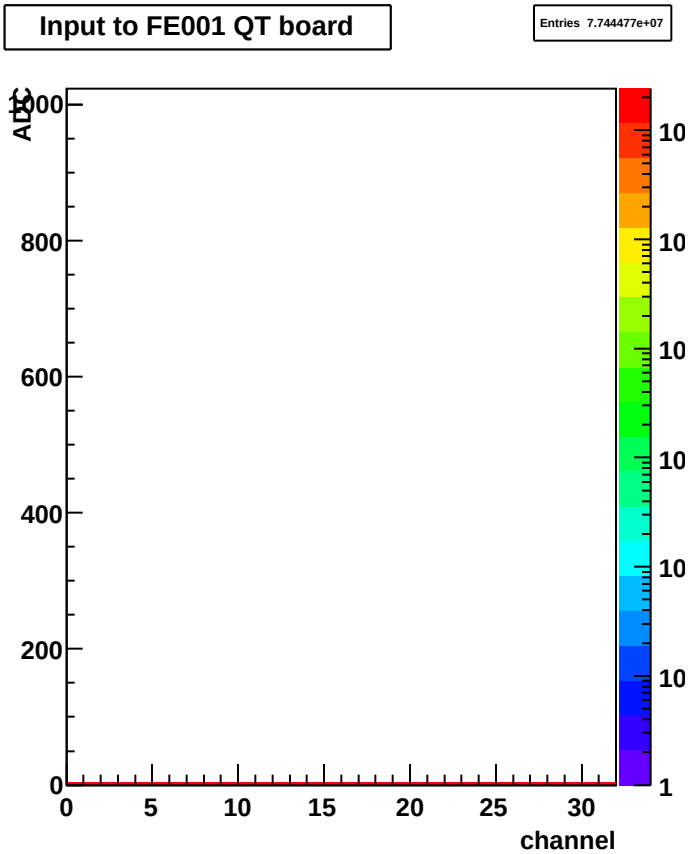

Input to FPD L2 DSM
Entries 7260447
CL bits
10401
5315
171034
115647
67015
111952
52535
28528
2.13716e+06
2.24157e+06
2.31929e+06
SMALL
LARGE-S
LARGE-N

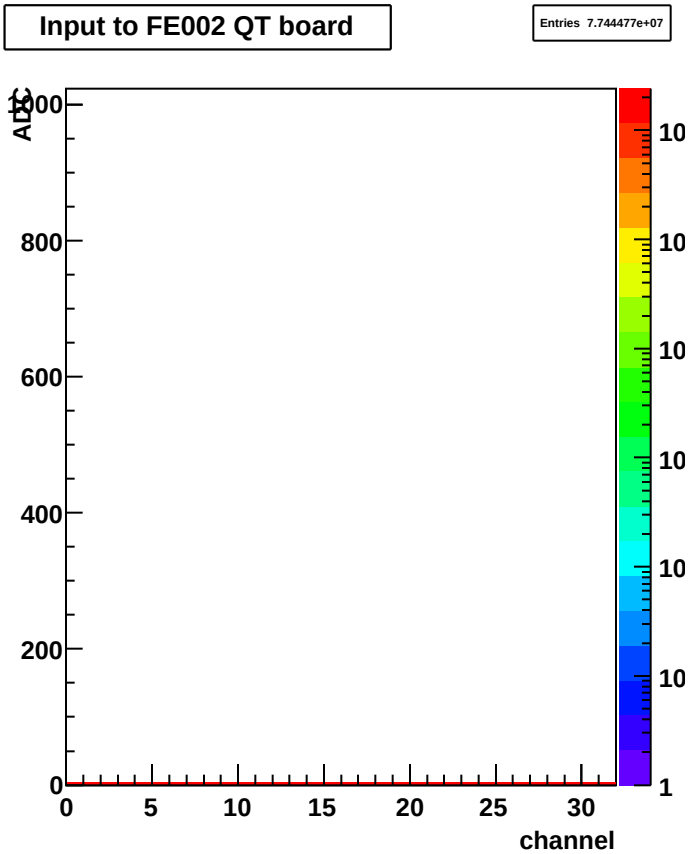

Input to FPD L2 DSM
Entries 270
CL bits - simulated
2
41
36
31
8
38
16
10
2
28
1
57
SMALL
LARGE-S
LARGE-N

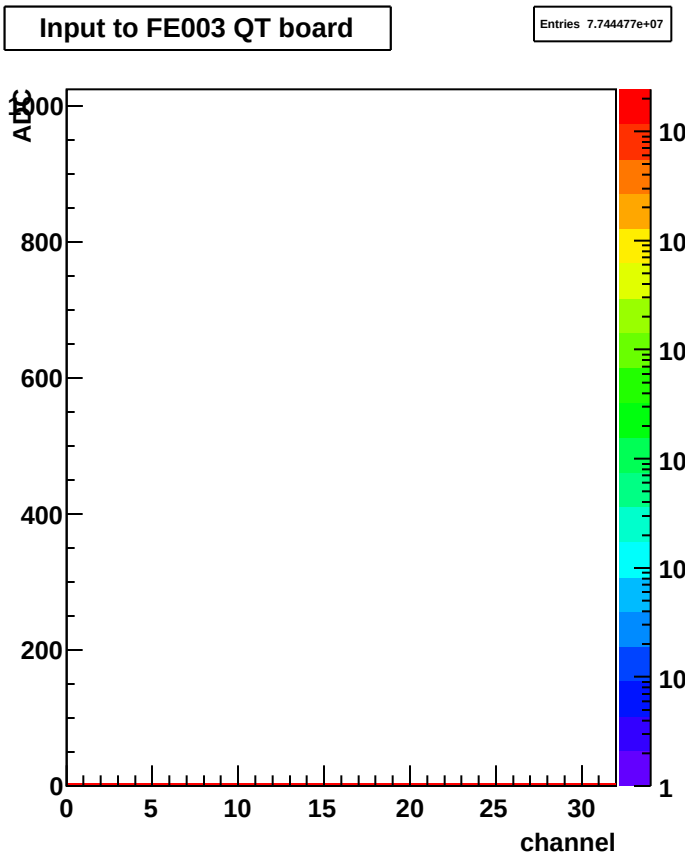

Input to FPD L2 DSM
Entries 7260447
HT bits
28563
134429
62709
60774
142455
57595
31651
2.18836e+06
2.22812e+06
2.32579e+06
SMALL
LARGE-S
LARGE-N

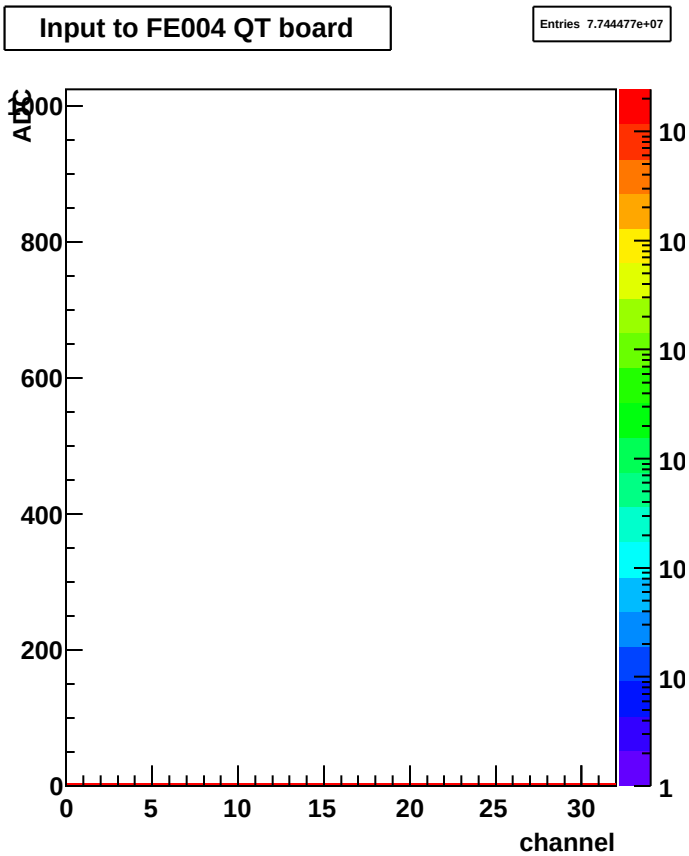

Input to FPD L2 DSM
Entries 1523
HT bits - simulated
654
43
25
398
371
12
12
4
3
1
SMALL
LARGE-S
LARGE-N

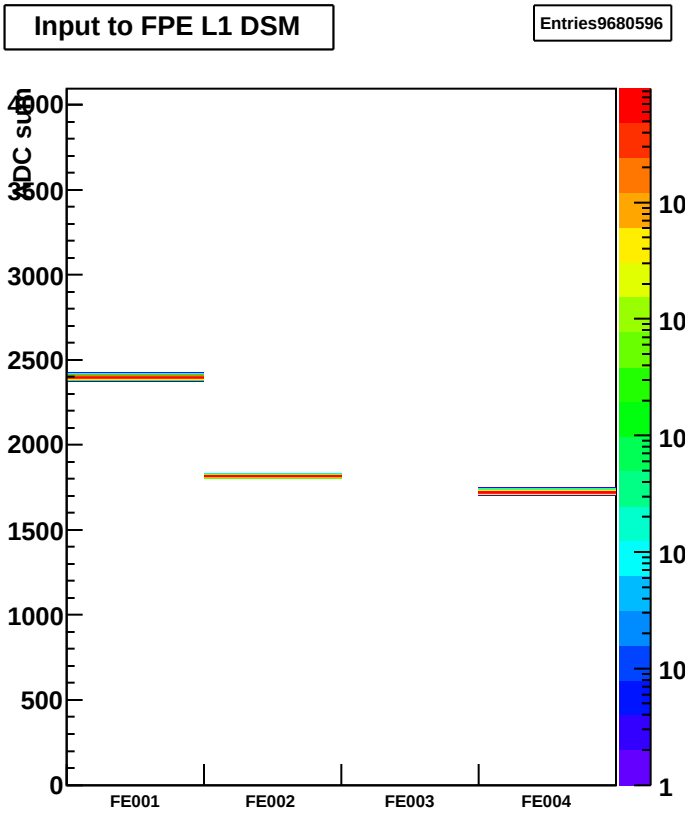

Input to FPD L2 DSM
Entries 1.452089e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.744477e+07
ADC
channel

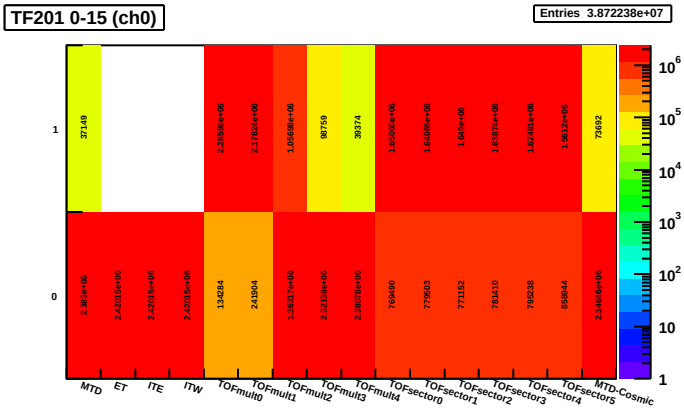

Input to FE002 QT board
Entries 7.744477e+07
ADC
channel


Input to FE003 QT board
Entries 7.744477e+07
ADC
channel

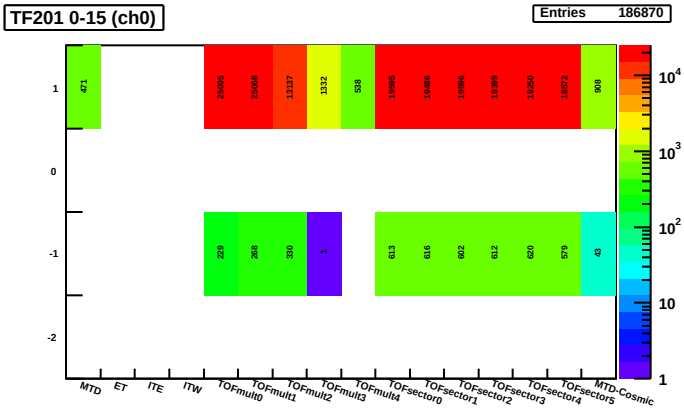

Input to FE004 QT board
Entries 7.744477e+07
ADC
channel


Input to FPE L1 DSM
Entries9680596
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

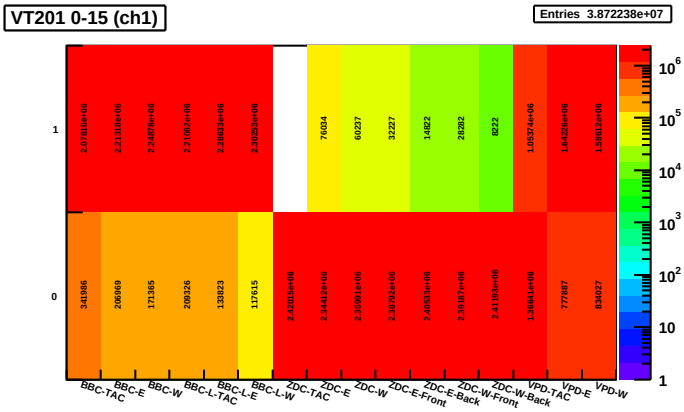
TF201 0-15 (ch0)



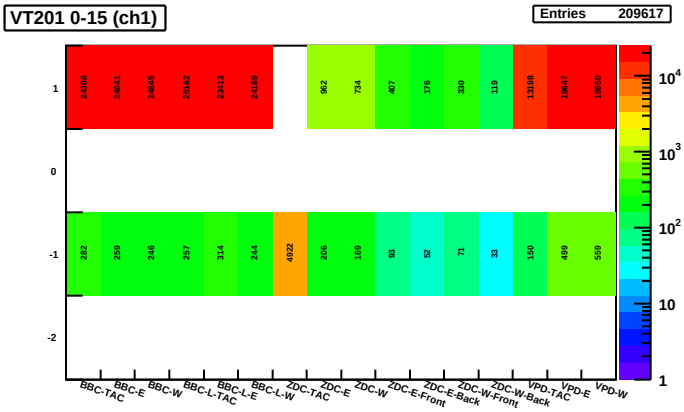
TF201 0-15 (ch0)



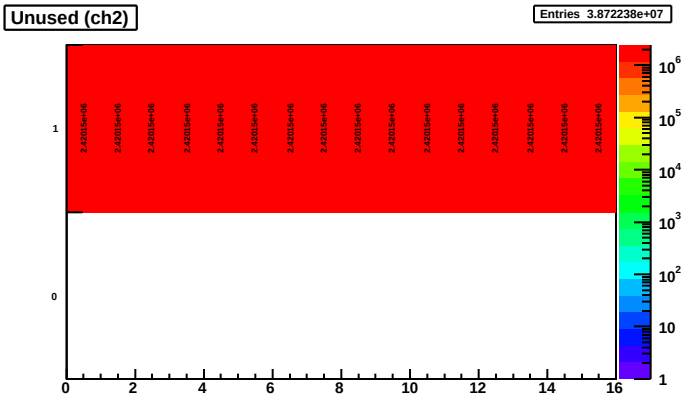
VT201 0-15 (ch1)

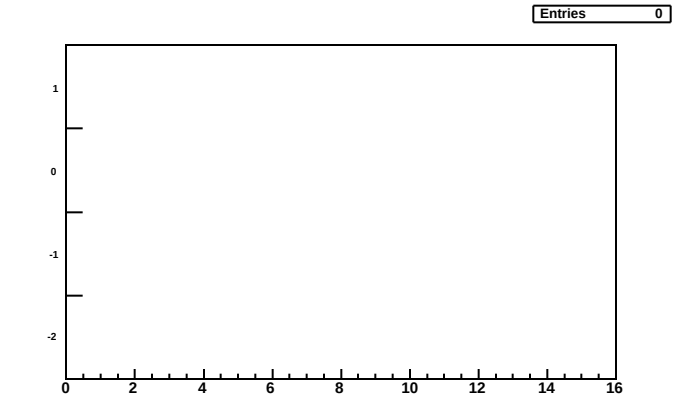


VT201 0-15 (ch1)

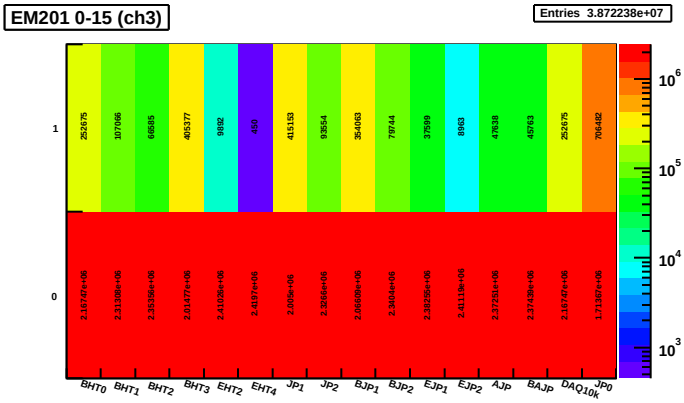


Unused (ch2)

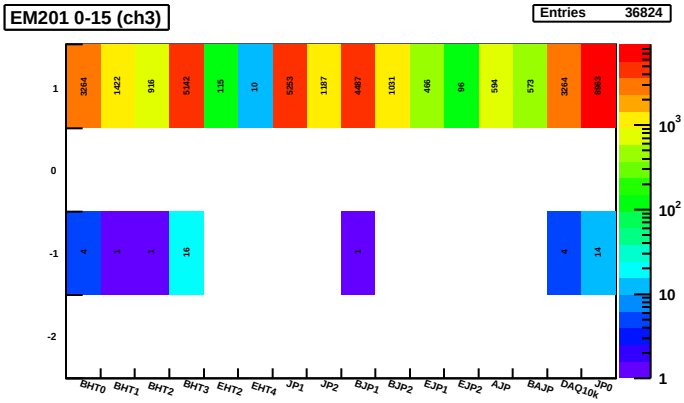




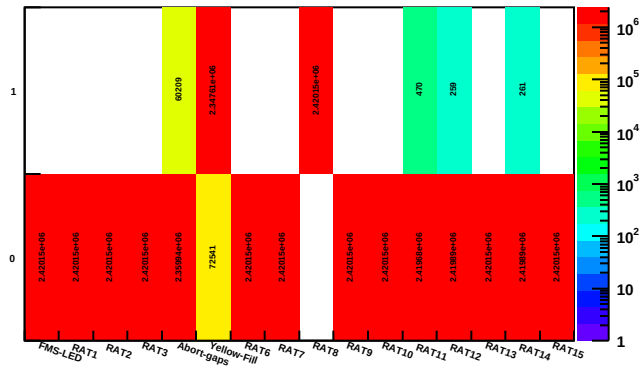
EM201 0-15 (ch3)

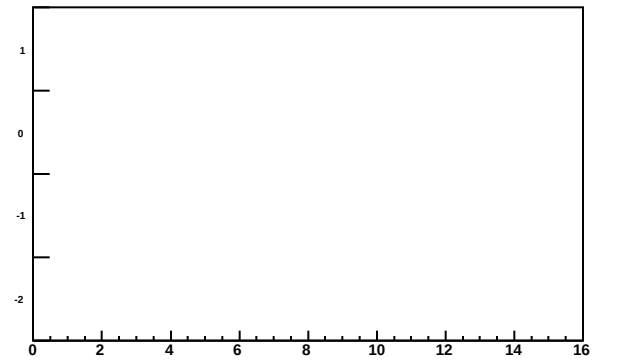


EM201 0-15 (ch3)

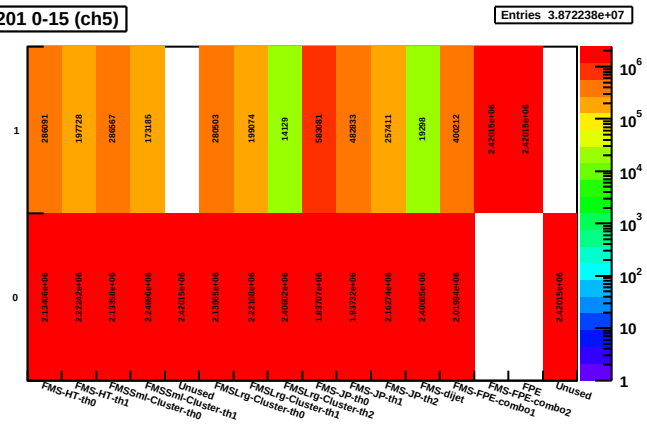


RAT board (ch4)

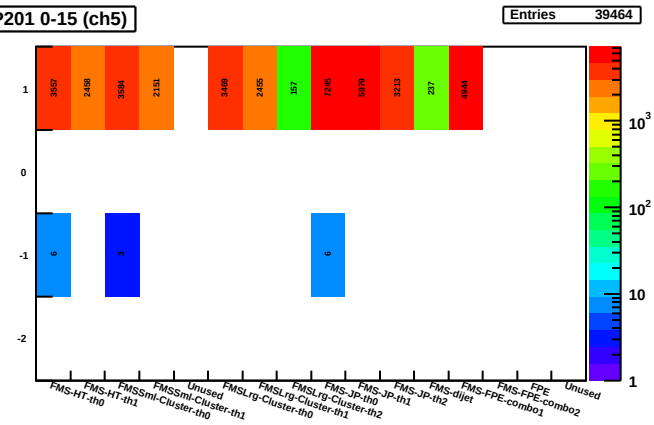




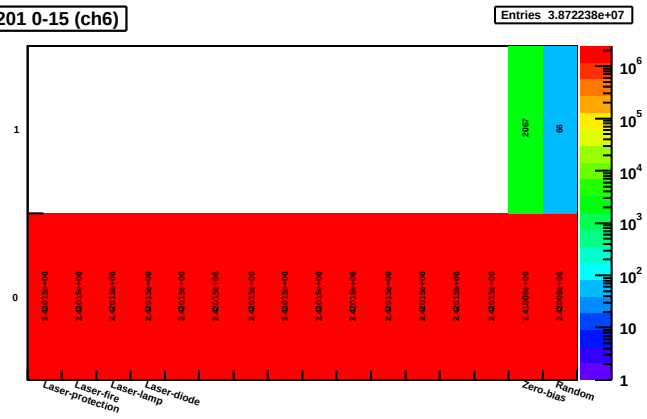
FP201 0-15 (ch5)

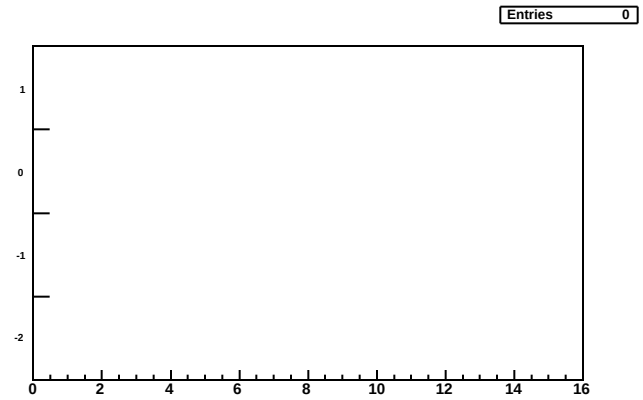
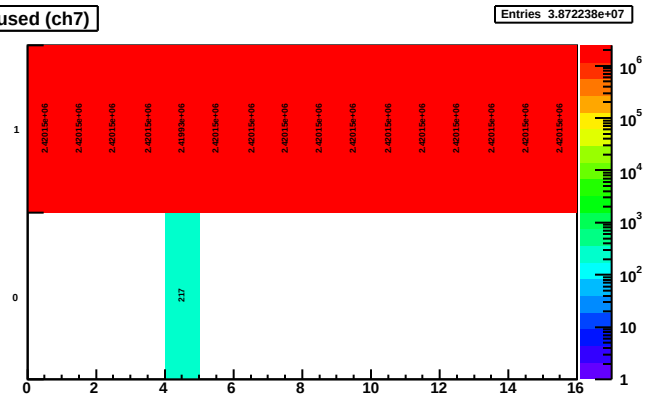
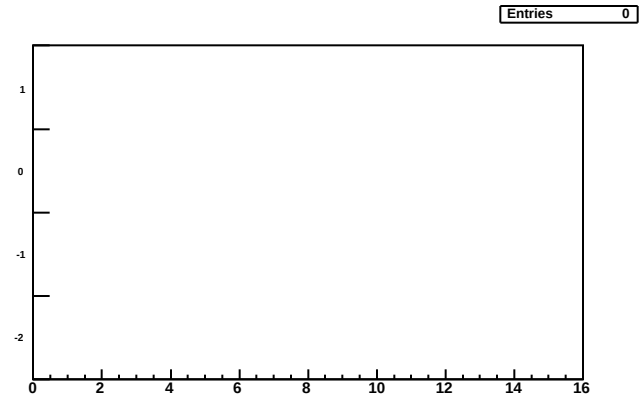


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)