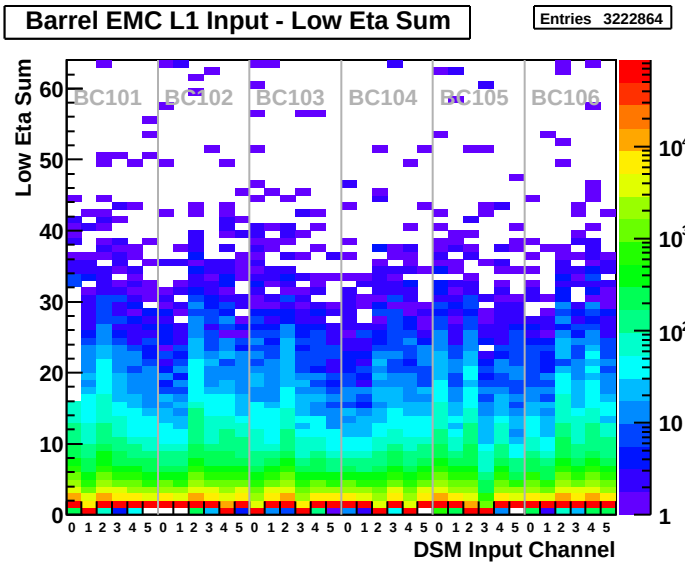
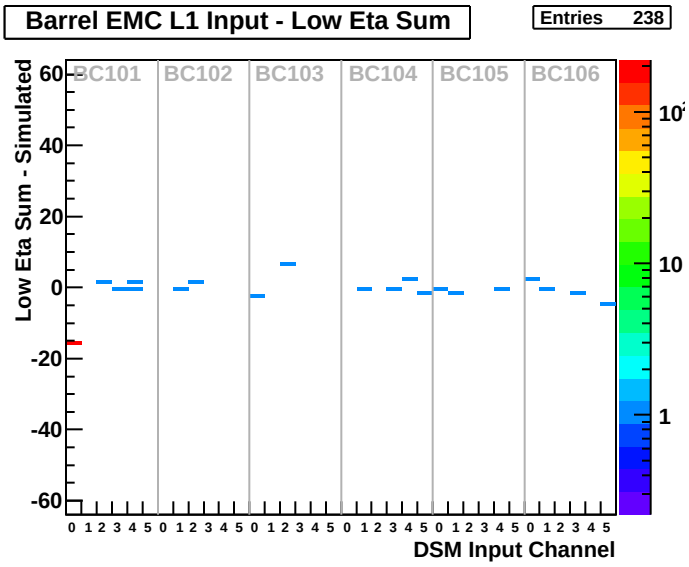
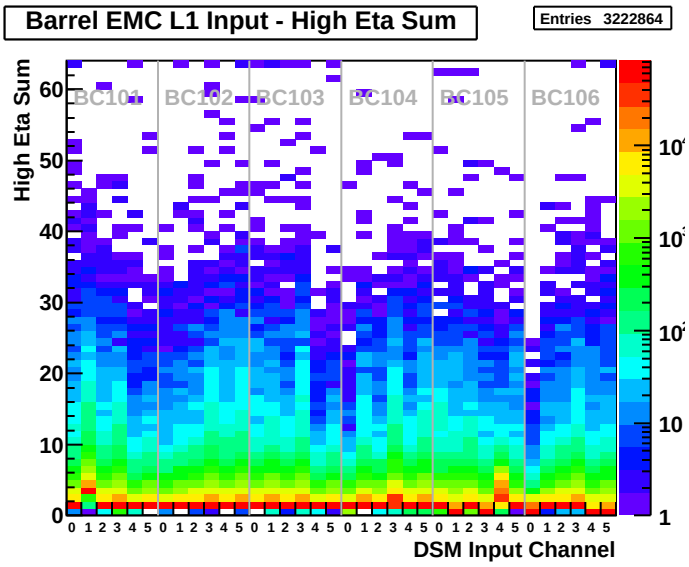
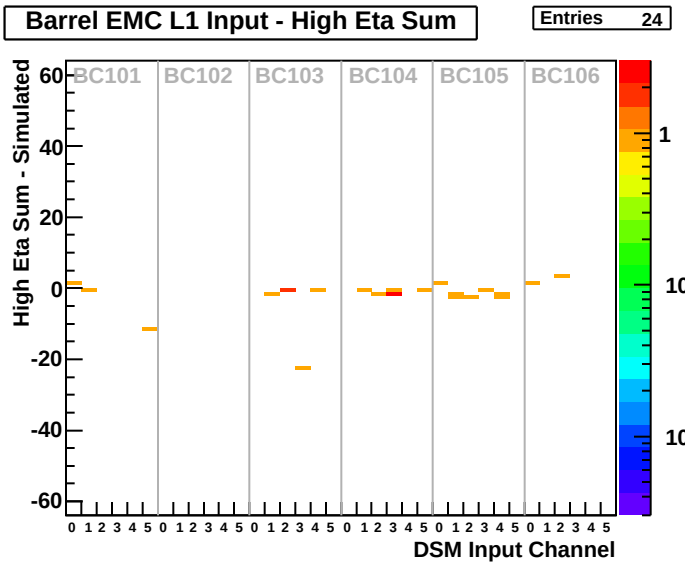
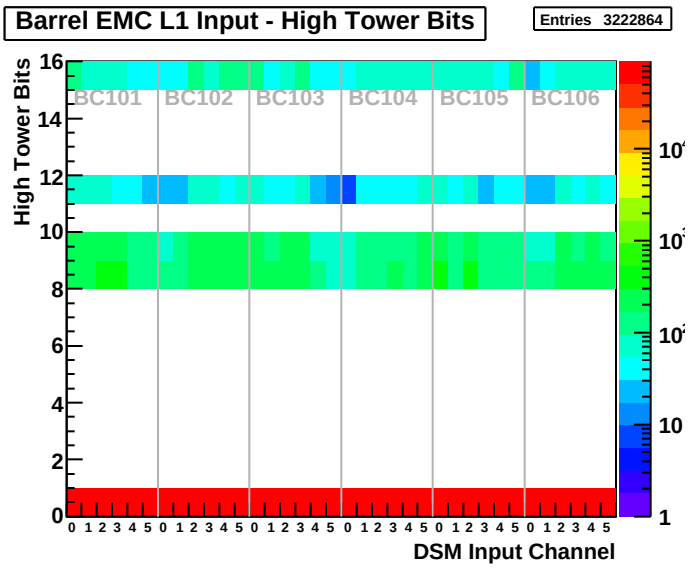
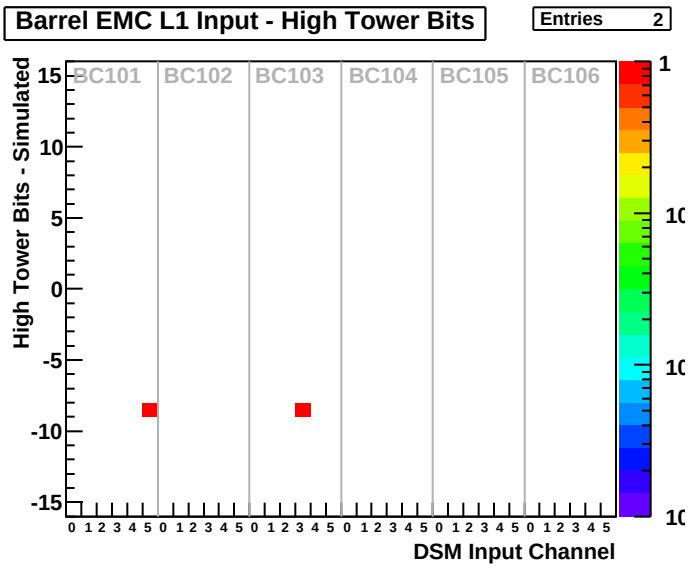

Barrel EMC L1 Input - Low Eta Sum
Entries 3222864
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

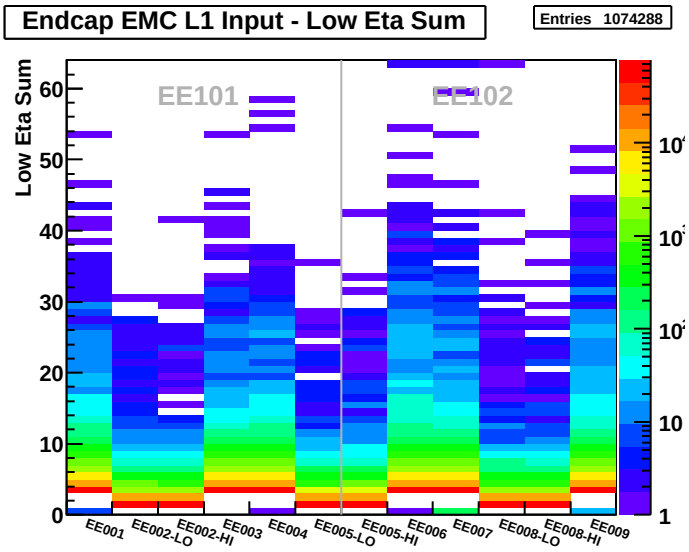

Barrel EMC L1 Input - Low Eta Sum
Entries 238
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

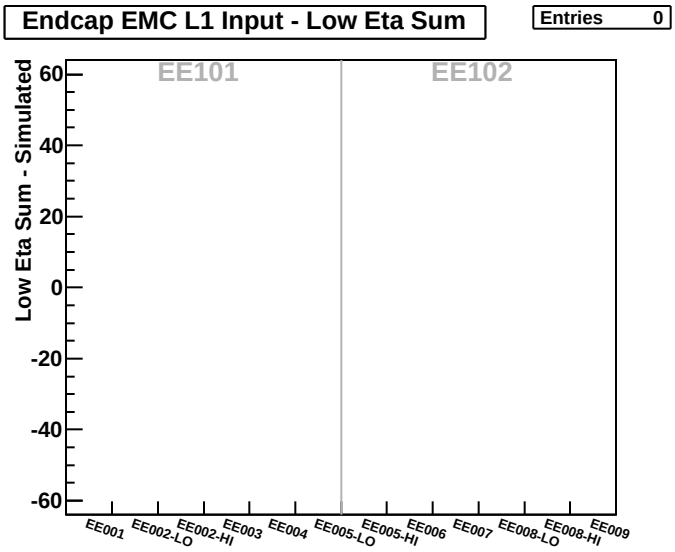

Barrel EMC L1 Input - High Eta Sum
Entries 3222864
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

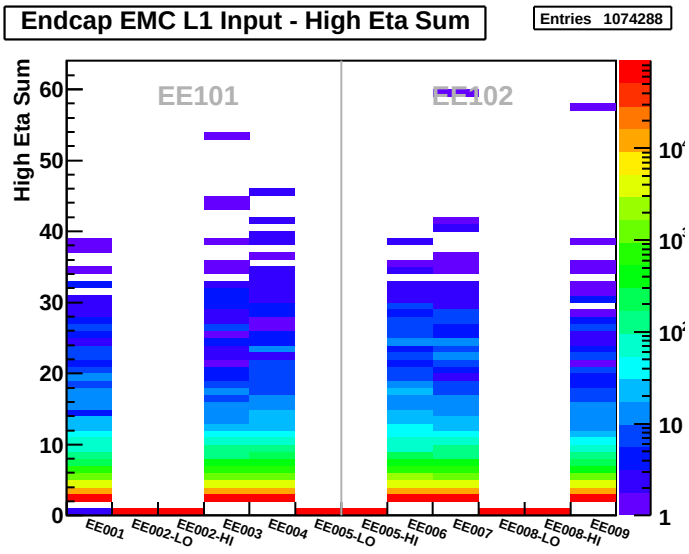

Barrel EMC L1 Input - High Eta Sum
Entries 24
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

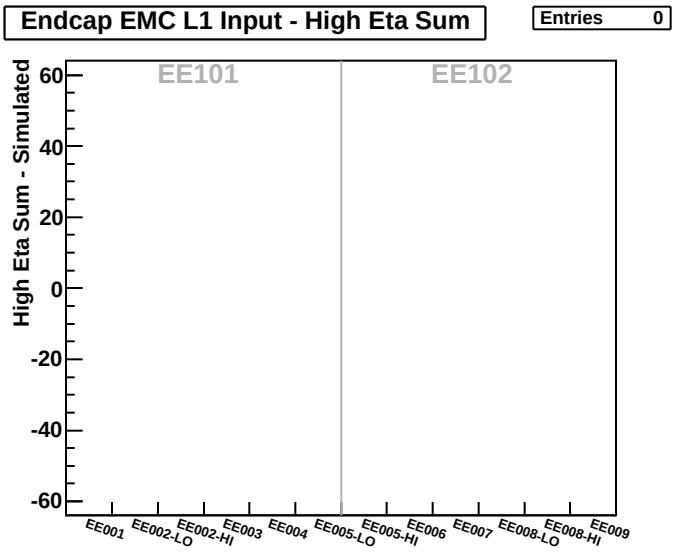

Barrel EMC L1 Input - High Tower Bits
Entries 3222864
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

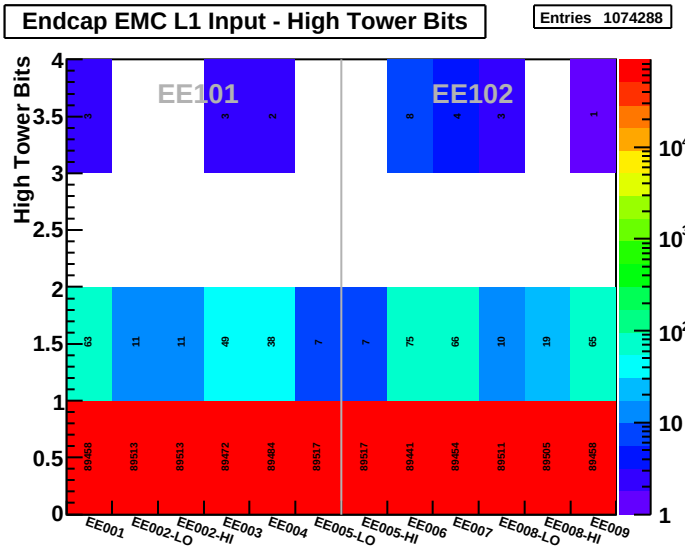

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

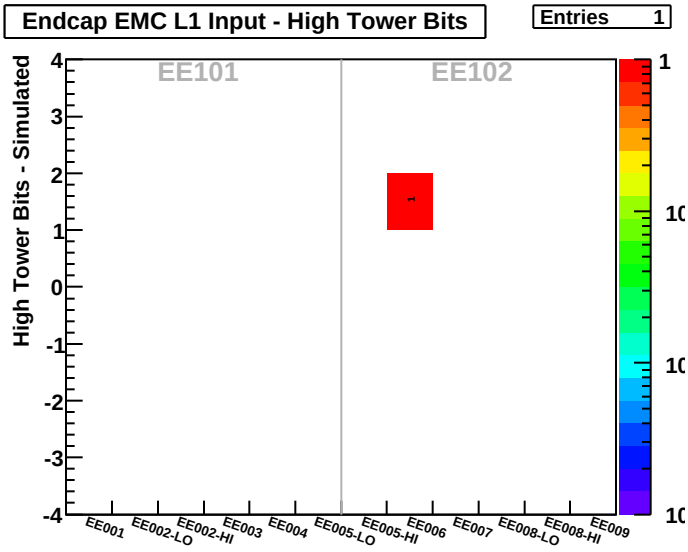

Endcap EMC L1 Input - Low Eta Sum
Entries 1074288
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

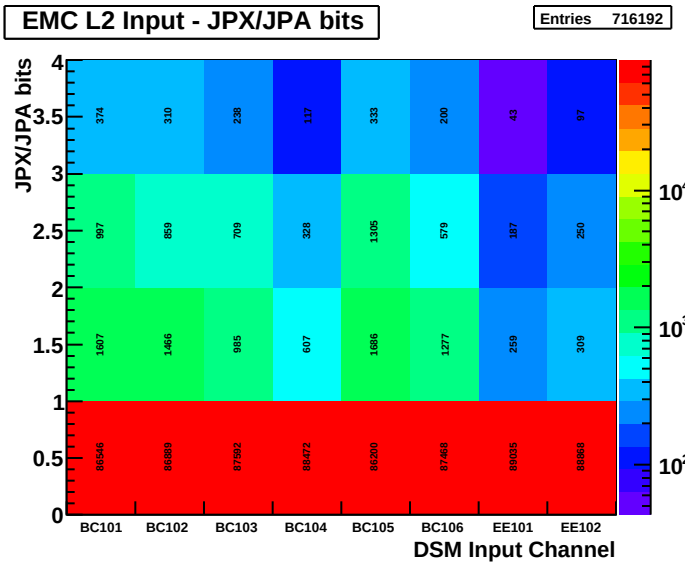

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

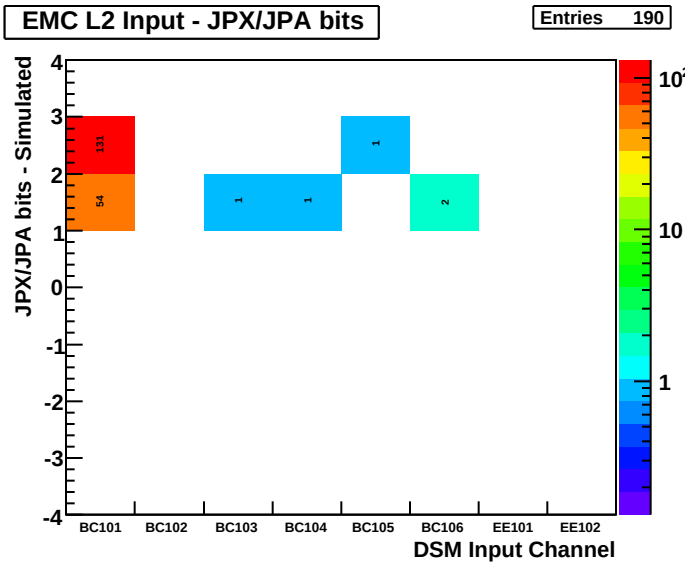

Endcap EMC L1 Input - High Eta Sum
Entries 1074288
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

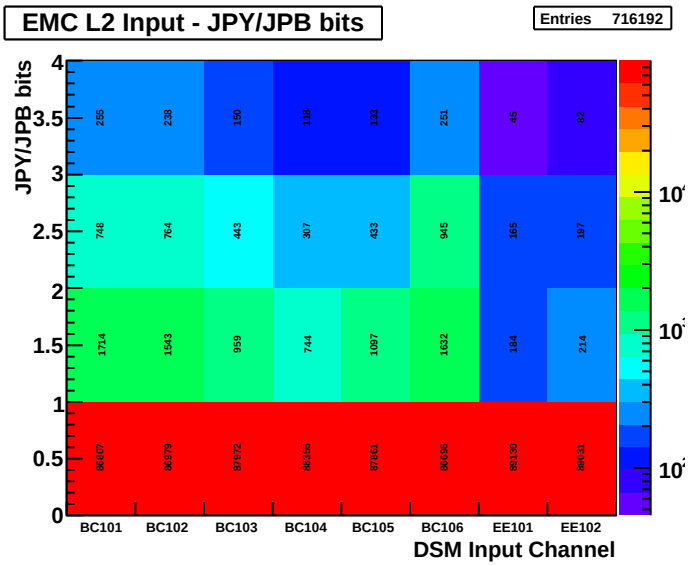

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

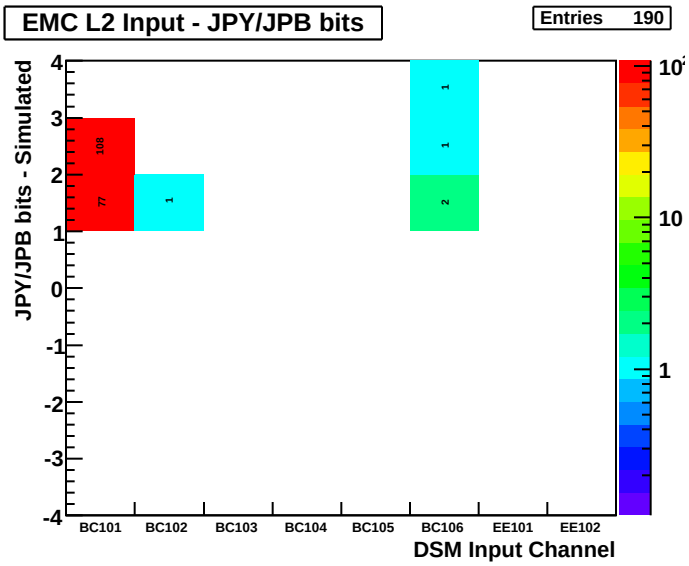

Endcap EMC L1 Input - High Tower Bits
Entries 1074288
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

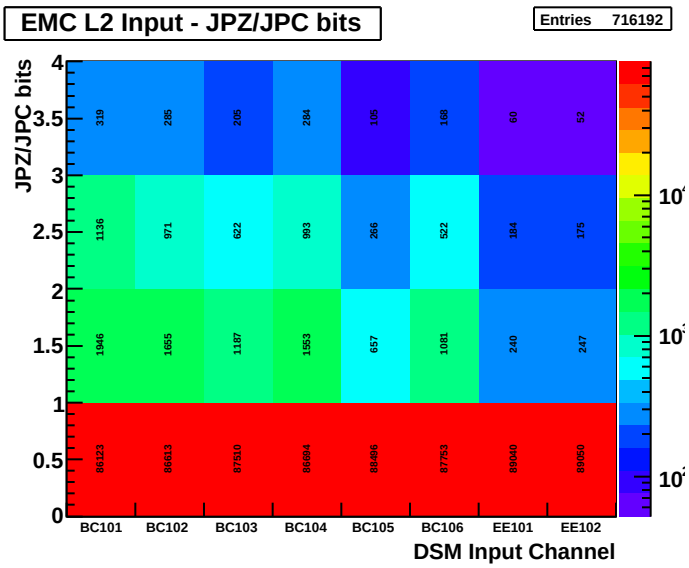

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

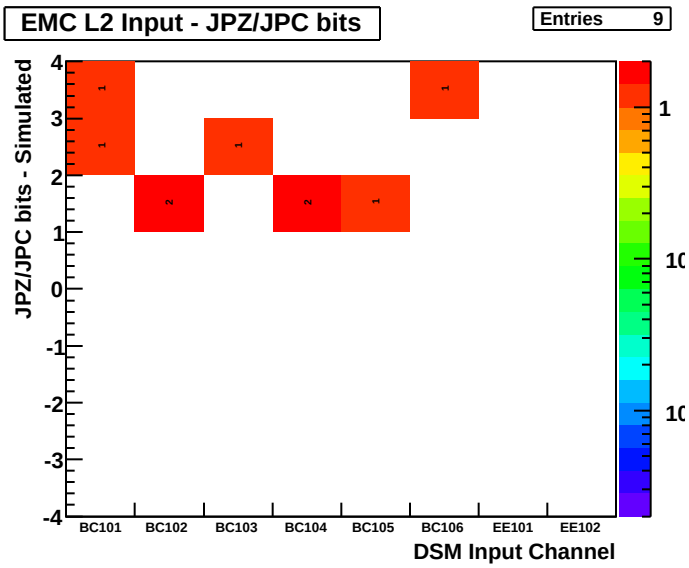

EMC L2 Input - JPX/JPA bits
Entries 716192
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
374 310 238 117 333 200 43 97
997 859 709 328 1305 579 187 250
1607 1466 985 607 1686 1277 259 309
86546 86889 87592 88472 88200 87468 89035 88868

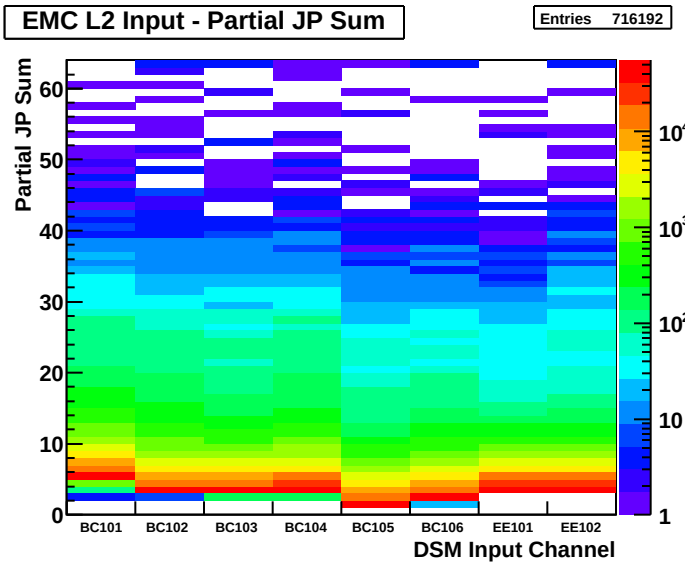

EMC L2 Input - JPX/JPA bits
Entries 190
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
131
54
1
1
1
2

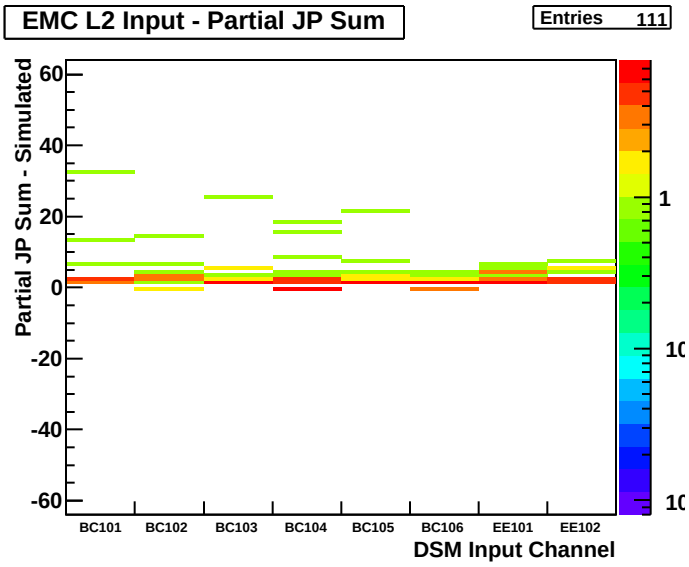

EMC L2 Input - JPY/JPB bits
Entries 716192
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
255 238 150 118 133 251 45 82
748 764 443 307 433 945 165 197
1714 1543 959 744 1097 1632 184 214
86807 86979 87372 88355 87861 86696 89130 89031

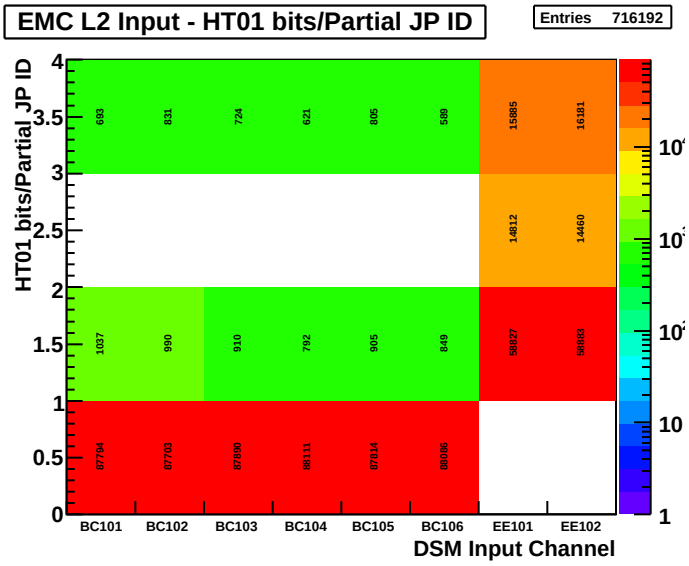

EMC L2 Input - JPY/JPB bits
Entries 190
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
108
77
1
1
1
2

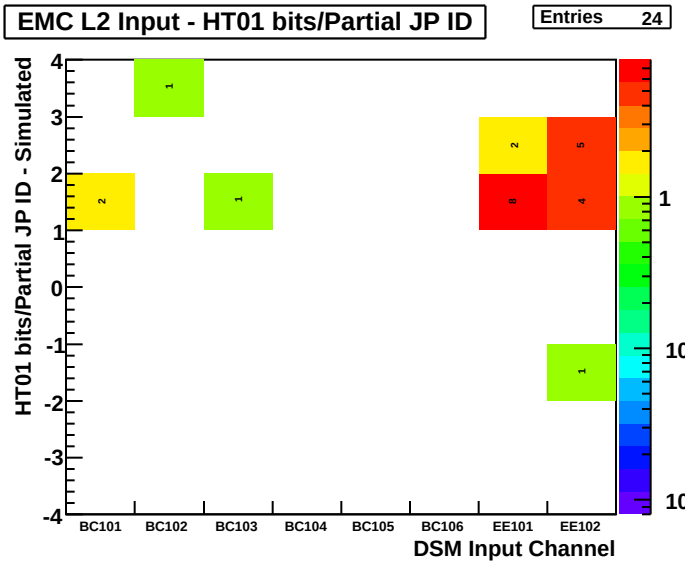

EMC L2 Input - JPZ/JPC bits
Entries 716192
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
319 285 205 284 105 168 60 52
1136 971 622 993 266 522 184 175
1946 1655 1187 1553 657 1081 240 247
86123 86613 87510 86694 88496 87753 89040 89050

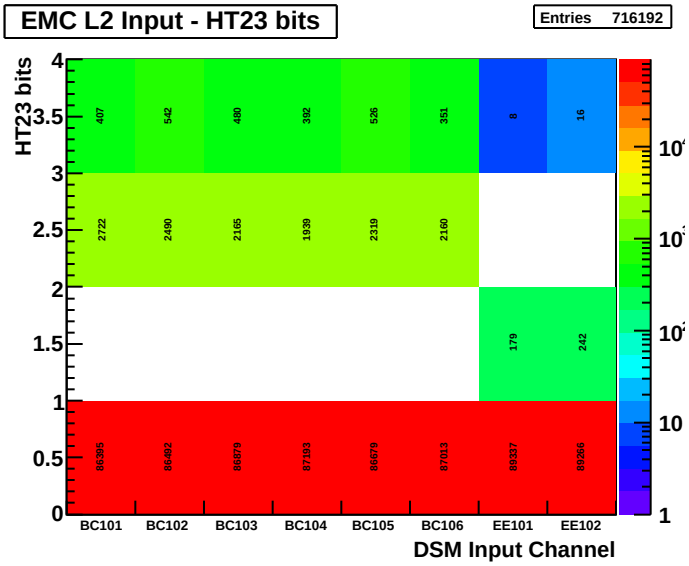

EMC L2 Input - JPZ/JPC bits
Entries 9
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1
1
2
1
2
1
1

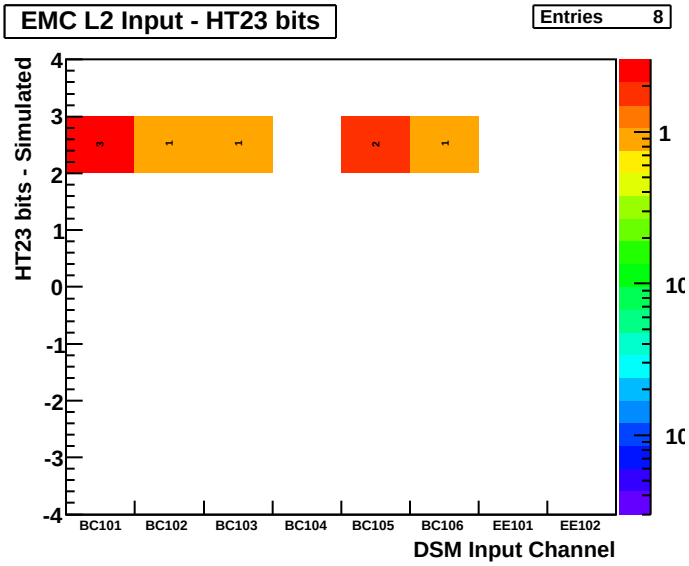

EMC L2 Input - Partial JP Sum
Entries 716192
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

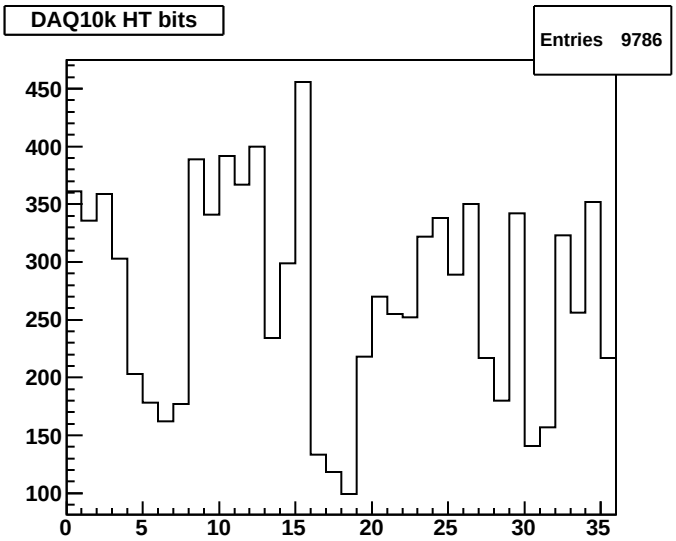

EMC L2 Input - Partial JP Sum
Entries 111
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

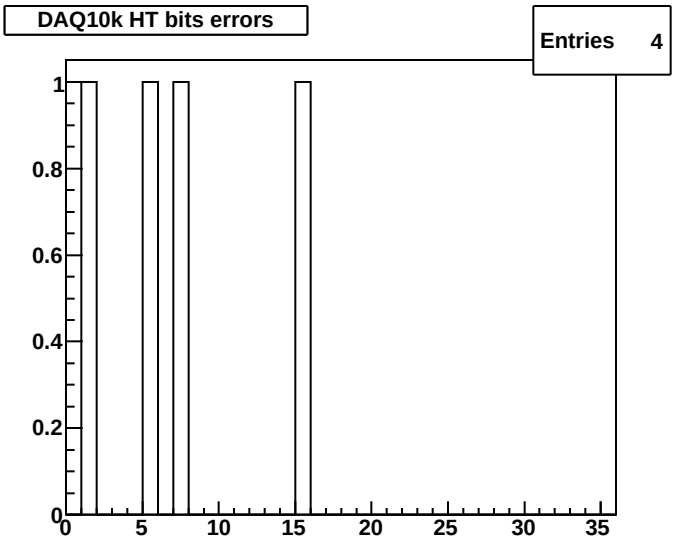

EMC L2 Input - HT01 bits/Partial JP ID
Entries 716192
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

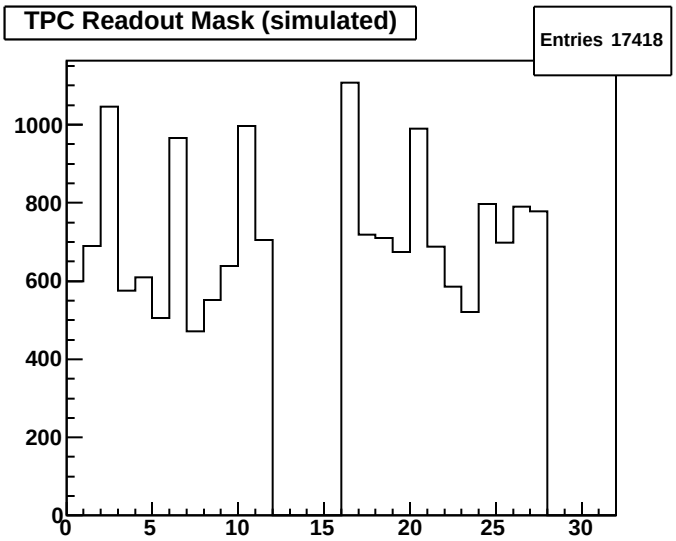

EMC L2 Input - HT01 bits/Partial JP ID
Entries 24
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

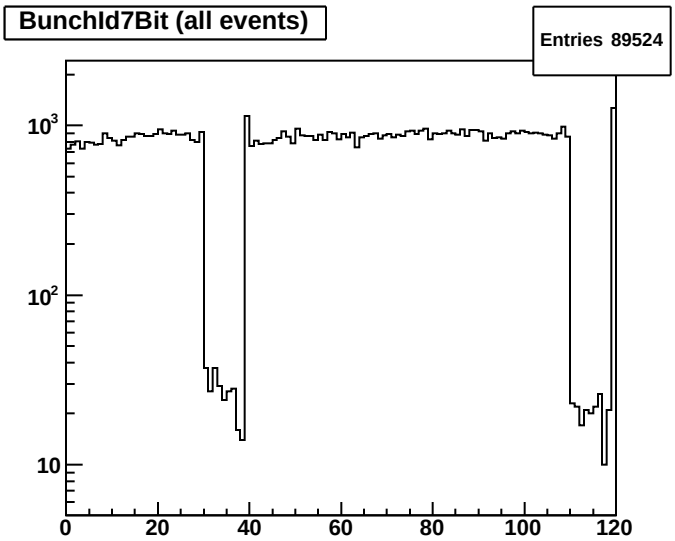

EMC L2 Input - HT23 bits
Entries 716192
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

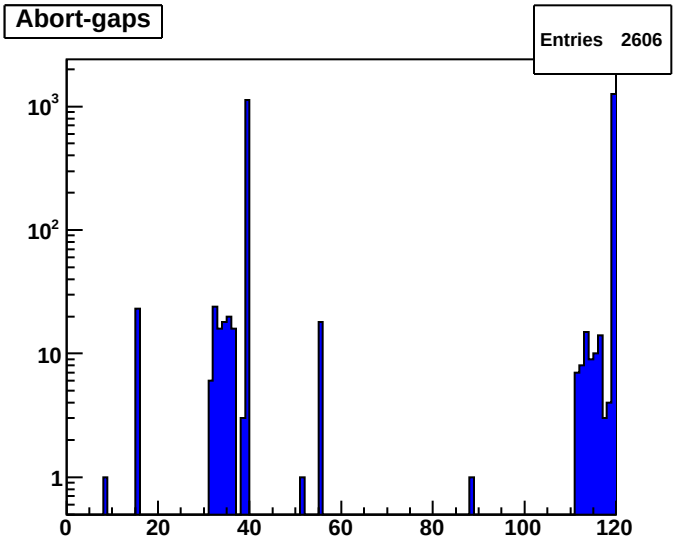

EMC L2 Input - HT23 bits
Entries 8
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

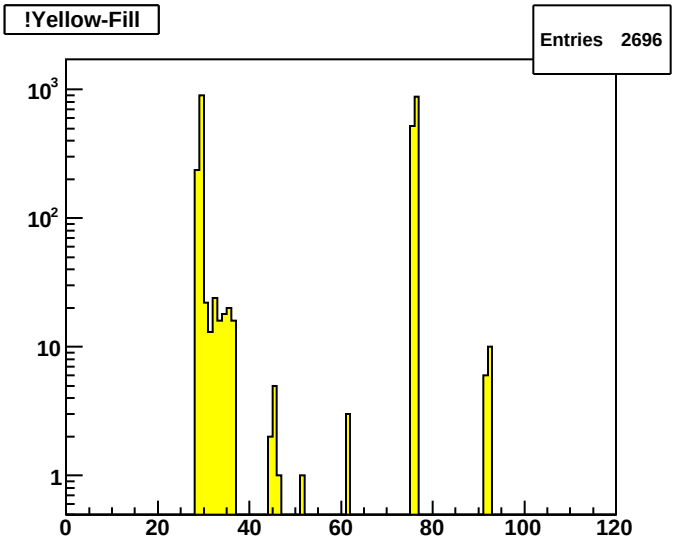

DAQ10k HT bits
Entries 9786

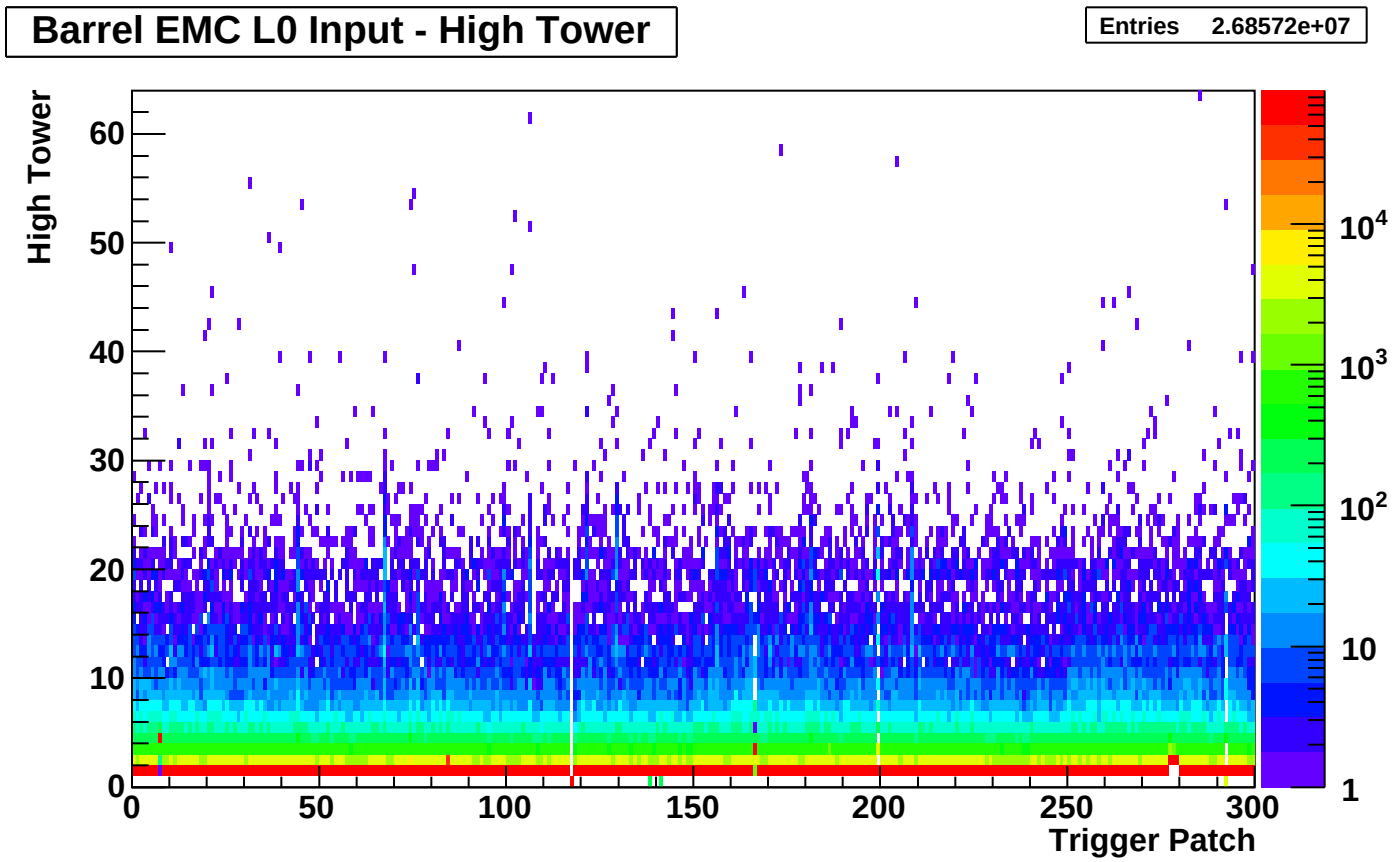

DAQ10k HT bits errors
Entries 4

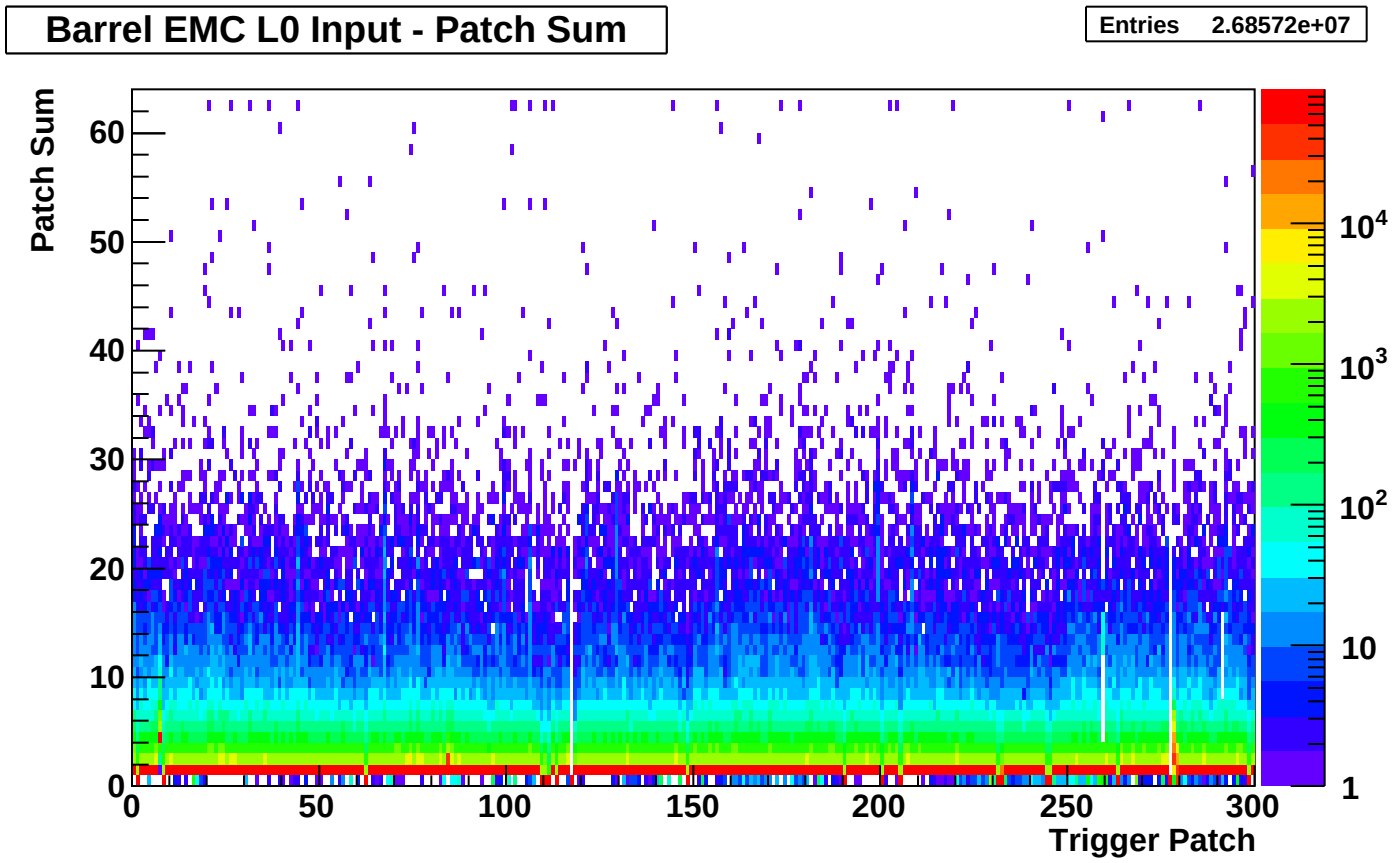

TPC Readout Mask (simulated)
Entries 17418

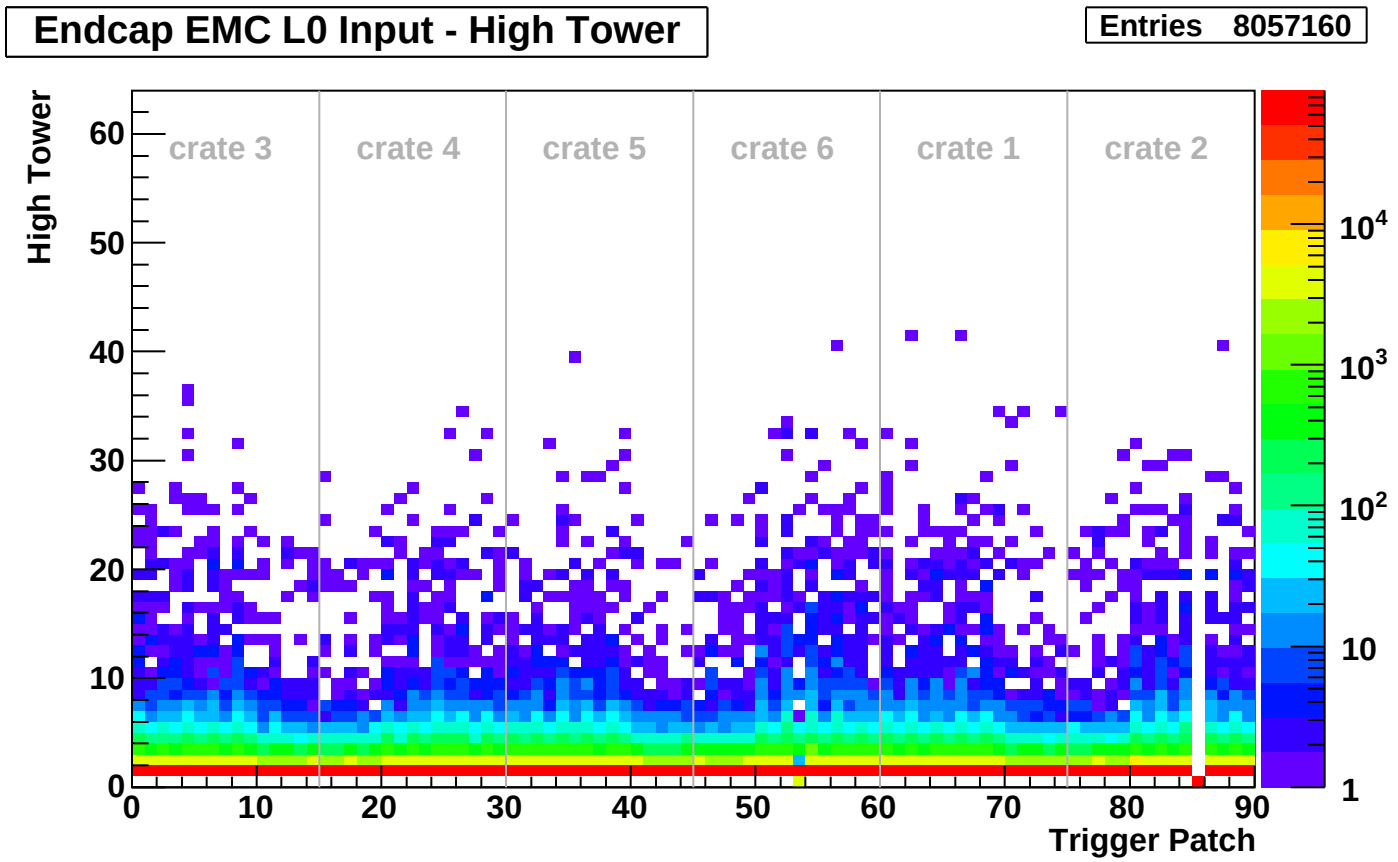

BunchId7Bit (all events)
Entries 89524

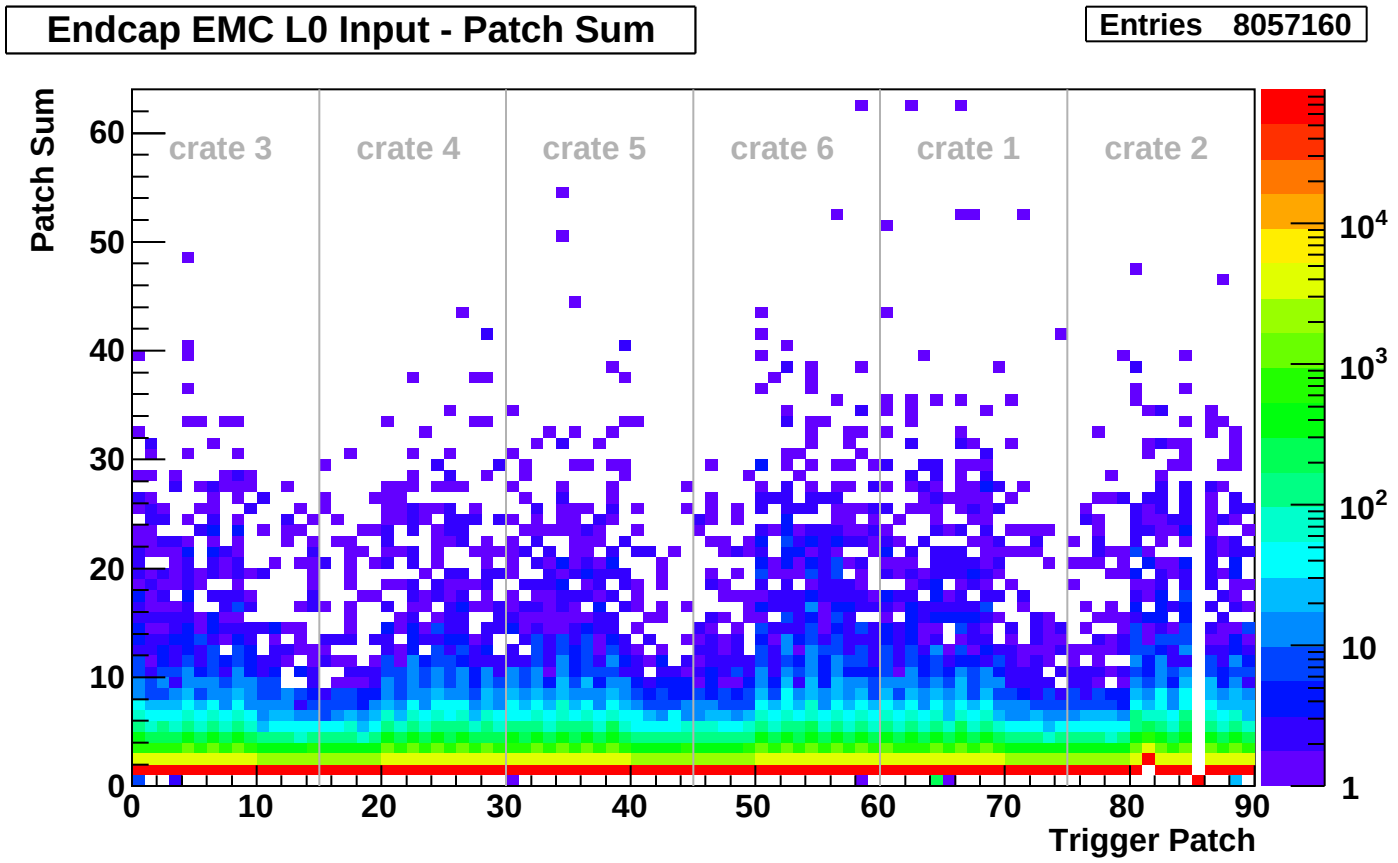

Abort-gaps
Entries 2606

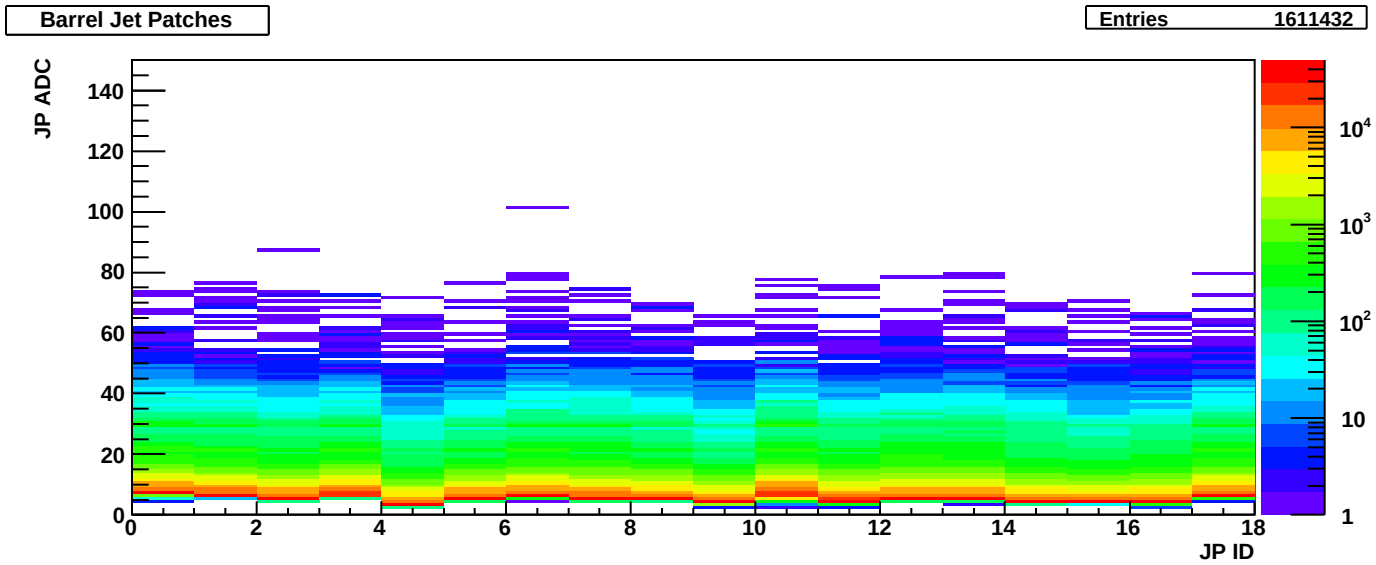

!Yellow-Fill
Entries 2696

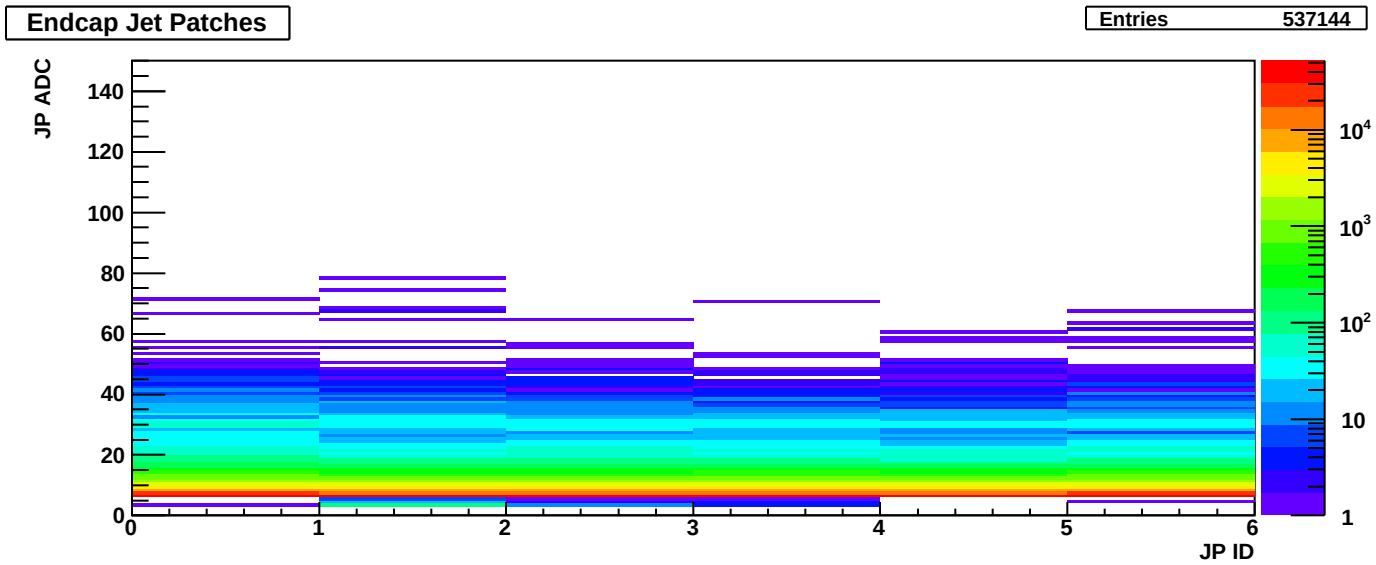

Barrel EMC L0 Input - High Tower
Entries 2.68572e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

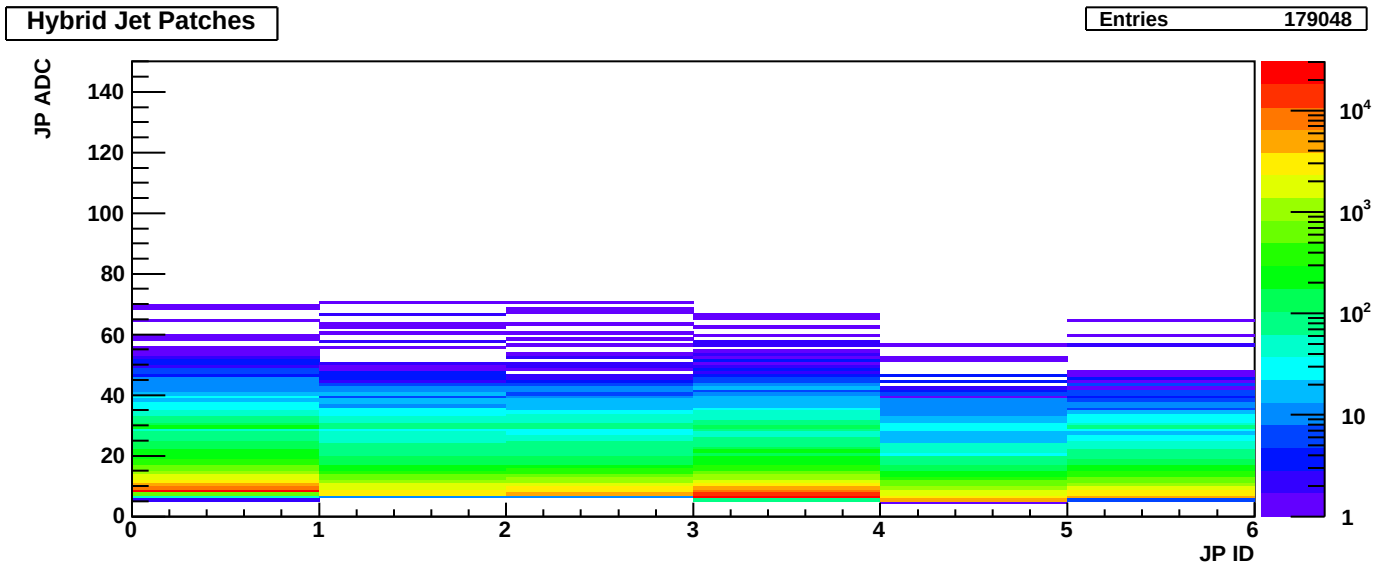

Barrel EMC L0 Input - Patch Sum
Entries 2.68572e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

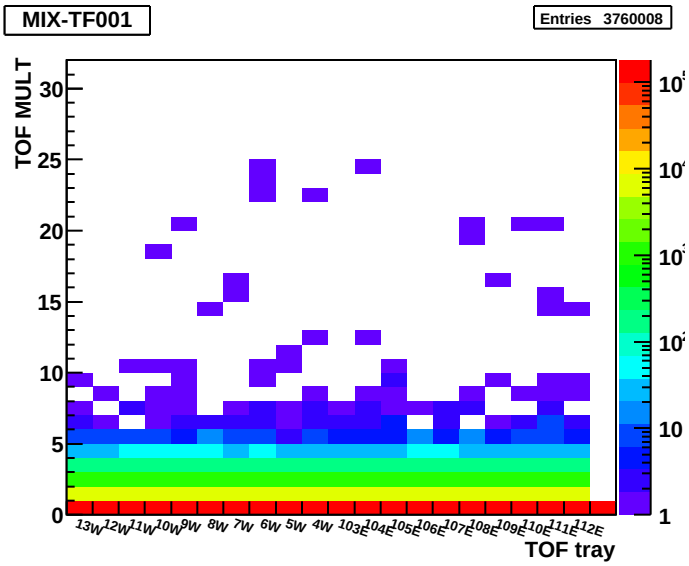

Endcap EMC L0 Input - High Tower
Entries 8057160
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

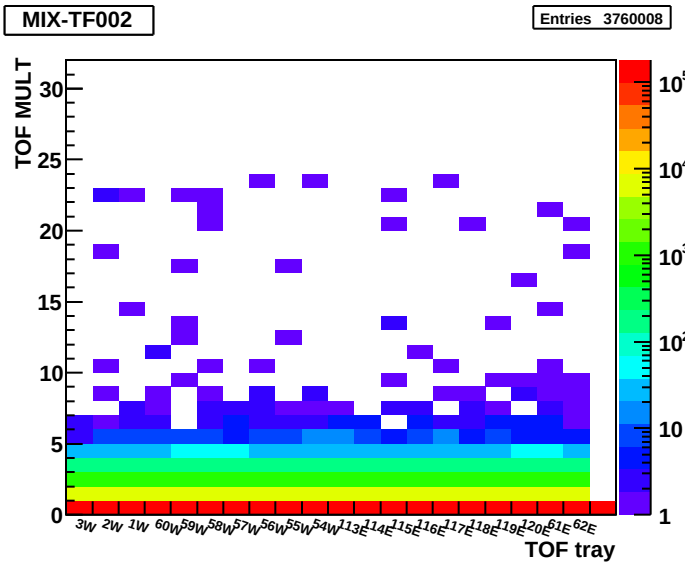

Endcap EMC L0 Input - Patch Sum
Entries 8057160
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

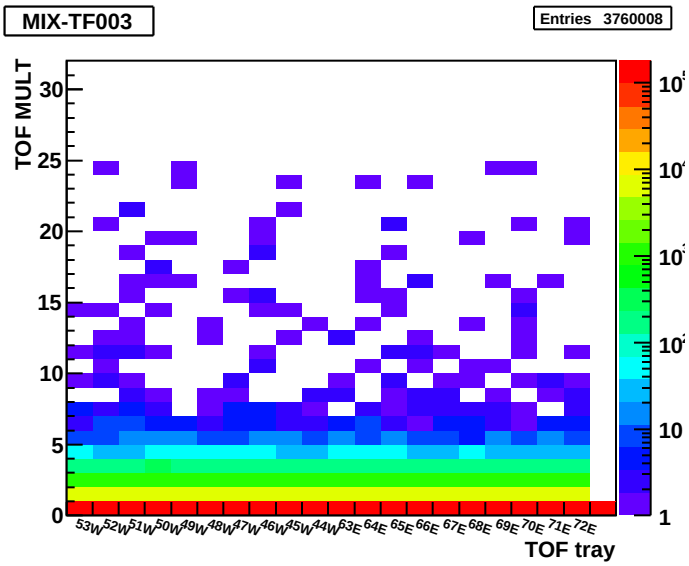

Barrel Jet Patches
Entries 1611432
JP ADC
JP ID

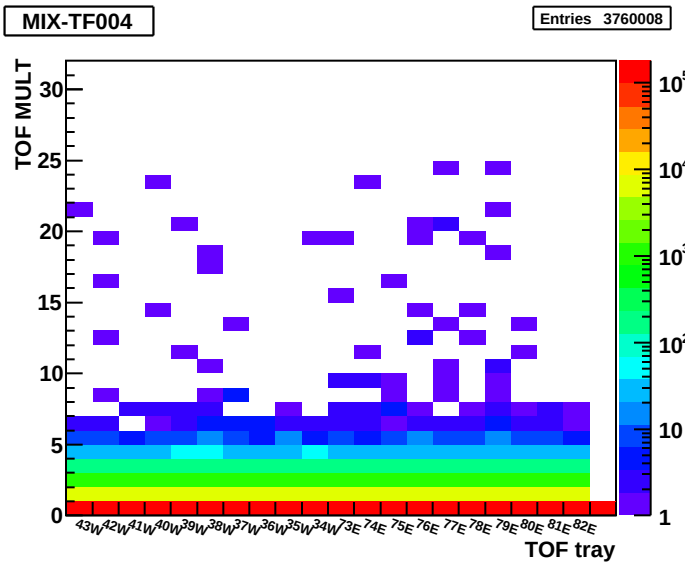

Endcap Jet Patches
Entries 537144
JP ADC
JP ID

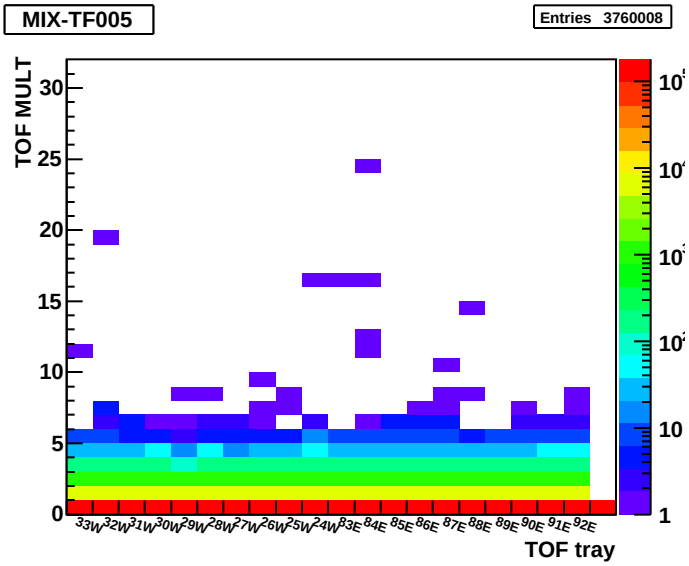

Hybrid Jet Patches
Entries 179048
JP ADC
JP ID

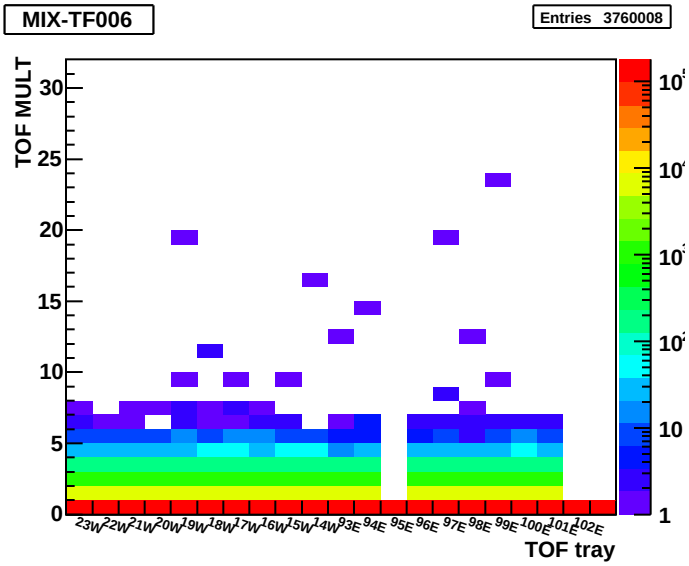

MIX-TF001
Entries 3760008
TOF MULT
TOF tray

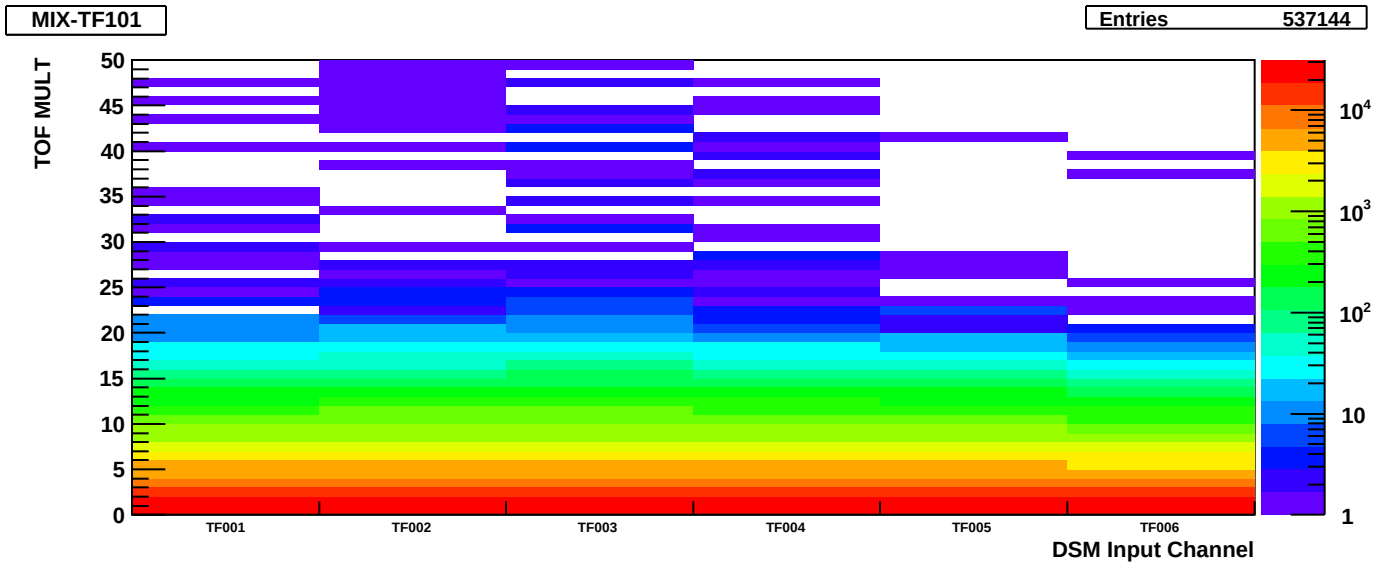

MIX-TF002
Entries 3760008
TOF MULT
TOF tray

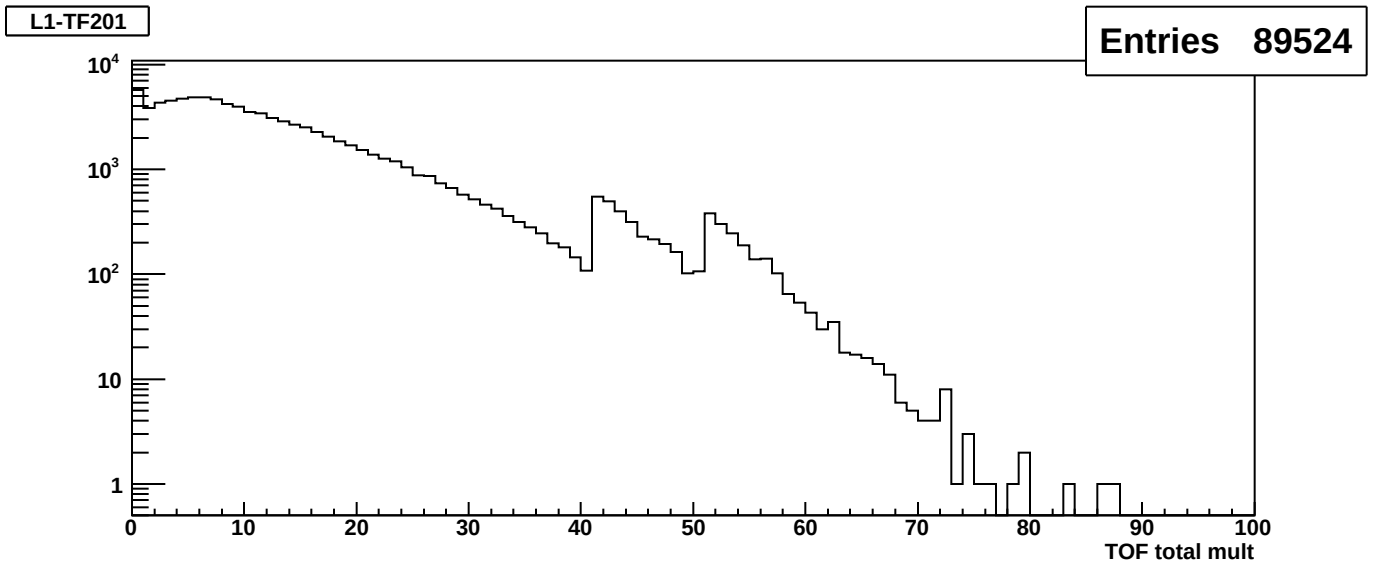

MIX-TF003
Entries 3760008
TOF MULT
TOF tray

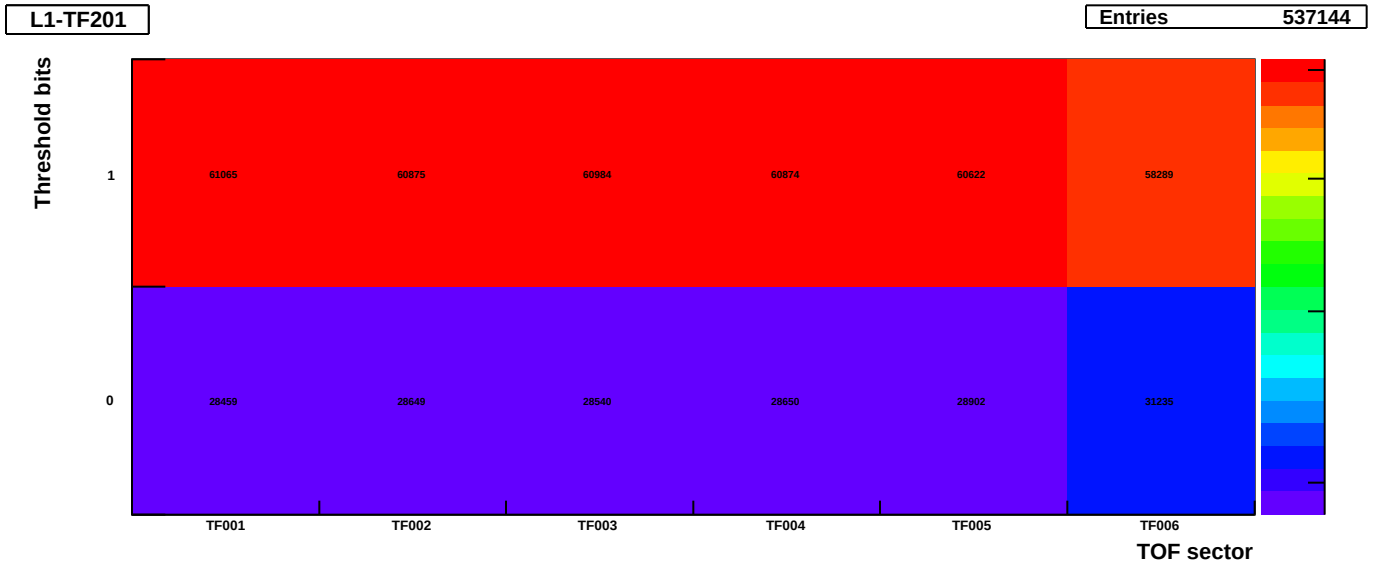

MIX-TF004
Entries 3760008
TOF MULT
TOF tray

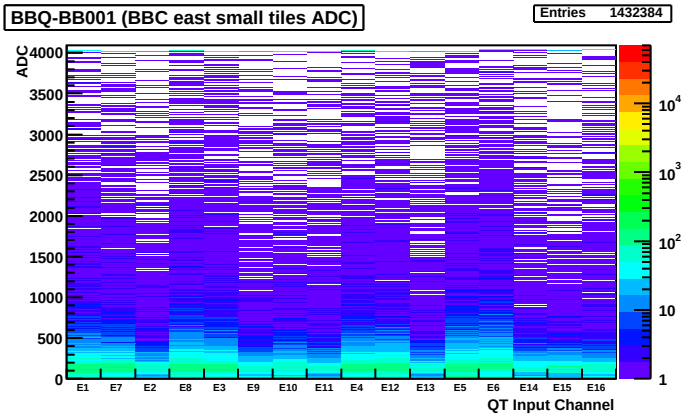

MIX-TF005
Entries 3760008
TOF MULT
TOF tray

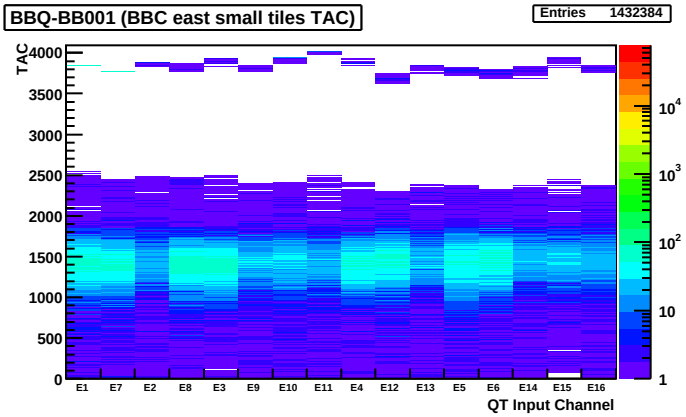

MIX-TF006
Entries 3760008
TOF MULT
TOF tray

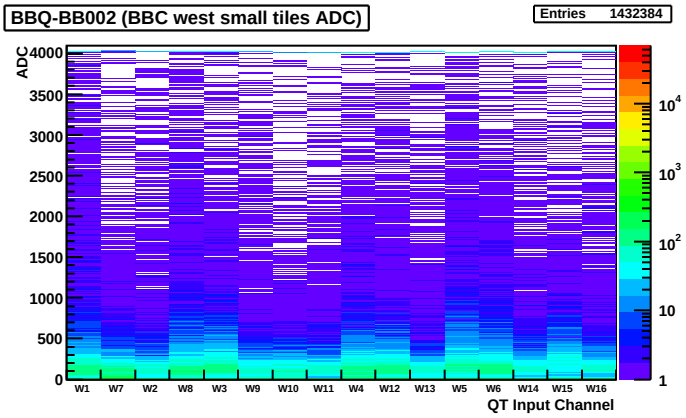

MIX-TF101
Entries 537144
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

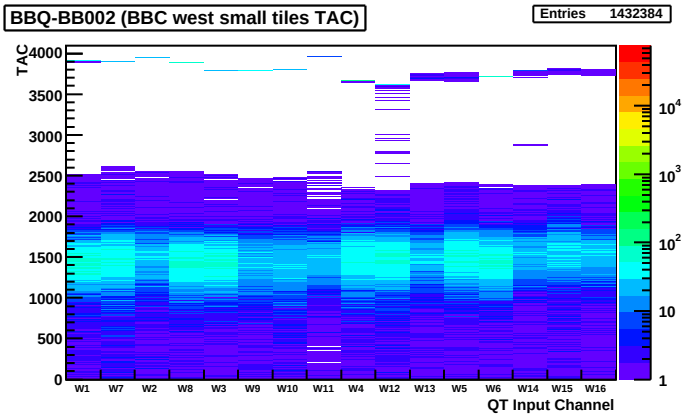

L1-TF201
Entries 89524
TOF total mult

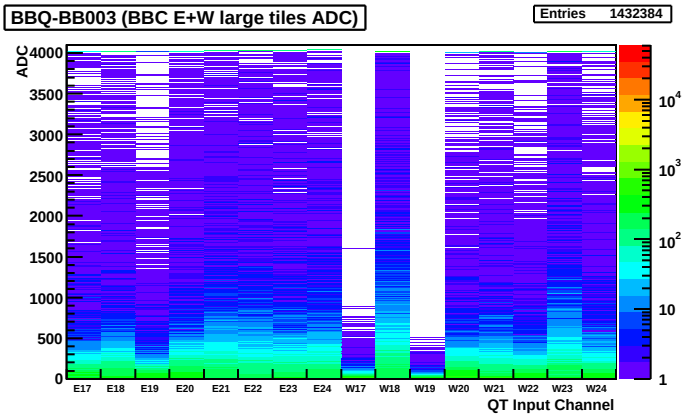

L1-TF201
Entries 537144
Threshold bits
61065
60875
60984
60874
60622
58289
28459
28649
28540
28650
28902
31235
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

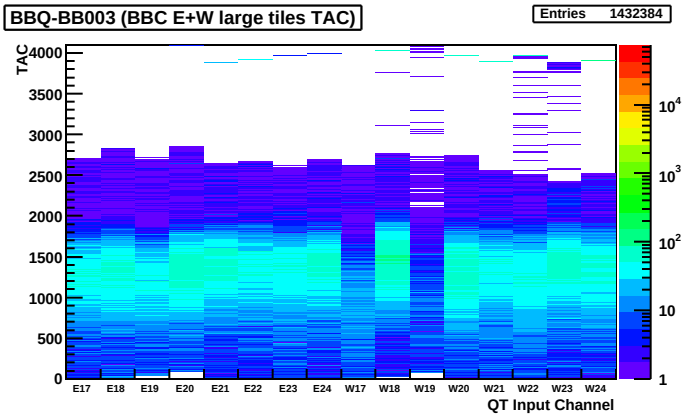

BBQ-BB001 (BBC east small tiles ADC)
Entries 1432384
ADC
QT Input Channel

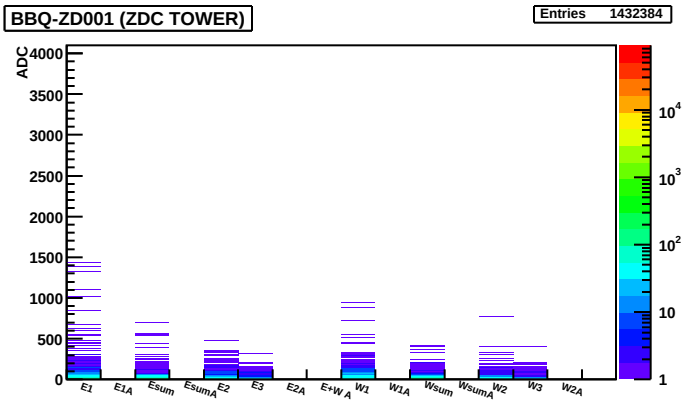

BBQ-BB001 (BBC east small tiles TAC)
Entries 1432384
TAC
QT Input Channel

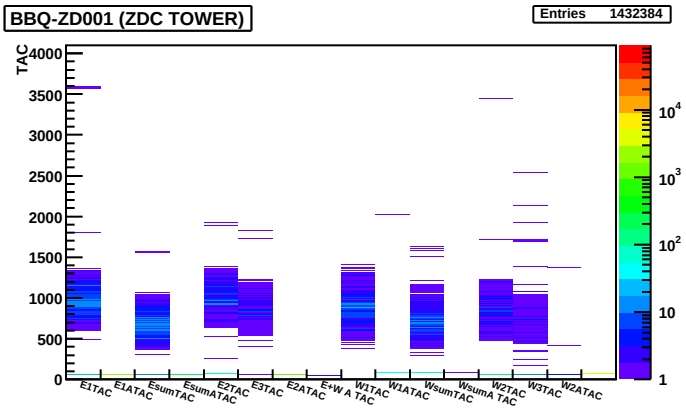

BBQ-BB002 (BBC west small tiles ADC)
Entries 1432384
ADC
QT Input Channel

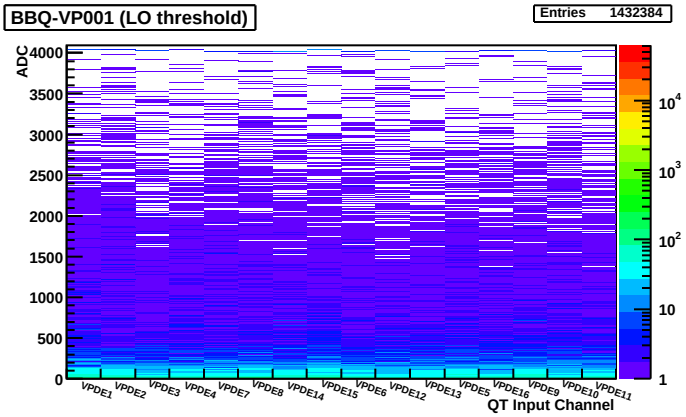

BBQ-BB002 (BBC west small tiles TAC)
Entries 1432384
TAC
QT Input Channel

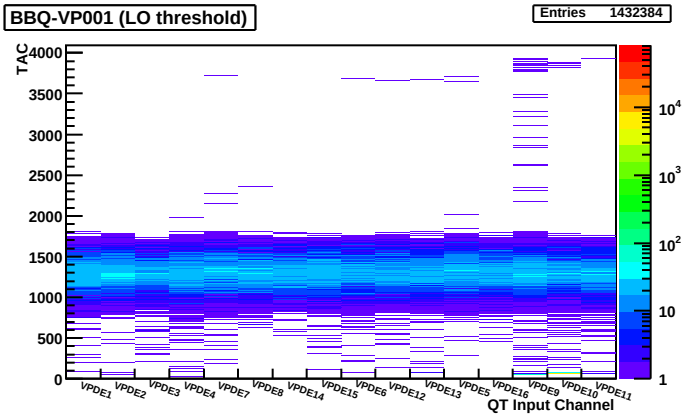

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1432384
ADC
QT Input Channel

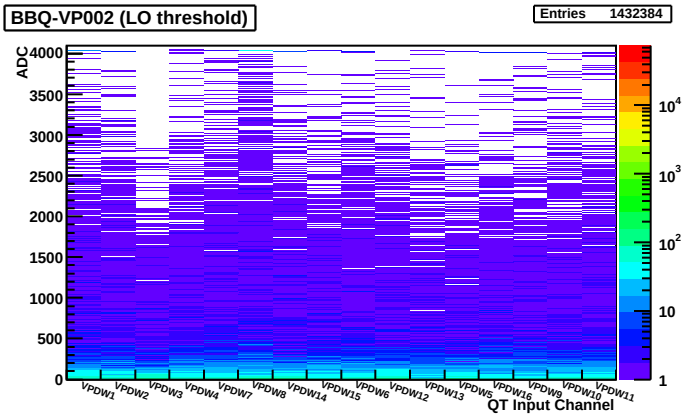

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1432384
TAC
QT Input Channel

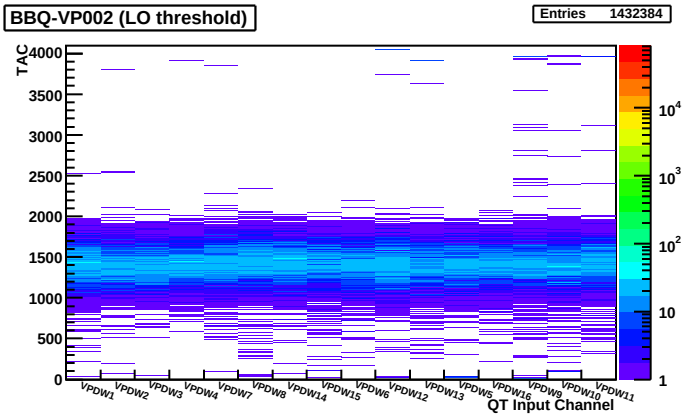

BBQ-ZD001 (ZDC TOWER)
Entries 1432384
ADC

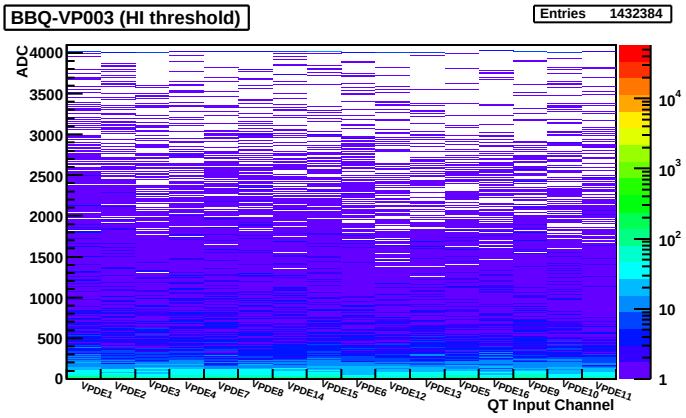

BBQ-ZD001 (ZDC TOWER)
Entries 1432384
TAC

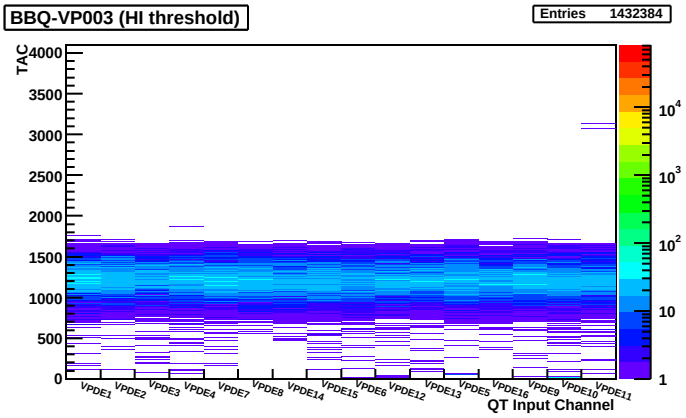

BBQ-VP001 (LO threshold)
Entries 1432384
ADC
QT Input Channel

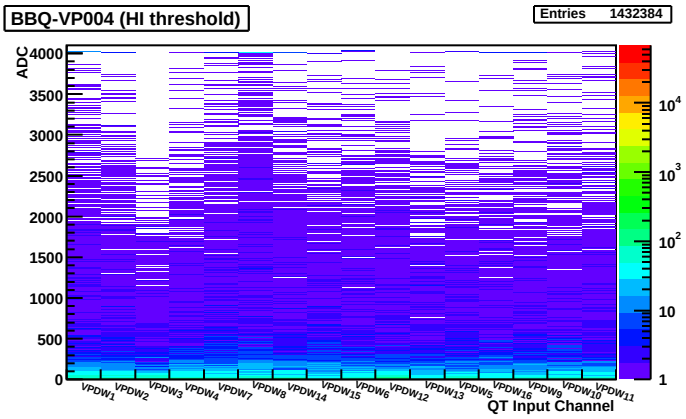

BBQ-VP001 (LO threshold)
Entries 1432384
TAC
QT Input Channel

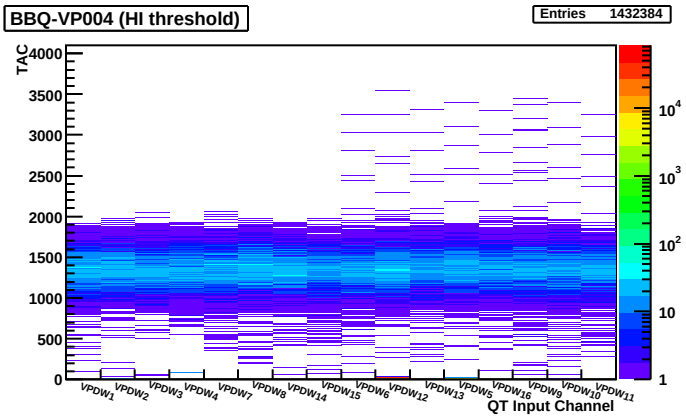

BBQ-VP002 (LO threshold)
Entries 1432384
ADC
QT Input Channel

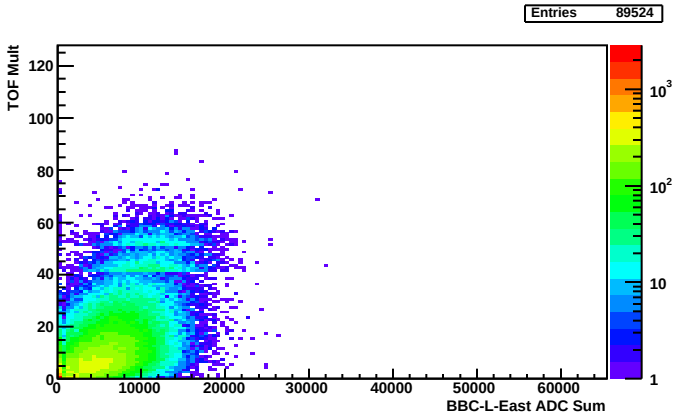

BBQ-VP002 (LO threshold)
Entries 1432384
TAC
QT Input Channel

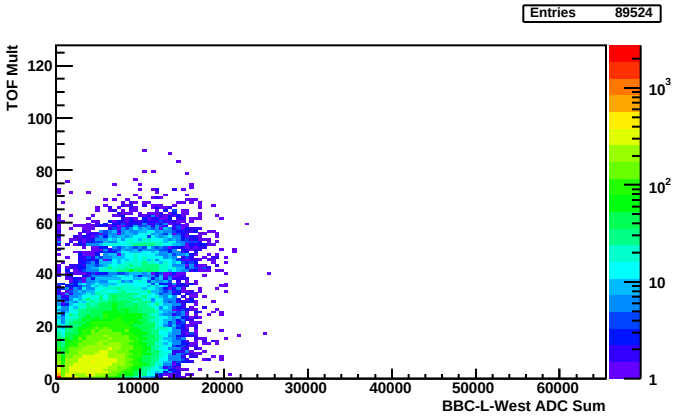

BBQ-VP003 (HI threshold)
Entries 1432384
ADC
QT Input Channel

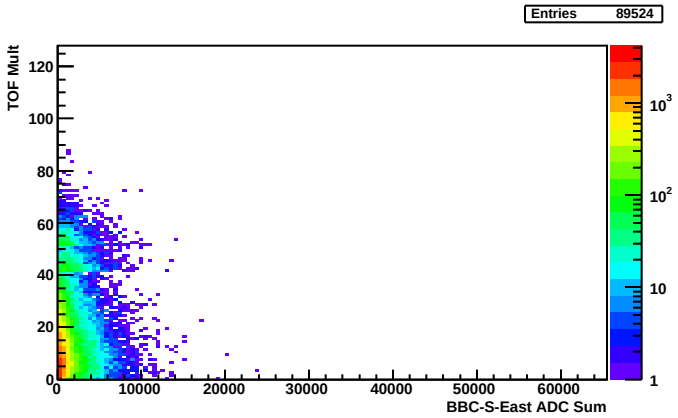

BBQ-VP003 (HI threshold)
Entries 1432384
TAC
QT Input Channel

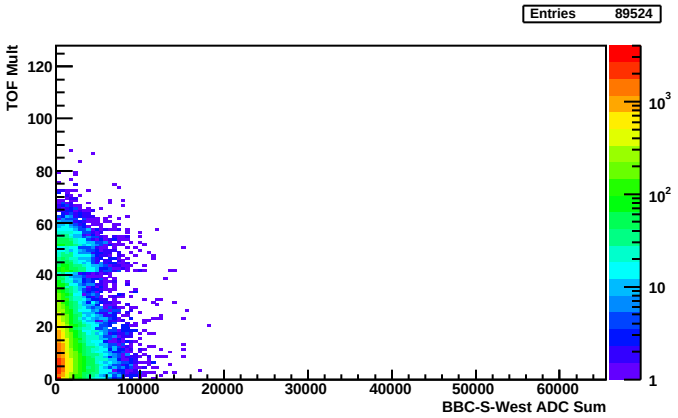

BBQ-VP004 (HI threshold)
Entries 1432384
ADC
QT Input Channel

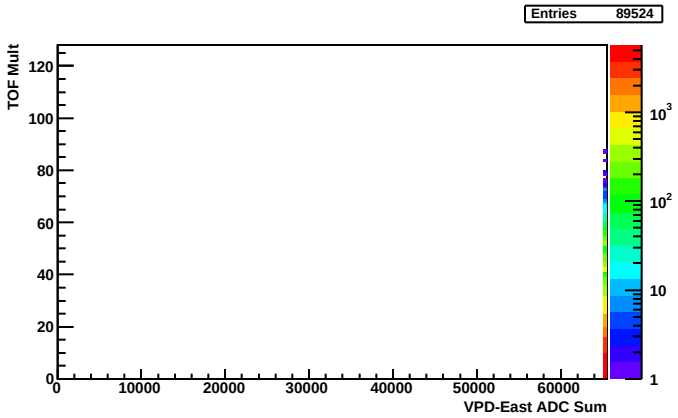

BBQ-VP004 (HI threshold)
Entries 1432384
TAC
QT Input Channel

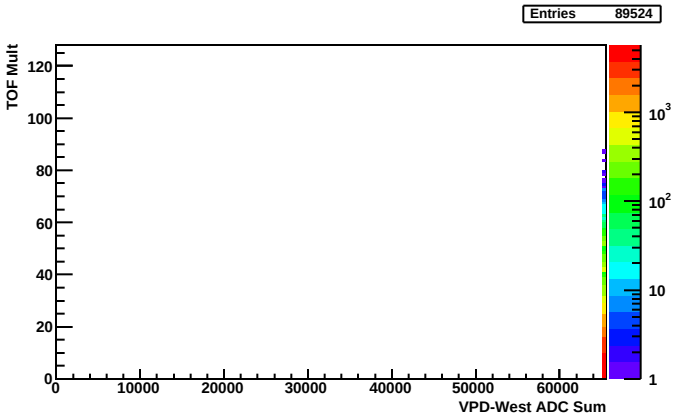

Entries 89524
TOF Mult
BBC-L-East ADC Sum

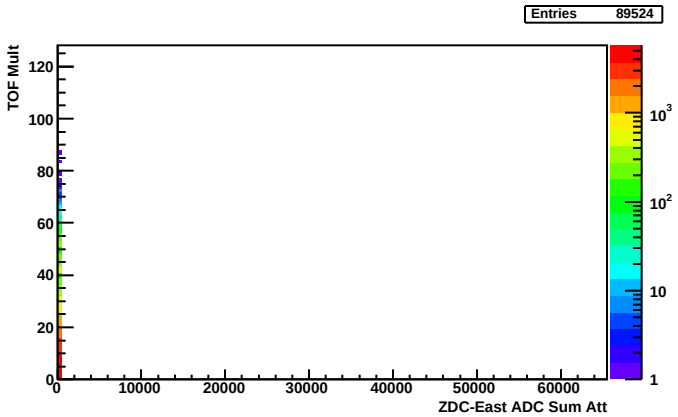

Entries 89524
TOF Mult
BBC-L-West ADC Sum

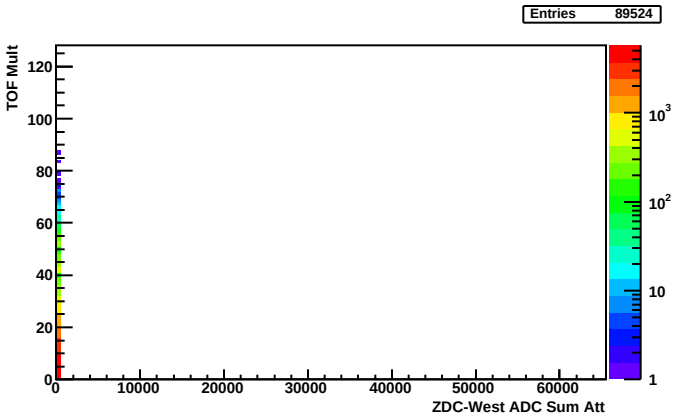

Entries 89524
TOF Mult
BBC-S-East ADC Sum

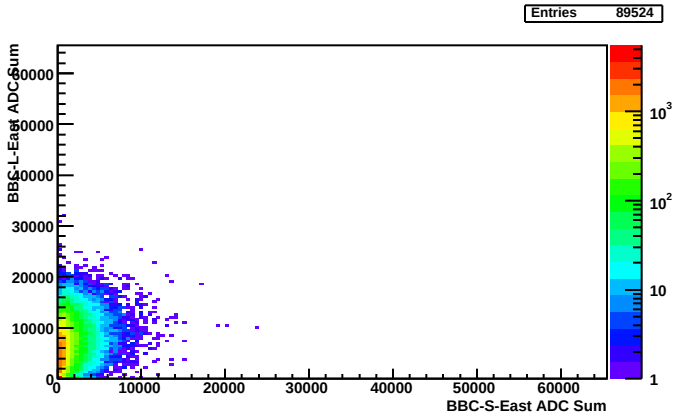

Entries 89524
TOF Mult
BBC-S-West ADC Sum

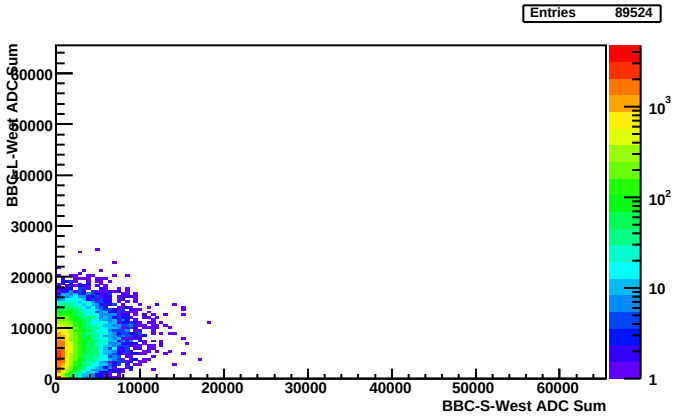

Entries 89524
TOF Mult
VPD-East ADC Sum

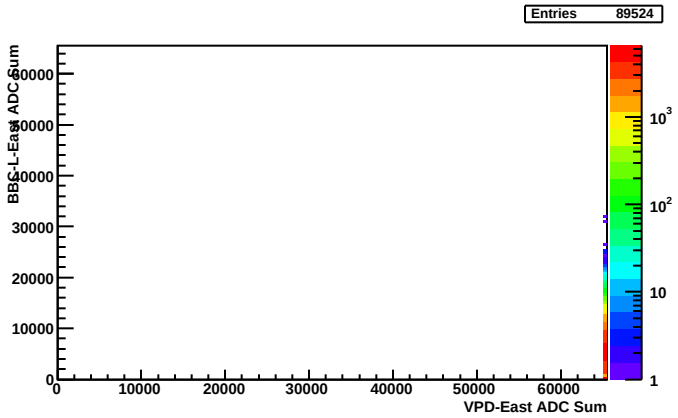

Entries 89524
TOF Mult
VPD-West ADC Sum

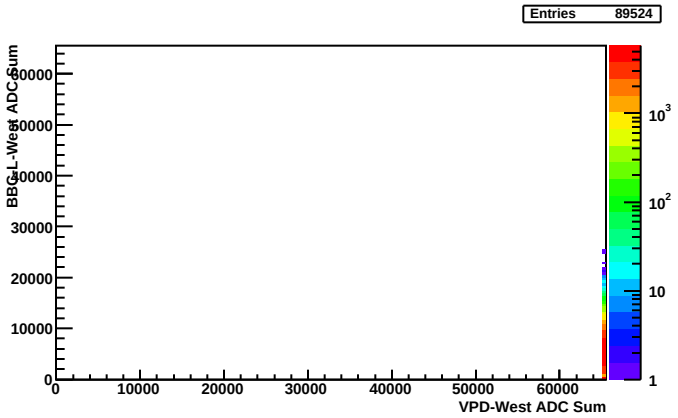

Entries 89524
TOF Mult
ZDC-East ADC Sum Att


Entries 89524
TOF Mult
ZDC-West ADC Sum Att


Entries 89524
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 89524
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 89524
BBC-L-East ADC Sum
VPD-East ADC Sum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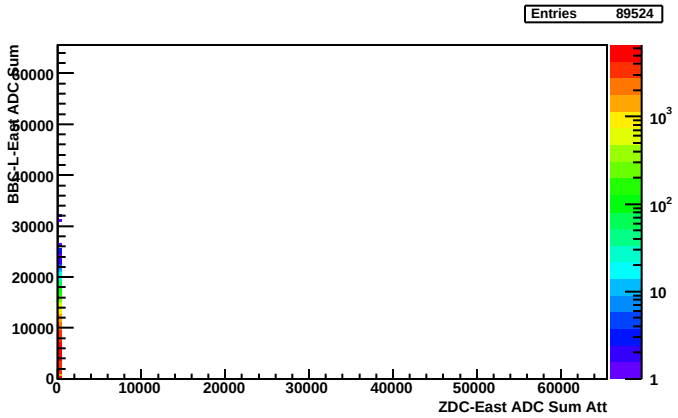
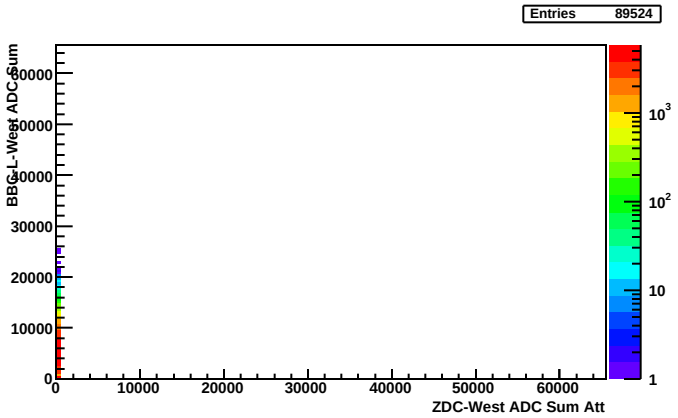
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 89524
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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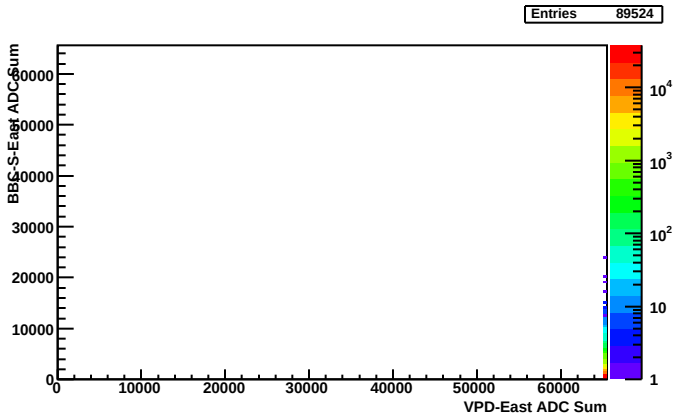
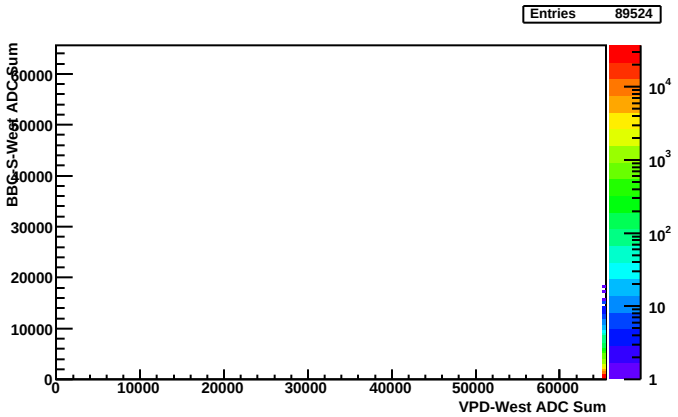
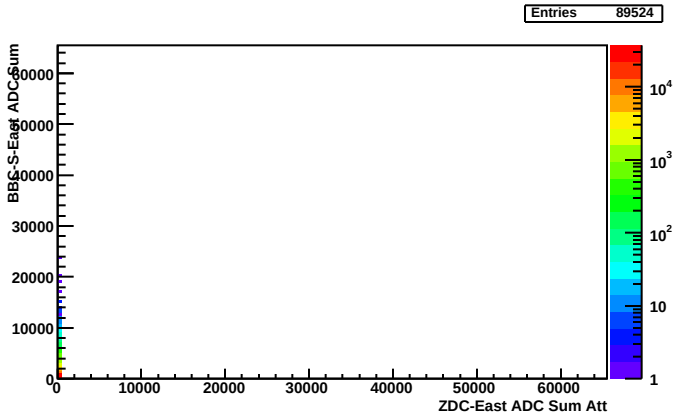
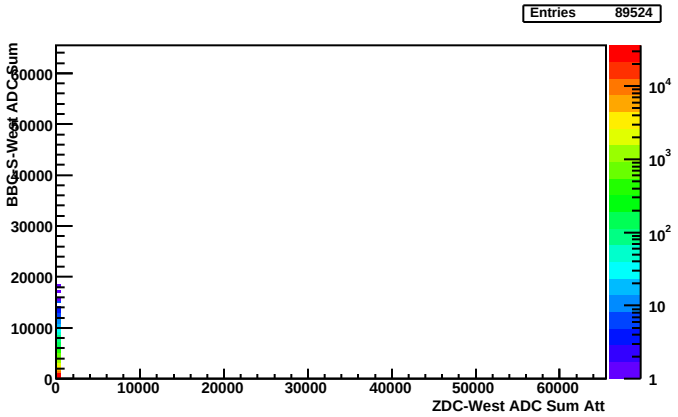
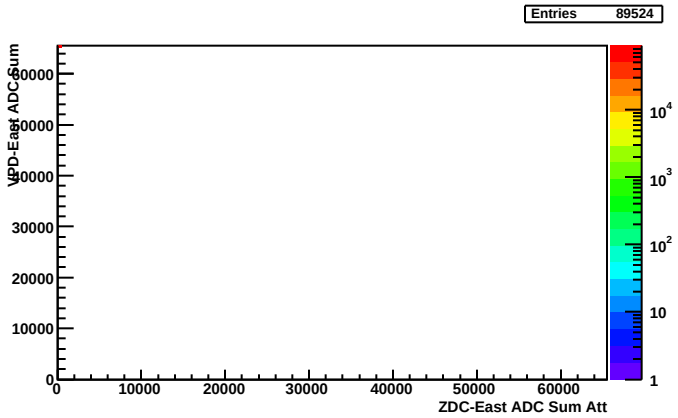
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

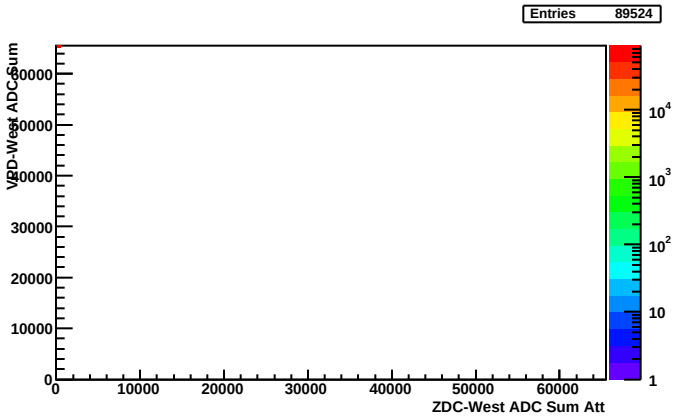

Entries 89524
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 89524
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 89524
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 89524
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 89524
VPD-East ADC Sum
ZDC-East ADC Sum Att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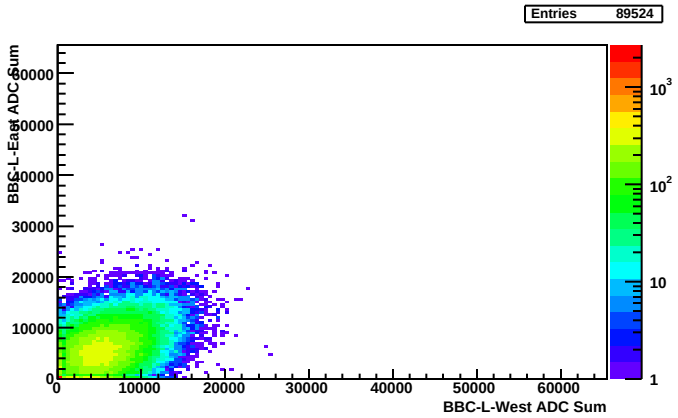
VPD-West ADC Sum
ZDC-West ADC Sum Att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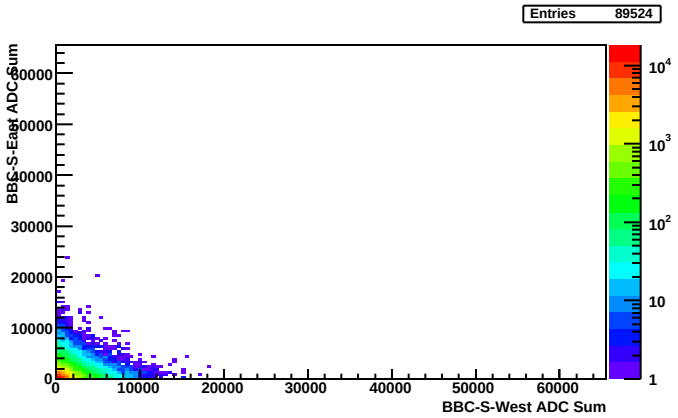
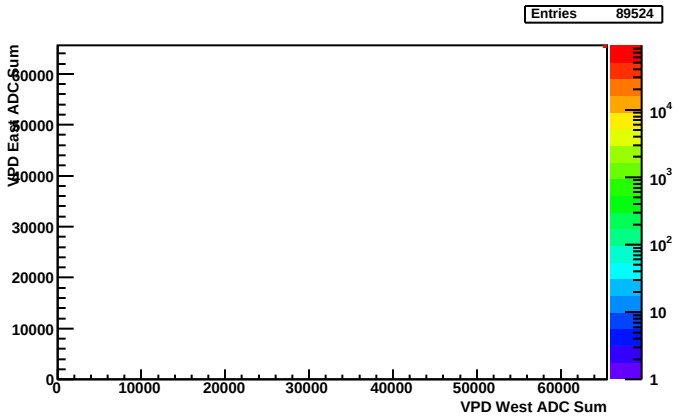
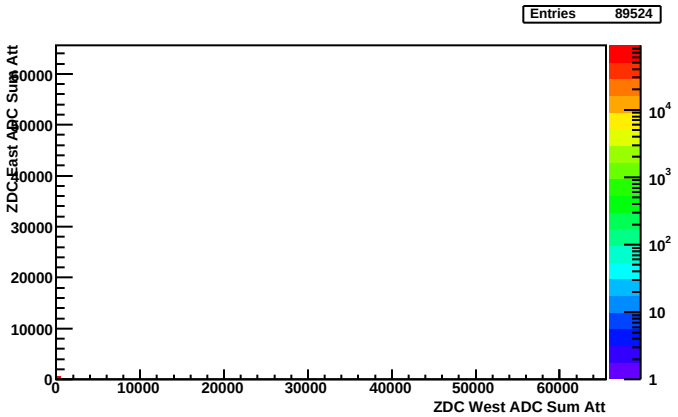
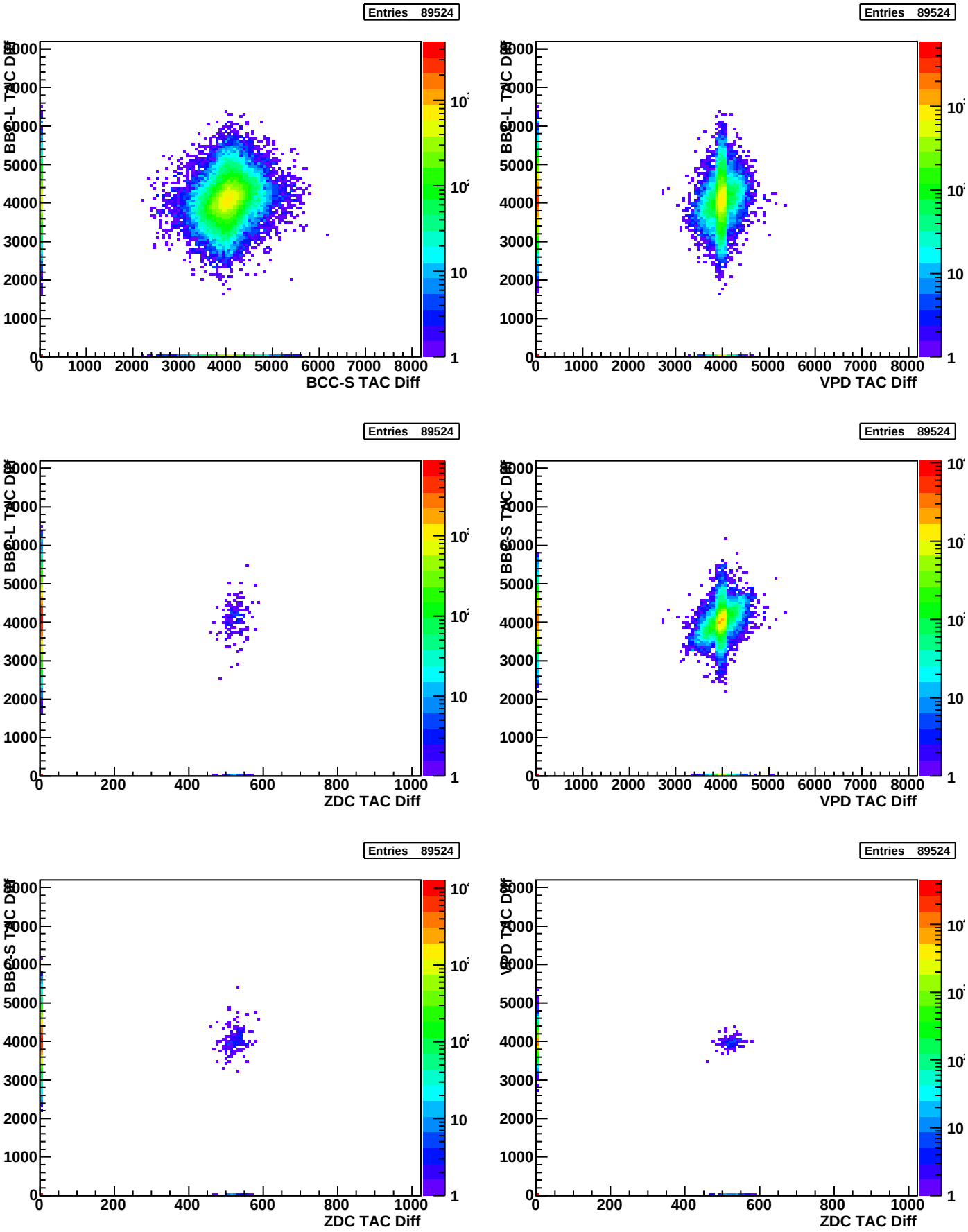
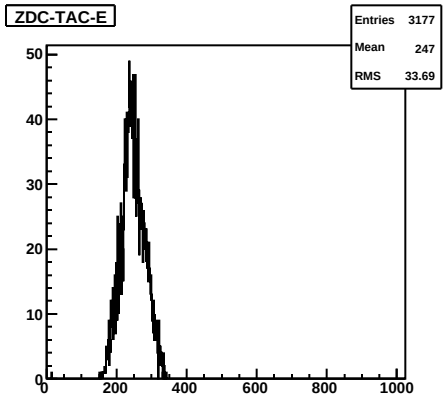
BBC-L-East ADC Sum
BBC-L-West ADC Sum

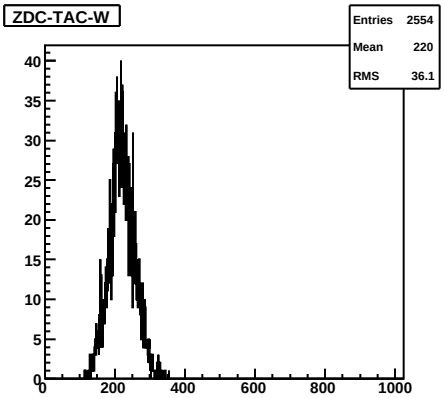

Entries 89524
BBC-S-East ADC Sum
BBC-S-West ADC Sum

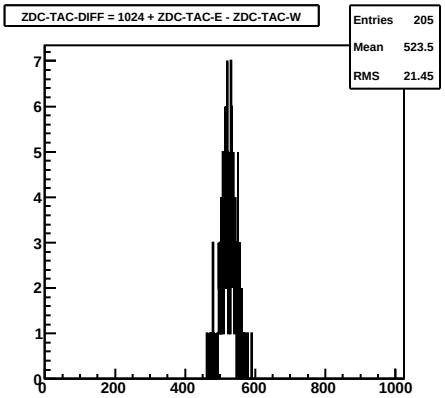

Entries 89524
VPD East ADC Sum
VPD West ADC Sum

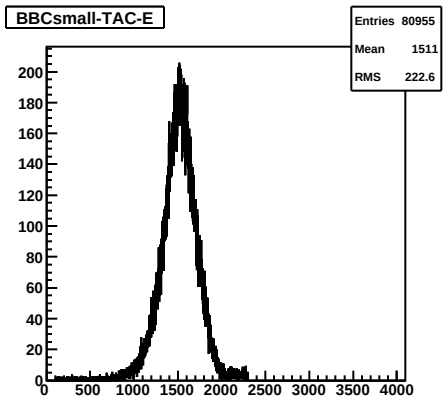

Entries 89524
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

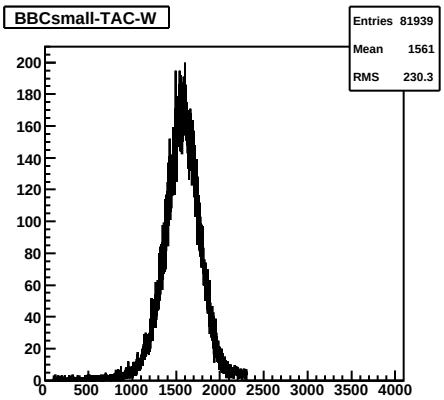

Entries 89524
BBC-L TAC Diff
BCC-S TAC Diff
Entries 89524
BBC-L TAC Diff
VPD TAC Diff
Entries 89524
BBC-L TAC Diff
ZDC TAC Diff
Entries 89524
BBC-S TAC Diff
VPD TAC Diff
Entries 89524
BBC-S TAC Diff
ZDC TAC Diff
Entries 89524
VPD TAC Diff
ZDC TAC Diff

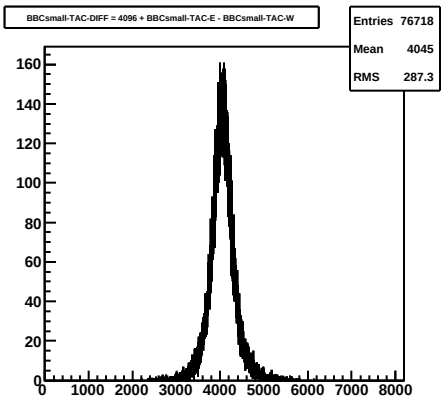

ZDC-TAC-E
Entries 3177
Mean 247
RMS 33.69

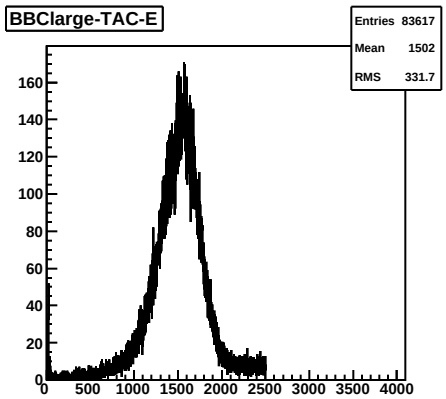

ZDC-TAC-W
Entries 2554
Mean 220
RMS 36.1

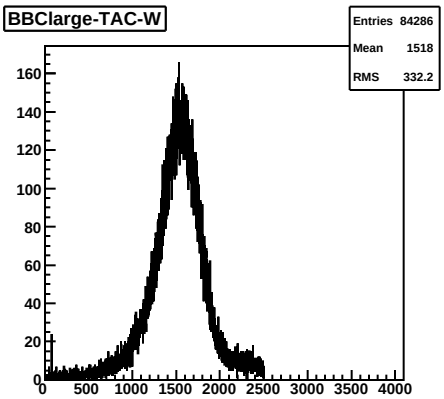

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 205
Mean 523.5
RMS 21.45

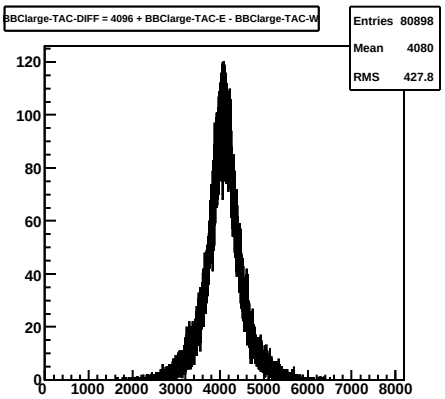

BBCsmall-TAC-E
Entries 80955
Mean 1511
RMS 222.6

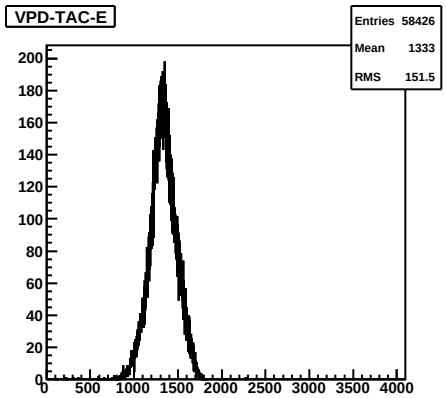

BBCsmall-TAC-W
Entries 81939
Mean 1561
RMS 230.3

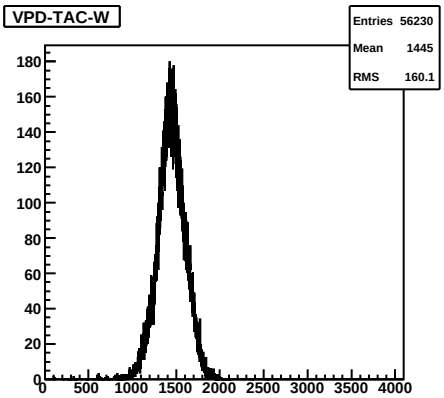

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 76718
Mean 4045
RMS 287.3

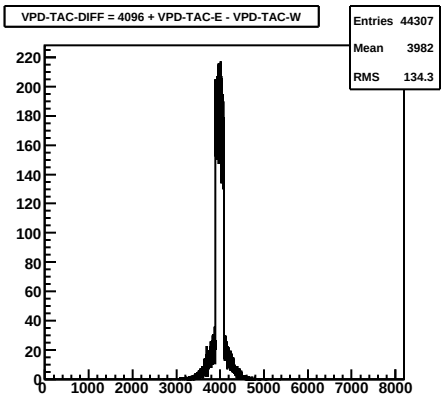

BBClarge-TAC-E
Entries 83617
Mean 1502
RMS 331.7

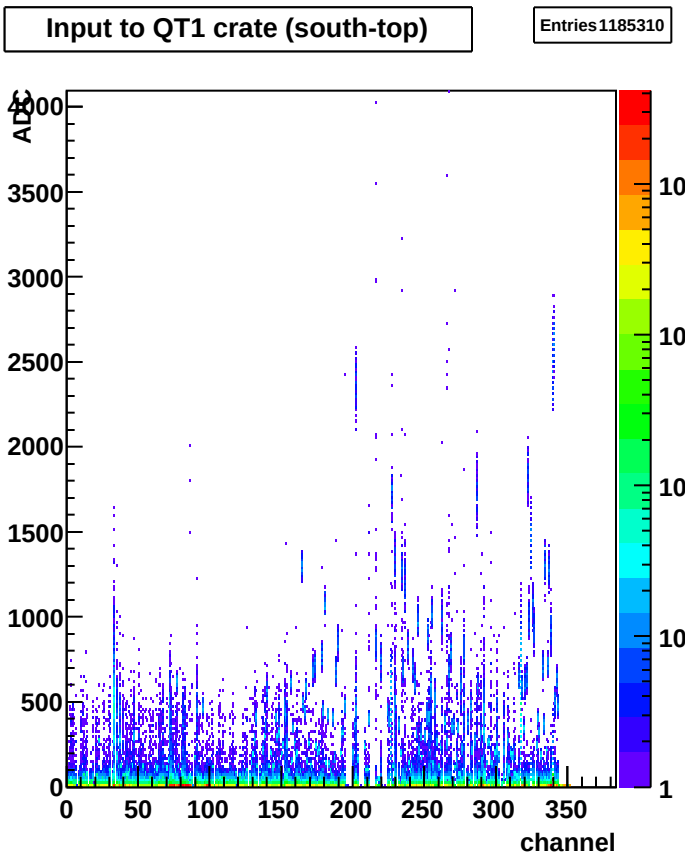

BBClarge-TAC-W
Entries 84286
Mean 1518
RMS 332.2

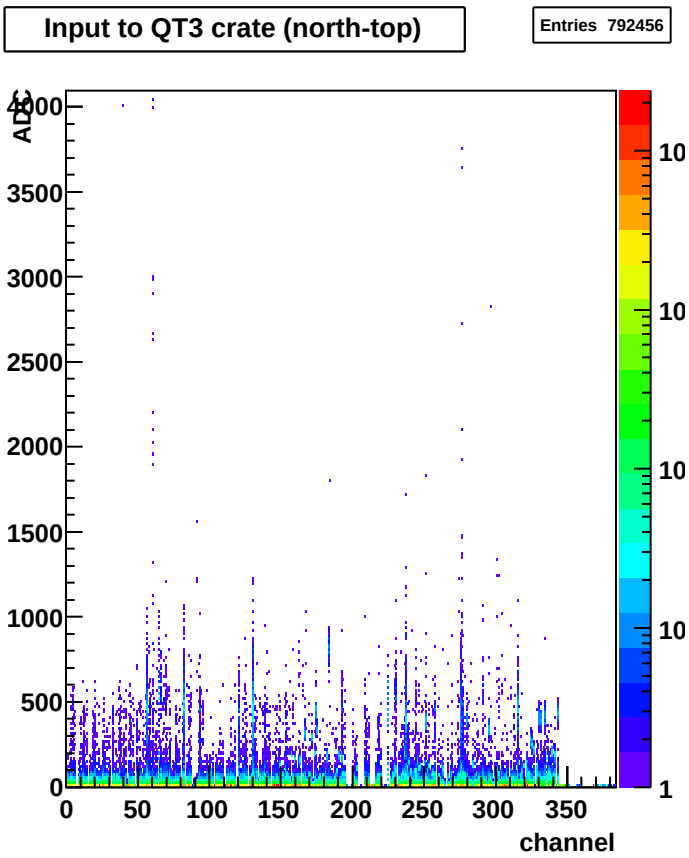

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 80898
Mean 4080
RMS 427.8

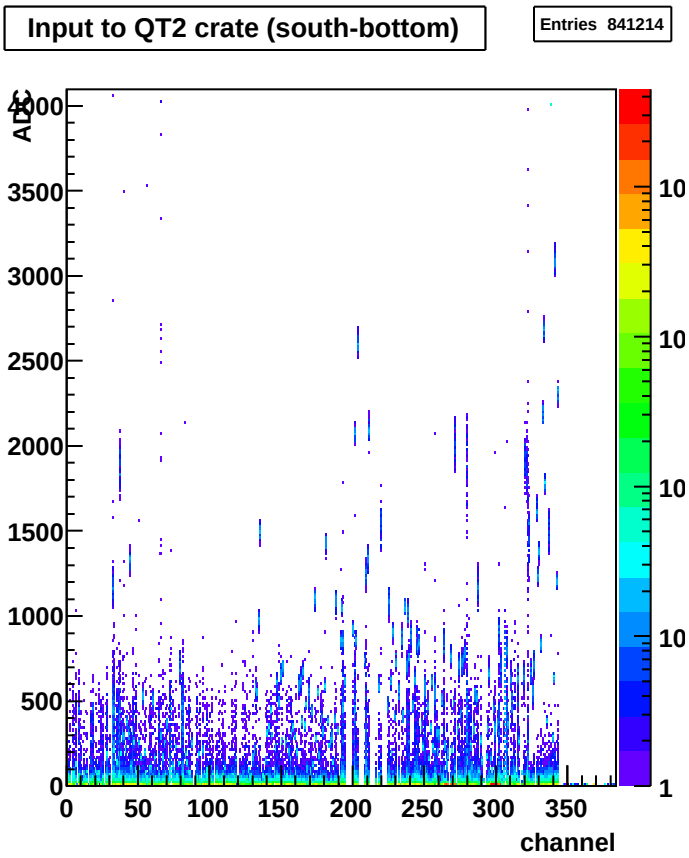

VPD-TAC-E
Entries 58426
Mean 1333
RMS 151.5

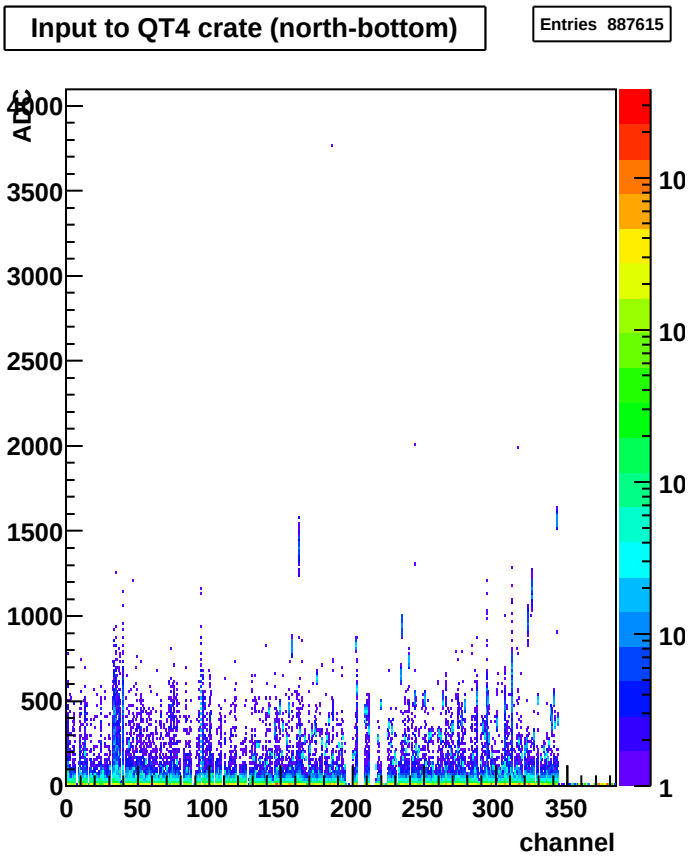

VPD-TAC-W
Entries 56230
Mean 1445
RMS 160.1

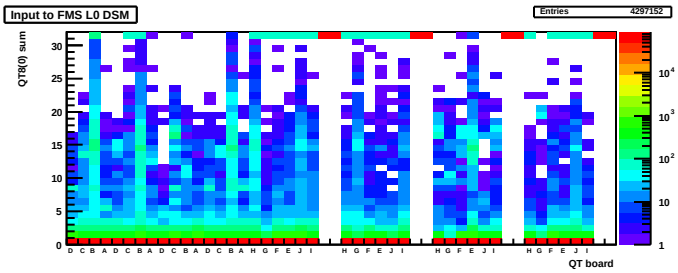

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 44307
Mean 3982
RMS 134.3

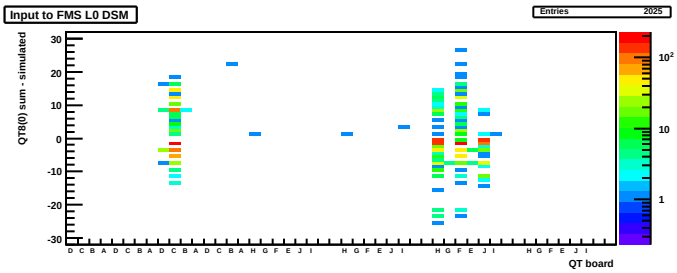

Input to QT1 crate (south-top)
Entries 1185310
ADC
channel

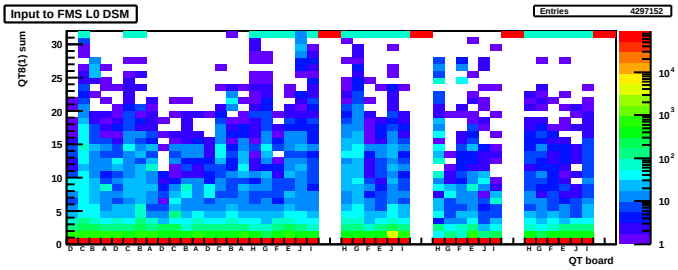

Input to QT3 crate (north-top)
Entries 792456
ADC
channel

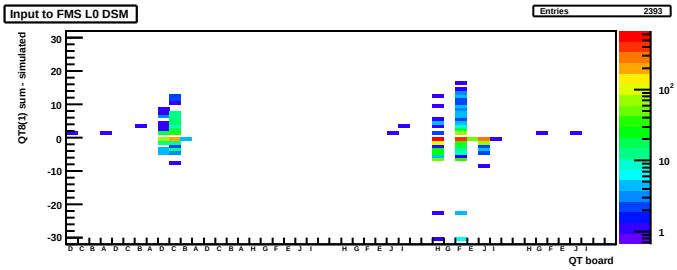

Input to QT2 crate (south-bottom)
Entries 841214
ADC
channel

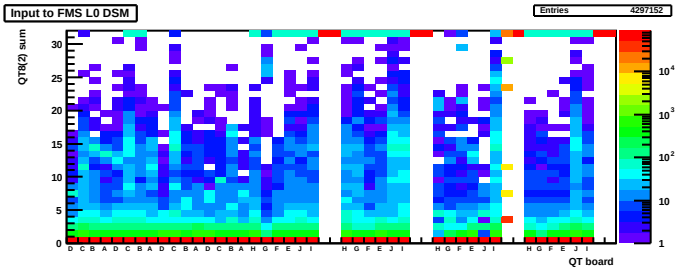

Input to QT4 crate (north-bottom)
Entries 887615
ADC
channel

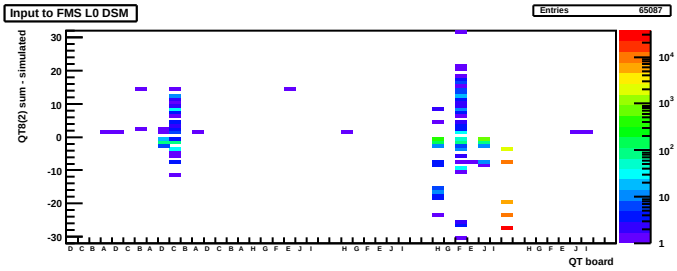

Input to FMS L0 DSM
Entries 4297152
QT8(0) sum
QT board

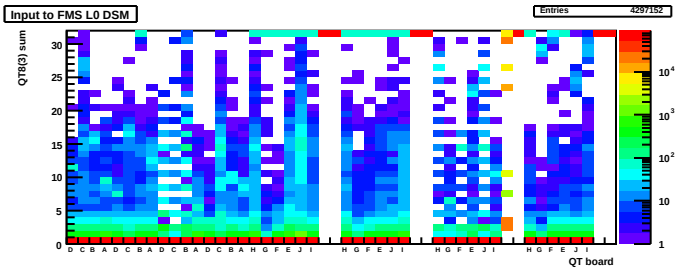

Input to FMS L0 DSM
Entries 2025
QT8(0) sum - simulated
QT board

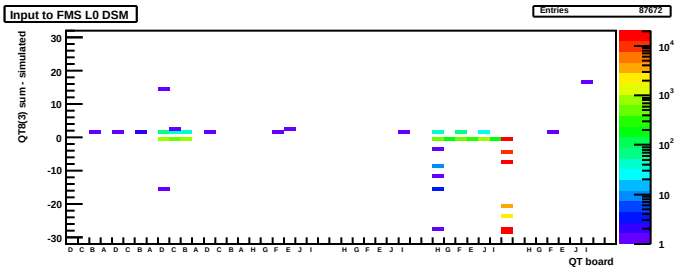

Input to FMS L0 DSM
Entries 4297152
QT8(1) sum
QT board

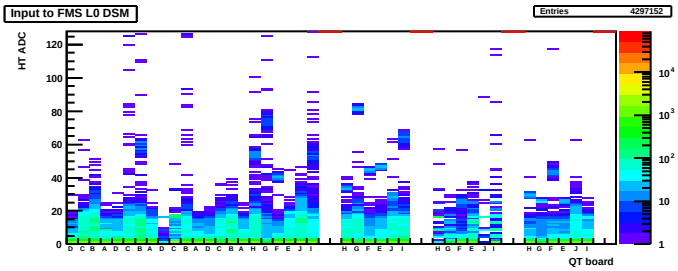

Input to FMS L0 DSM
Entries 2393
QT8(1) sum - simulated
QT board

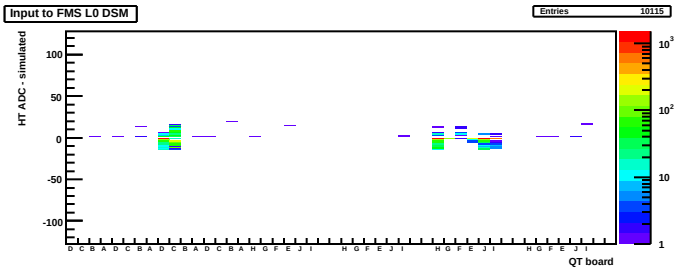

Input to FMS L0 DSM
Entries 4297152
QT8(2) sum
QT board

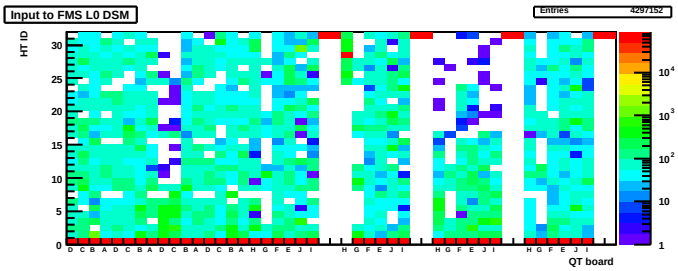

Input to FMS L0 DSM
Entries 65087
QT8(2) sum - simulated
QT board

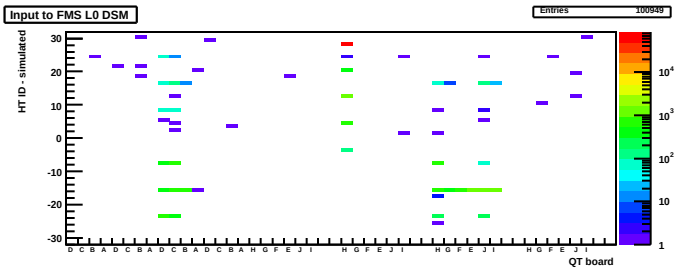

Input to FMS L0 DSM
Entries 4297152
QT8(3) sum
QT board

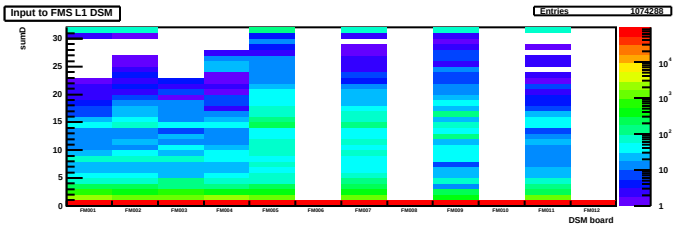

Input to FMS L0 DSM
Entries 87672
QT8(3) sum - simulated
QT board

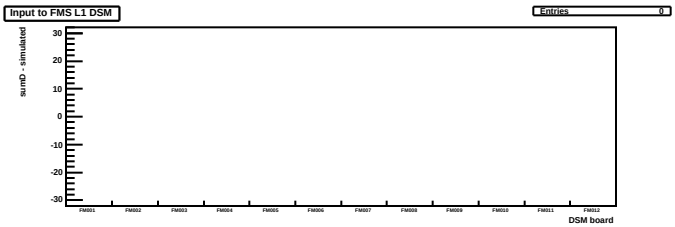

Input to FMS L0 DSM
Entries 4297152
HT ADC
QT board

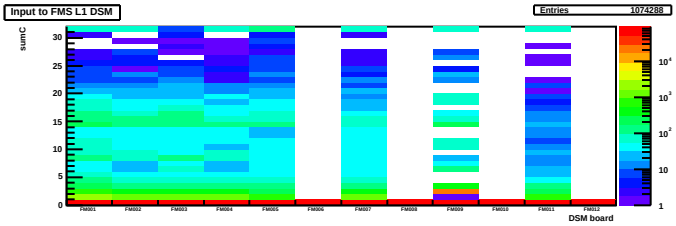

Input to FMS L0 DSM
Entries 10115
HT ADC - simulated
QT board

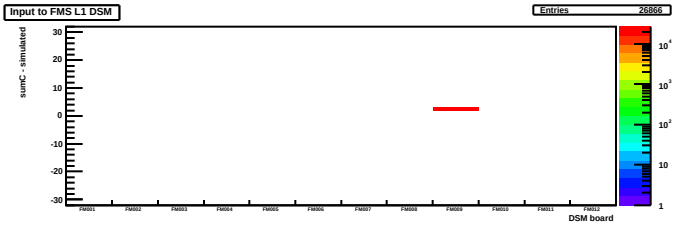

Input to FMS L0 DSM
Entries 4297152
HT ID
QT board

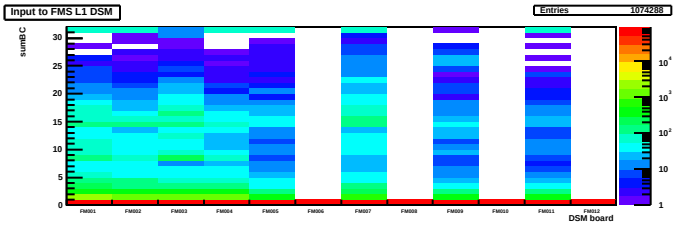

Input to FMS L0 DSM
Entries 100949
HT ID - simulated
QT board

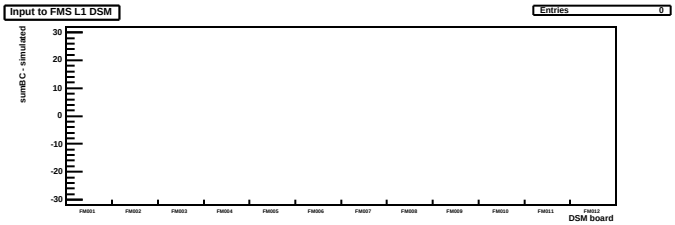

Input to FMS L1 DSM
Entries 1074288
sumD
DSM board

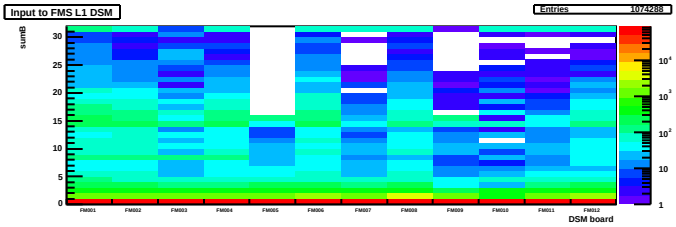

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

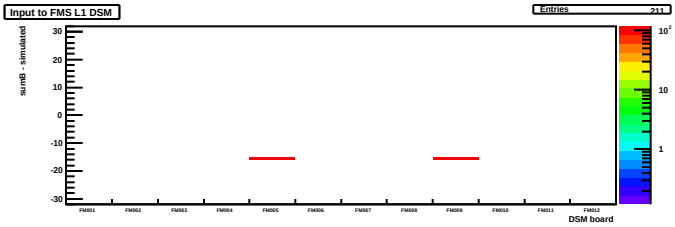

Input to FMS L1 DSM
Entries 1074288
sumC
DSM board

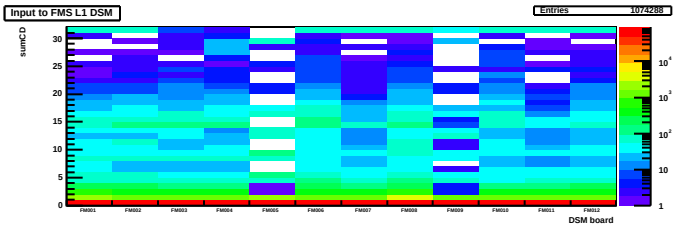

Input to FMS L1 DSM
Entries 26866
sumC - simulated
DSM board

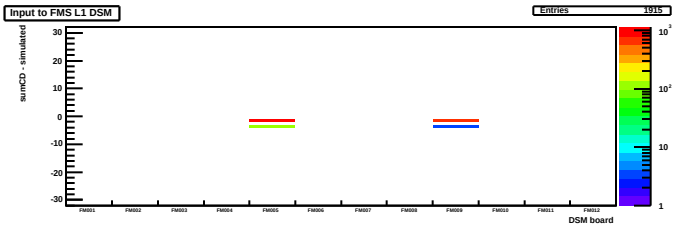

Input to FMS L1 DSM
Entries 1074288
sumBC
DSM board

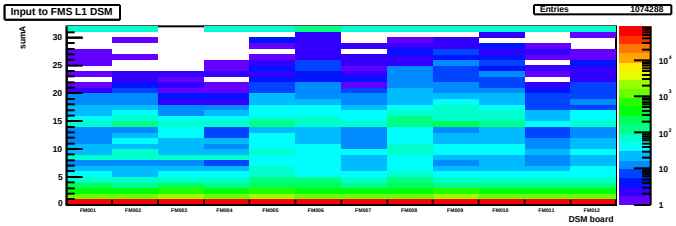

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

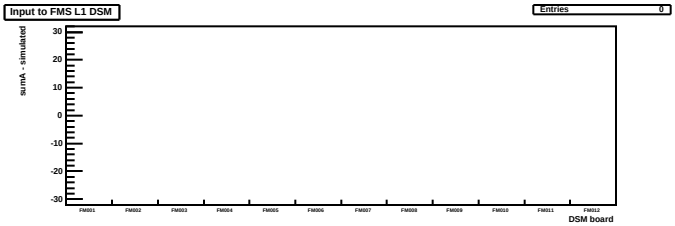

Input to FMS L1 DSM
Entries 1074288
sumB
DSM board

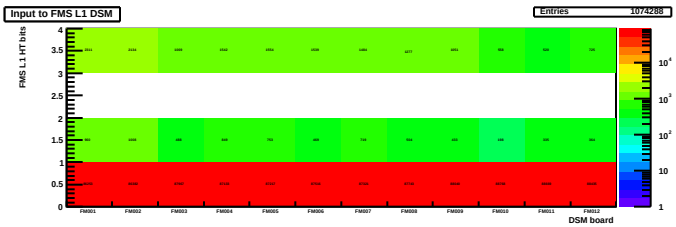

Input to FMS L1 DSM
Entries 211
sumB - simulated
DSM board

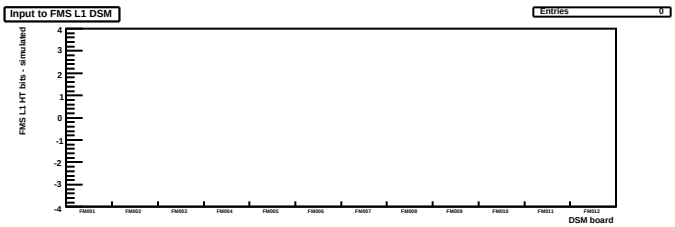

Input to FMS L1 DSM
Entries 1074288
sumCD
DSM board

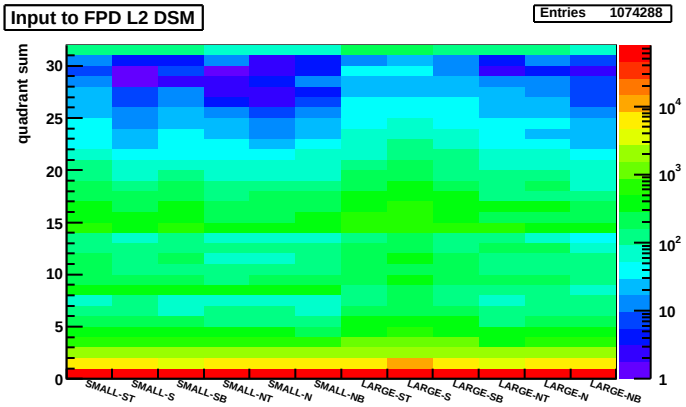

Input to FMS L1 DSM
Entries 1915
sumCD - simulated
DSM board

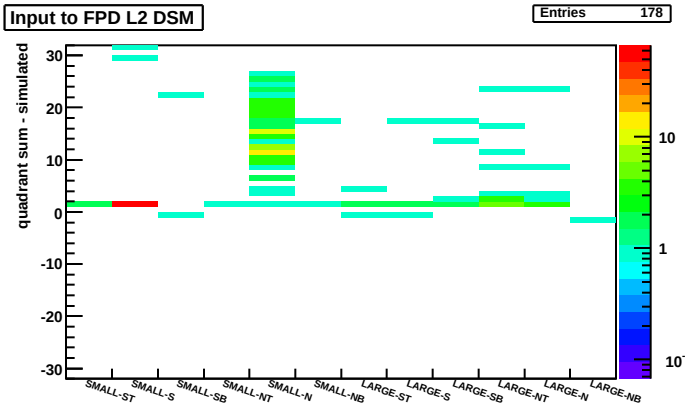

Input to FMS L1 DSM
Entries 1074288
sumA
DSM board

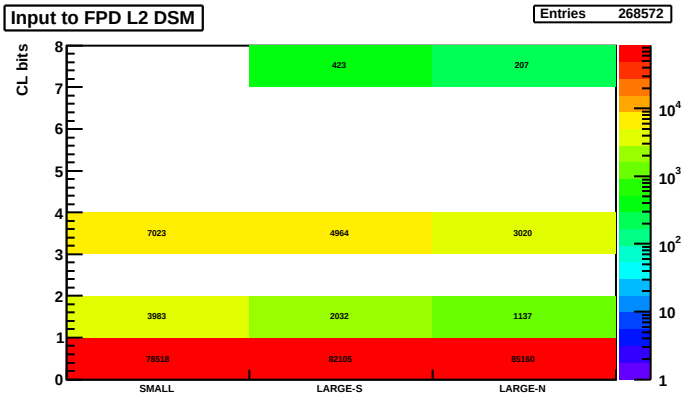

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

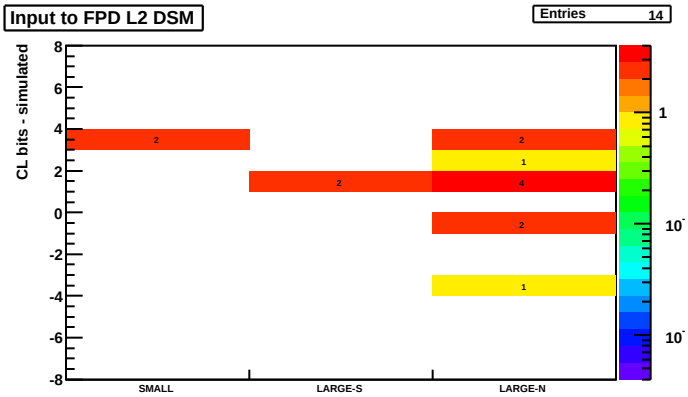

Input to FMS L1 DSM
Entries 1074288
FMS L1 HT bits
DSM board

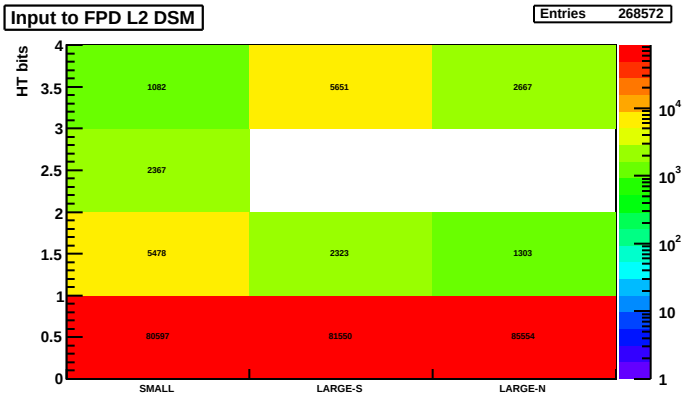

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

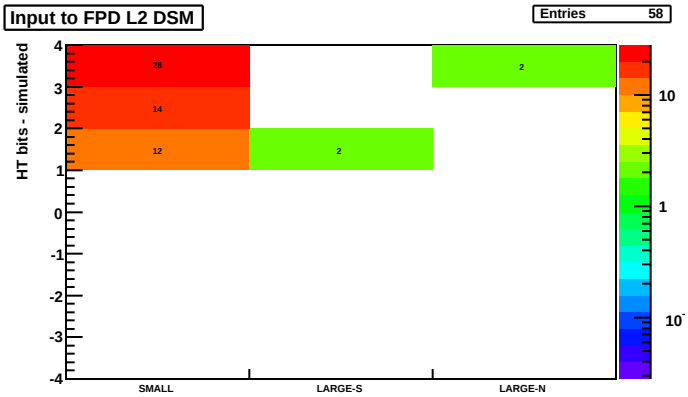

Input to FPD L2 DSM
Entries 1074288
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

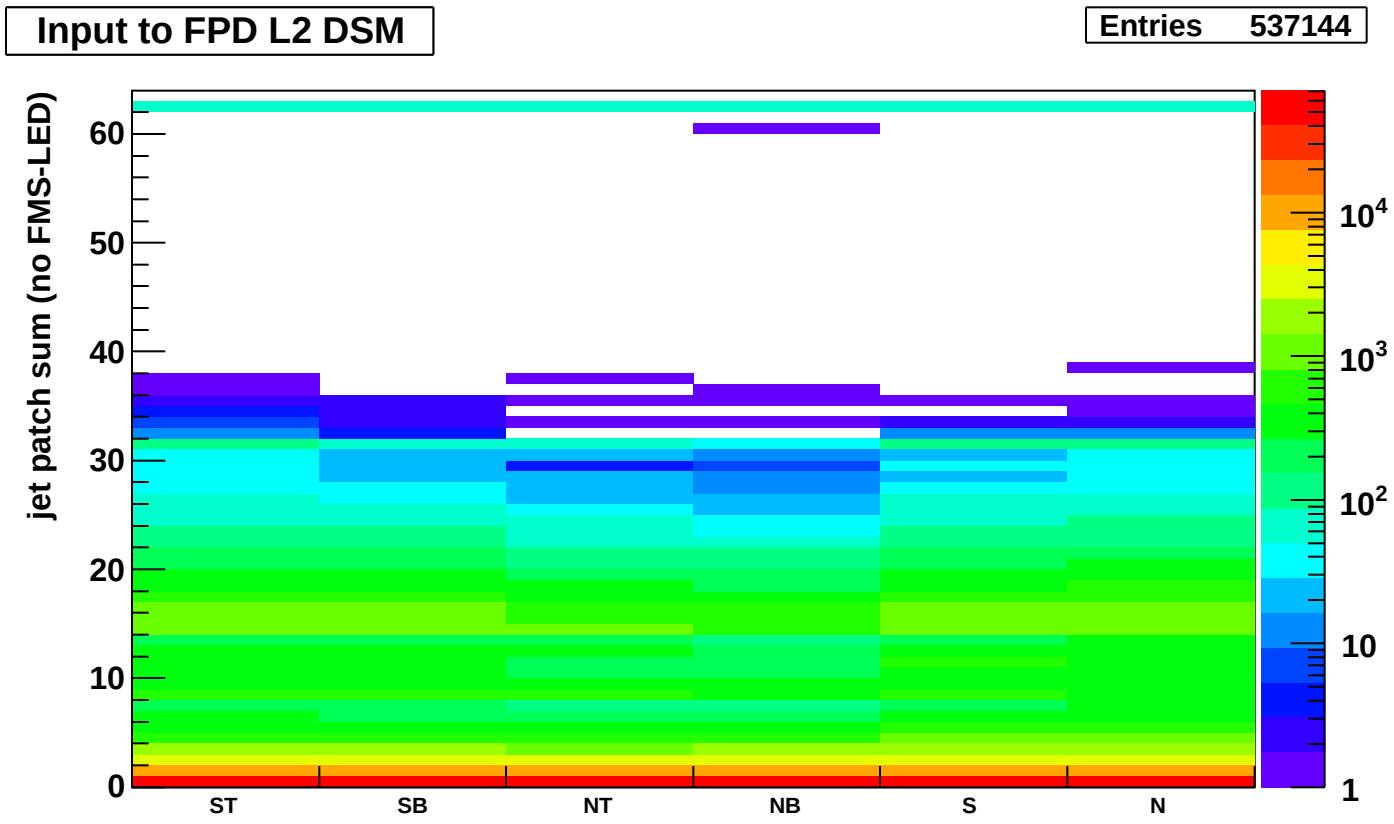

Input to FPD L2 DSM
Entries 178
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

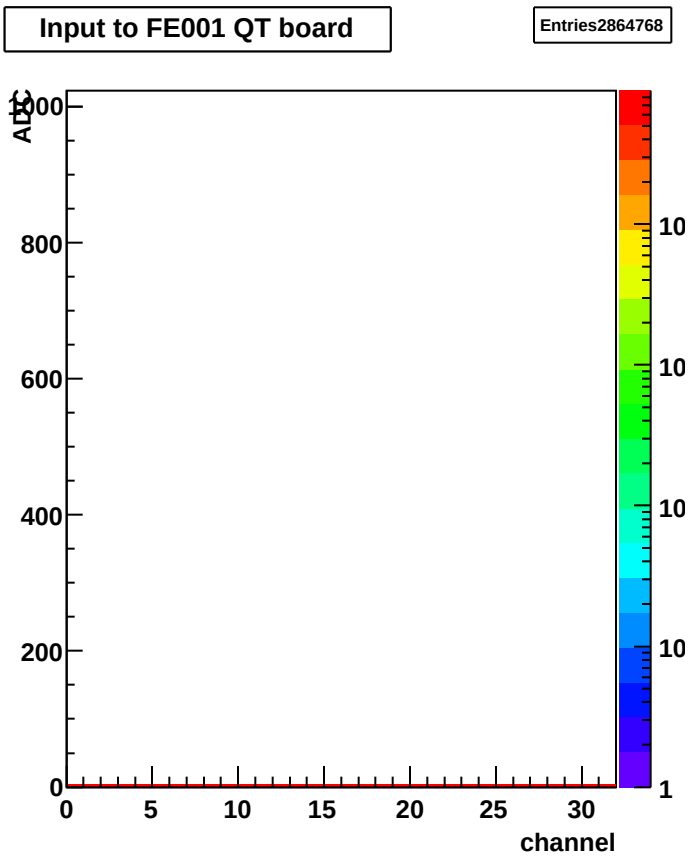

Input to FPD L2 DSM
Entries 268572
CL bits
423
207
7023
4964
3020
3983
2032
1137
78518
82105
85160
SMALL
LARGE-S
LARGE-N

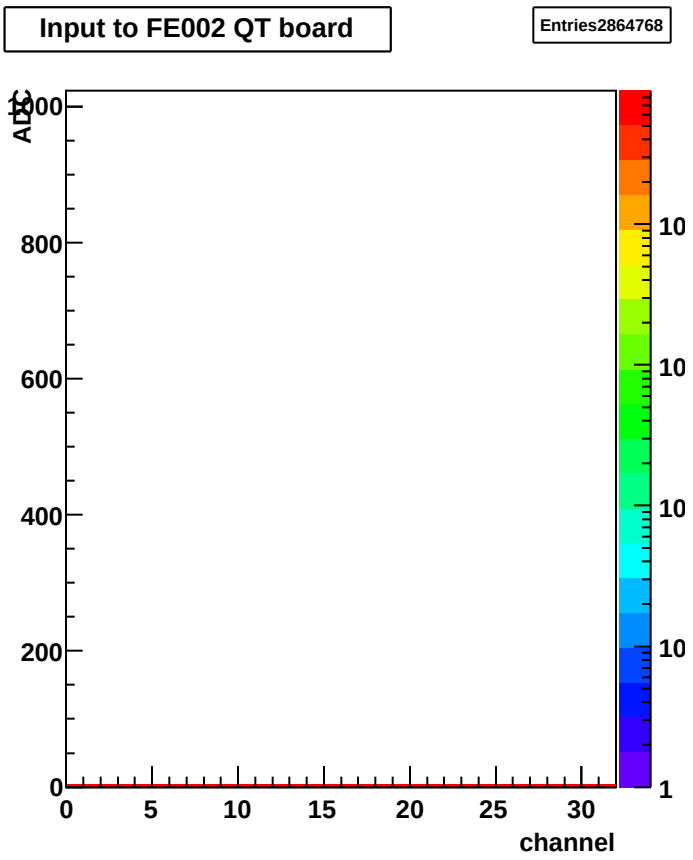

Input to FPD L2 DSM
Entries 14
CL bits - simulated
SMALL
LARGE-S
LARGE-N

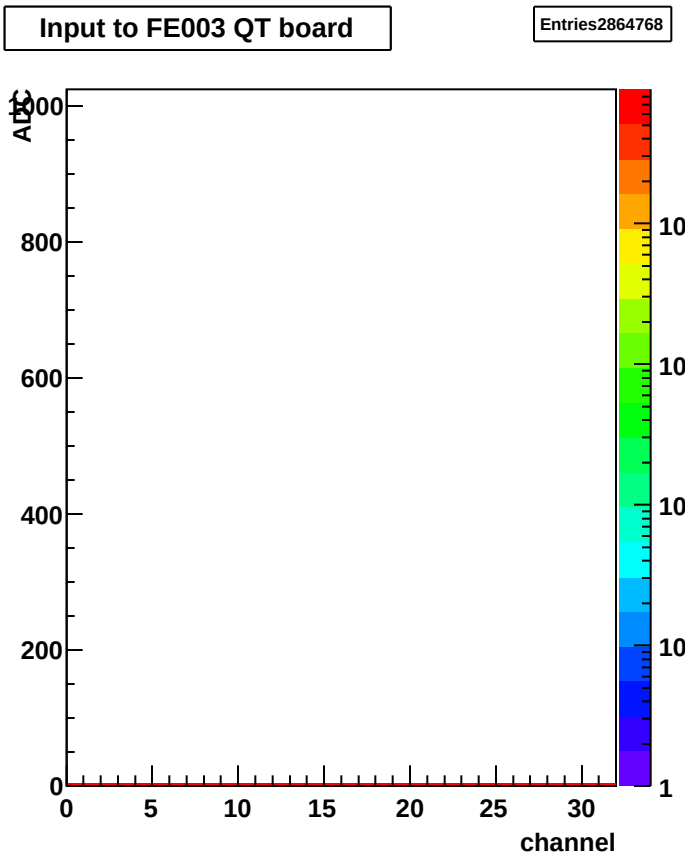

Input to FPD L2 DSM
Entries 268572
HT bits
1082
5651
2667
2367
5478
2323
1303
80597
81550
85554
SMALL
LARGE-S
LARGE-N

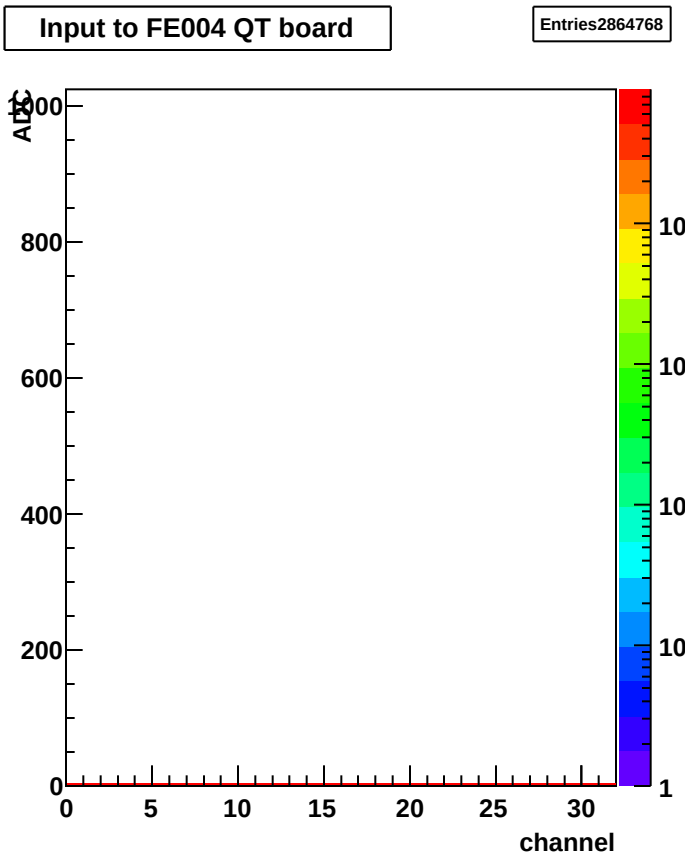

Input to FPD L2 DSM
Entries 58
HT bits - simulated
28
2
14
12
2
SMALL
LARGE-S
LARGE-N

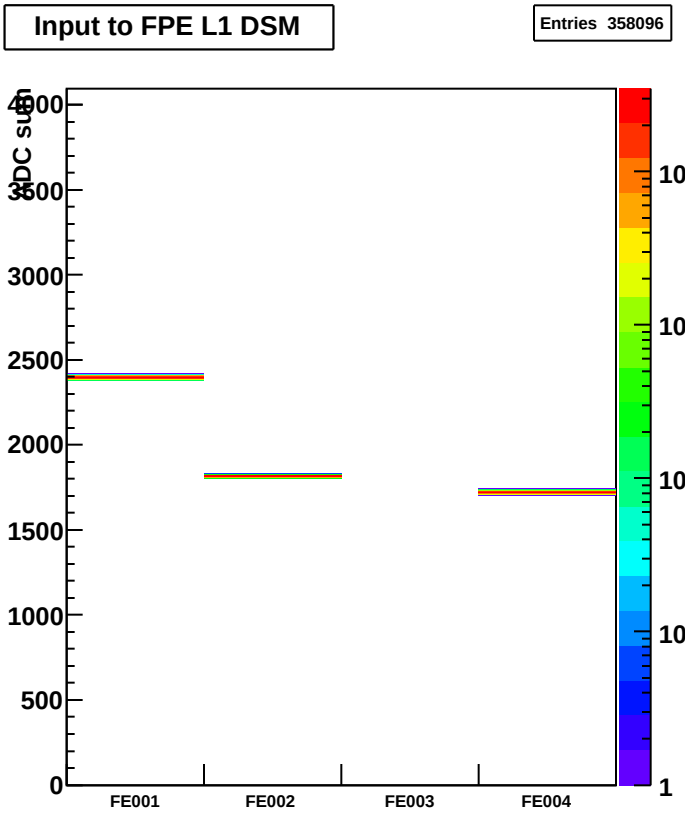

Input to FPD L2 DSM
Entries 537144
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4


Input to FE001 QT board
Entries2864768
ADC
channel

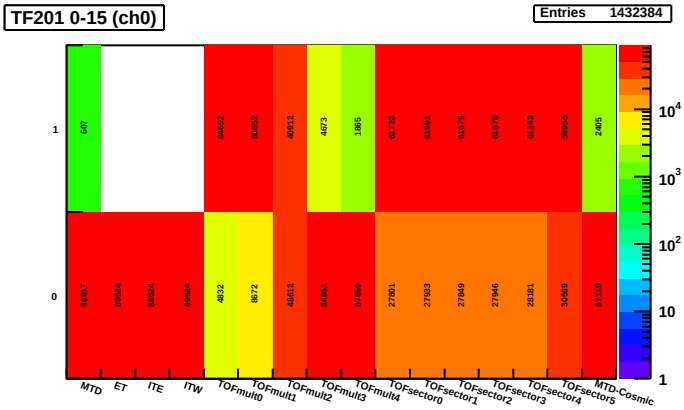

Input to FE002 QT board
Entries2864768
ADC
channel


Input to FE003 QT board
Entries2864768
ADC
channel

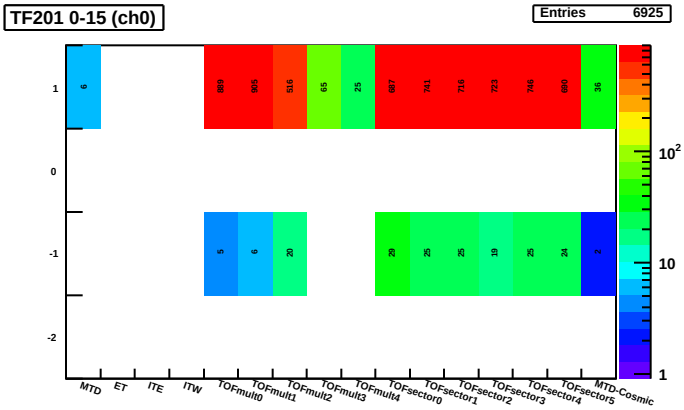

Input to FE004 QT board
Entries2864768
ADC
channel


Input to FPE L1 DSM
Entries 358096
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

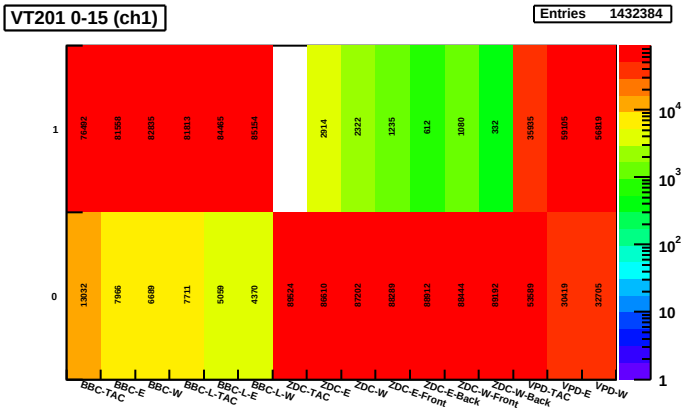
TF201 0-15 (ch0)



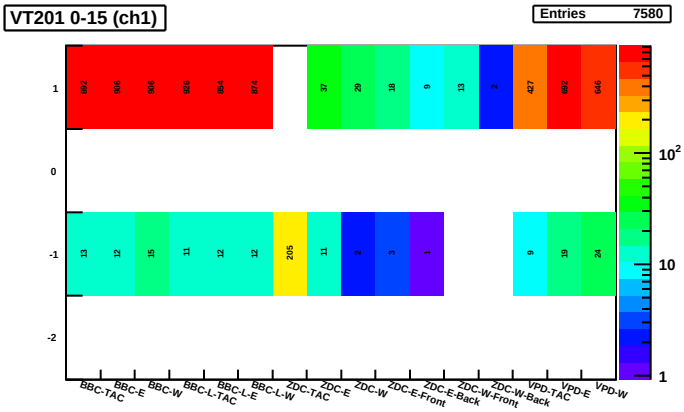
TF201 0-15 (ch0)



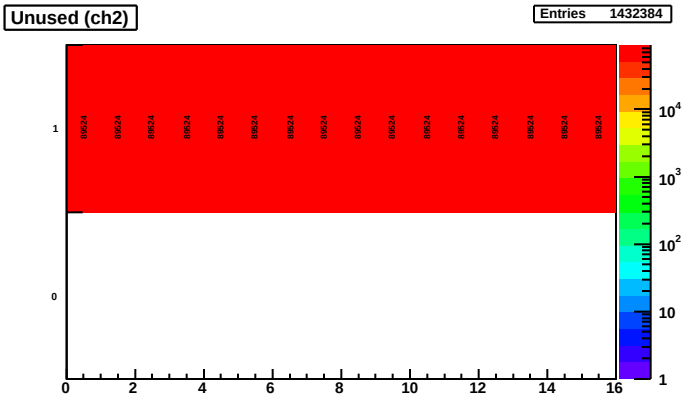
VT201 0-15 (ch1)

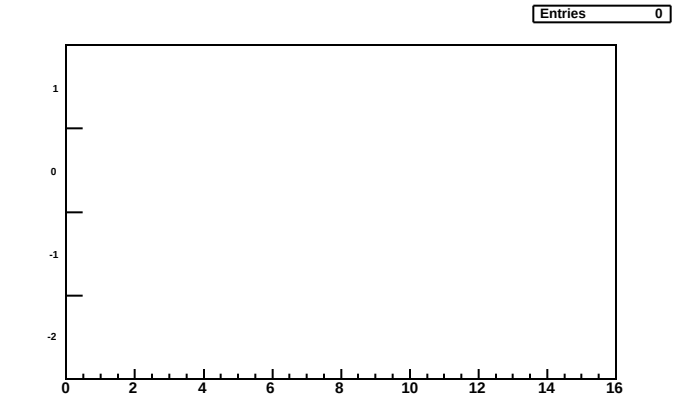


VT201 0-15 (ch1)

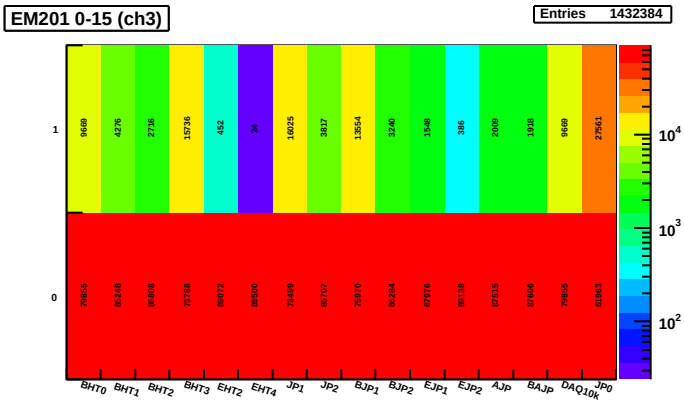


Unused (ch2)

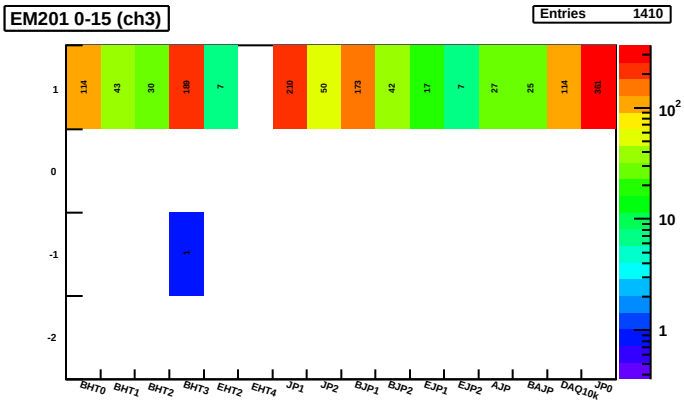




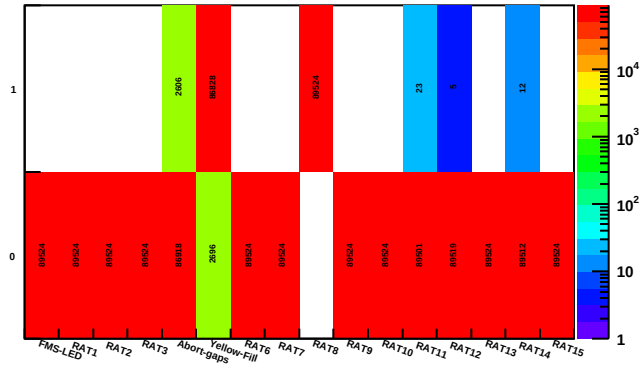
EM201 0-15 (ch3)

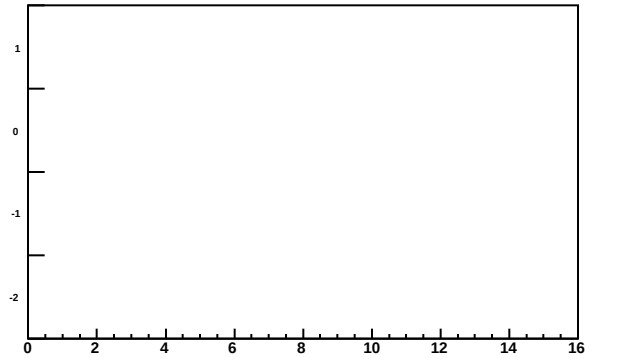


EM201 0-15 (ch3)

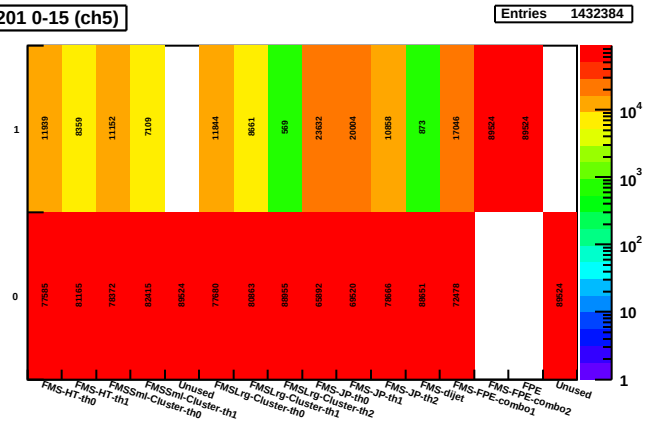


RAT board (ch4)





FP201 0-15 (ch5)



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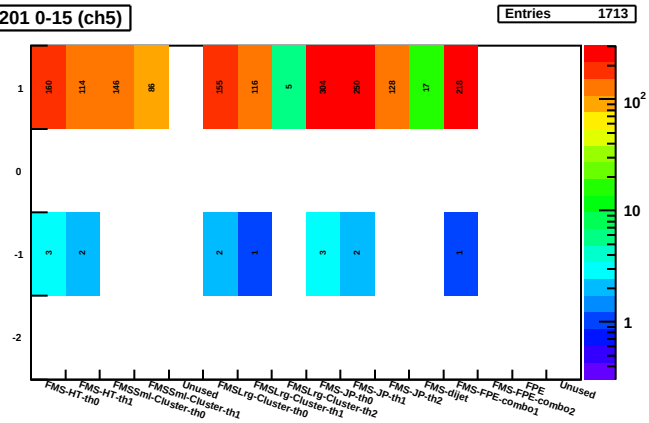

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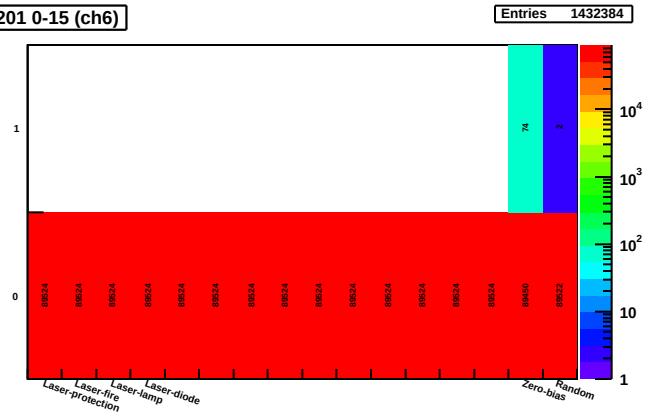

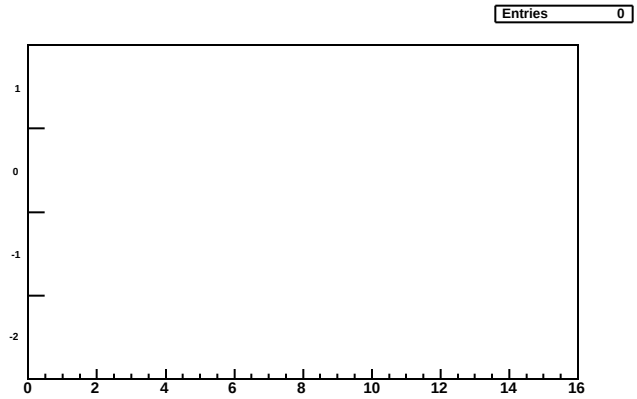
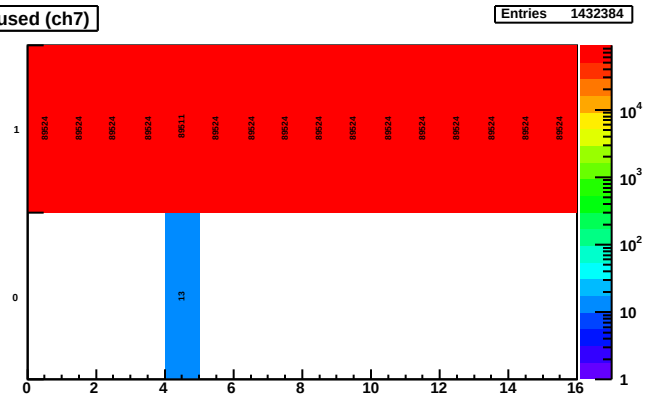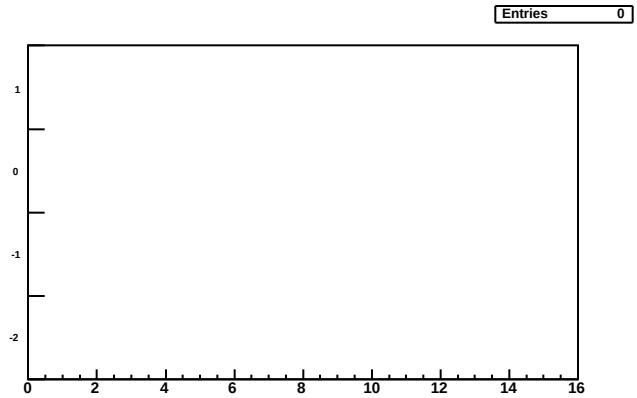
Unused (ch7)