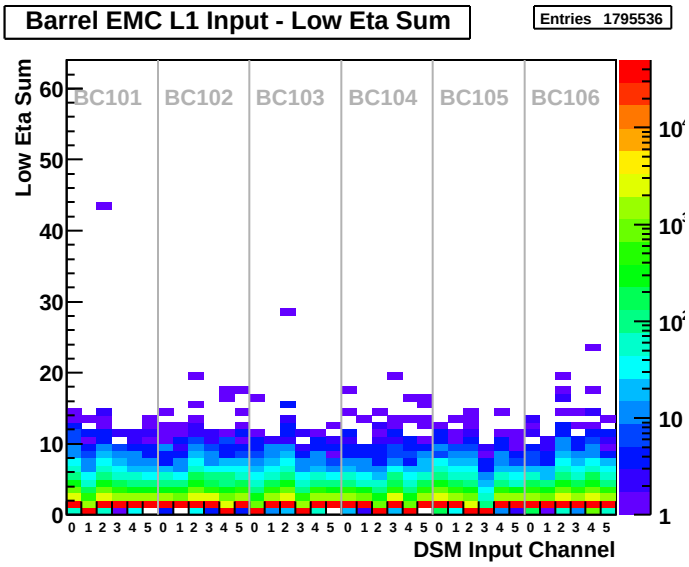
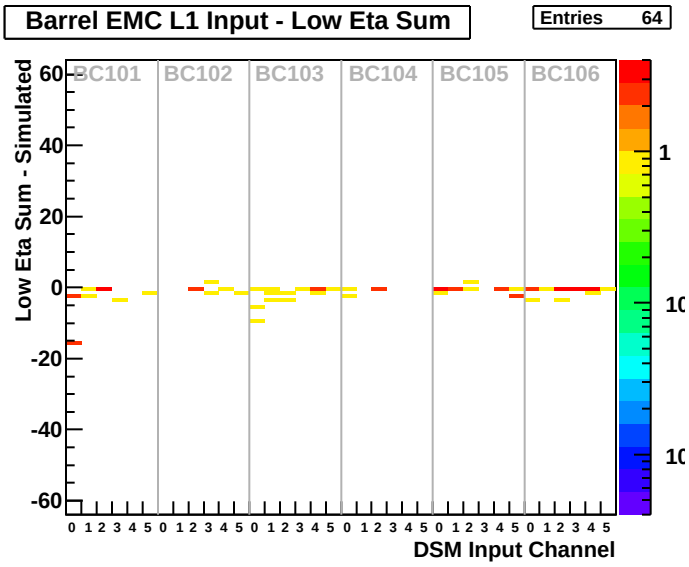
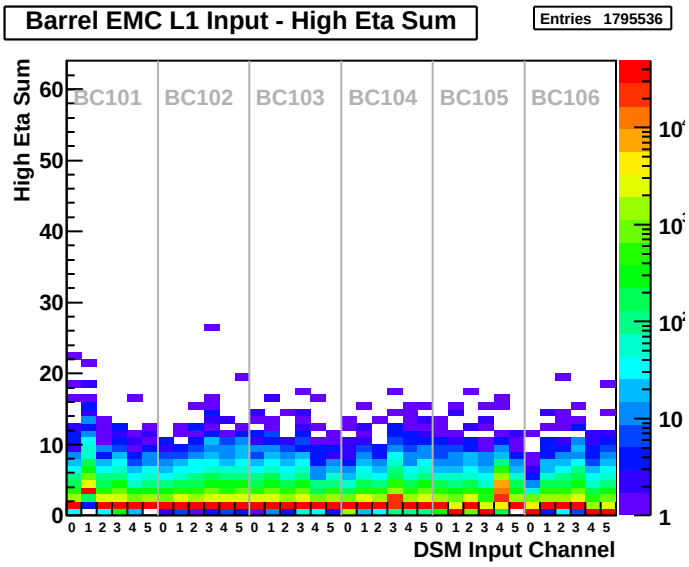
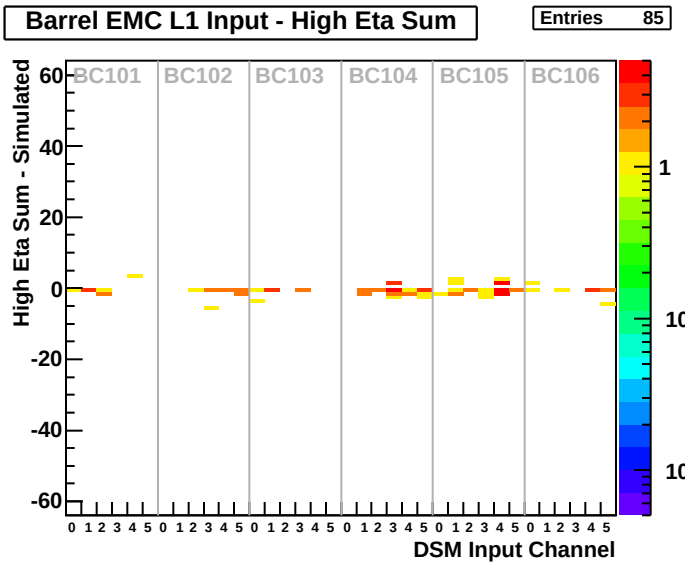
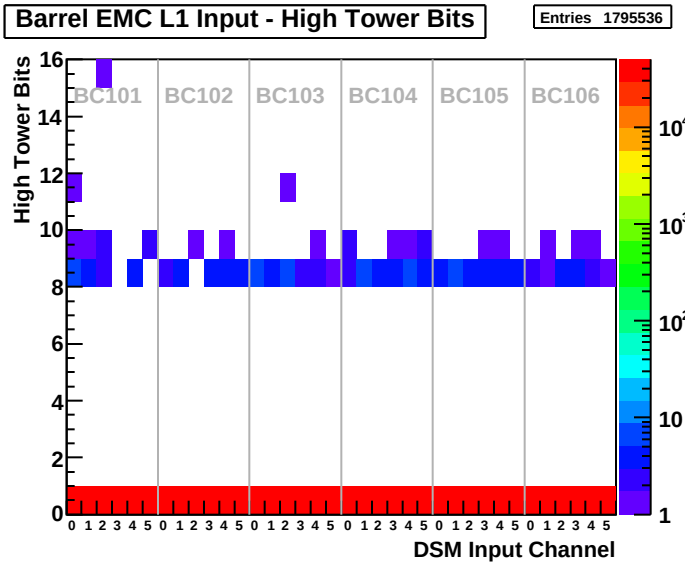
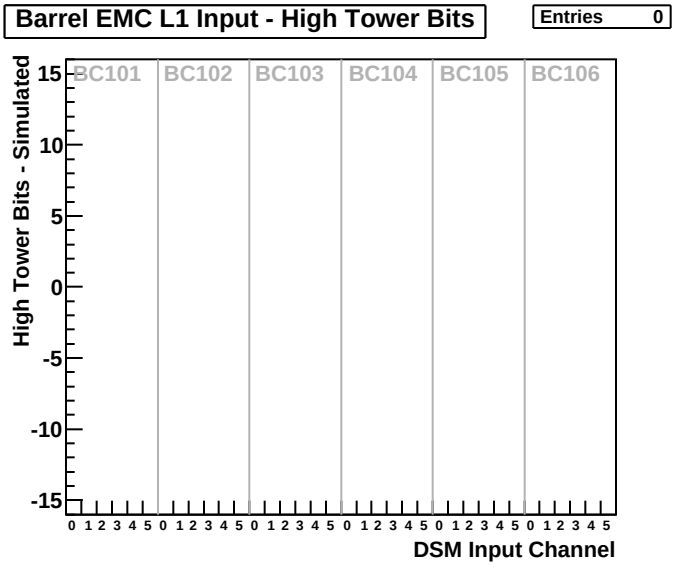

Barrel EMC L1 Input - Low Eta Sum
Entries 1795536
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

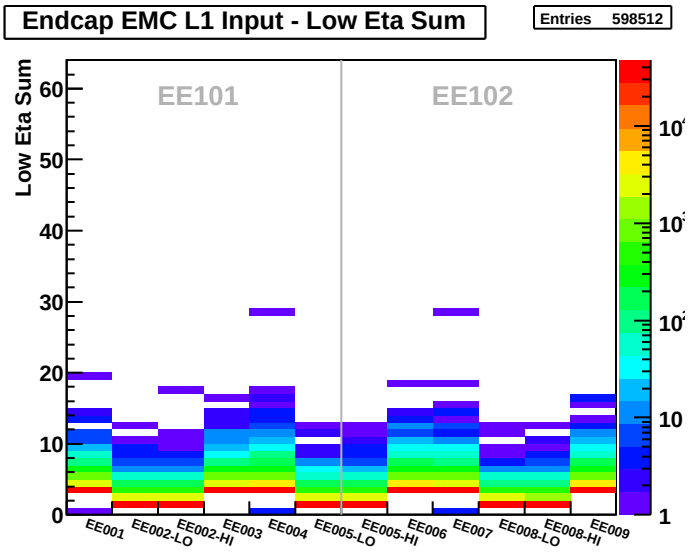

Barrel EMC L1 Input - Low Eta Sum
Entries 64
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

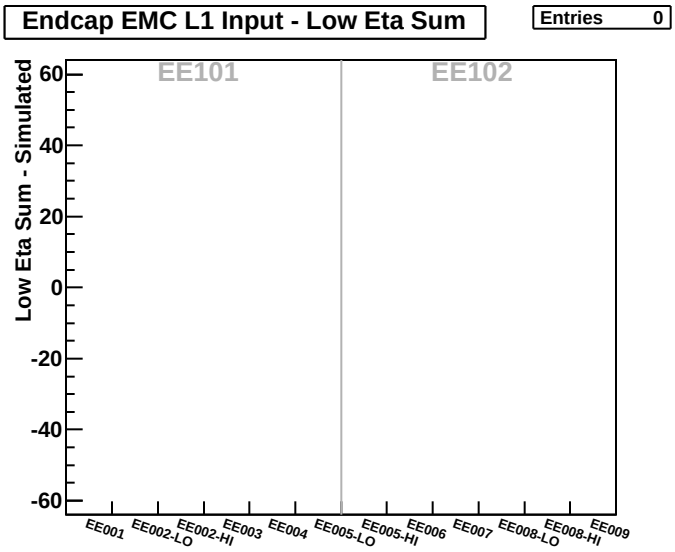

Barrel EMC L1 Input - High Eta Sum
Entries 1795536
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

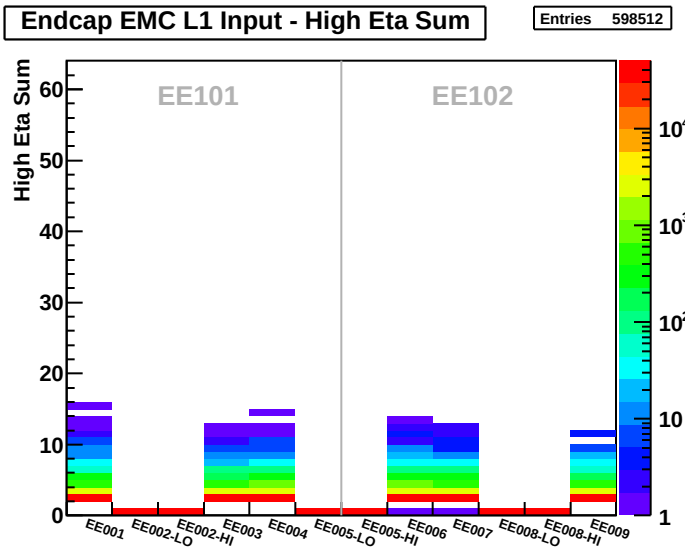

Barrel EMC L1 Input - High Eta Sum
Entries 85
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

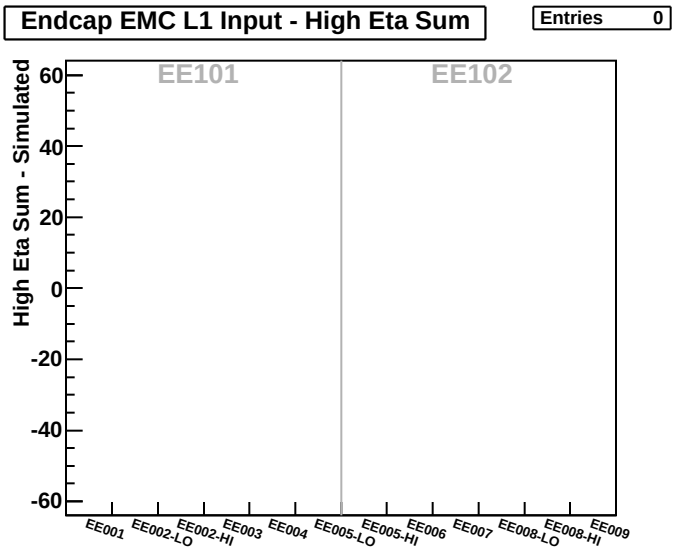

Barrel EMC L1 Input - High Tower Bits
Entries 1795536
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

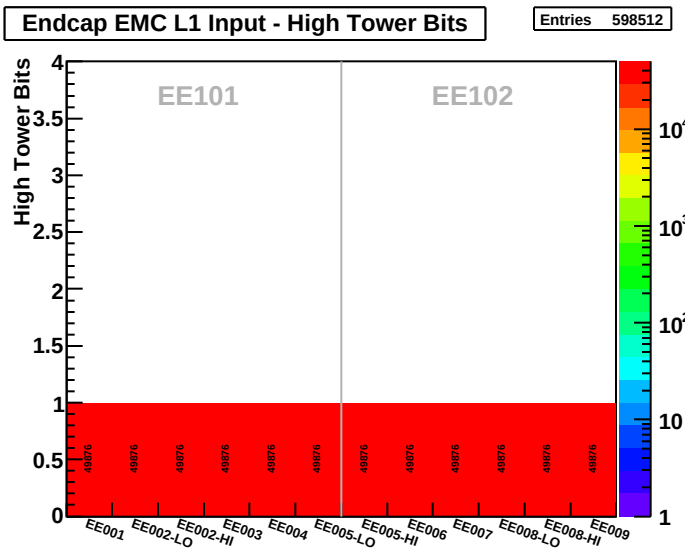

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

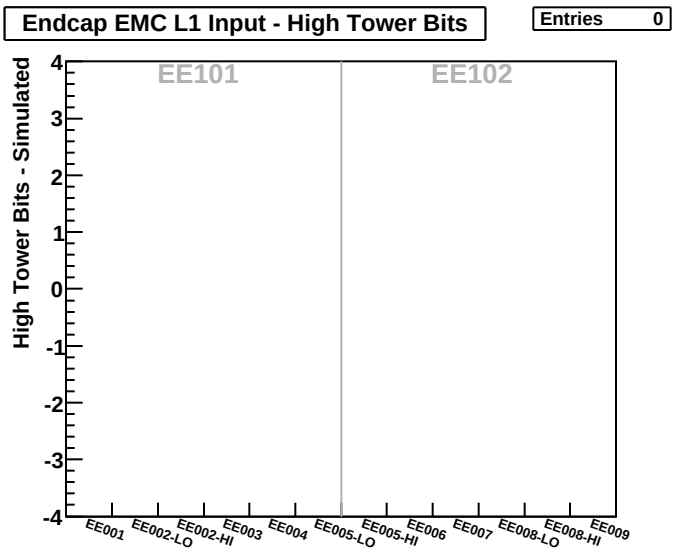

Endcap EMC L1 Input - Low Eta Sum
Entries 598512
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

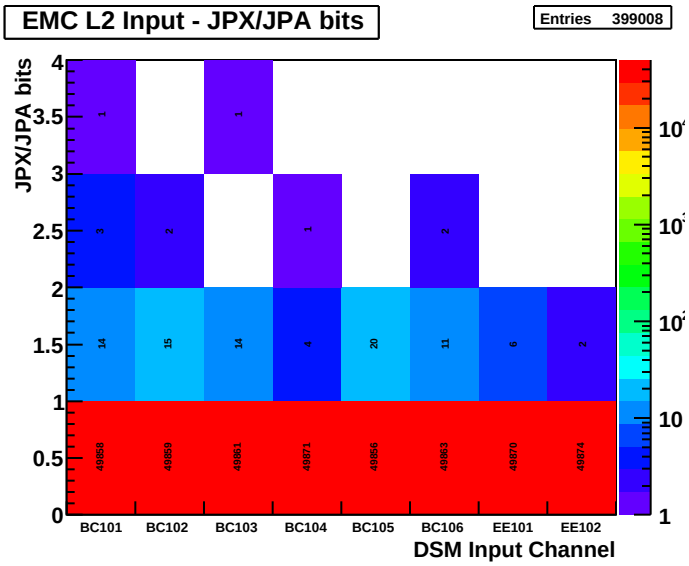

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

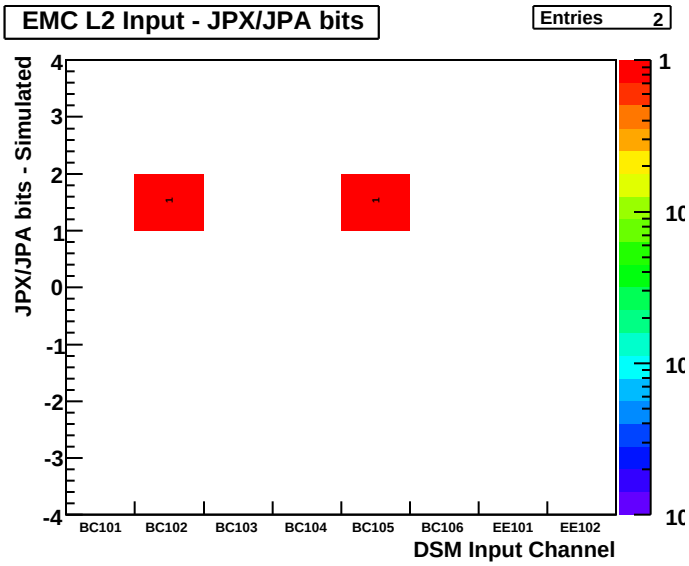

Endcap EMC L1 Input - High Eta Sum
Entries 598512
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

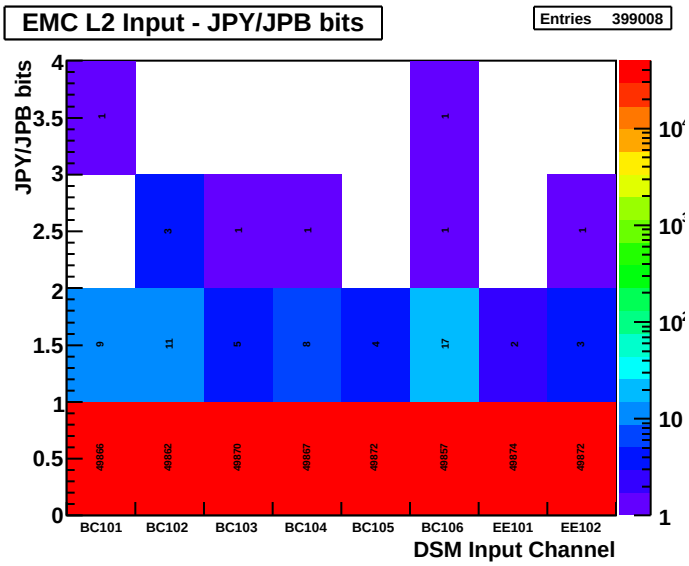

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

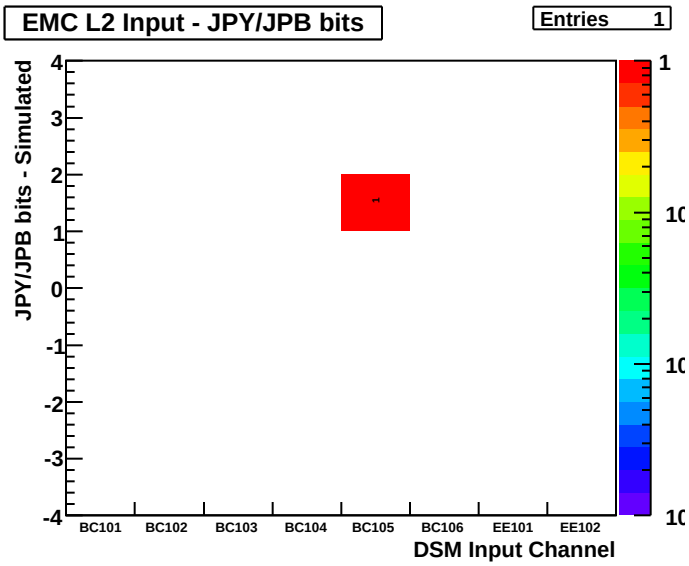

Endcap EMC L1 Input - High Tower Bits
Entries 598512
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

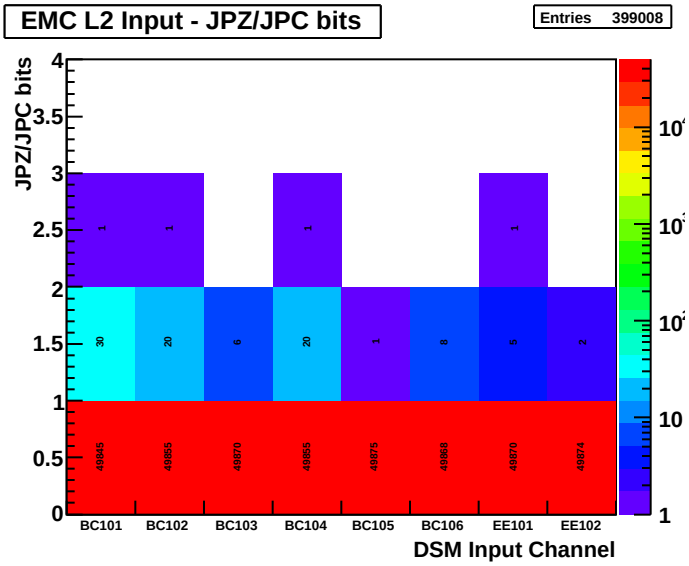

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

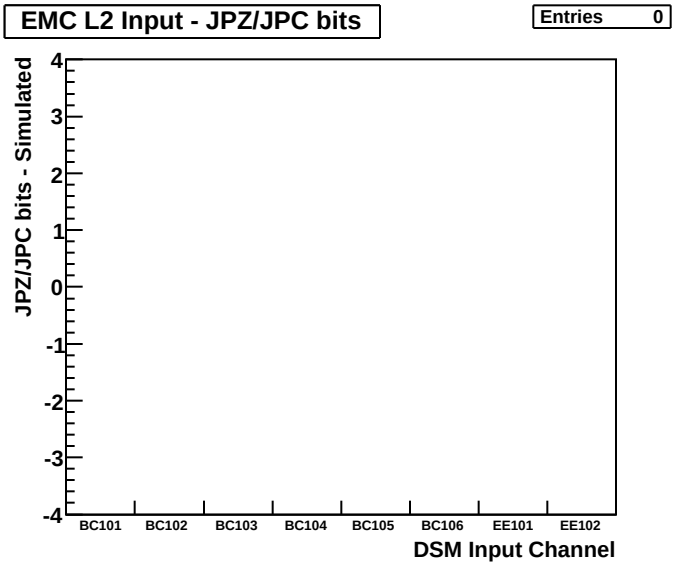

EMC L2 Input - JPX/JPA bits
Entries 399008
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

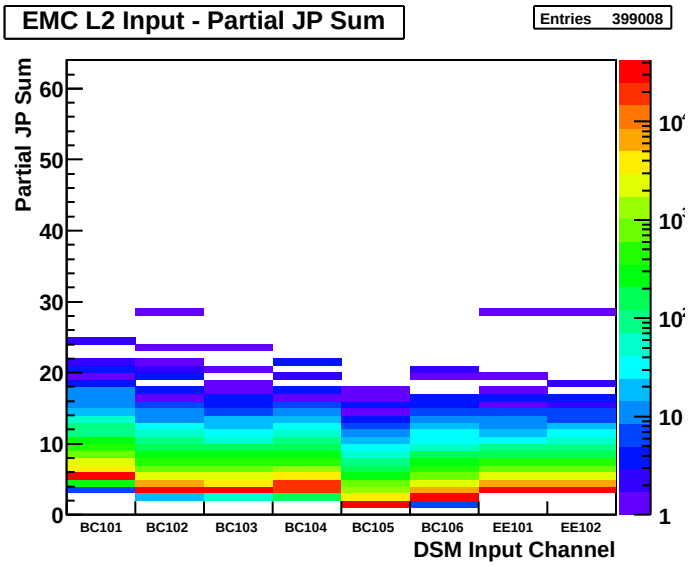

EMC L2 Input - JPX/JPA bits
Entries 2
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

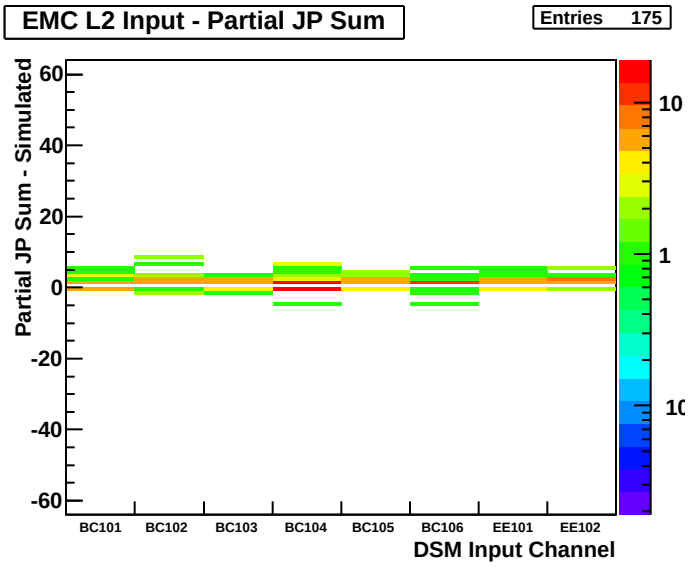

EMC L2 Input - JPY/JPB bits
Entries 399008
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

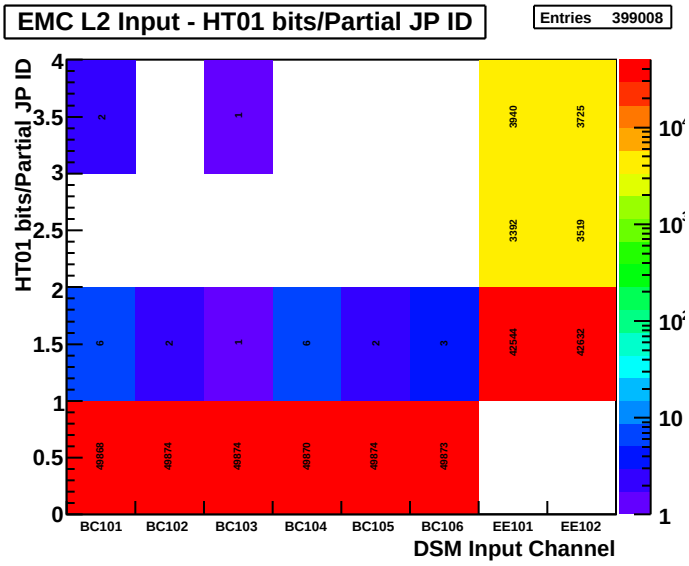

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

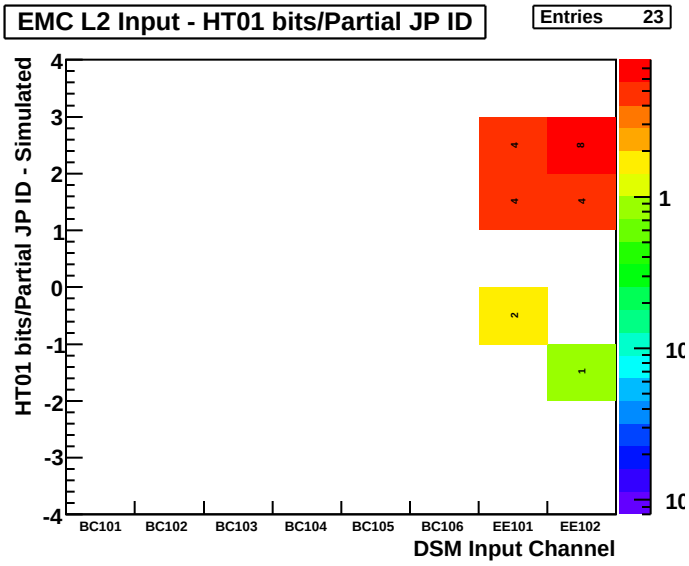

EMC L2 Input - JPZ/JPC bits
Entries 399008
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

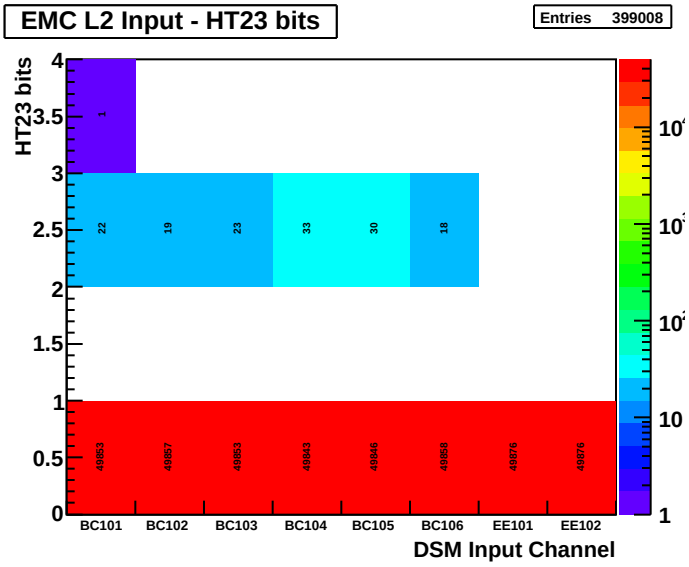

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

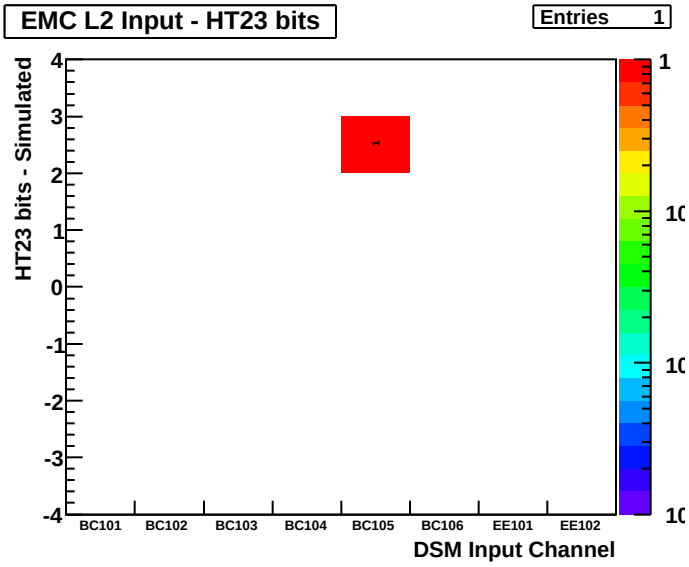

EMC L2 Input - Partial JP Sum
Entries 399008
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

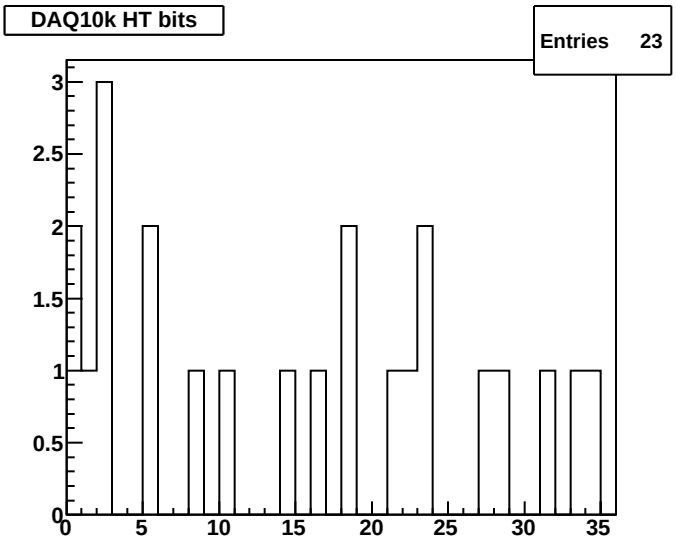

EMC L2 Input - Partial JP Sum
Entries 175
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

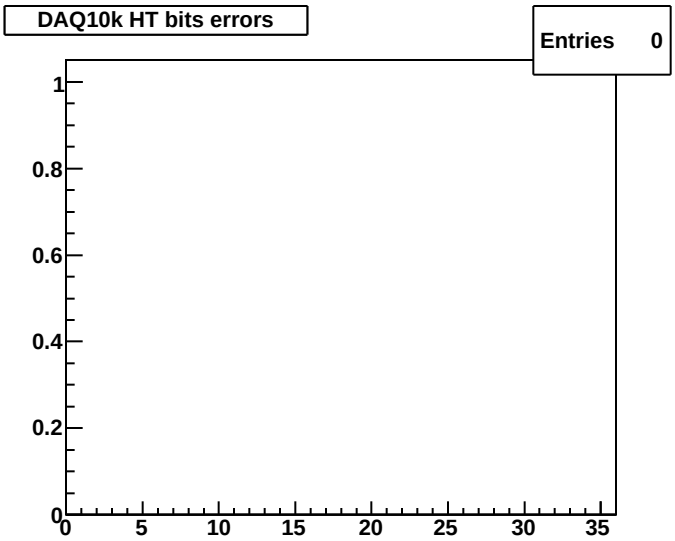

EMC L2 Input - HT01 bits/Partial JP ID
Entries 399008
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

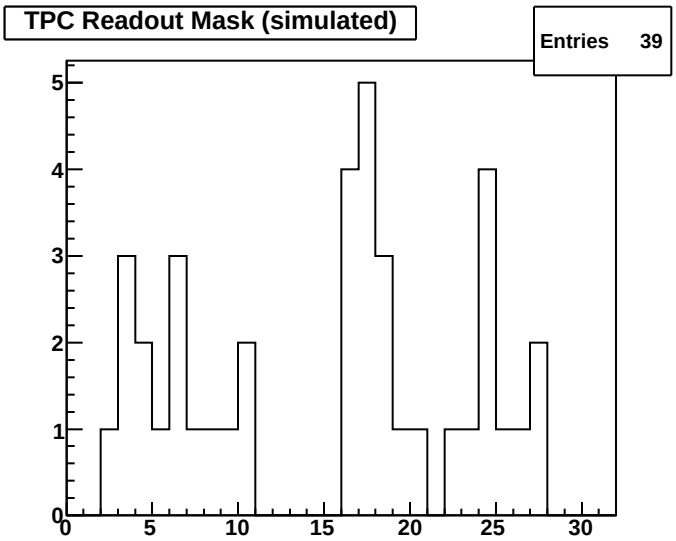

EMC L2 Input - HT01 bits/Partial JP ID
Entries 23
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

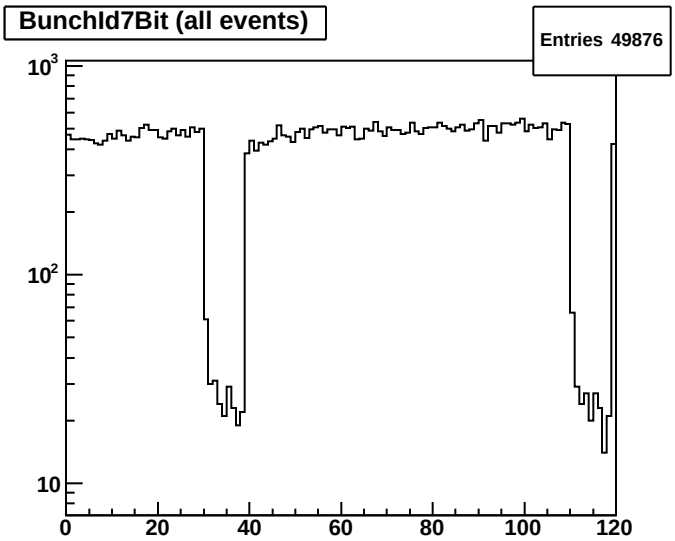

EMC L2 Input - HT23 bits
Entries 399008
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

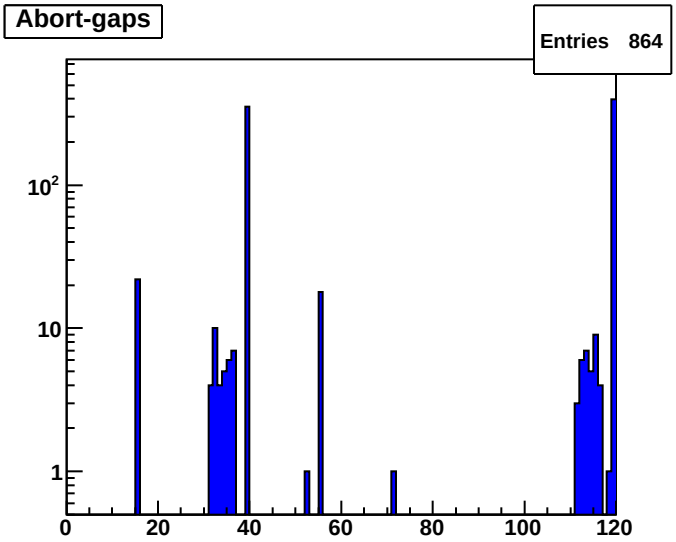

EMC L2 Input - HT23 bits
Entries 1
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

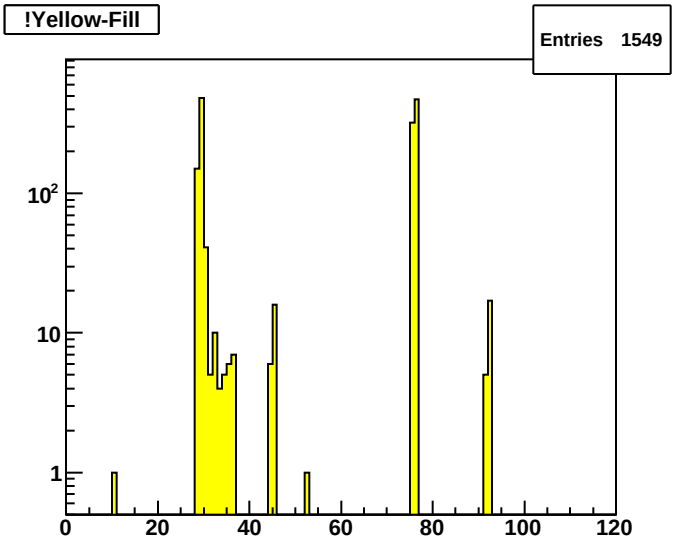

DAQ10k HT bits
Entries 23

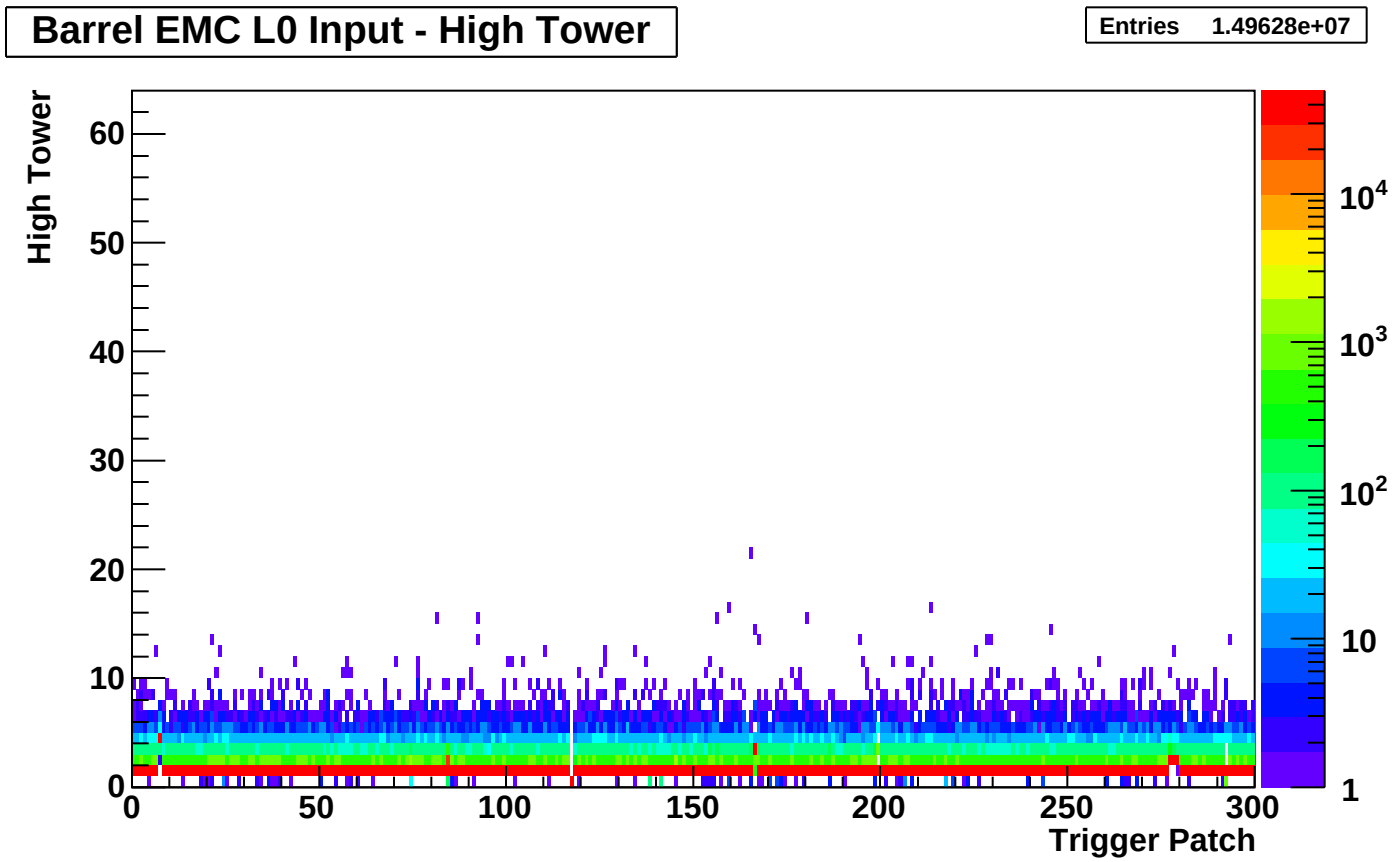

DAQ10k HT bits errors
Entries 0

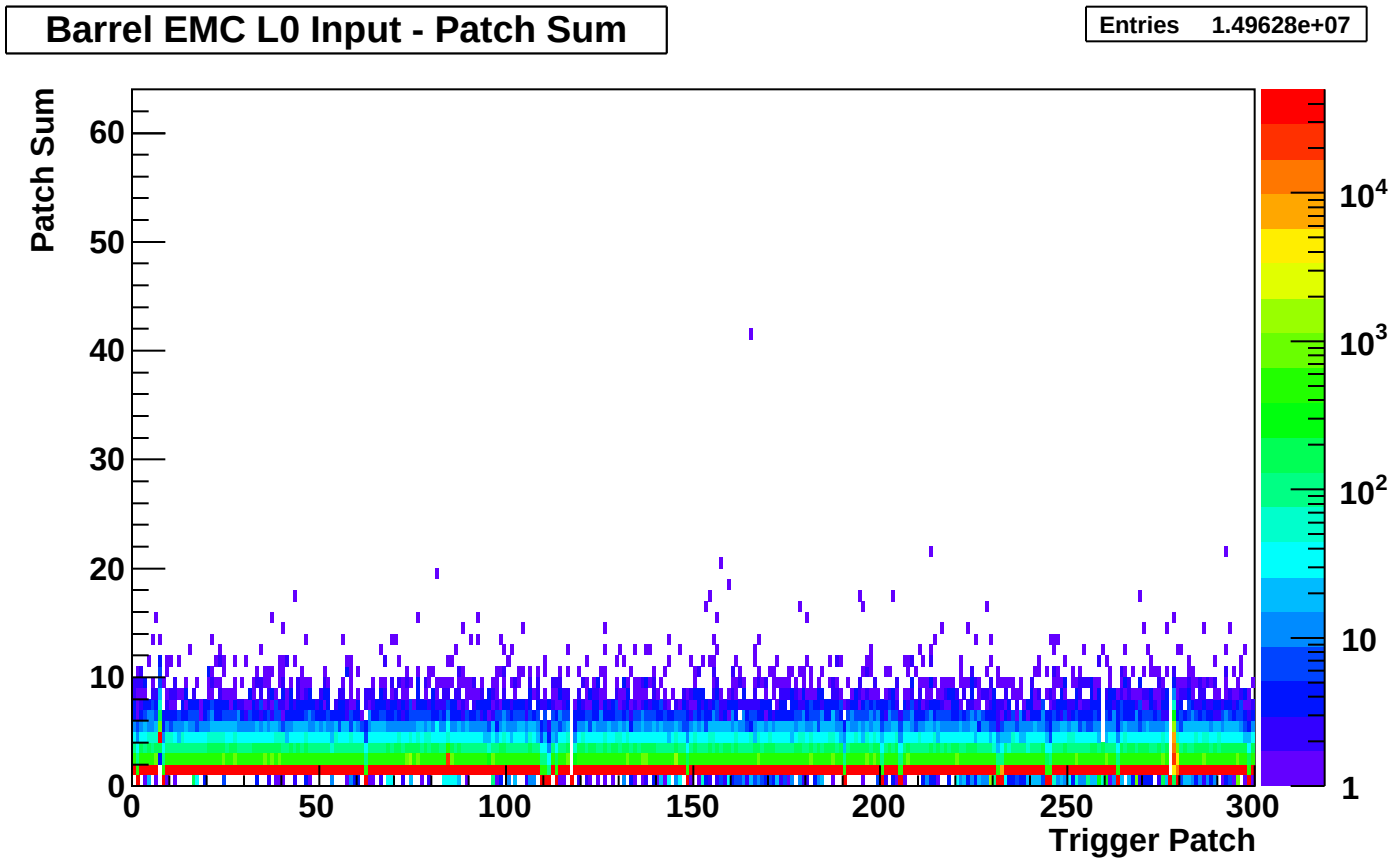

TPC Readout Mask (simulated)
Entries 39

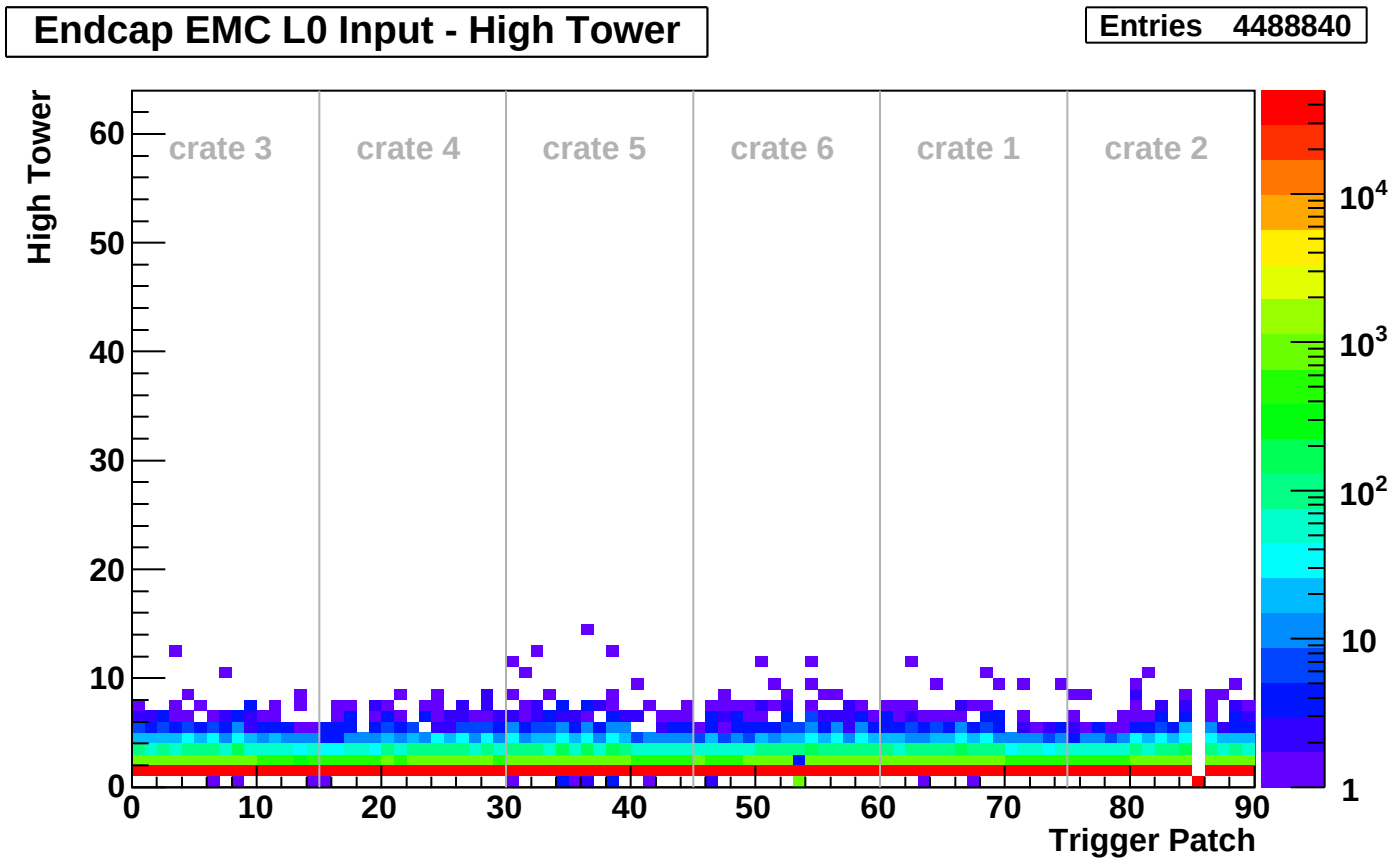

BunchId7Bit (all events)
Entries 49876

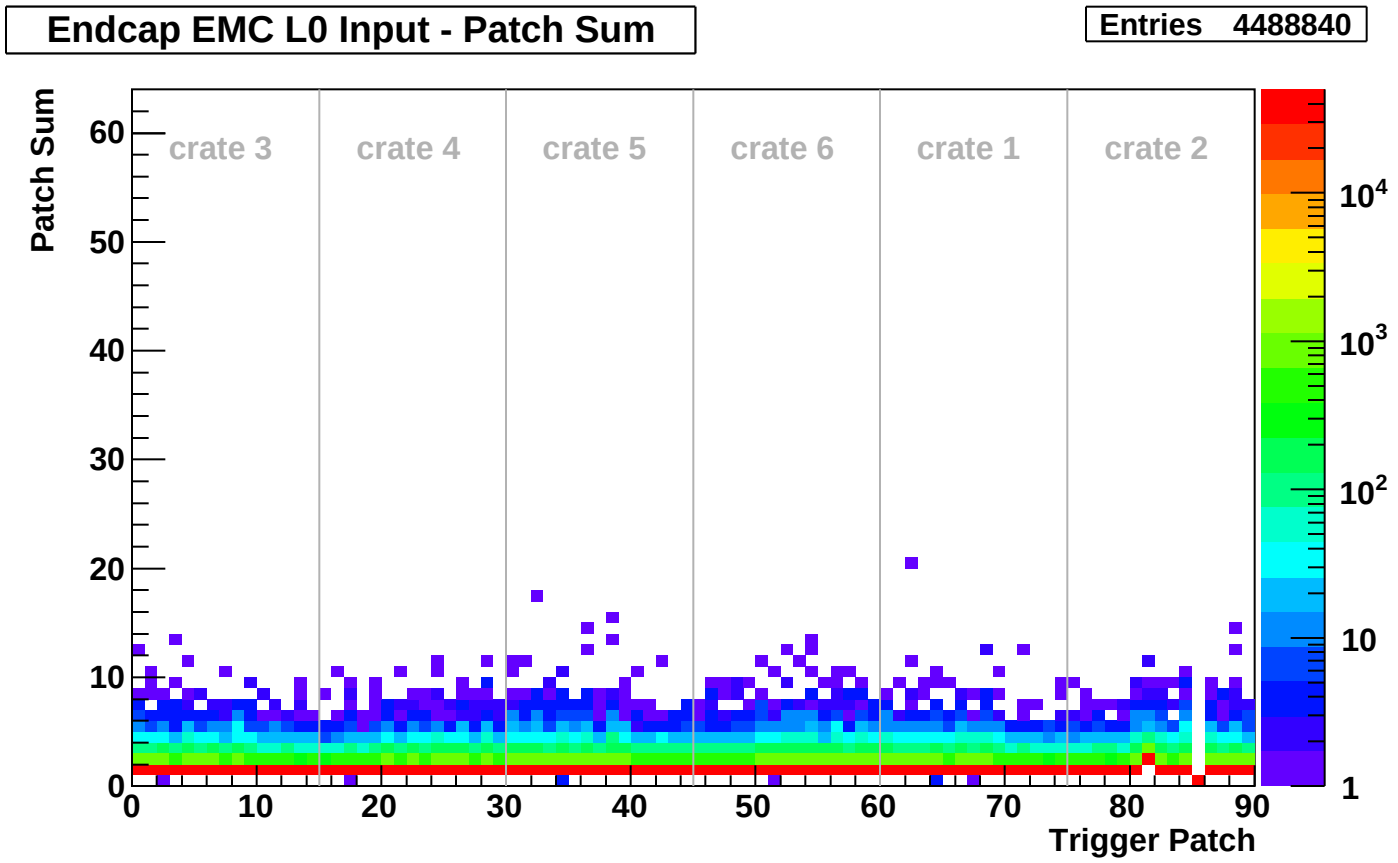

Abort-gaps
Entries 864

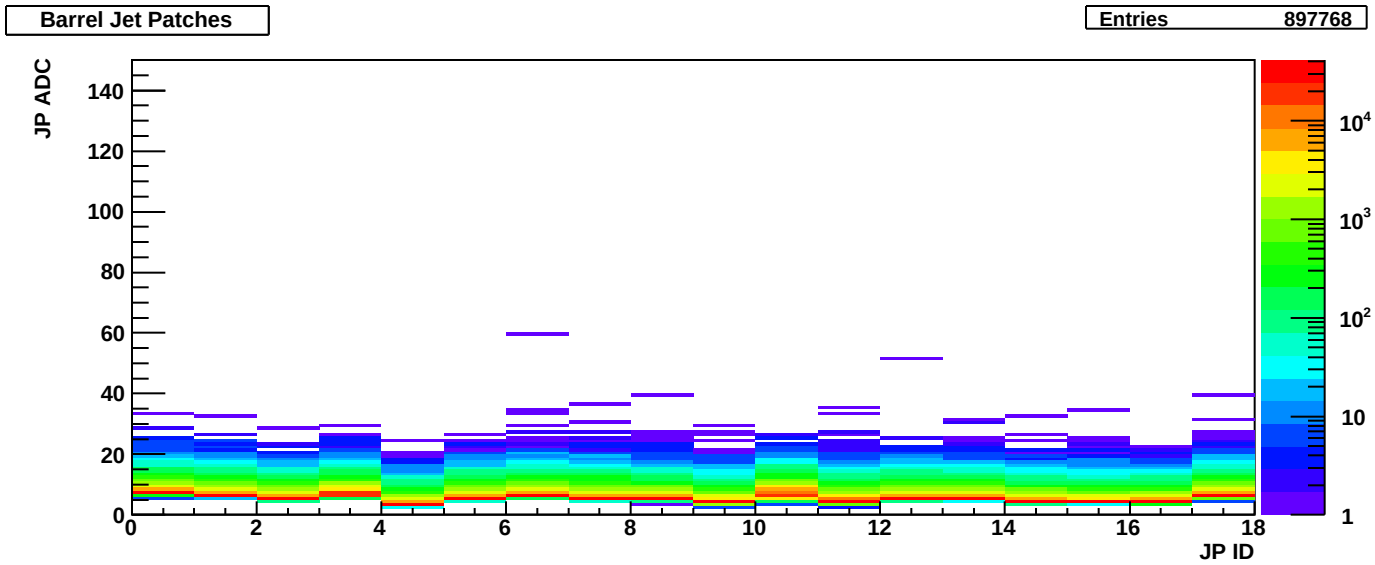

!Yellow-Fill
Entries 1549

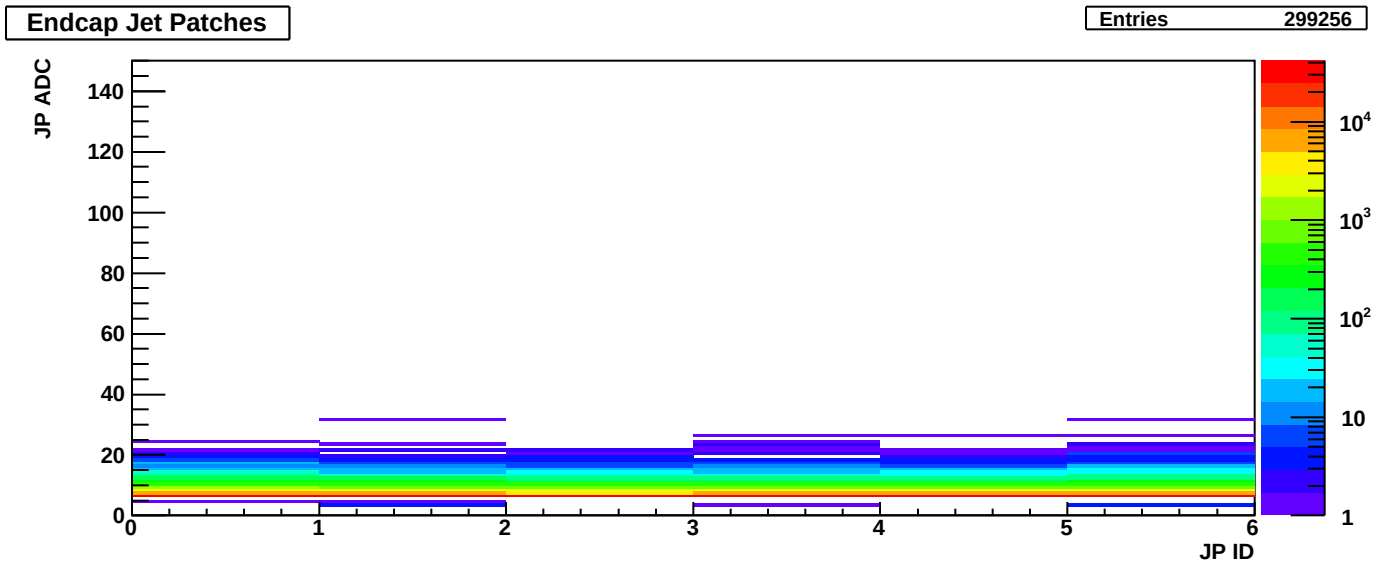

Barrel EMC L0 Input - High Tower
Entries 1.49628e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

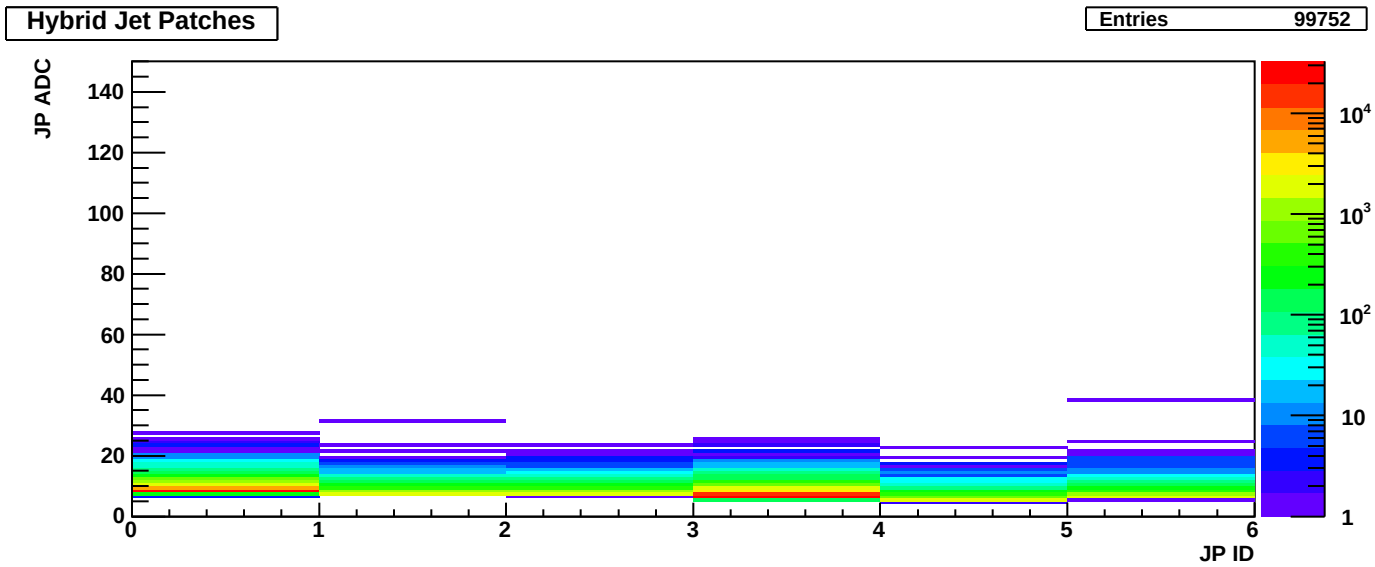

Barrel EMC L0 Input - Patch Sum
Entries 1.49628e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

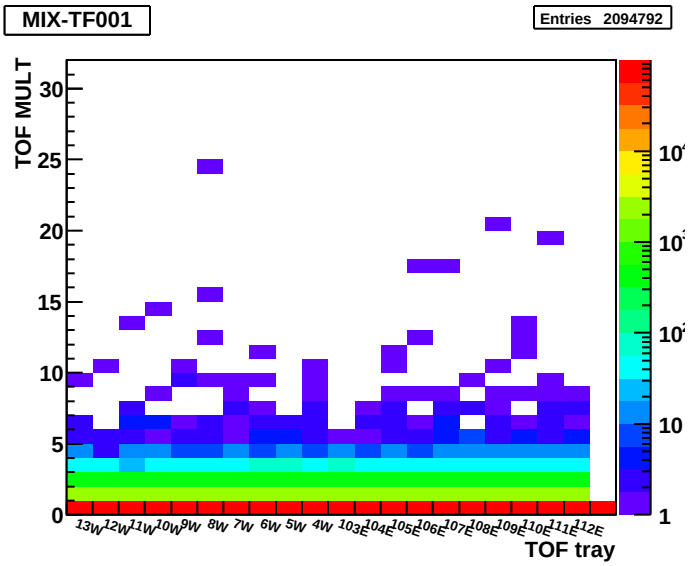

Endcap EMC L0 Input - High Tower
Entries 4488840
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

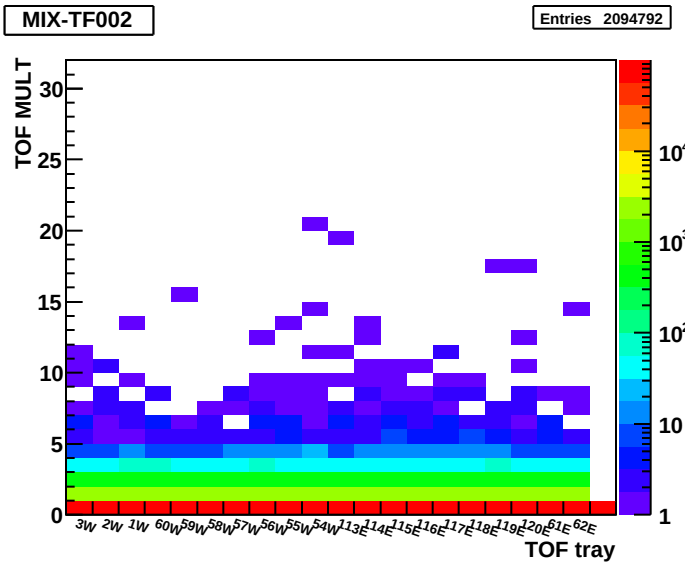

Endcap EMC L0 Input - Patch Sum
Entries 4488840
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

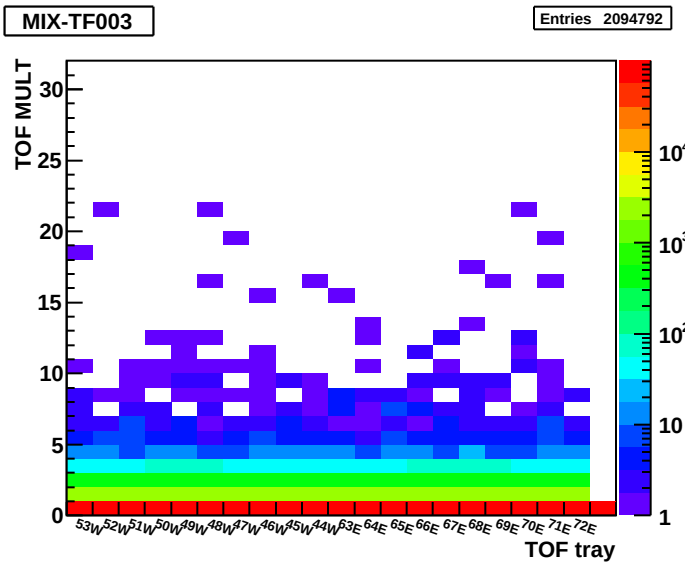

Barrel Jet Patches
Entries 897768
JP ADC
JP ID

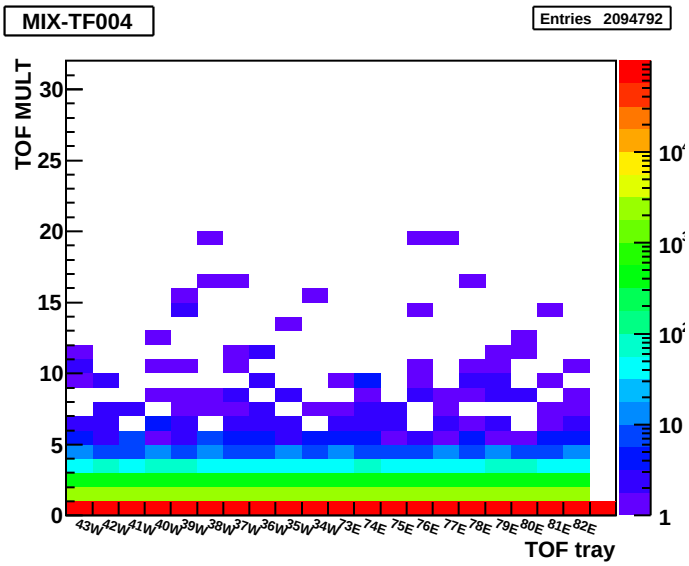

Endcap Jet Patches
Entries 299256
JP ADC
JP ID

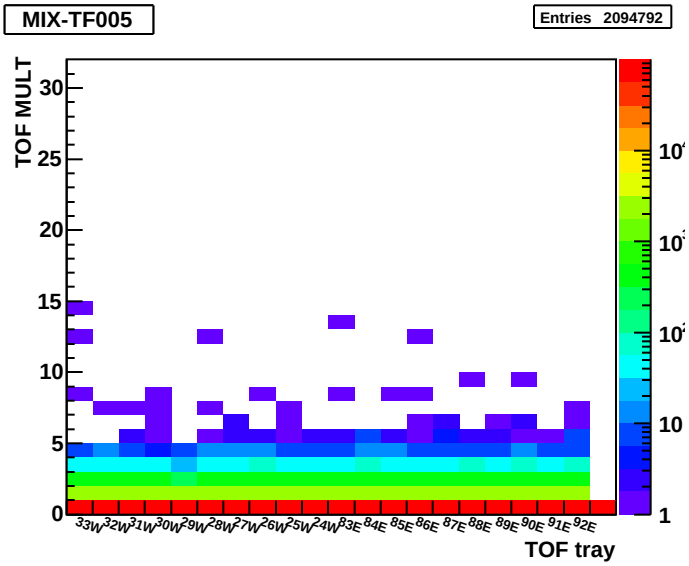

Hybrid Jet Patches
Entries 99752
JP ADC
JP ID

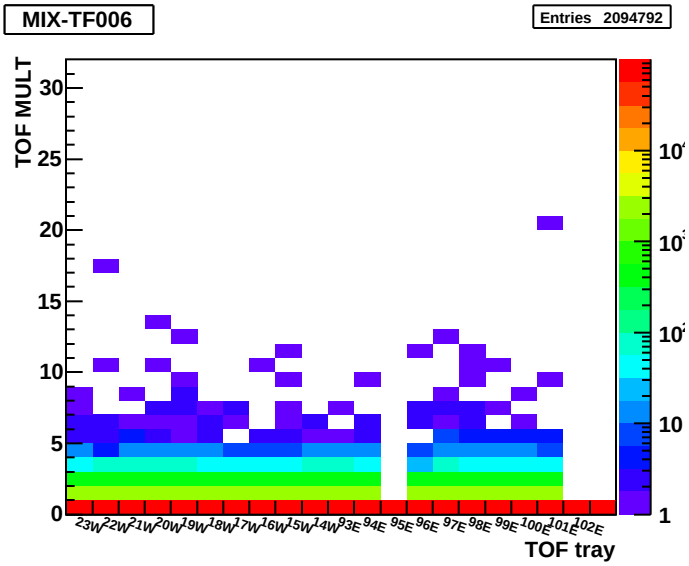

MIX-TF001
Entries 2094792
TOF MULT
TOF tray

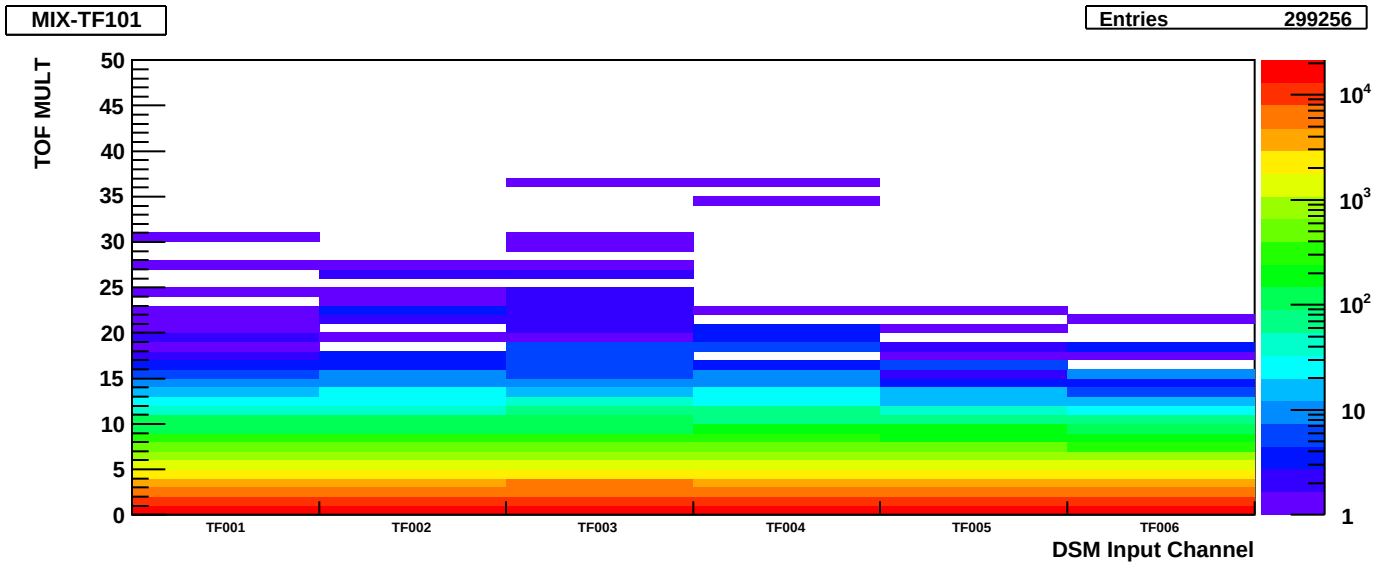

MIX-TF002
Entries 2094792
TOF MULT
TOF tray

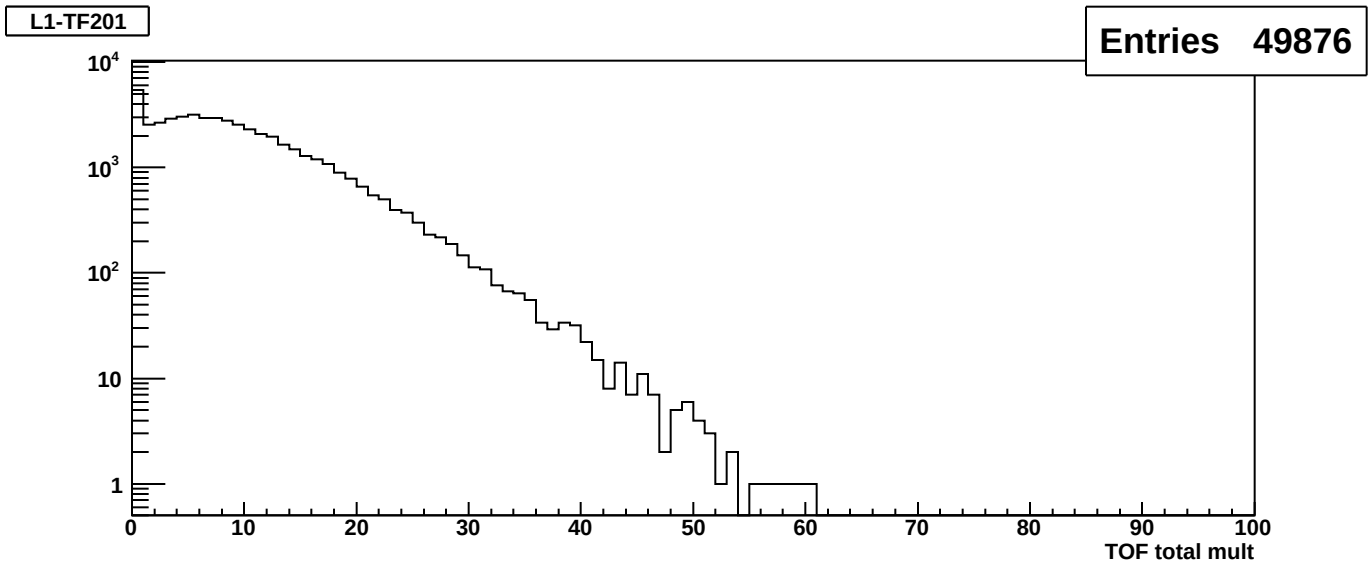

MIX-TF003
Entries 2094792
TOF MULT
TOF tray

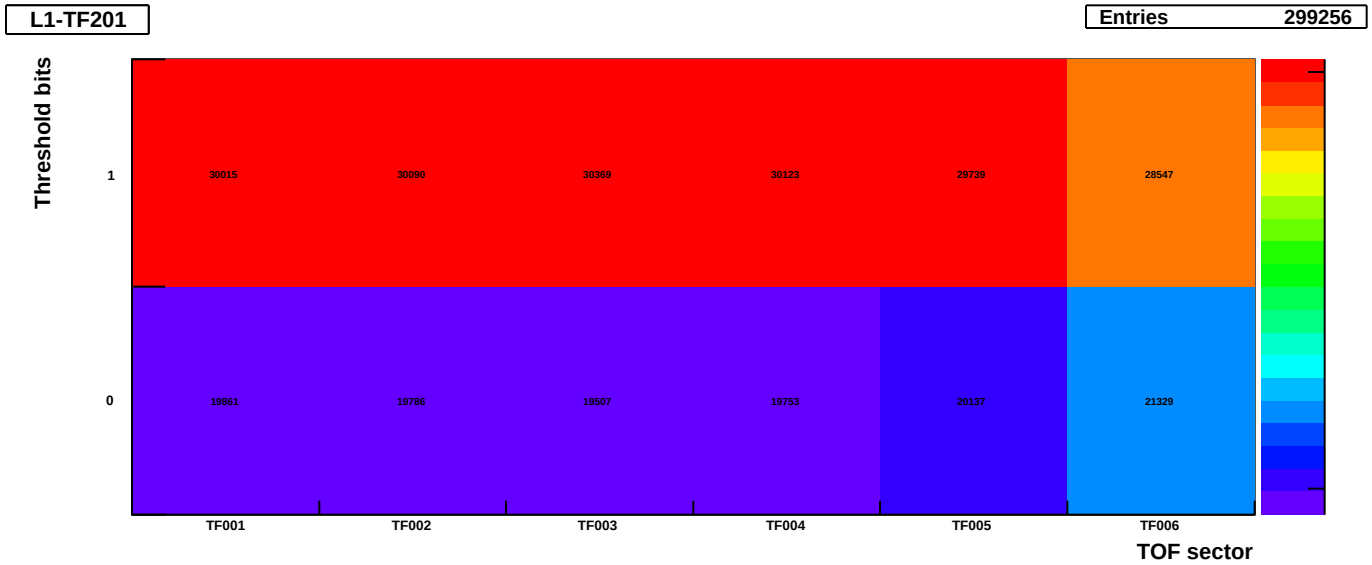

MIX-TF004
Entries 2094792
TOF MULT
TOF tray

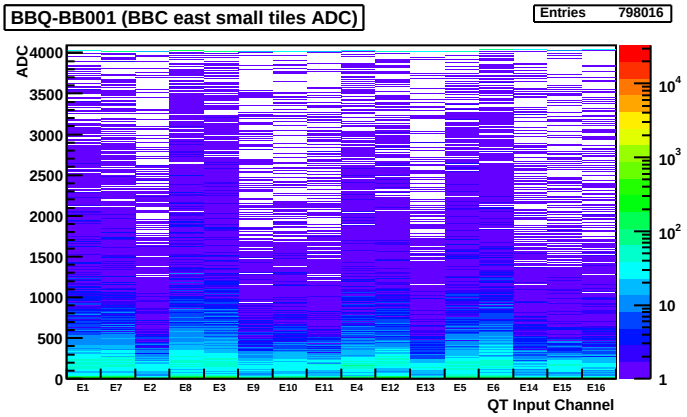

MIX-TF005
Entries 2094792
TOF MULT
TOF tray

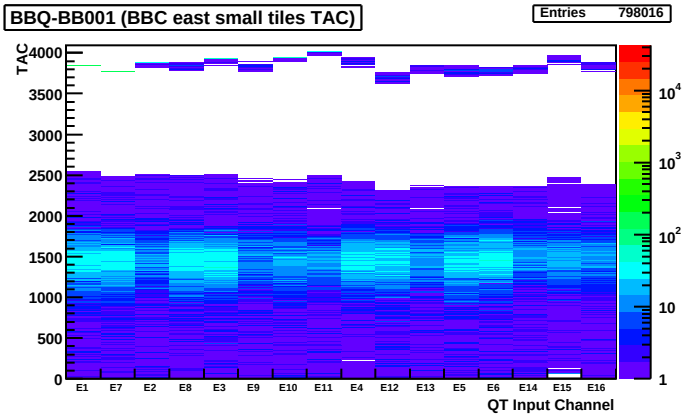

MIX-TF006
Entries 2094792
TOF MULT
TOF tray

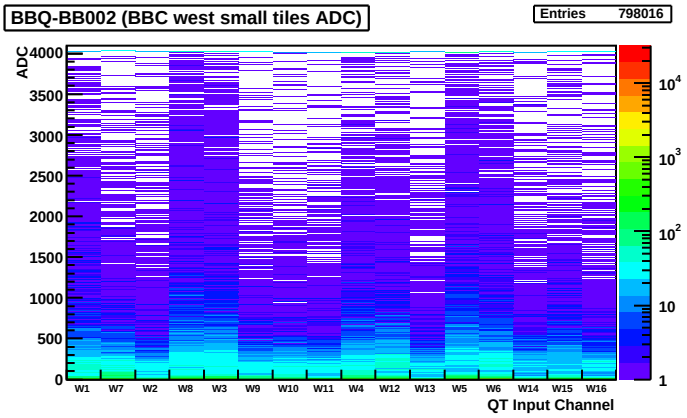

MIX-TF101
Entries 299256
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

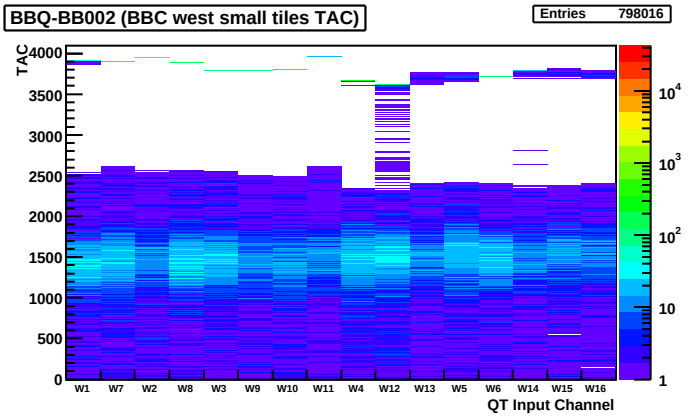

L1-TF201
Entries 49876
TOF total mult

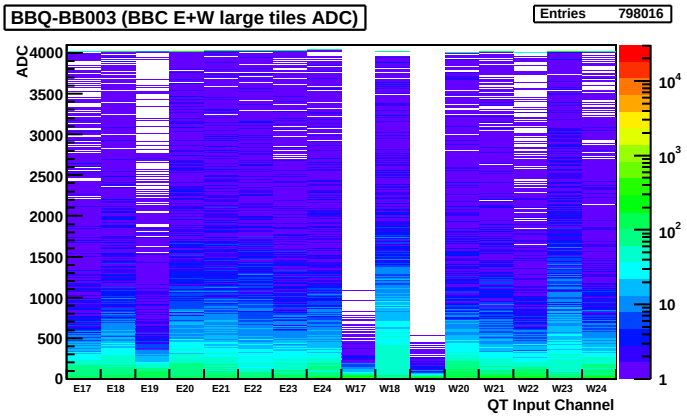

L1-TF201
Entries 299256
Threshold bits
30015
30090
30369
30123
29739
28547
19861
19786
19507
19753
20137
21329
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

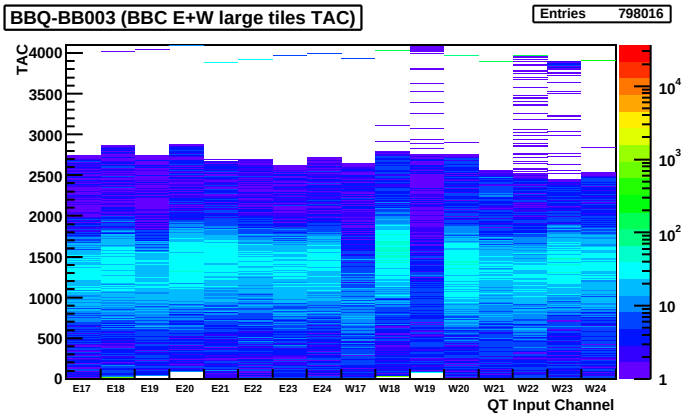

BBQ-BB001 (BBC east small tiles ADC)
Entries 798016
ADC
QT Input Channel

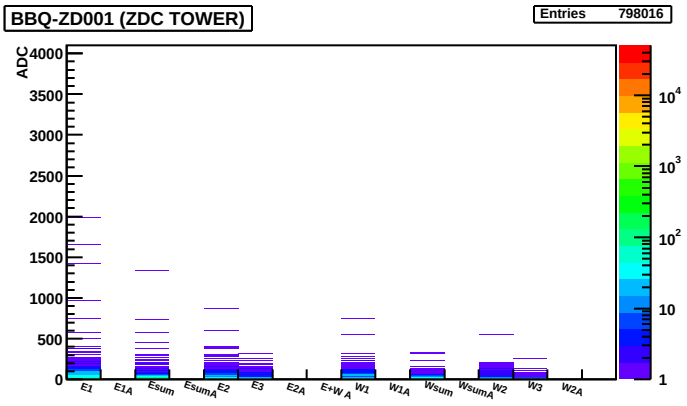

BBQ-BB001 (BBC east small tiles TAC)
Entries 798016
TAC
QT Input Channel

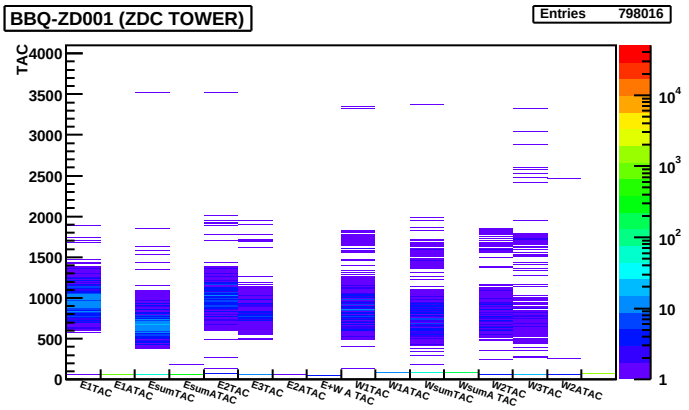

BBQ-BB002 (BBC west small tiles ADC)
Entries 798016
ADC
QT Input Channel

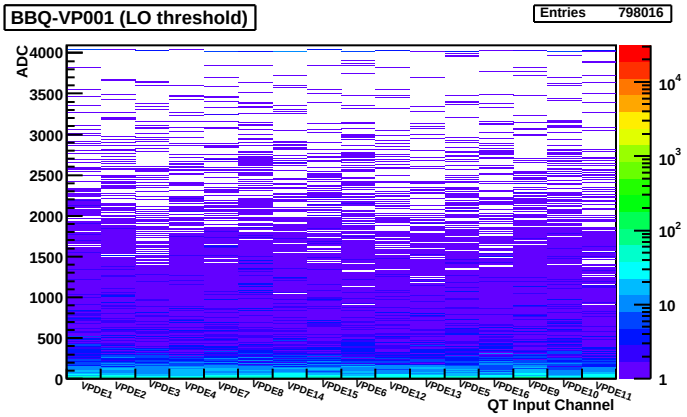

BBQ-BB002 (BBC west small tiles TAC)
Entries 798016
TAC
QT Input Channel

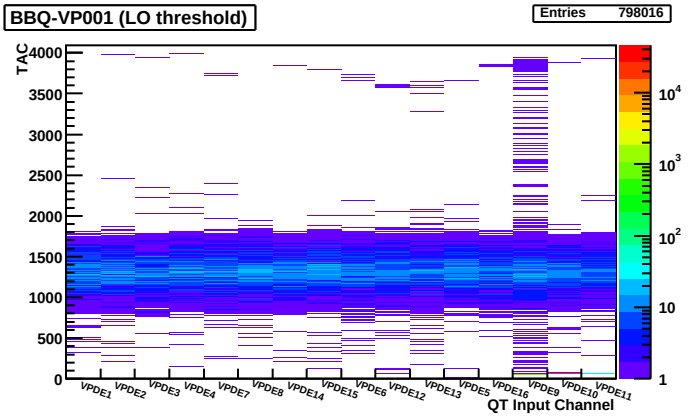

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 798016
ADC
QT Input Channel

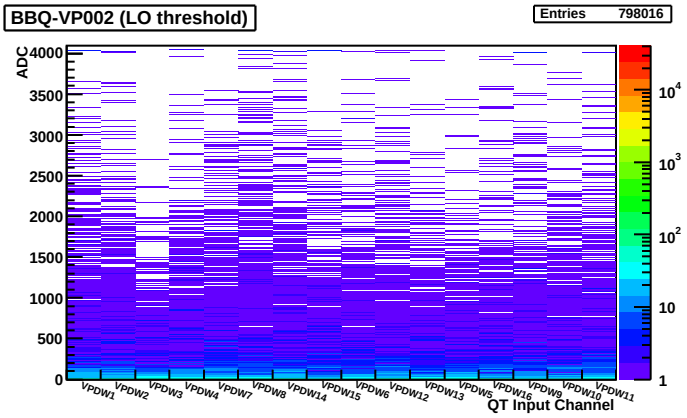

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 798016
TAC
QT Input Channel

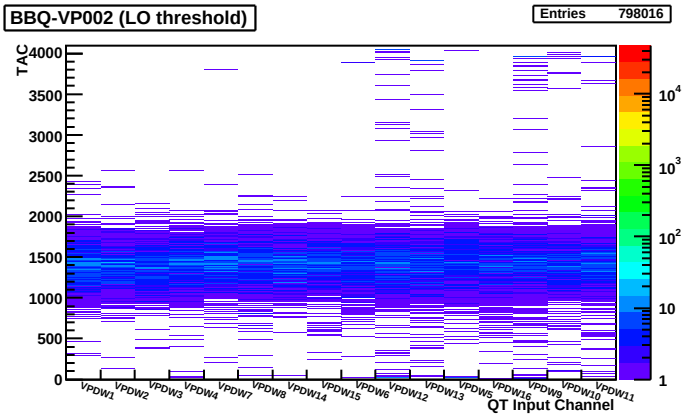

BBQ-ZD001 (ZDC TOWER)
Entries 798016
ADC

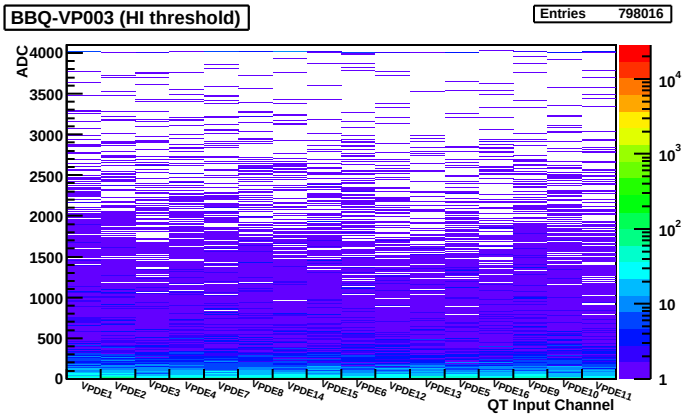

BBQ-ZD001 (ZDC TOWER)
Entries 798016
TAC

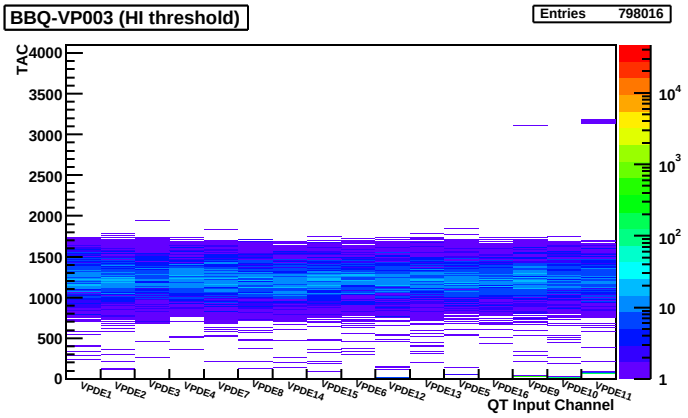

BBQ-VP001 (LO threshold)
Entries 798016
ADC
QT Input Channel

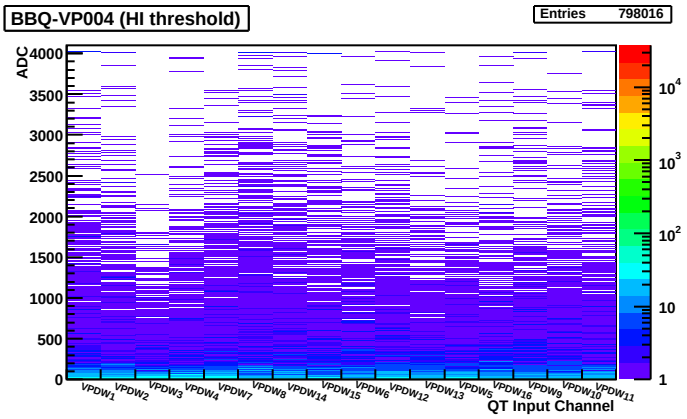

BBQ-VP001 (LO threshold)
Entries 798016
TAC
QT Input Channel

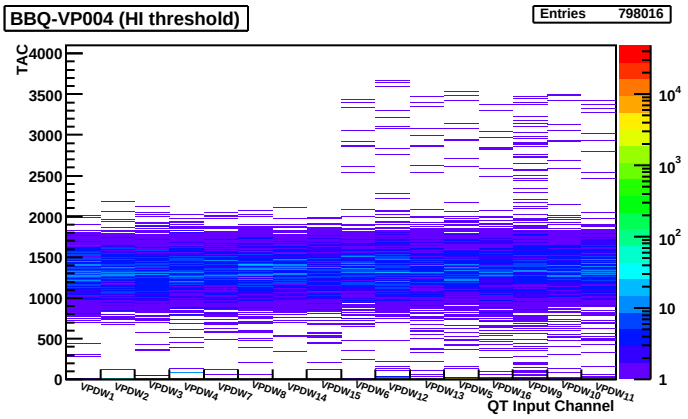

BBQ-VP002 (LO threshold)
Entries 798016
ADC
QT Input Channel

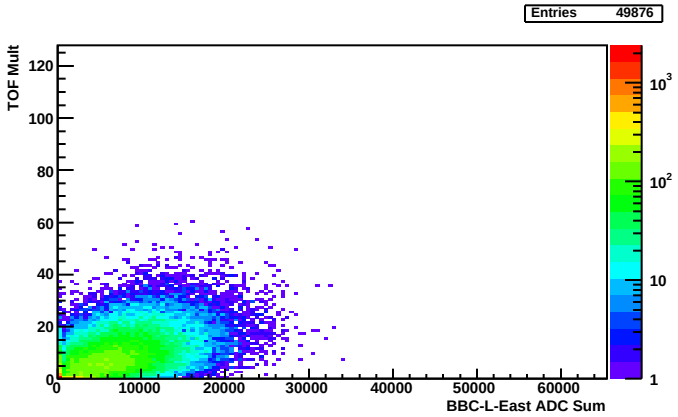

BBQ-VP002 (LO threshold)
Entries 798016
TAC
QT Input Channel

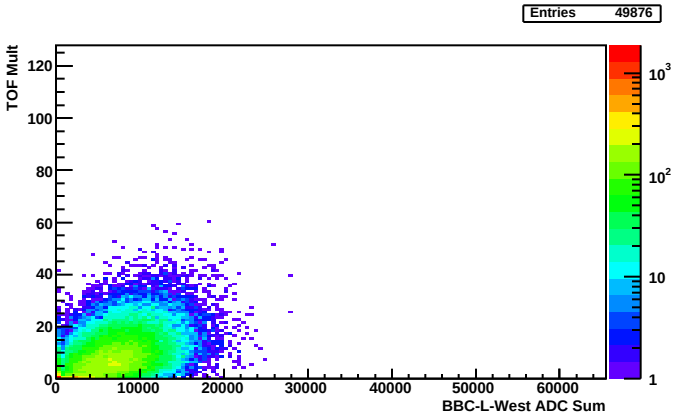

BBQ-VP003 (HI threshold)
Entries 798016
ADC
QT Input Channel

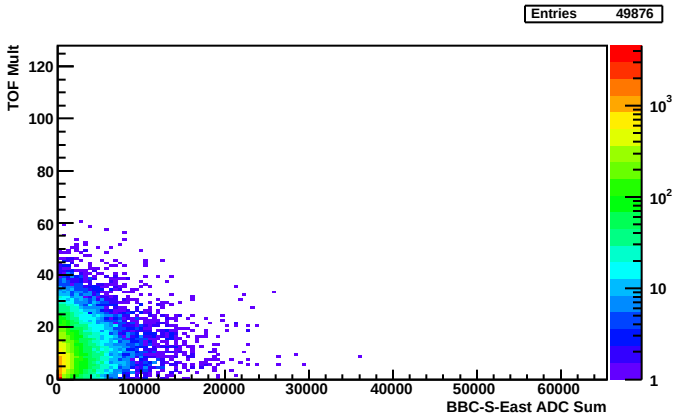

BBQ-VP003 (HI threshold)
Entries 798016
TAC
QT Input Channel

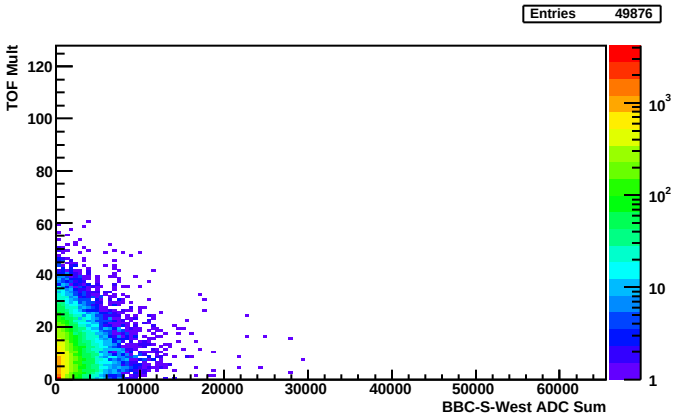

BBQ-VP004 (HI threshold)
Entries 798016
ADC
QT Input Channel

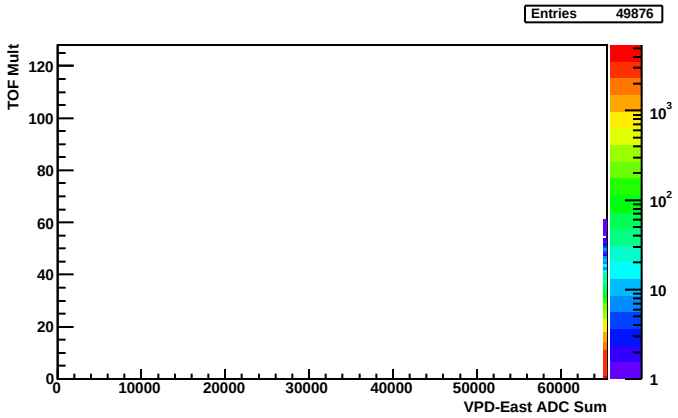

BBQ-VP004 (HI threshold)
Entries 798016
TAC
QT Input Channel

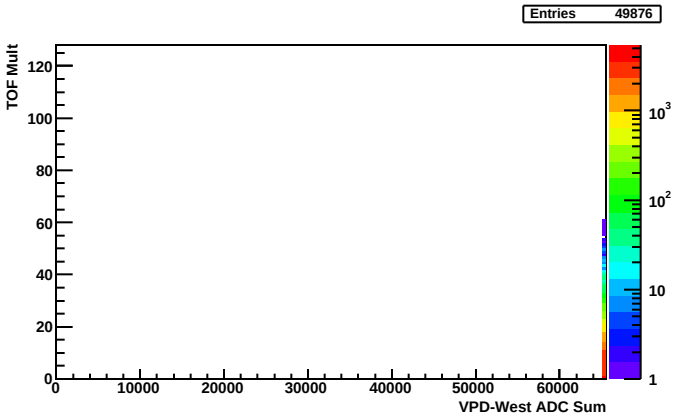

Entries 49876
TOF Mult
BBC-L-East ADC Sum

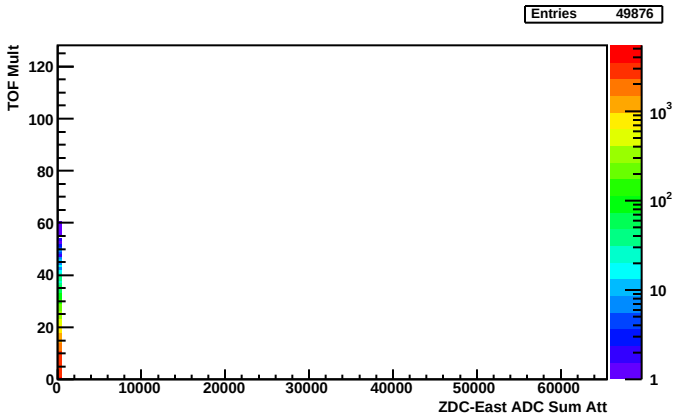

Entries 49876
TOF Mult
BBC-L-West ADC Sum

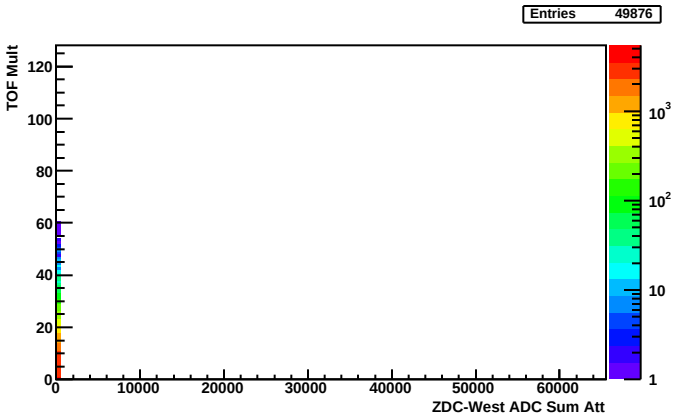

Entries 49876
TOF Mult
BBC-S-East ADC Sum

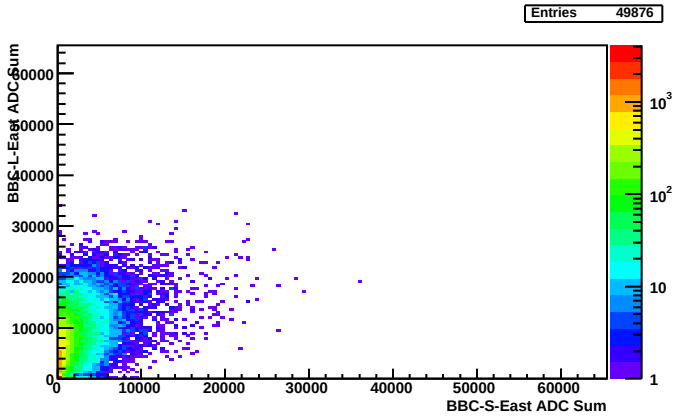

Entries 49876
TOF Mult
BBC-S-West ADC Sum

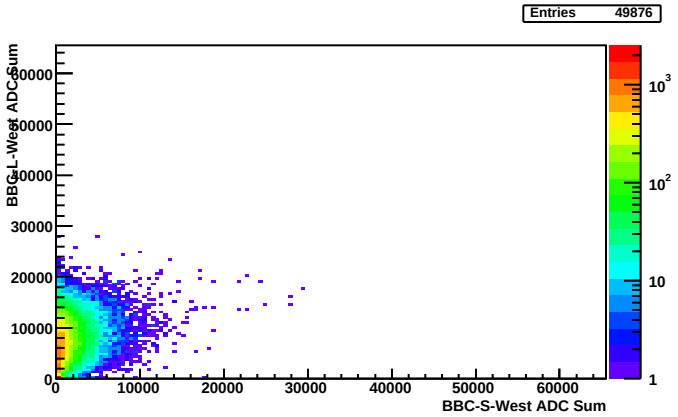

Entries 49876
TOF Mult
VPD-East ADC Sum

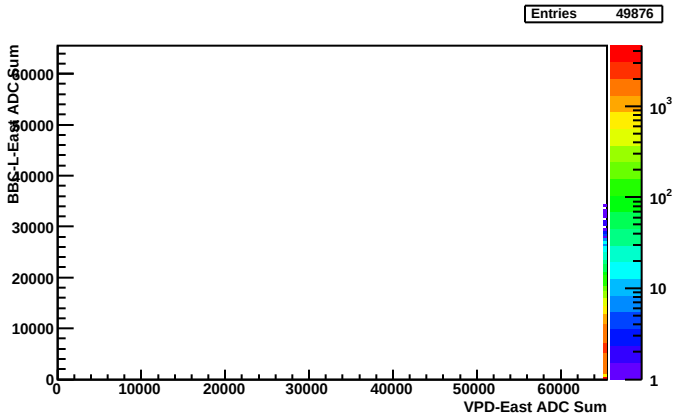

Entries 49876
TOF Mult
VPD-West ADC Sum

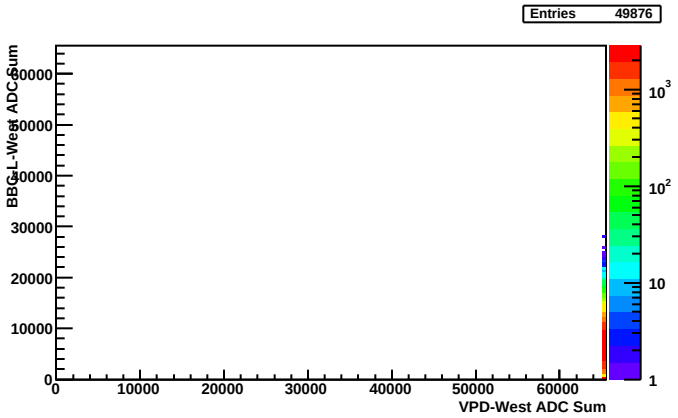

Entries 49876
TOF Mult
ZDC-East ADC Sum Att

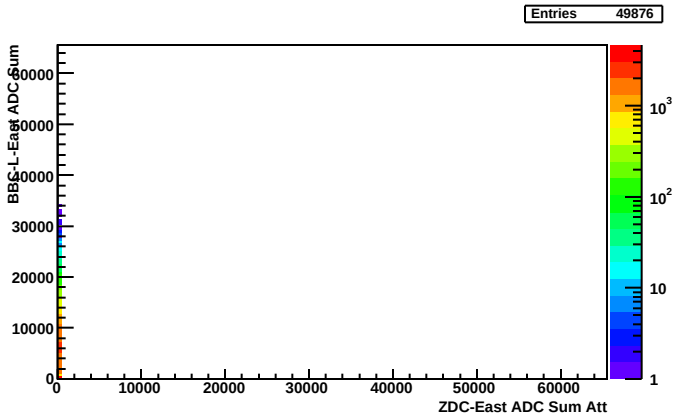

Entries 49876
TOF Mult
ZDC-West ADC Sum Att

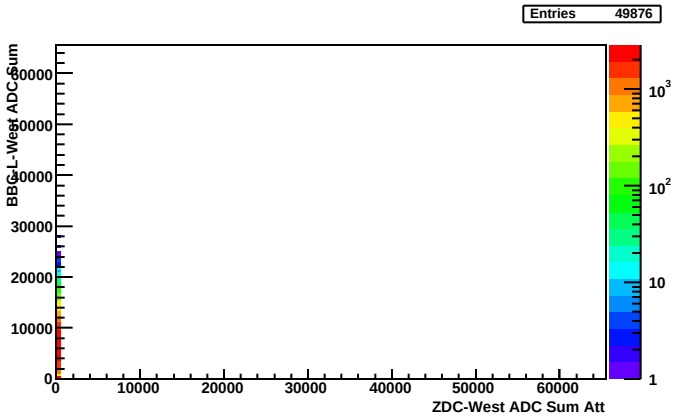

Entries 49876
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 49876
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 49876
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 49876
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 49876
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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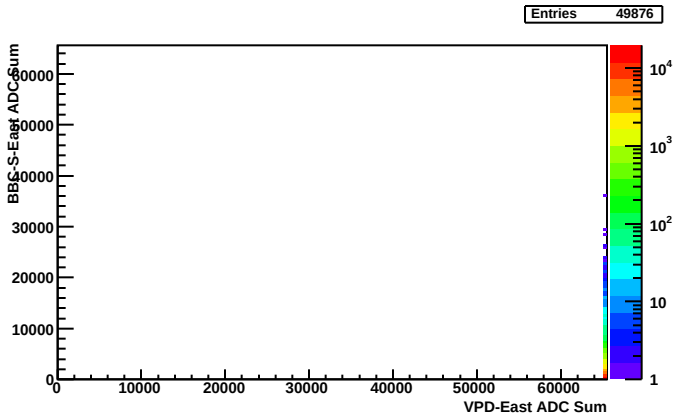
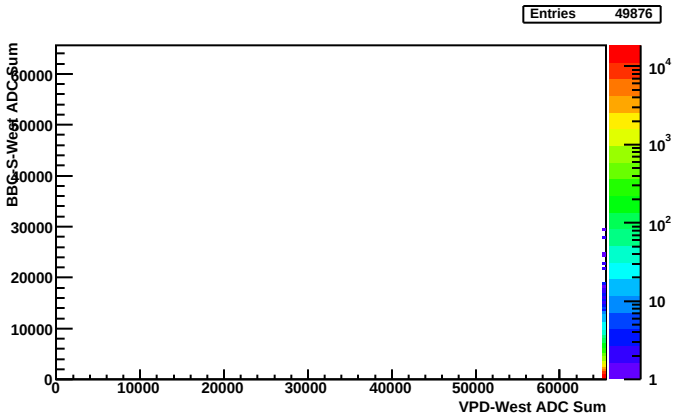
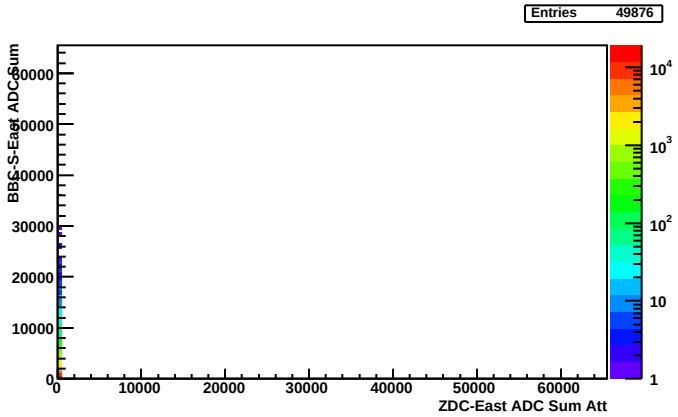
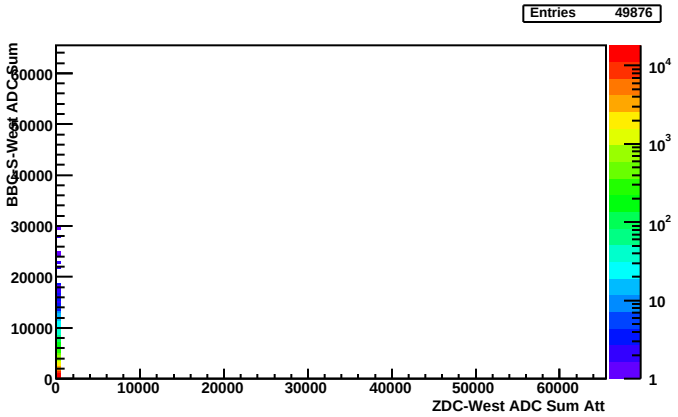
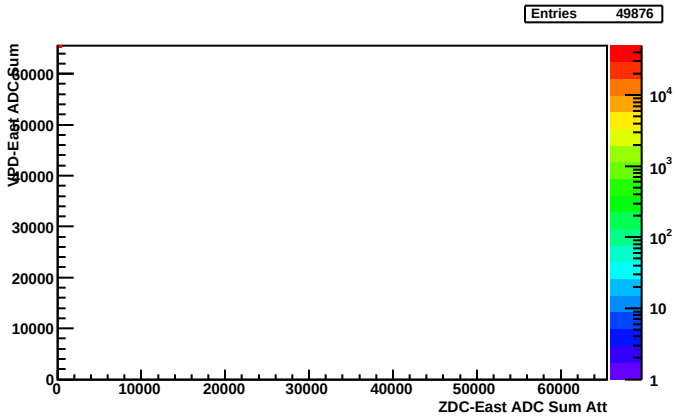
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

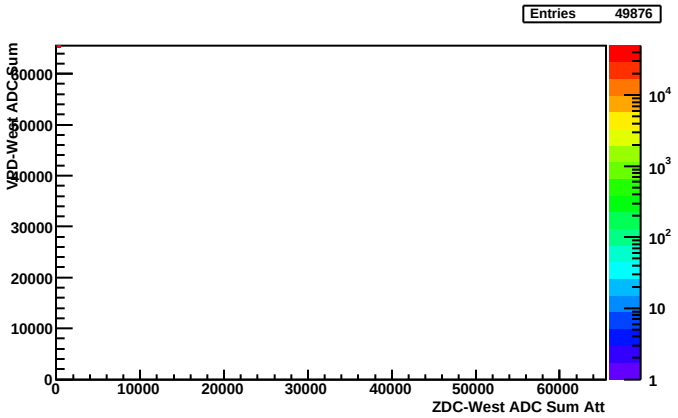

Entries 49876
BBC-S-East ADC Sum
VPD-East ADC Sum

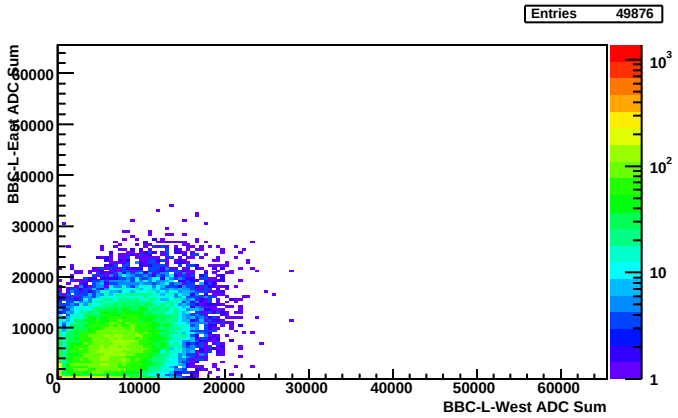

Entries 49876
BBC-S-West ADC Sum
VPD-West ADC Sum

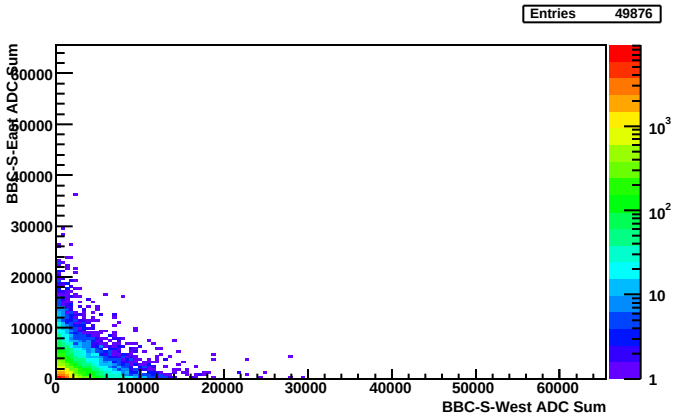

Entries 49876
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

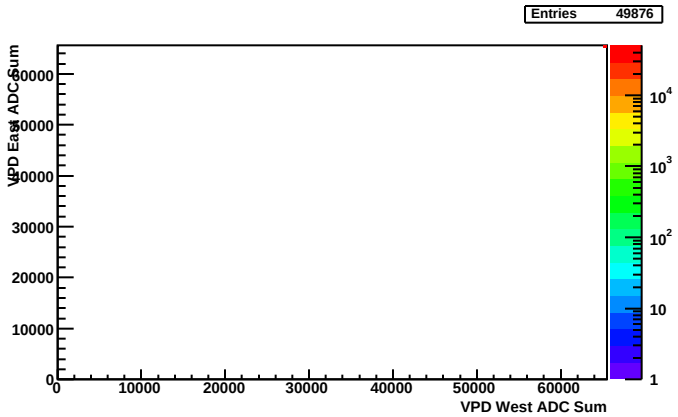

Entries 49876
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

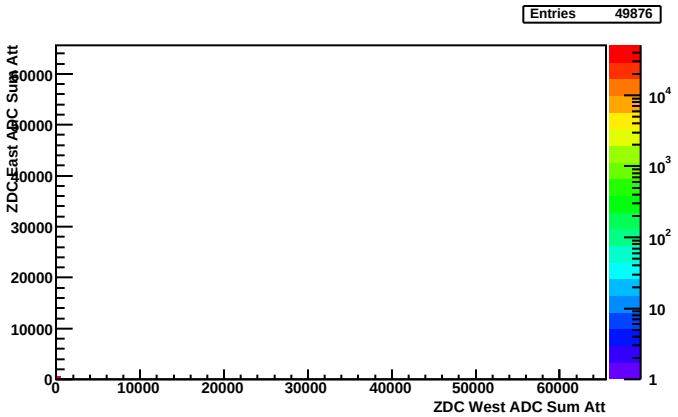

Entries 49876
VPD-East ADC Sum
ZDC-East ADC Sum Att

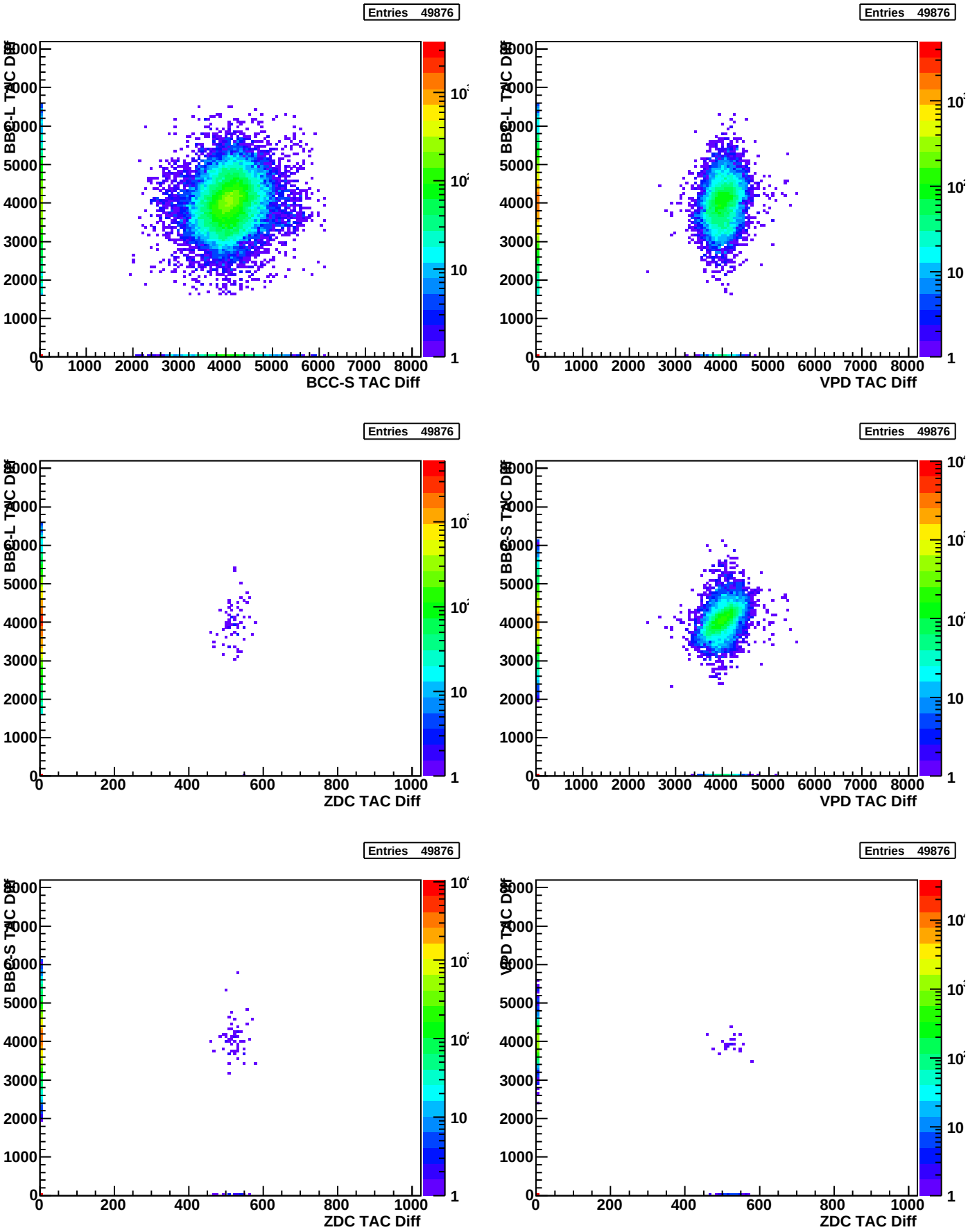

Entries 49876
VPD-West ADC Sum
ZDC-West ADC Sum Att

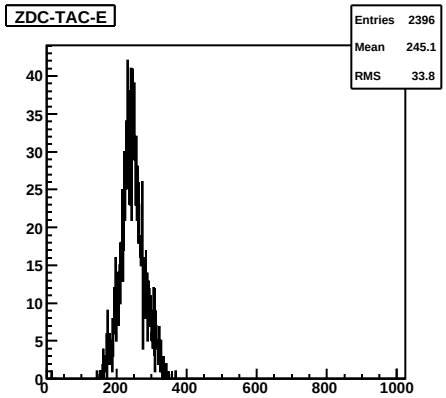

Entries 49876
BBC-L-East ADC Sum
BBC-L-West ADC Sum

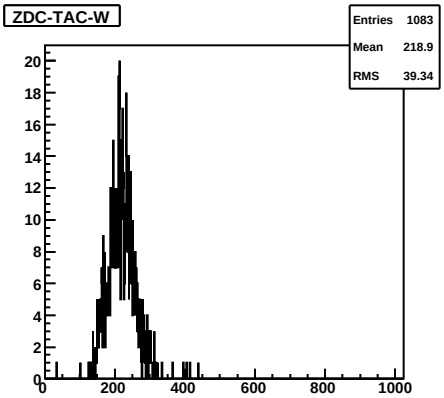

Entries 49876
BBC-S-East ADC Sum
BBC-S-West ADC Sum

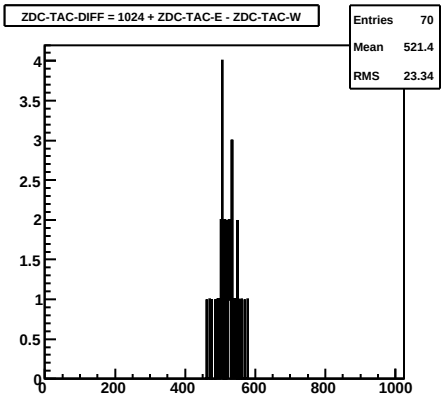

Entries 49876
VPD East ADC Sum
VPD West ADC Sum

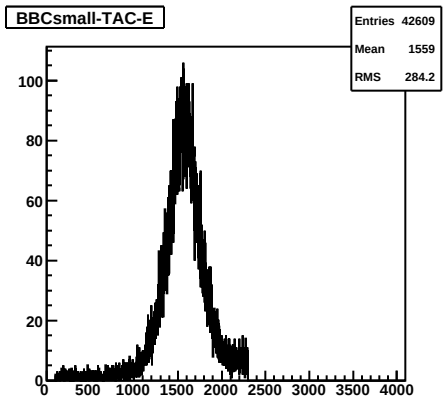

Entries 49876
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

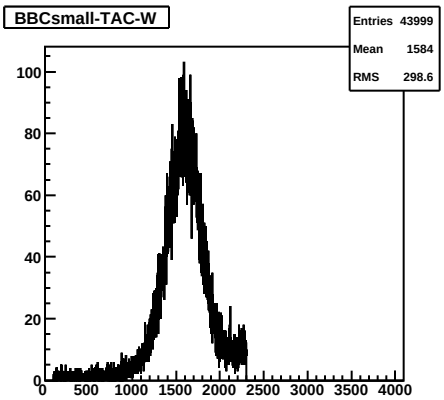

Entries 49876
BBC-L TAC Diff
BCC-S TAC Diff
Entries 49876
BBC-L TAC Diff
VPD TAC Diff
Entries 49876
BBC-L TAC Diff
ZDC TAC Diff
Entries 49876
BBC-S TAC Diff
VPD TAC Diff
Entries 49876
BBC-S TAC Diff
ZDC TAC Diff
Entries 49876
VPD TAC Diff
ZDC TAC Diff

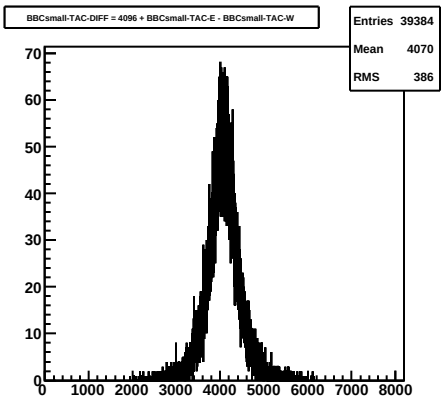

ZDC-TAC-E
Entries 2396
Mean 245.1
RMS 33.8

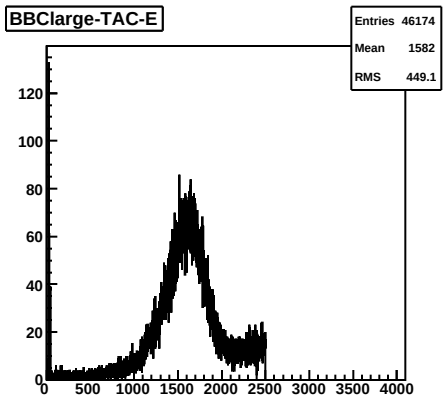

ZDC-TAC-W
Entries 1083
Mean 218.9
RMS 39.34

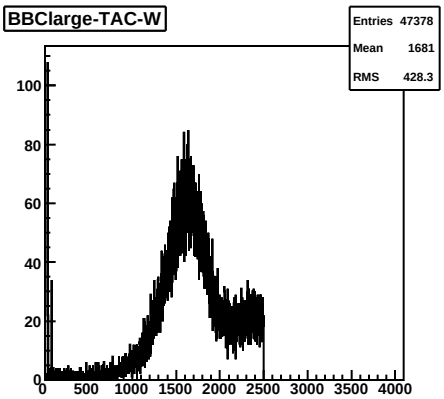

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 70
Mean 521.4
RMS 23.34

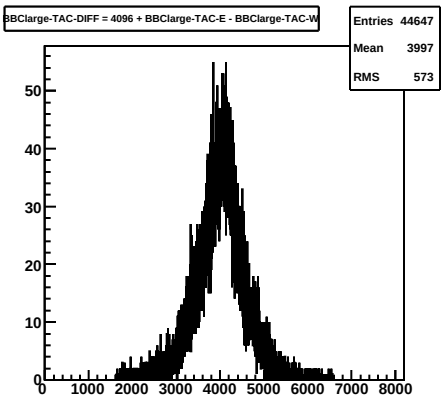

BBCsmall-TAC-E
Entries 42609
Mean 1559
RMS 284.2

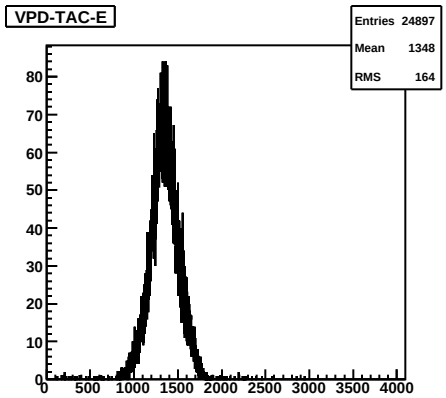

BBCsmall-TAC-W
Entries 43999
Mean 1584
RMS 298.6

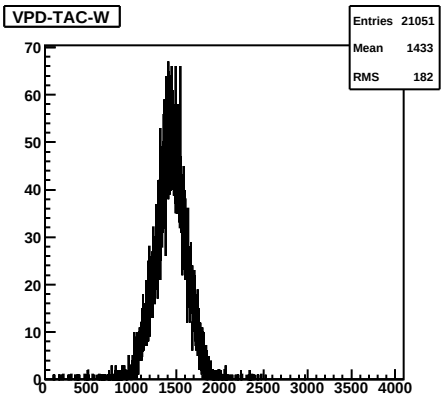

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 39384
Mean 4070
RMS 386

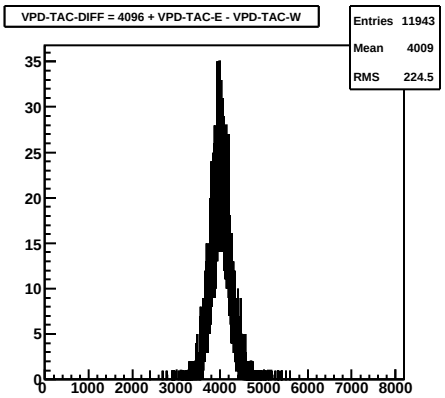

BBClarge-TAC-E
Entries 46174
Mean 1582
RMS 449.1

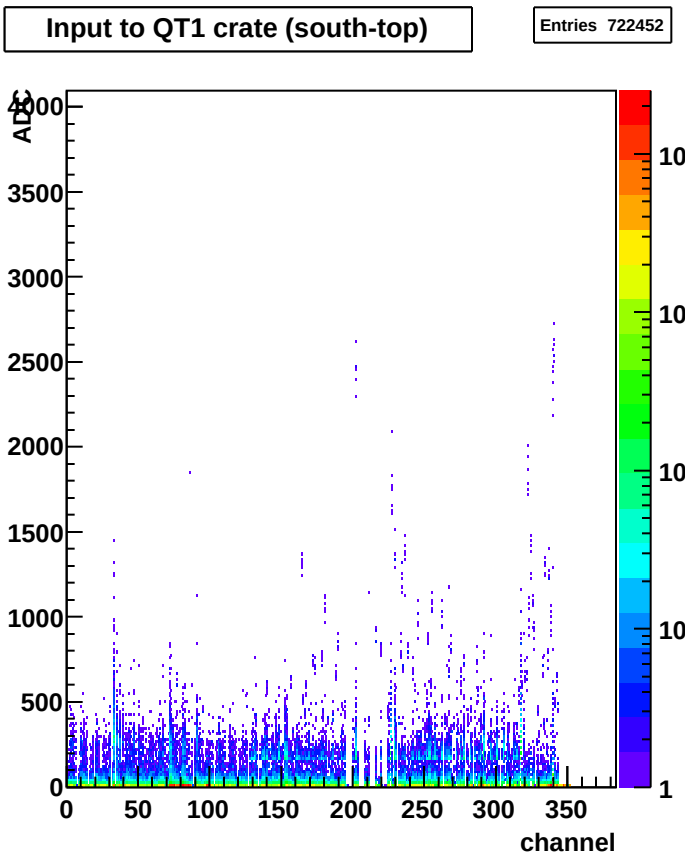

BBClarge-TAC-W
Entries 47378
Mean 1681
RMS 428.3

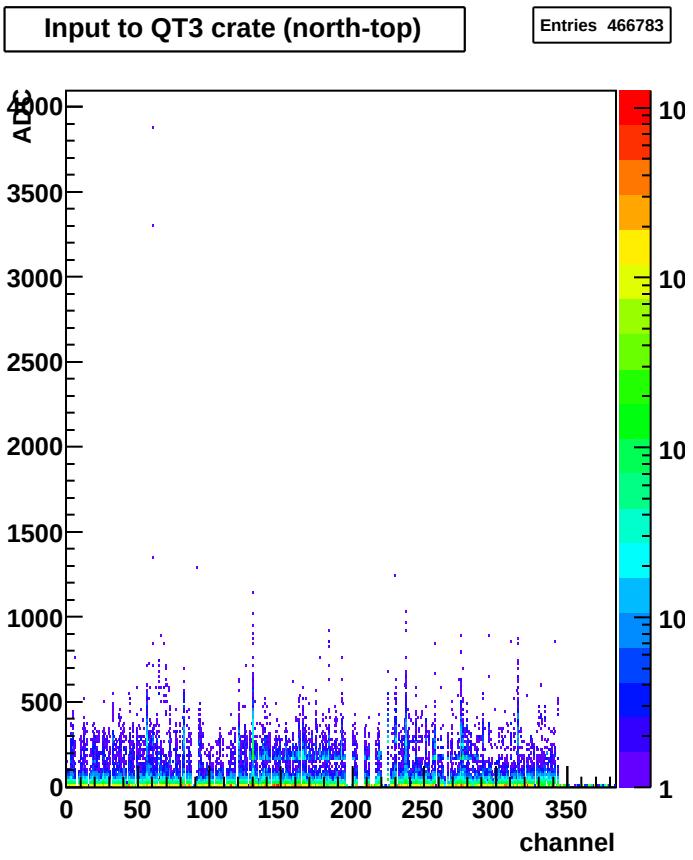

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 44647
Mean 3997
RMS 573

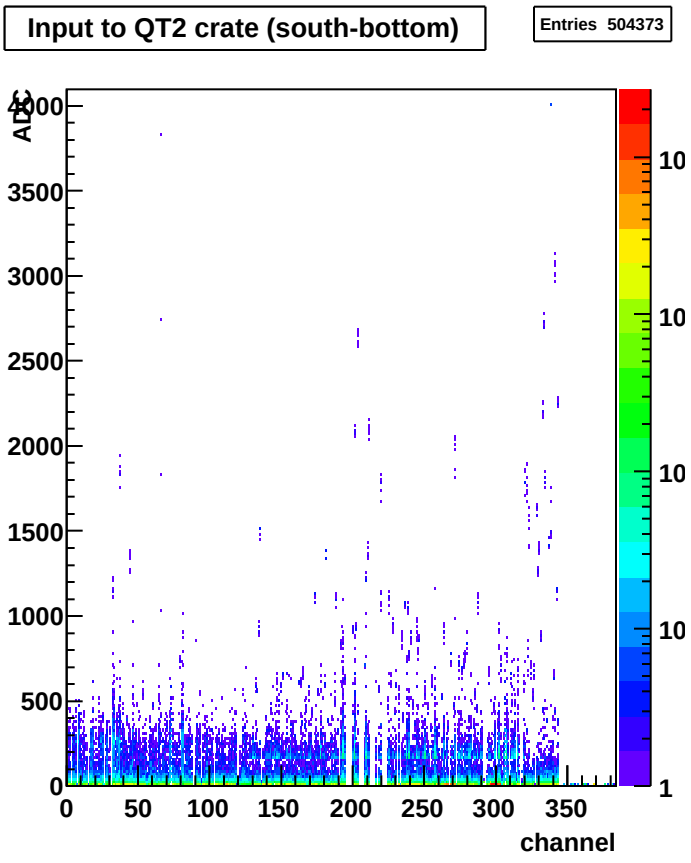

VPD-TAC-E
Entries 24897
Mean 1348
RMS 164

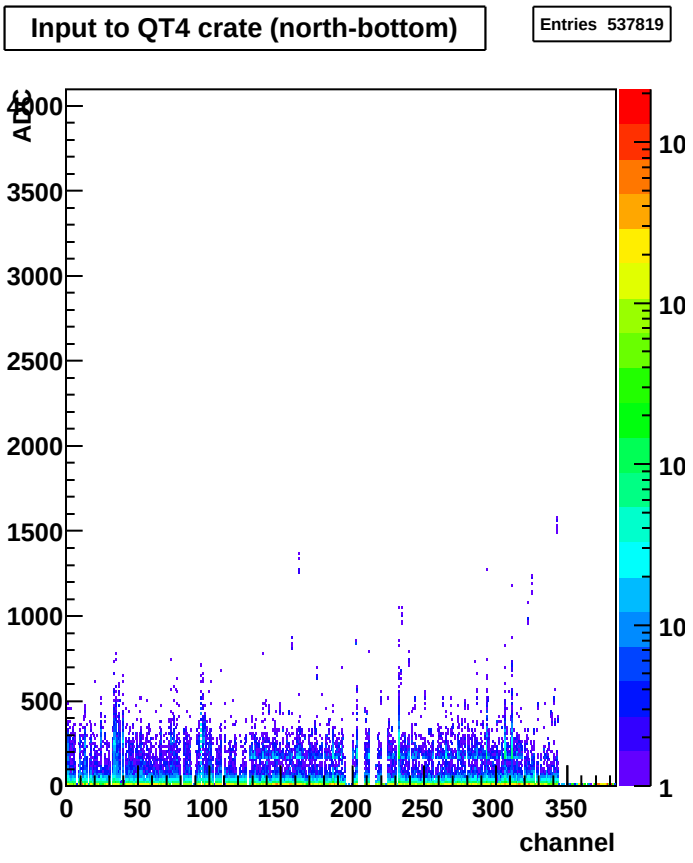

VPD-TAC-W
Entries 21051
Mean 1433
RMS 182

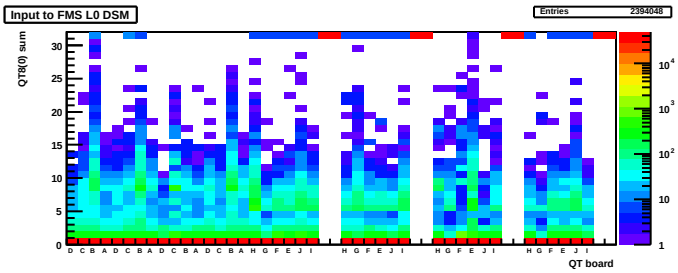

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 11943
Mean 4009
RMS 224.5

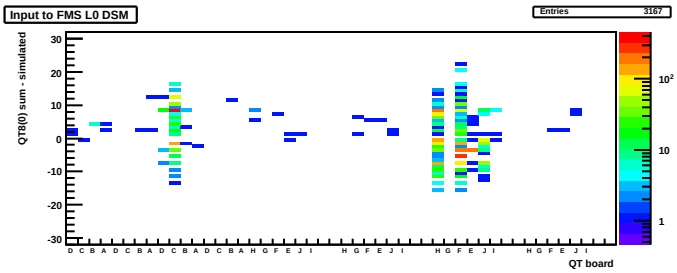

Input to QT1 crate (south-top)
Entries 722452
ADC
channel

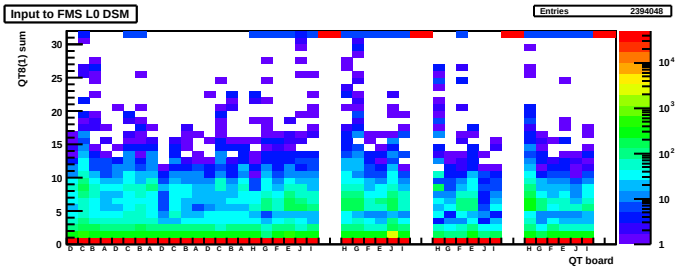

Input to QT3 crate (north-top)
Entries 466783
ADC
channel

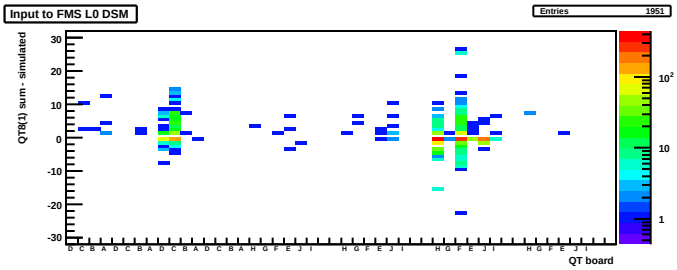

Input to QT2 crate (south-bottom)
Entries 504373
ADC
channel

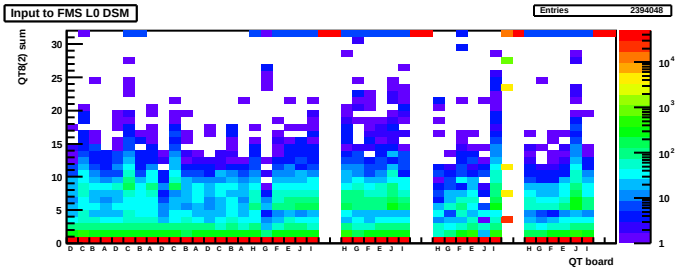

Input to QT4 crate (north-bottom)
Entries 537819
ADC
channel

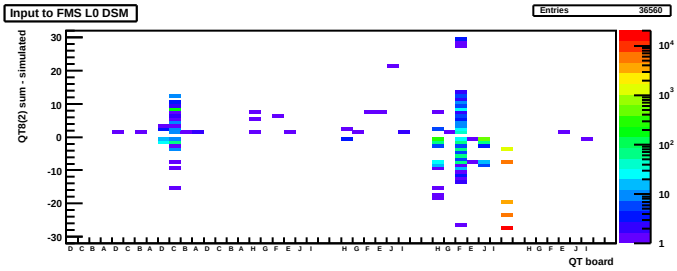

Input to FMS L0 DSM
Entries 2394048
QT8(0) sum
QT board

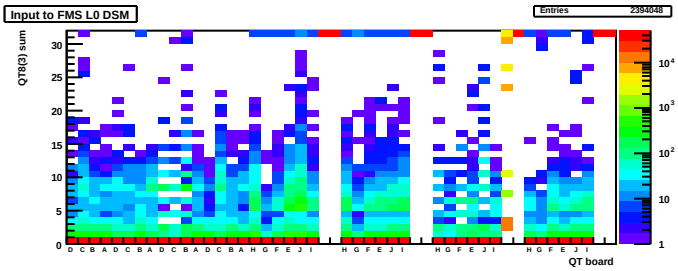

Input to FMS L0 DSM
Entries 3167
QT8(0) sum - simulated
QT board

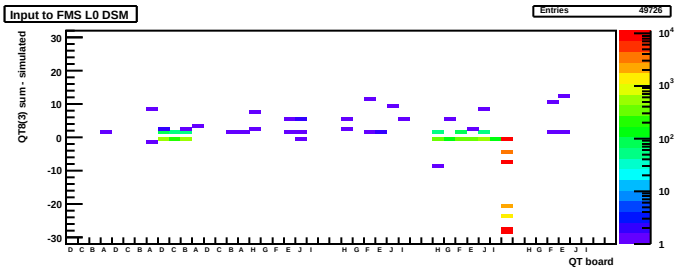

Input to FMS L0 DSM
Entries 2394048
QT8(1) sum
QT board

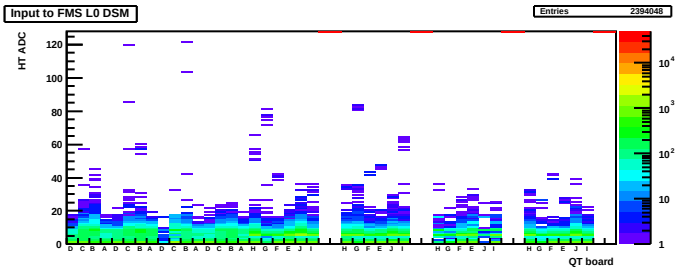

Input to FMS L0 DSM
Entries 1951
QT8(1) sum - simulated
QT board

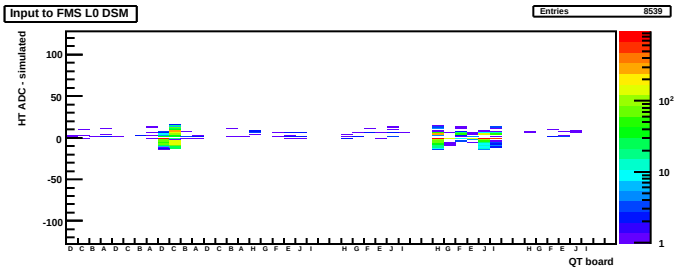

Input to FMS L0 DSM
Entries 2394048
QT8(2) sum
QT board

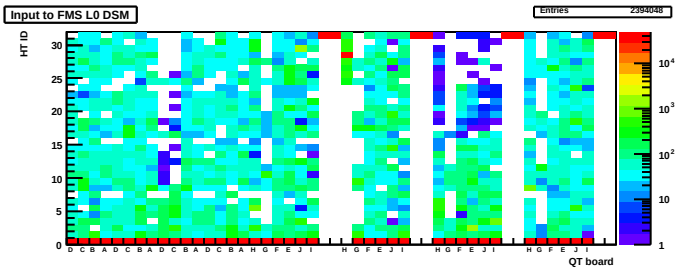

Input to FMS L0 DSM
Entries 36560
QT8(2) sum - simulated
QT board

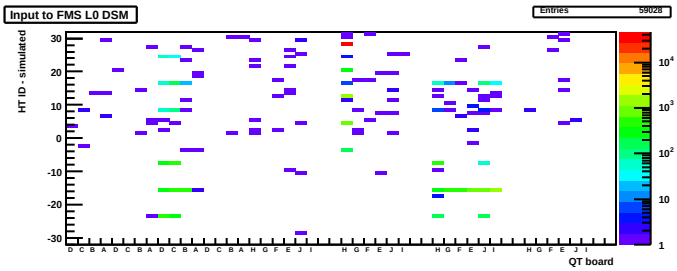

Input to FMS L0 DSM
Entries 2394048
QT8(3) sum
QT board

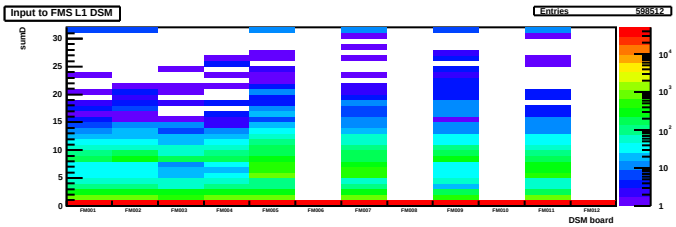

Input to FMS L0 DSM
Entries 49726
QT8(3) sum - simulated
QT board

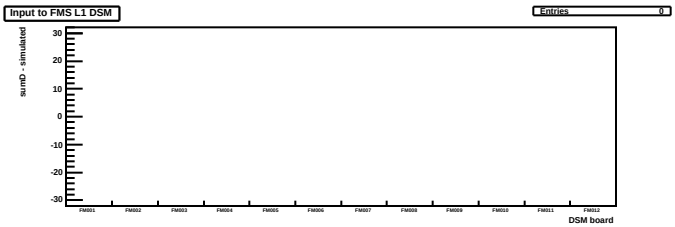

Input to FMS L0 DSM
Entries 2394048
HT ADC
QT board

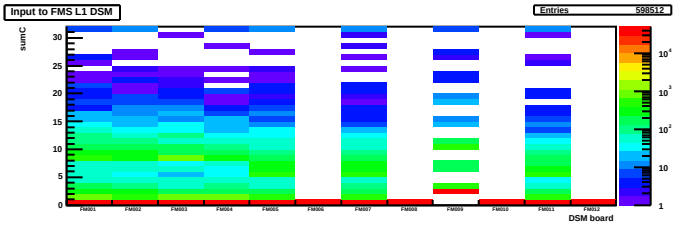

Input to FMS L0 DSM
Entries 8539
HT ADC - simulated
QT board

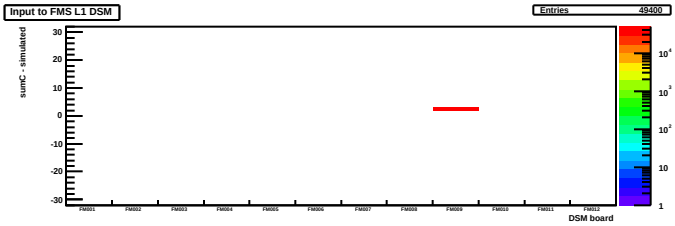

Input to FMS L0 DSM
Entries 2394048
HT ID
QT board

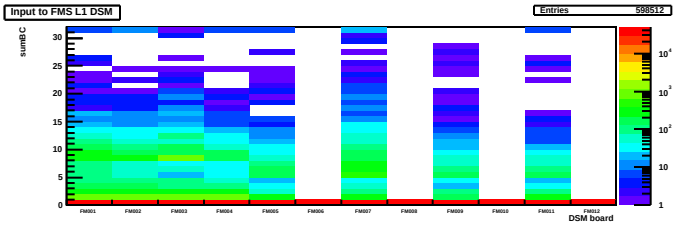

Input to FMS L0 DSM
Entries 59028
HT ID - simulated
QT board

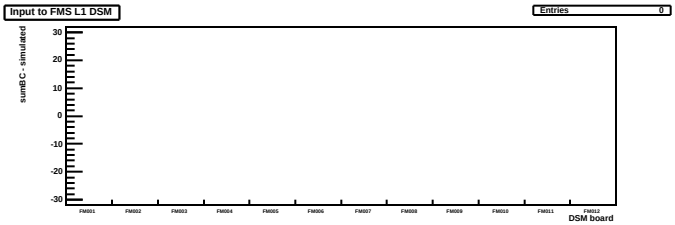

Input to FMS L1 DSM
Entries 598512
sumD
DSM board

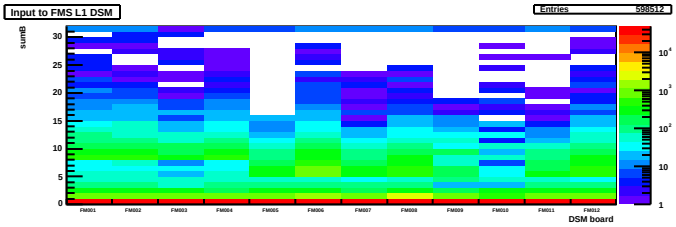

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

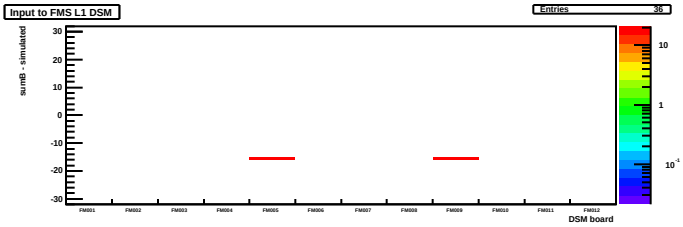

Input to FMS L1 DSM
Entries 598512
sumC
DSM board

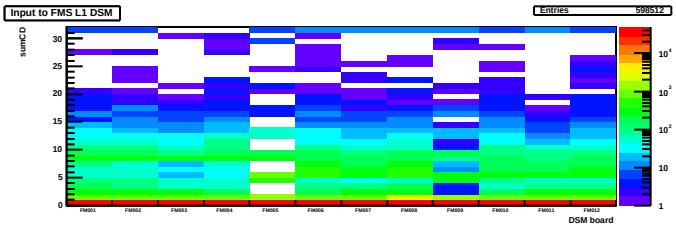

Input to FMS L1 DSM
Entries 49400
sumC - simulated
DSM board

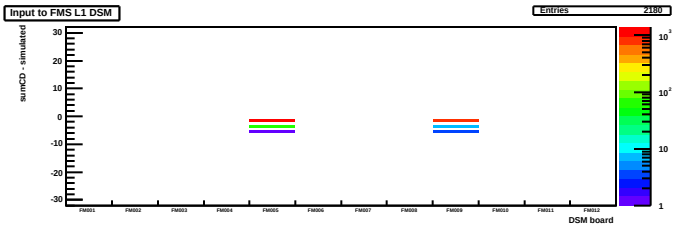

Input to FMS L1 DSM
Entries 598512
sumBC
DSM board

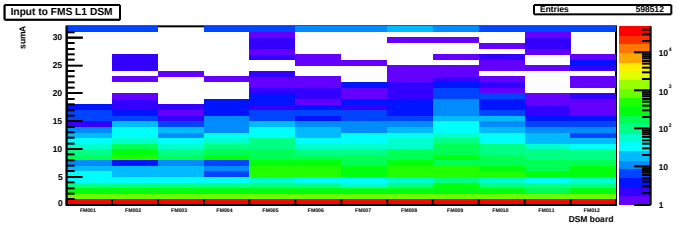

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

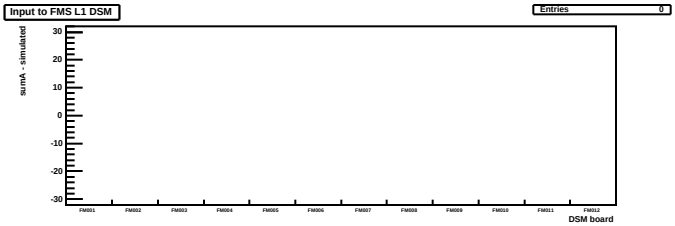

Input to FMS L1 DSM
Entries 598512
sumB
DSM board

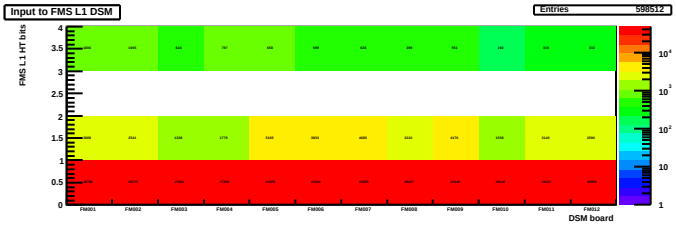

Input to FMS L1 DSM
Entries 36
sumB - simulated
DSM board

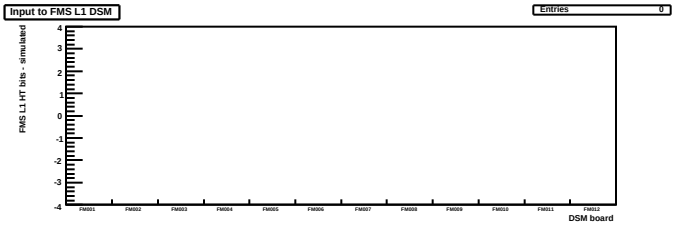

Input to FMS L1 DSM
Entries 598512
sumCD
DSM board

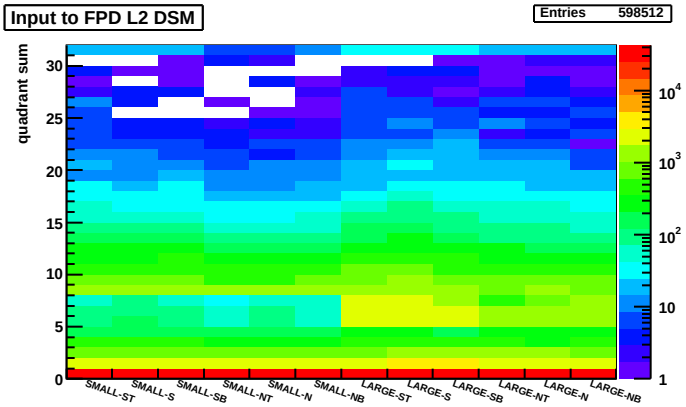

Input to FMS L1 DSM
Entries 2180
sumCD - simulated
DSM board

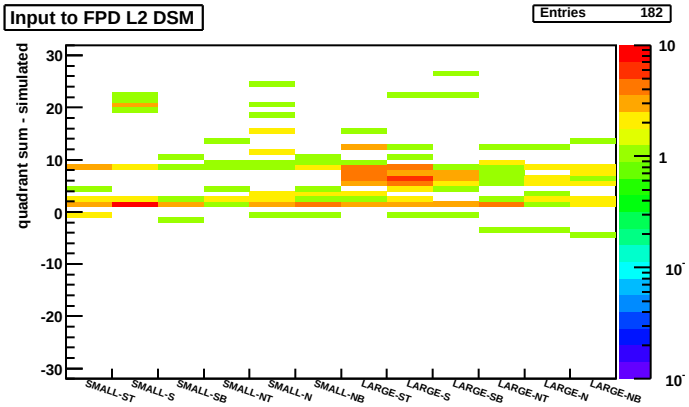

Input to FMS L1 DSM
Entries 598512
sumA
DSM board

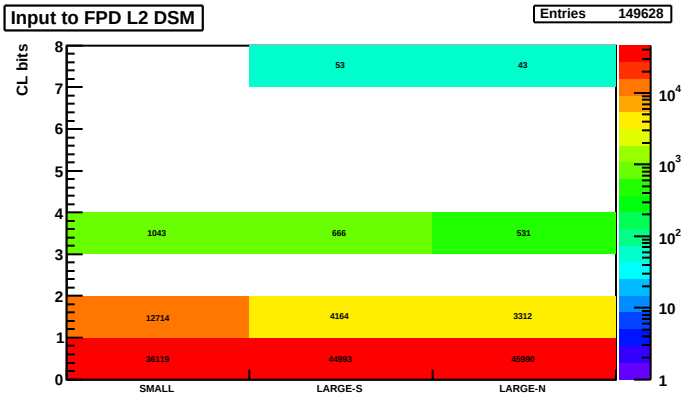

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

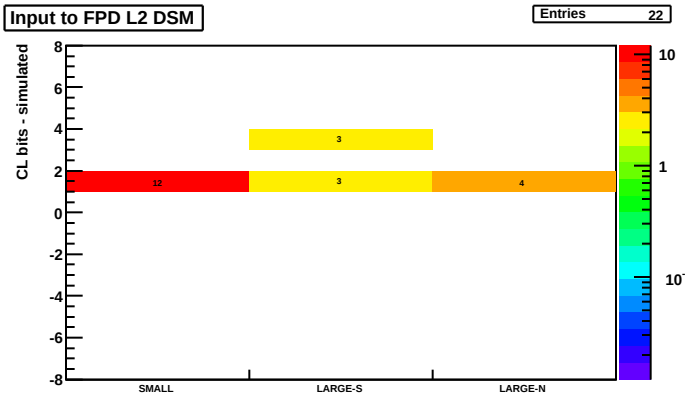

Input to FMS L1 DSM
Entries 598512
FMS L1 HT bits
DSM board

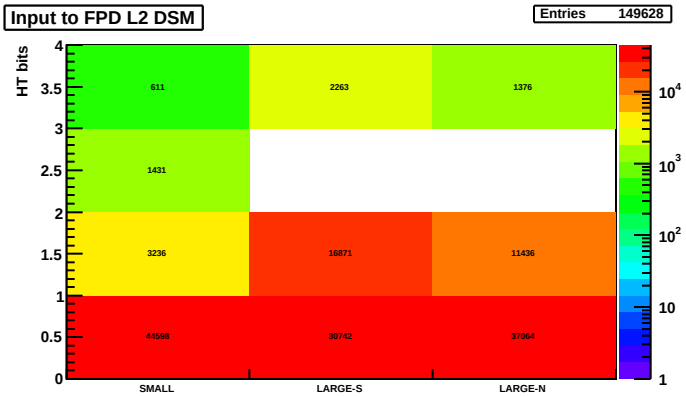

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

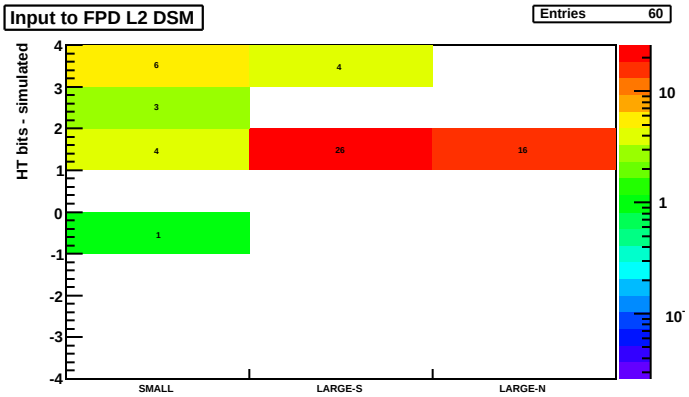

Input to FPD L2 DSM
Entries 598512
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

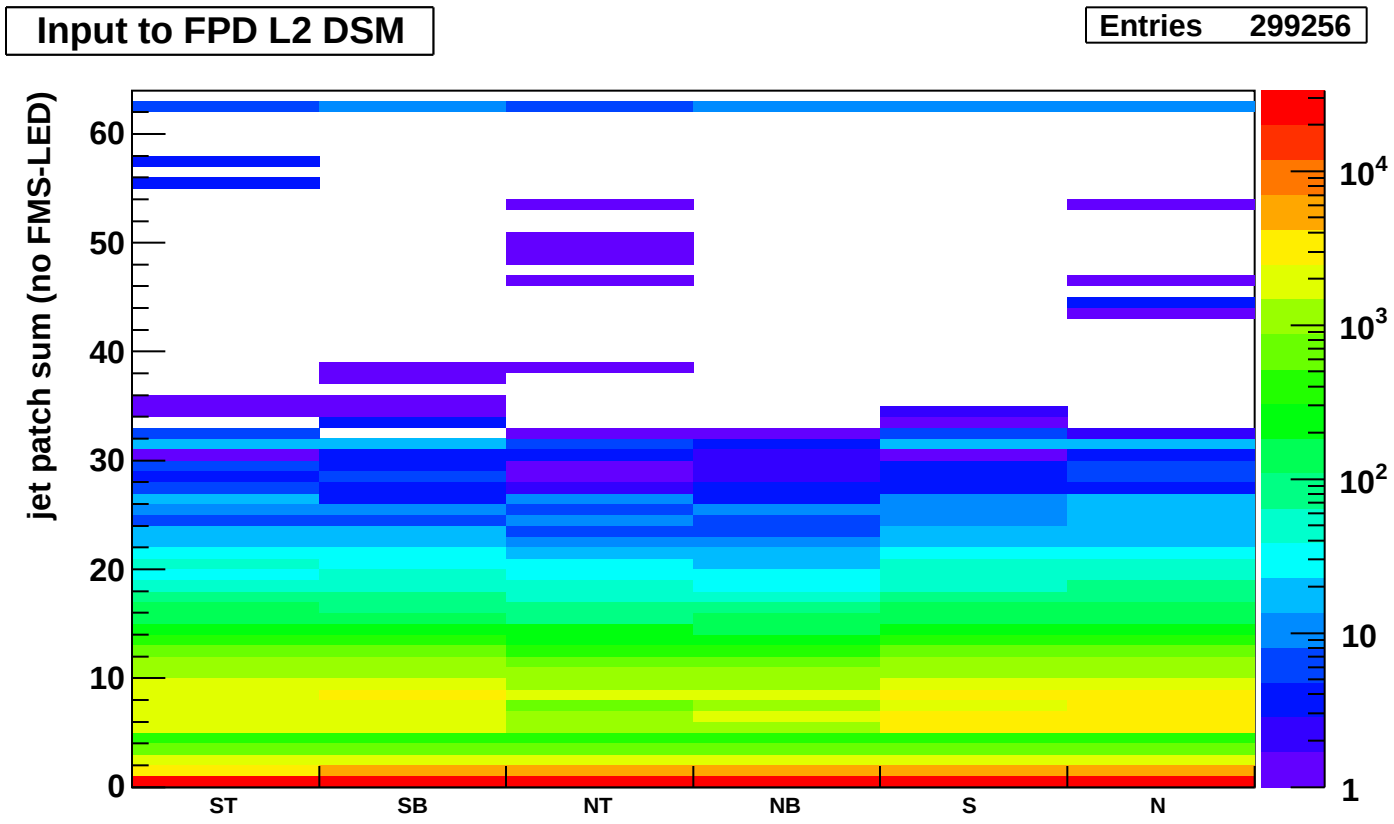

Input to FPD L2 DSM
Entries 182
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

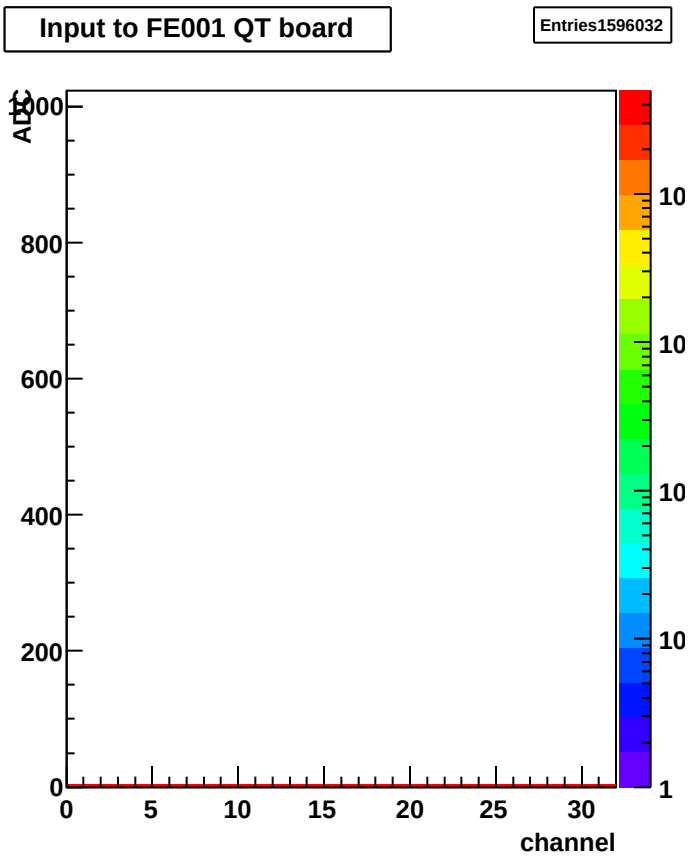

Input to FPD L2 DSM
Entries 149628
CL bits
53
43
1043
666
531
12714
4164
3312
36119
44993
45990
SMALL
LARGE-S
LARGE-N

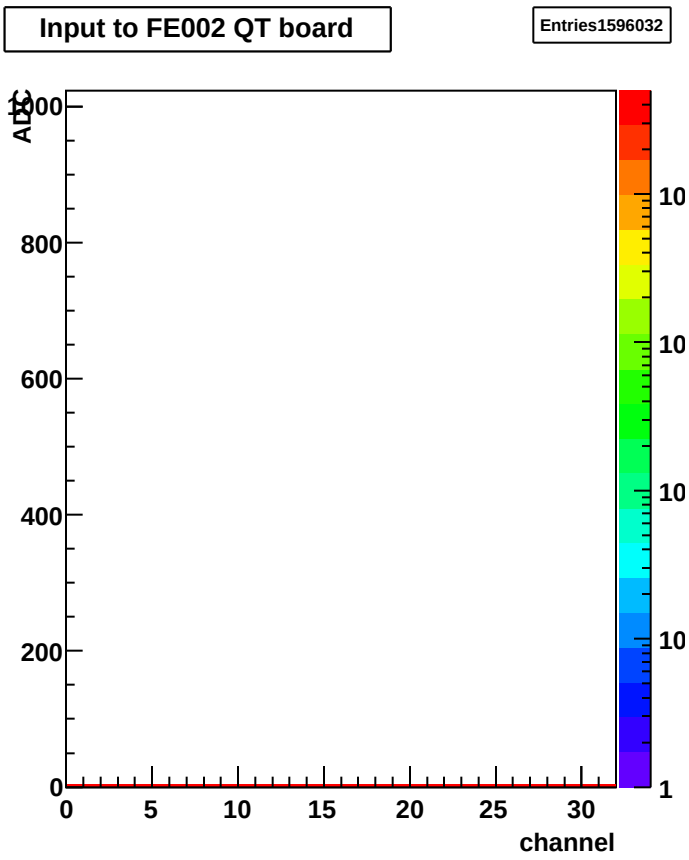

Input to FPD L2 DSM
Entries 22
CL bits - simulated
3
12
3
4
SMALL
LARGE-S
LARGE-N

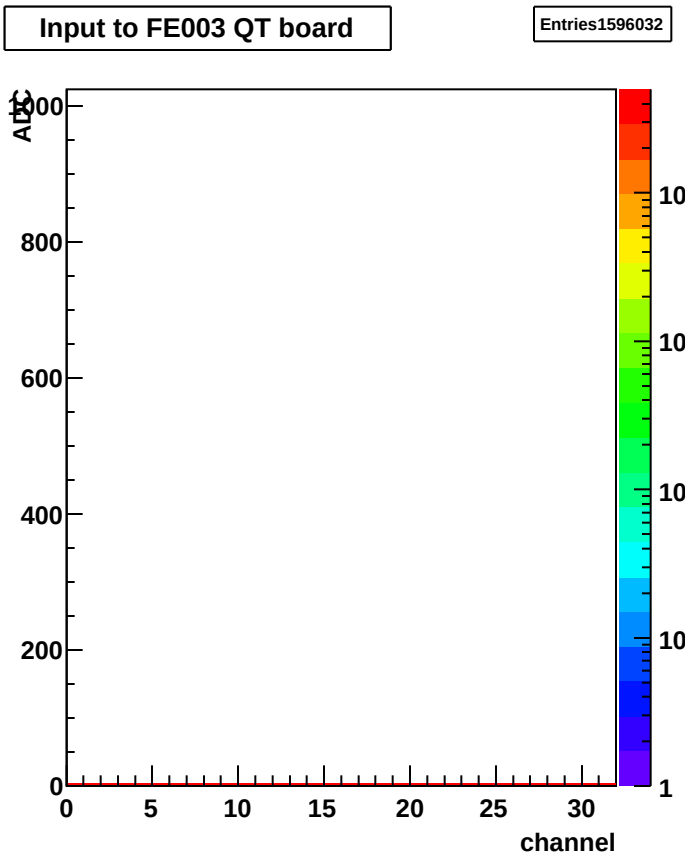

Input to FPD L2 DSM
Entries 149628
HT bits
611
2263
1376
1431
3236
16871
11436
44598
30742
37064
SMALL
LARGE-S
LARGE-N

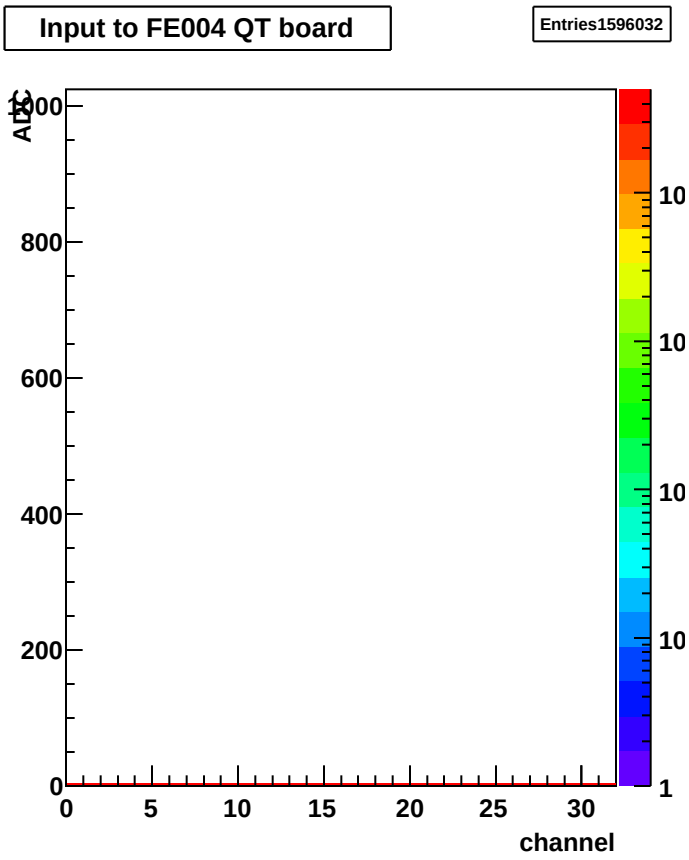

Input to FPD L2 DSM
Entries 60
HT bits - simulated
6
4
3
4
26
16
1
SMALL
LARGE-S
LARGE-N

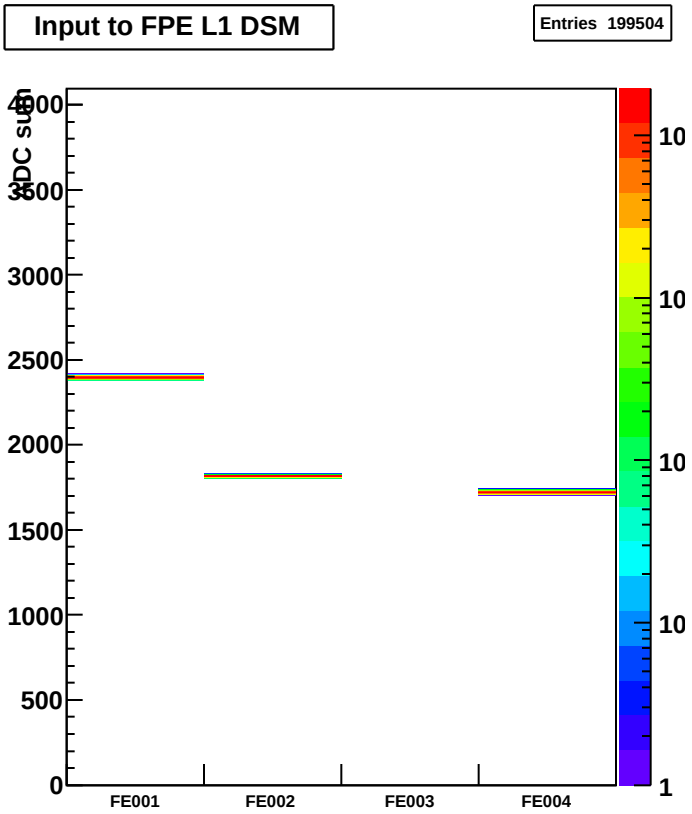

Input to FPD L2 DSM
Entries 299256
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

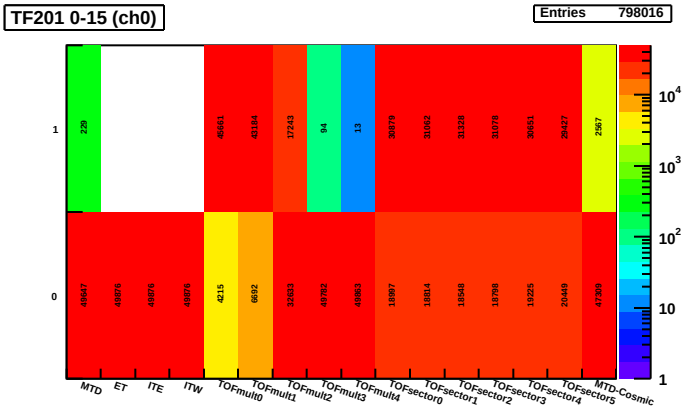

Input to FE001 QT board
Entries1596032
ADC
channel

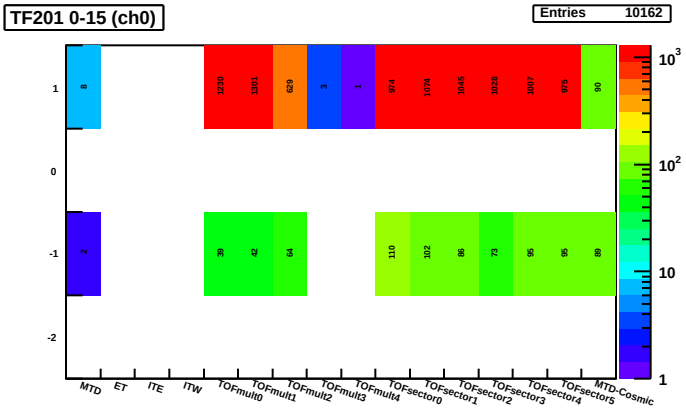

Input to FE002 QT board
Entries1596032
ADC
channel

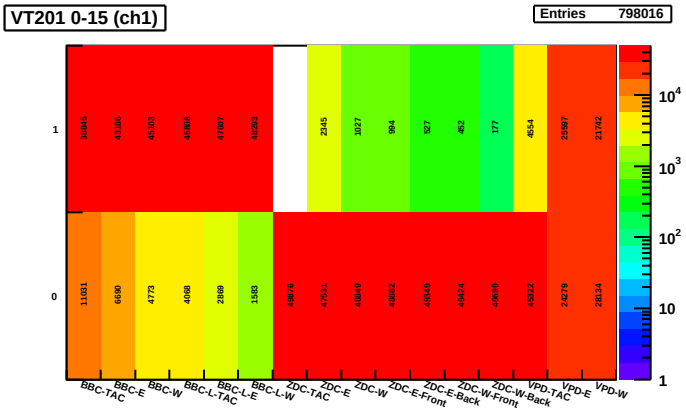

Input to FE003 QT board
Entries1596032
ADC
channel

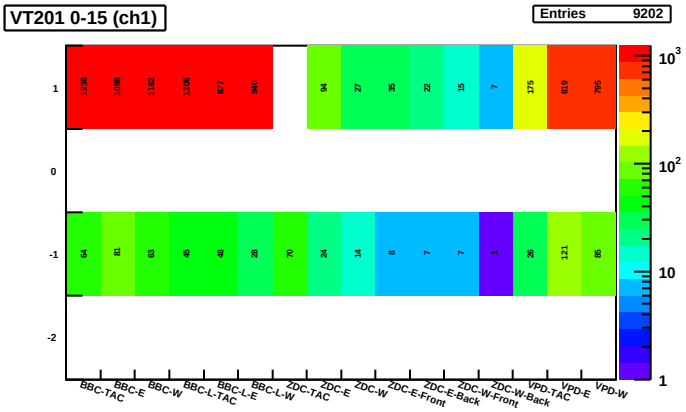

Input to FE004 QT board
Entries1596032
ADC
channel

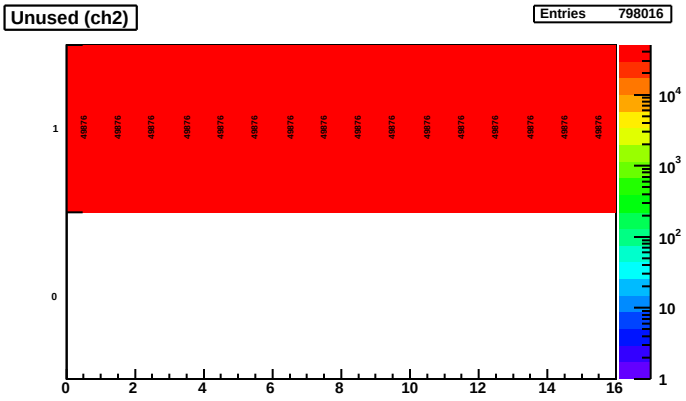

Input to FPE L1 DSM
Entries 199504
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

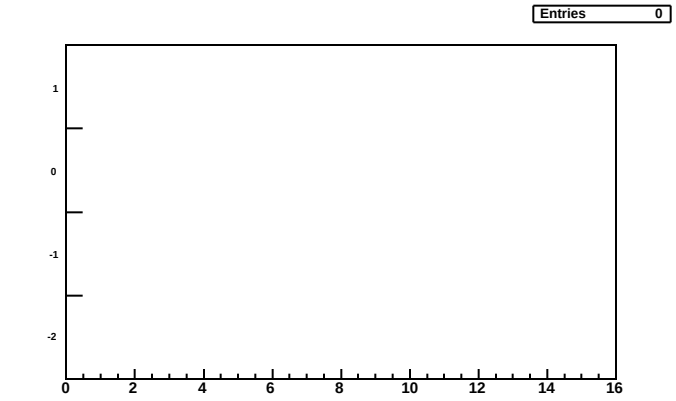

TF201 0-15 (ch0)
Entries 798016
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

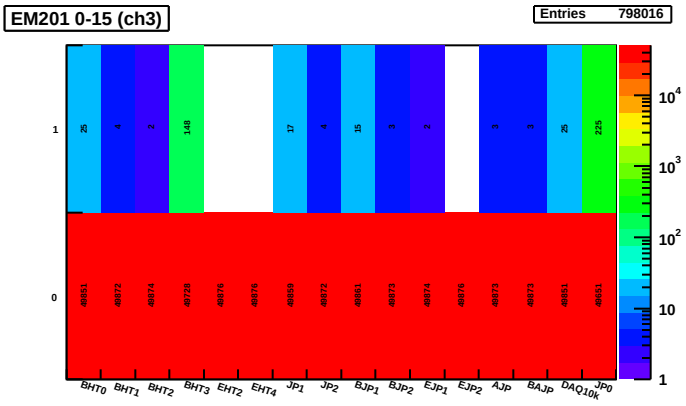

TF201 0-15 (ch0)
Entries 10162
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 798016
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 9202
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 798016


Entries 0


EM201 0-15 (ch3)
Entries 798016
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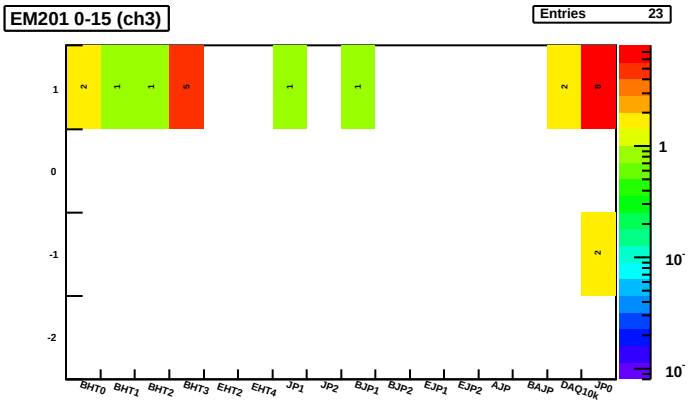
EM201 0-15 (ch3)
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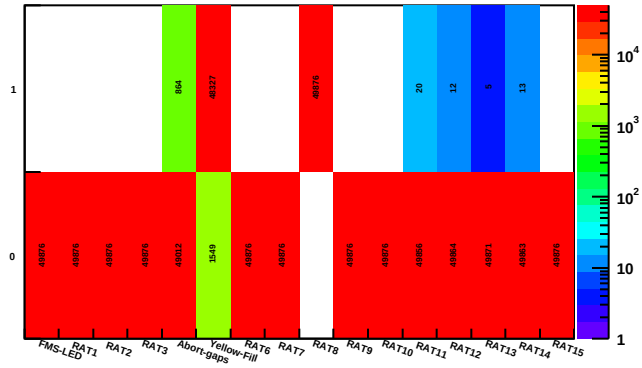
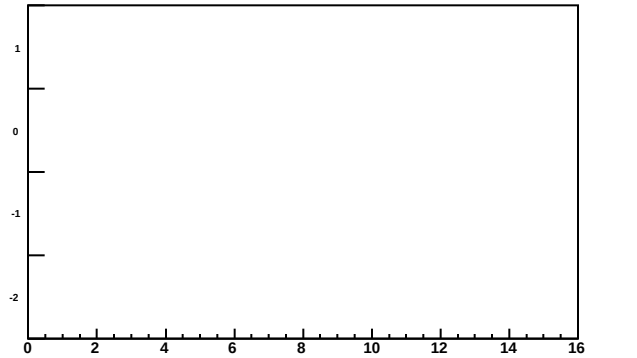
RAT board (ch4)





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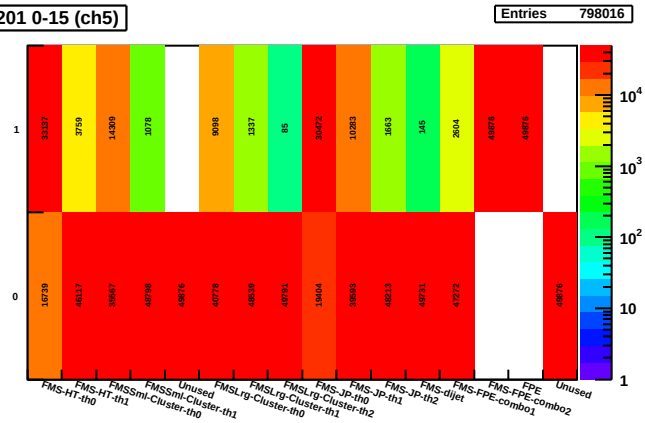

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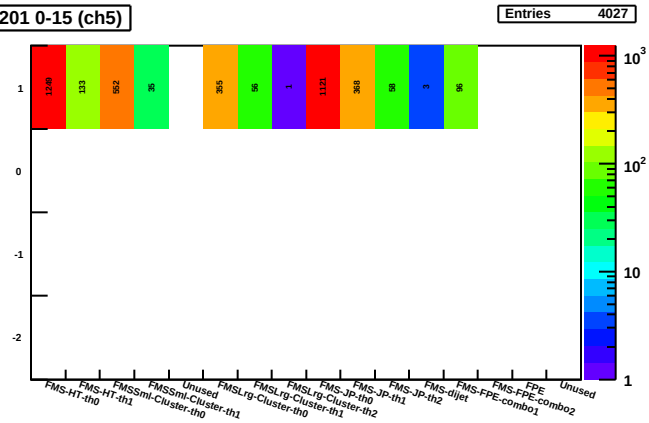
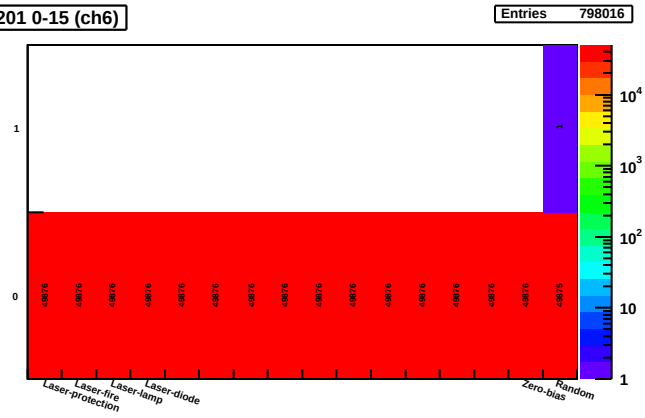
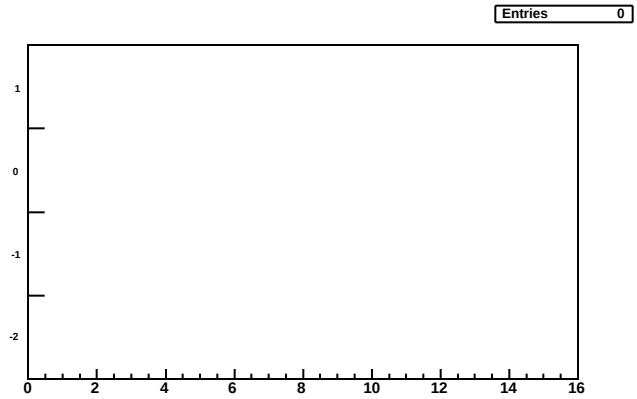
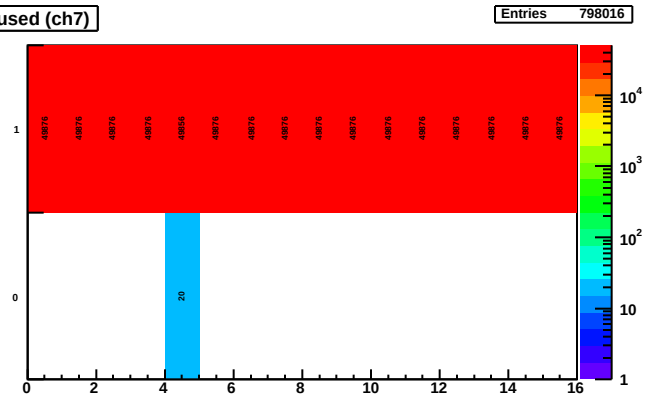
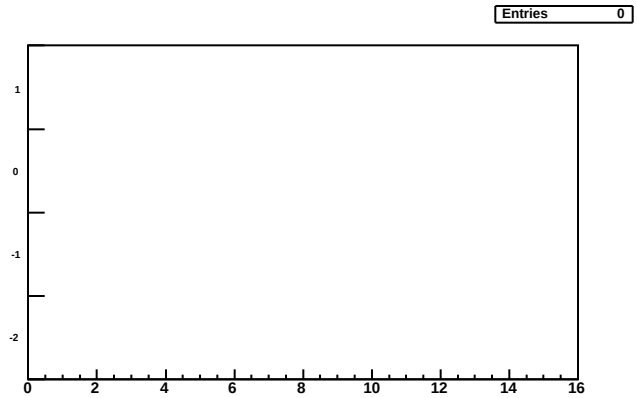


ST201 0-15 (ch6)





Unused (ch7)