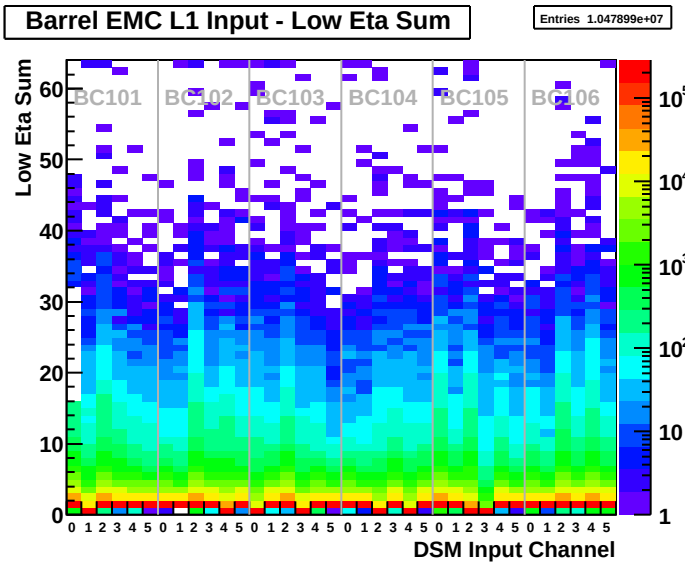
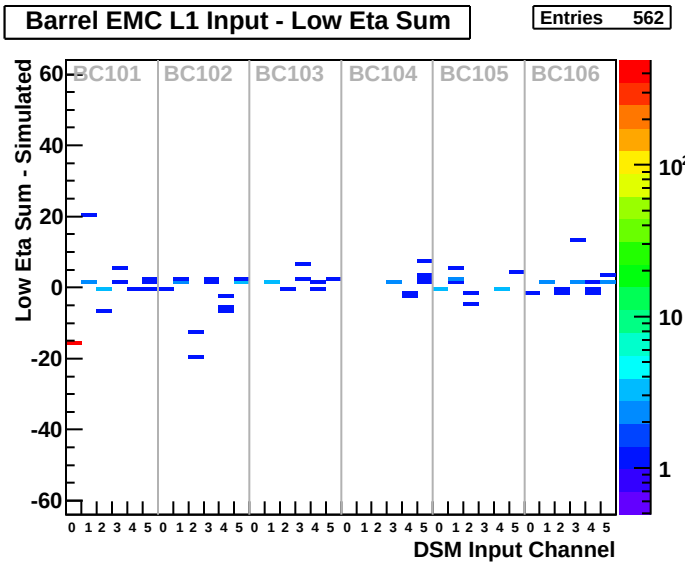
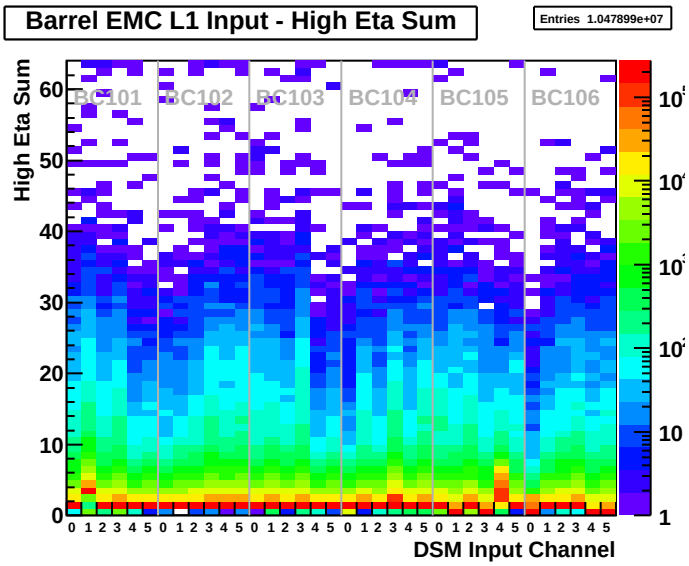
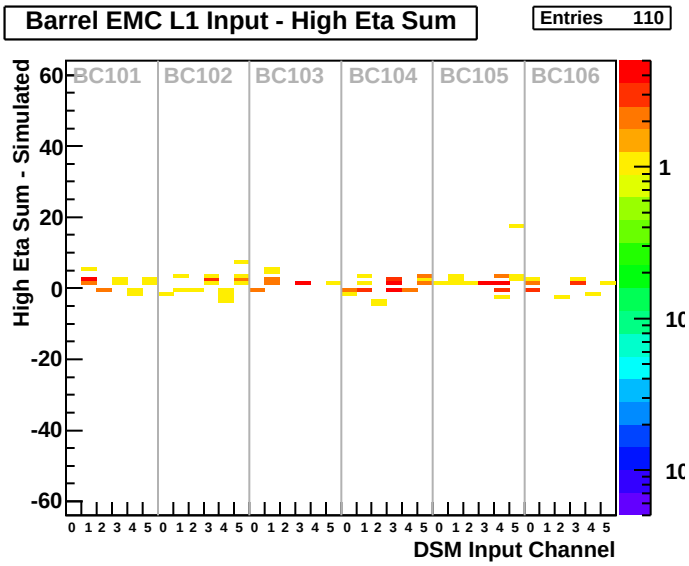
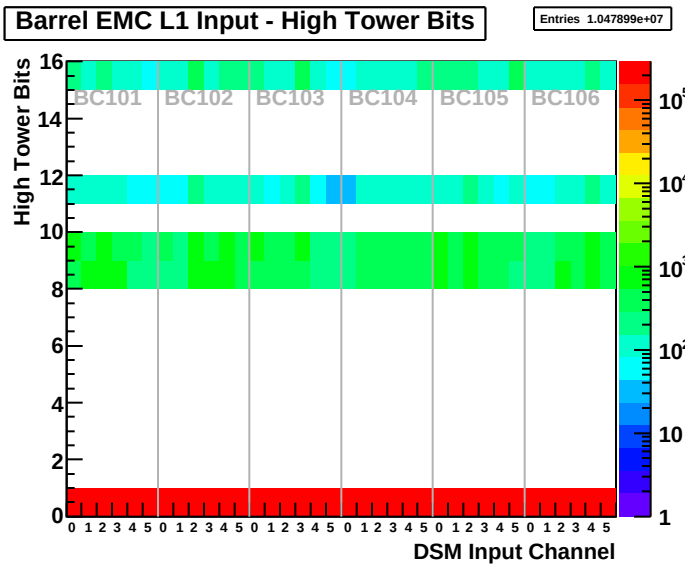
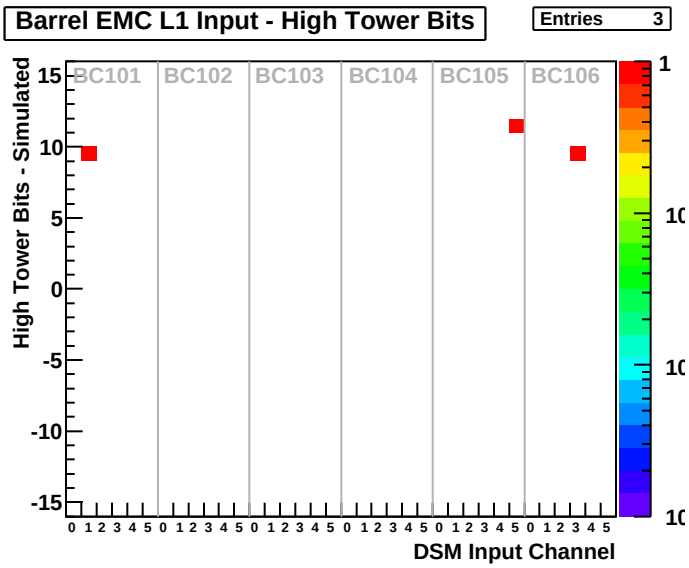

Barrel EMC L1 Input - Low Eta Sum
Entries 1.047899e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

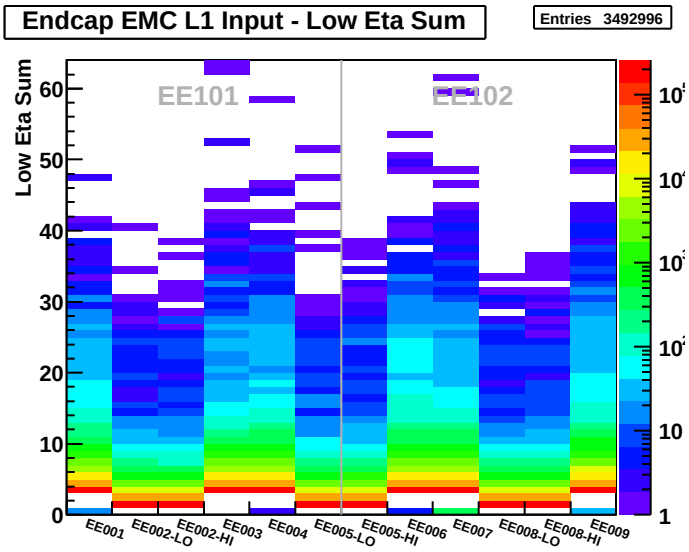

Barrel EMC L1 Input - Low Eta Sum
Entries 562
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

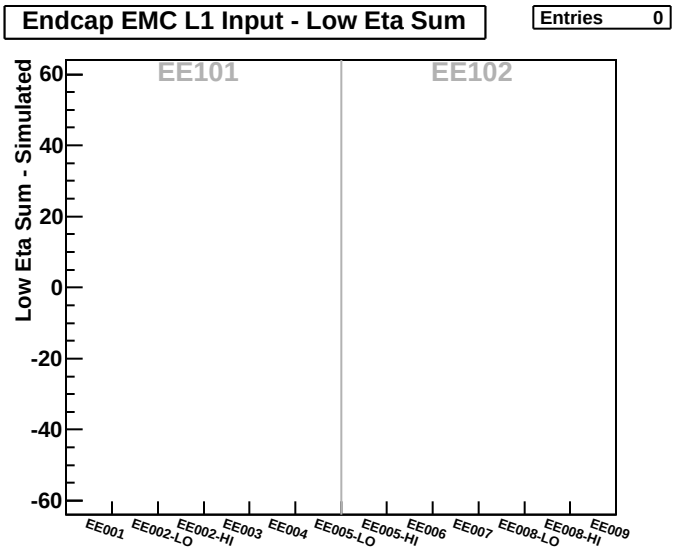

Barrel EMC L1 Input - High Eta Sum
Entries 1.047899e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

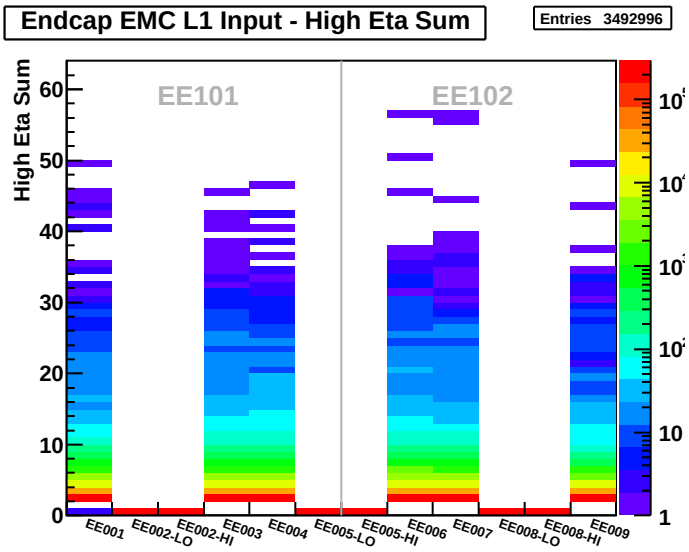

Barrel EMC L1 Input - High Eta Sum
Entries 110
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

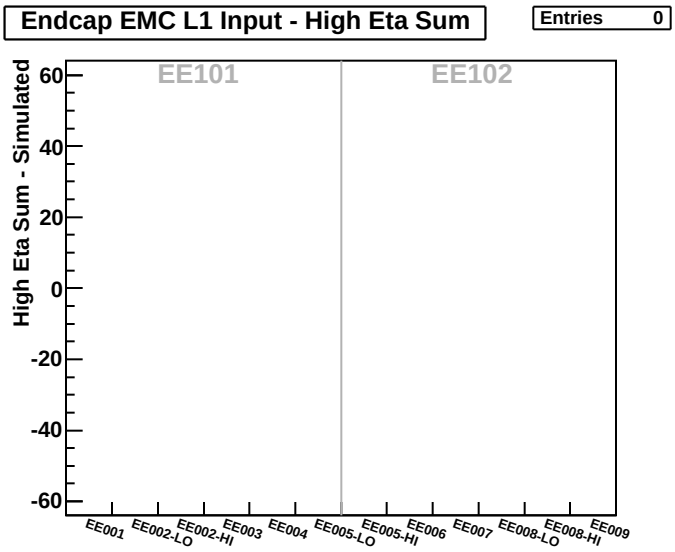

Barrel EMC L1 Input - High Tower Bits
Entries 1.047899e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

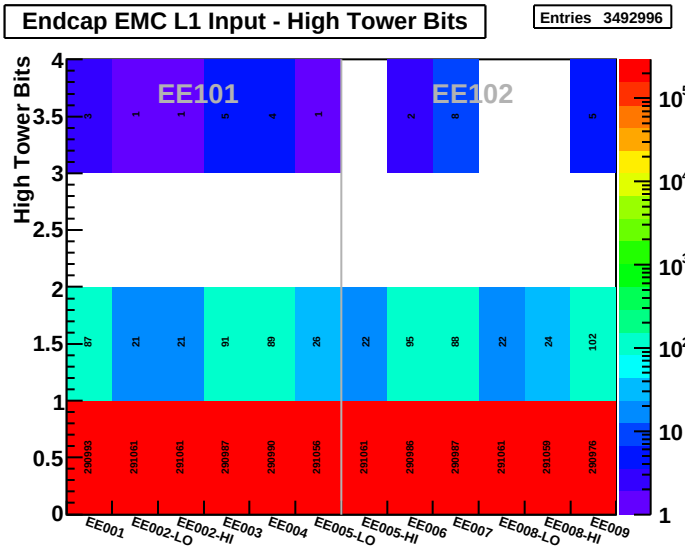

Barrel EMC L1 Input - High Tower Bits
Entries 3
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

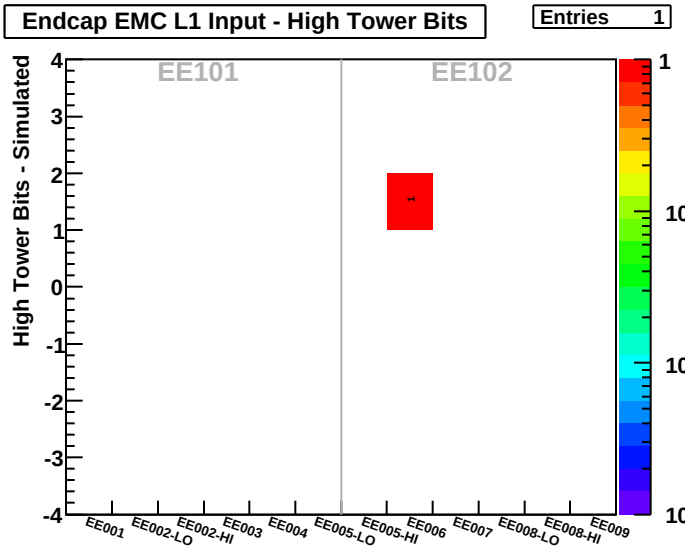

Endcap EMC L1 Input - Low Eta Sum
Entries 3492996
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

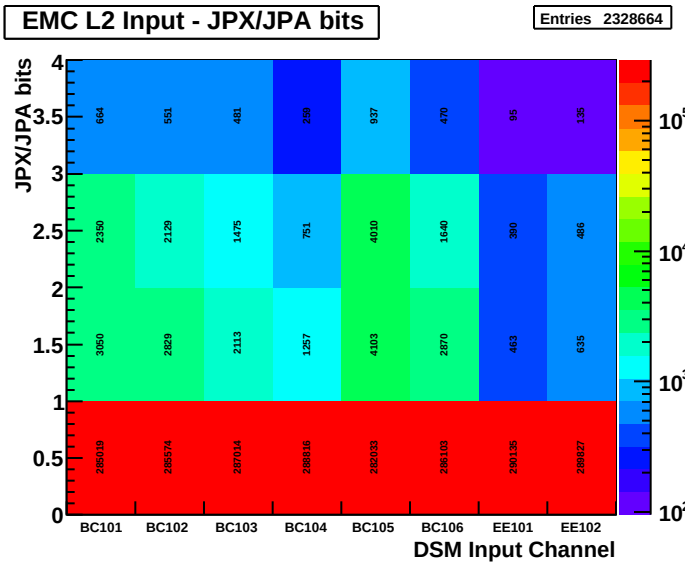

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

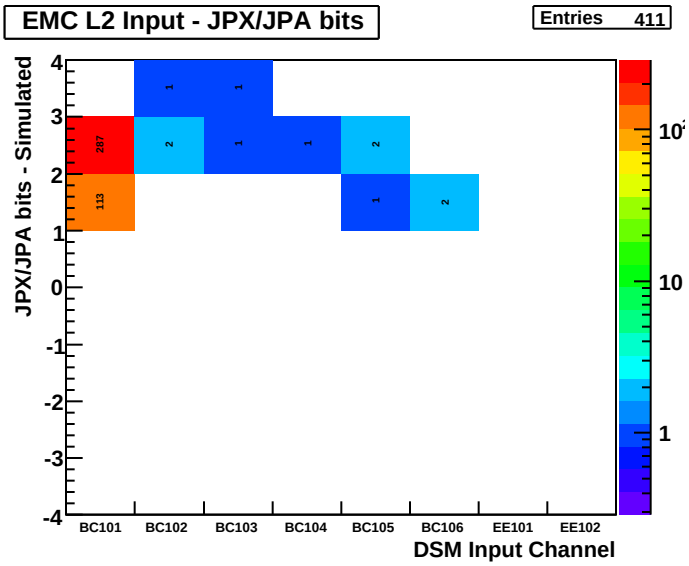

Endcap EMC L1 Input - High Eta Sum
Entries 3492996
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

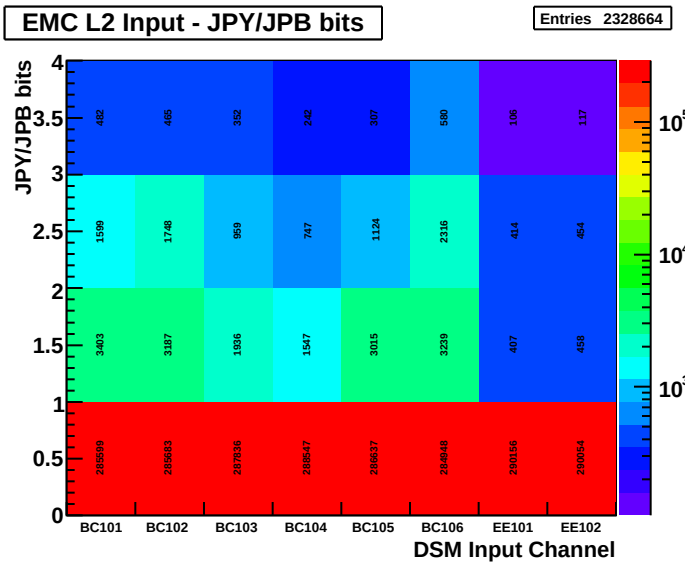

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

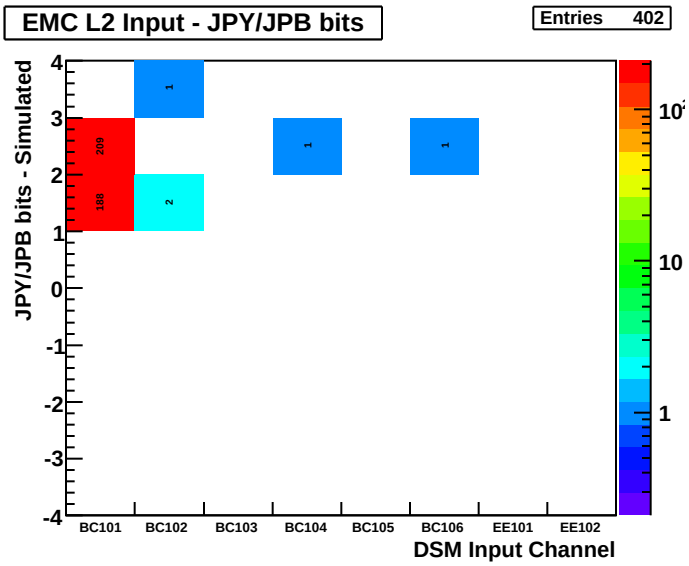

Endcap EMC L1 Input - High Tower Bits
Entries 3492996
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

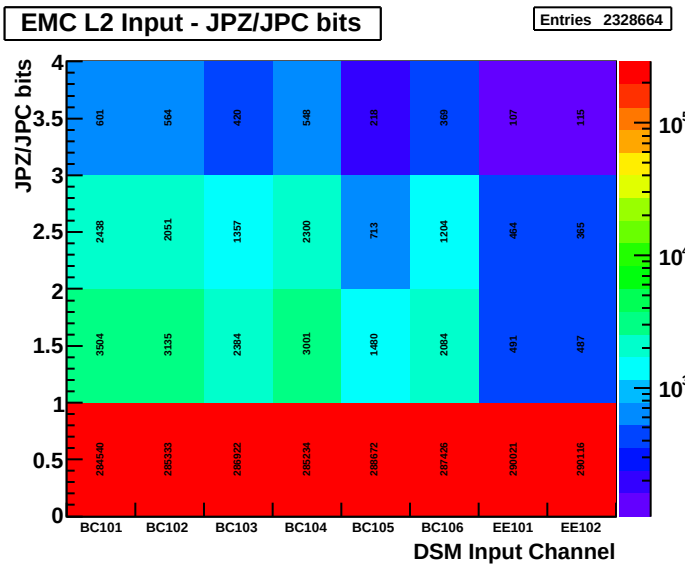

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

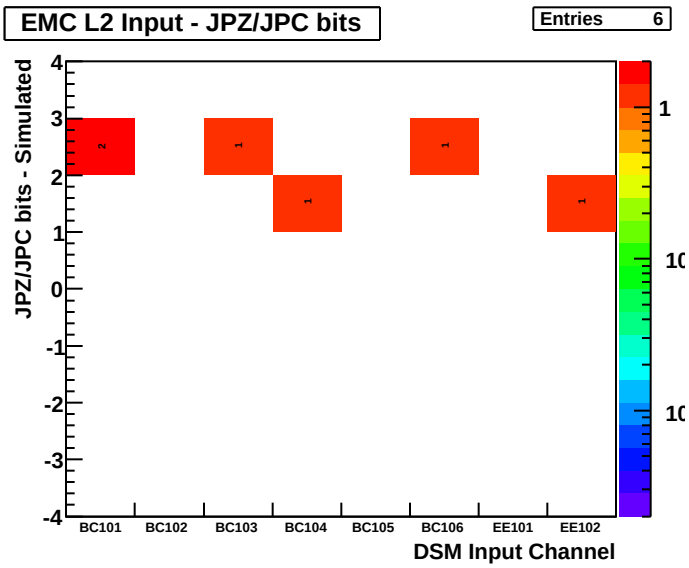

EMC L2 Input - JPX/JPA bits
Entries 2328664
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
664 551 481 259 937 470 95 135
2350 2129 1475 751 4010 1640 380 486
3050 2829 2113 1257 4103 2870 463 635
285019 285574 287014 288816 282033 285103 290135 288827

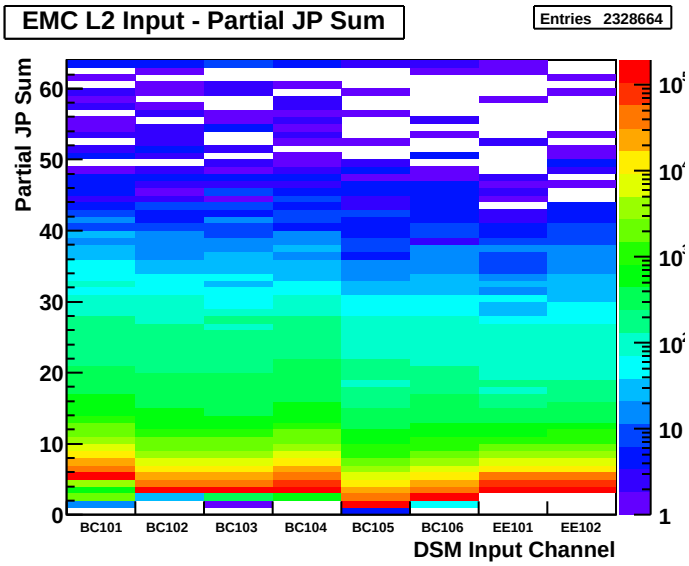

EMC L2 Input - JPX/JPA bits
Entries 411
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
287 113
1 1 2 1 1 2 2

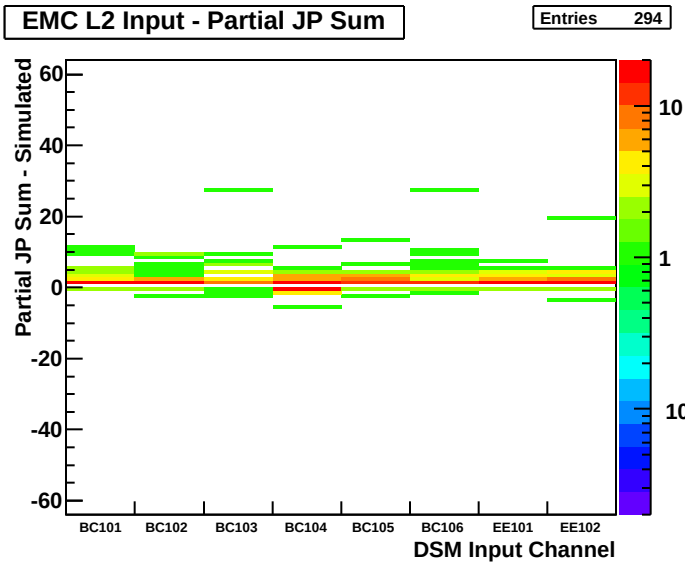

EMC L2 Input - JPY/JPB bits
Entries 2328664
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
482 465 352 242 307 580 106 117
1599 1748 959 747 1124 2316 414 454
3403 3187 1936 1547 3015 3239 407 458
285599 285683 287936 288547 286637 284948 290156 290054

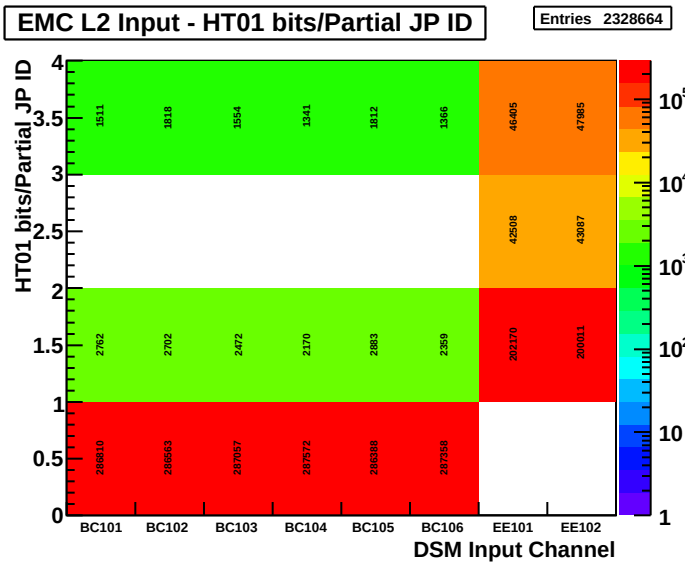

EMC L2 Input - JPY/JPB bits
Entries 402
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
209 188
2
1 1

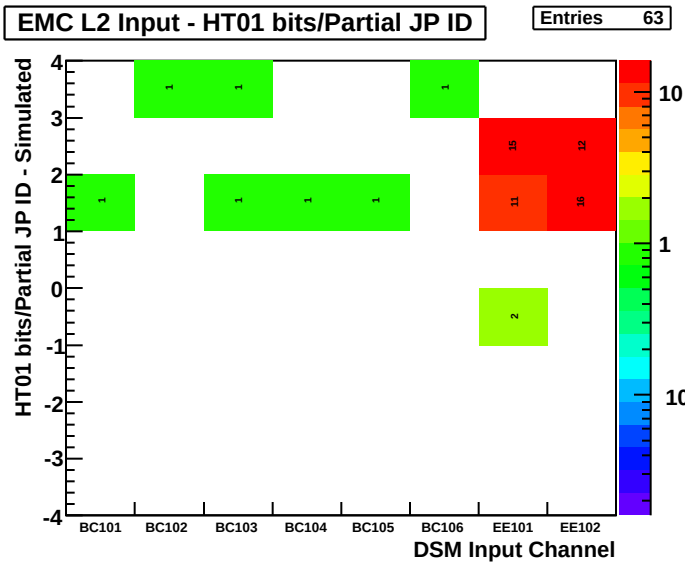

EMC L2 Input - JPZ/JPC bits
Entries 2328664
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
601 564 420 548 218 369 107 115
2438 2051 1357 2300 713 1204 464 365
3504 3135 2384 3001 1480 2084 491 487
284540 285333 286922 285234 288672 287426 290021 290116

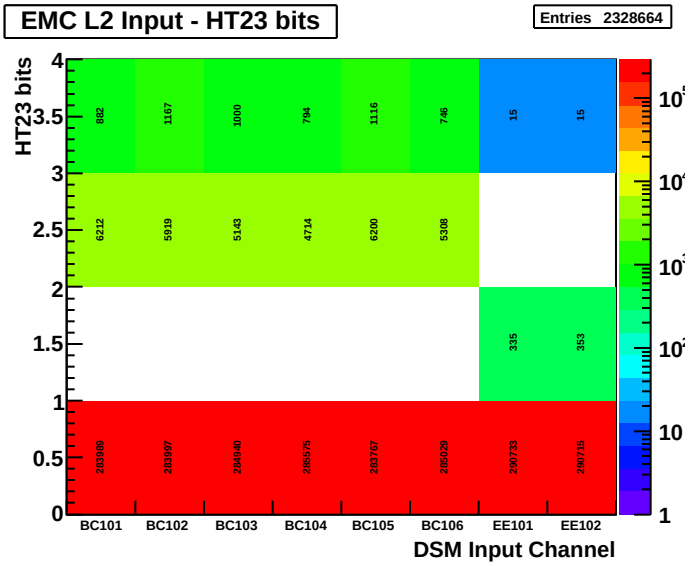

EMC L2 Input - JPZ/JPC bits
Entries 6
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2
1 1 1 1

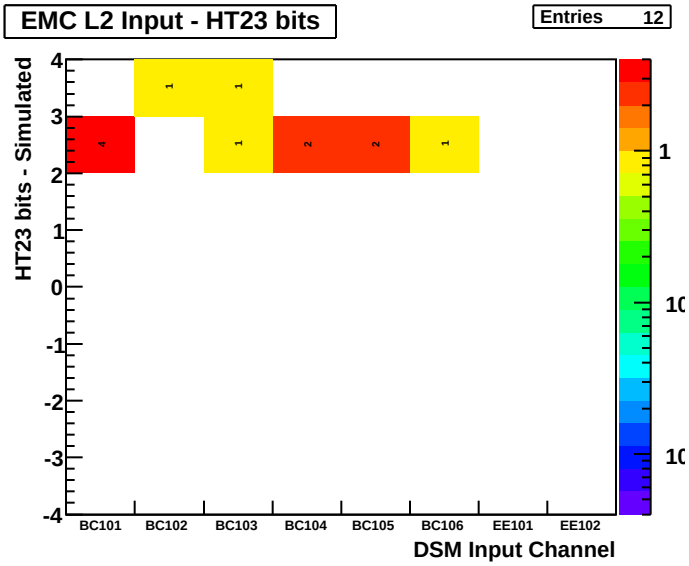

EMC L2 Input - Partial JP Sum
Entries 2328664
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

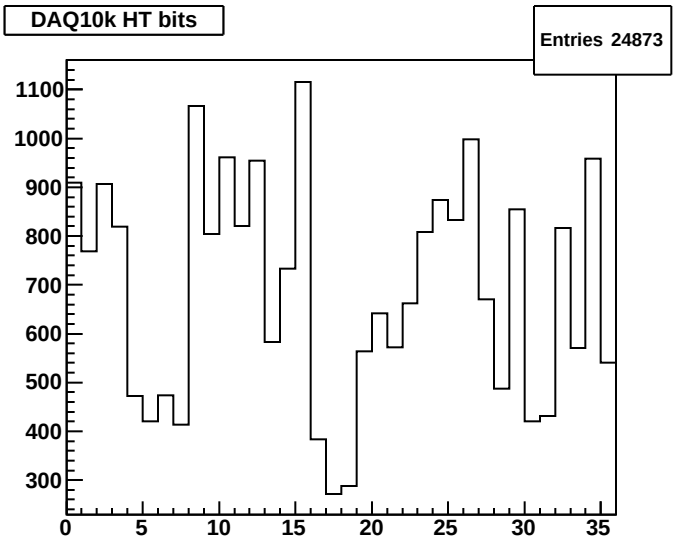

EMC L2 Input - Partial JP Sum
Entries 294
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

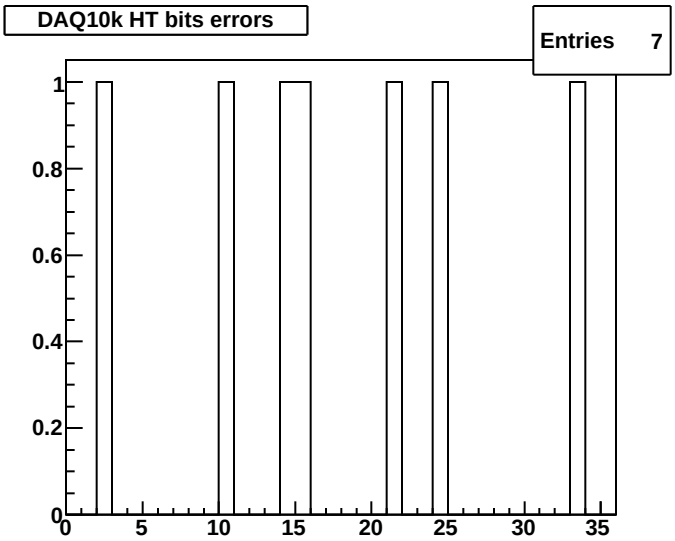

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2328664
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

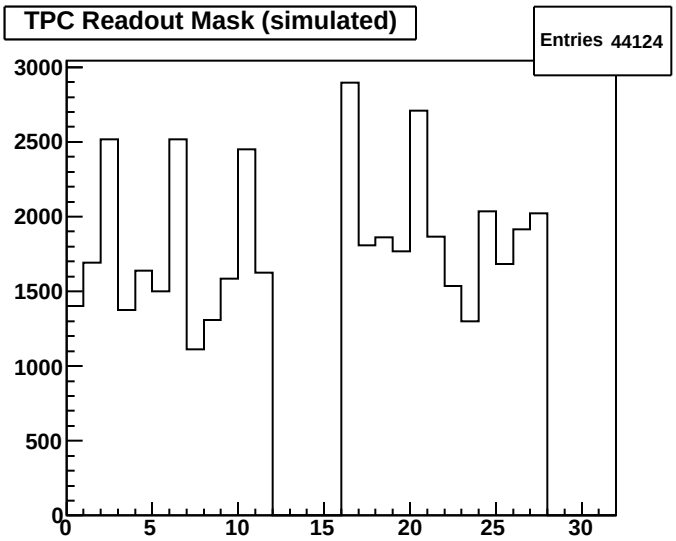

EMC L2 Input - HT01 bits/Partial JP ID
Entries 63
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

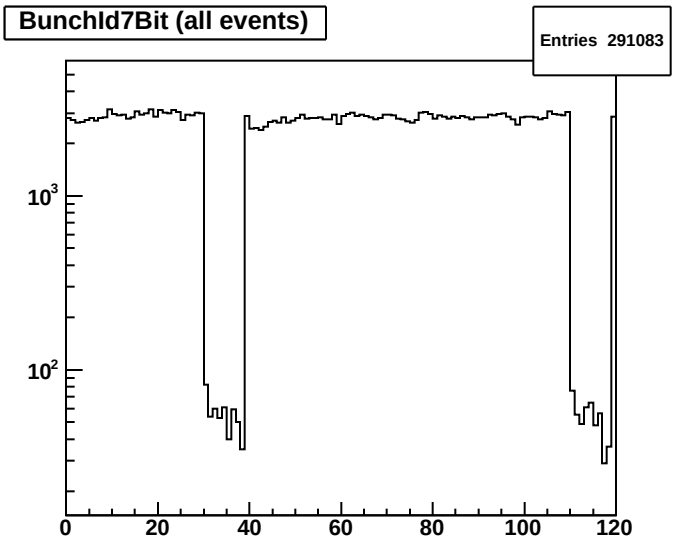

EMC L2 Input - HT23 bits
Entries 2328664
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

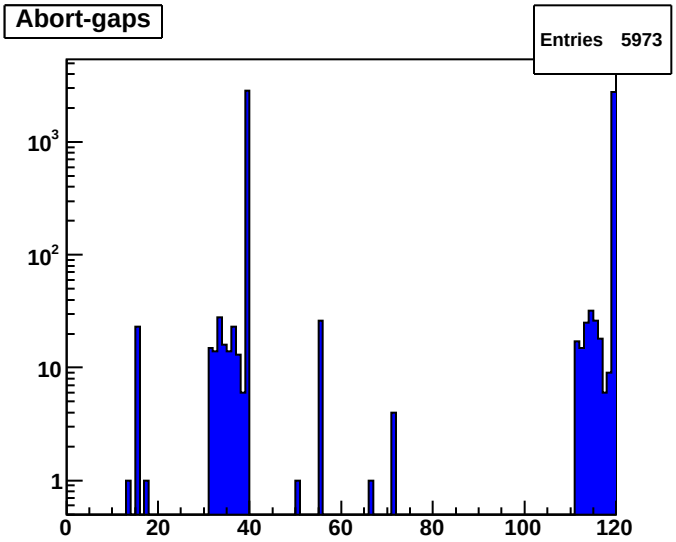

EMC L2 Input - HT23 bits
Entries 12
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

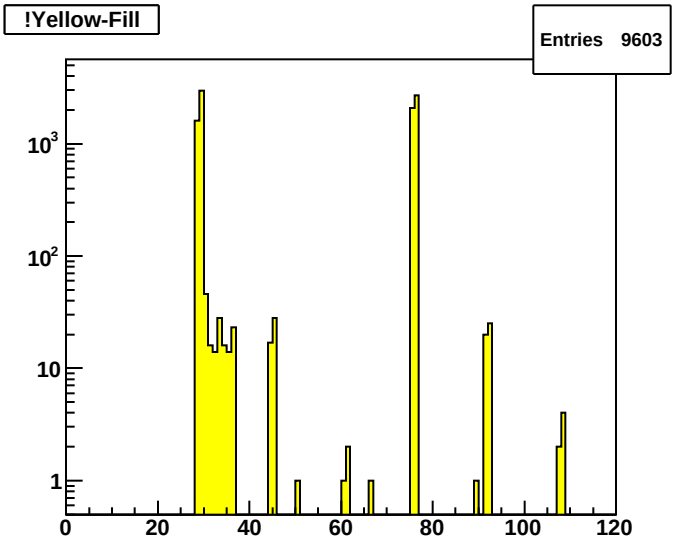

DAQ10k HT bits
Entries 24873

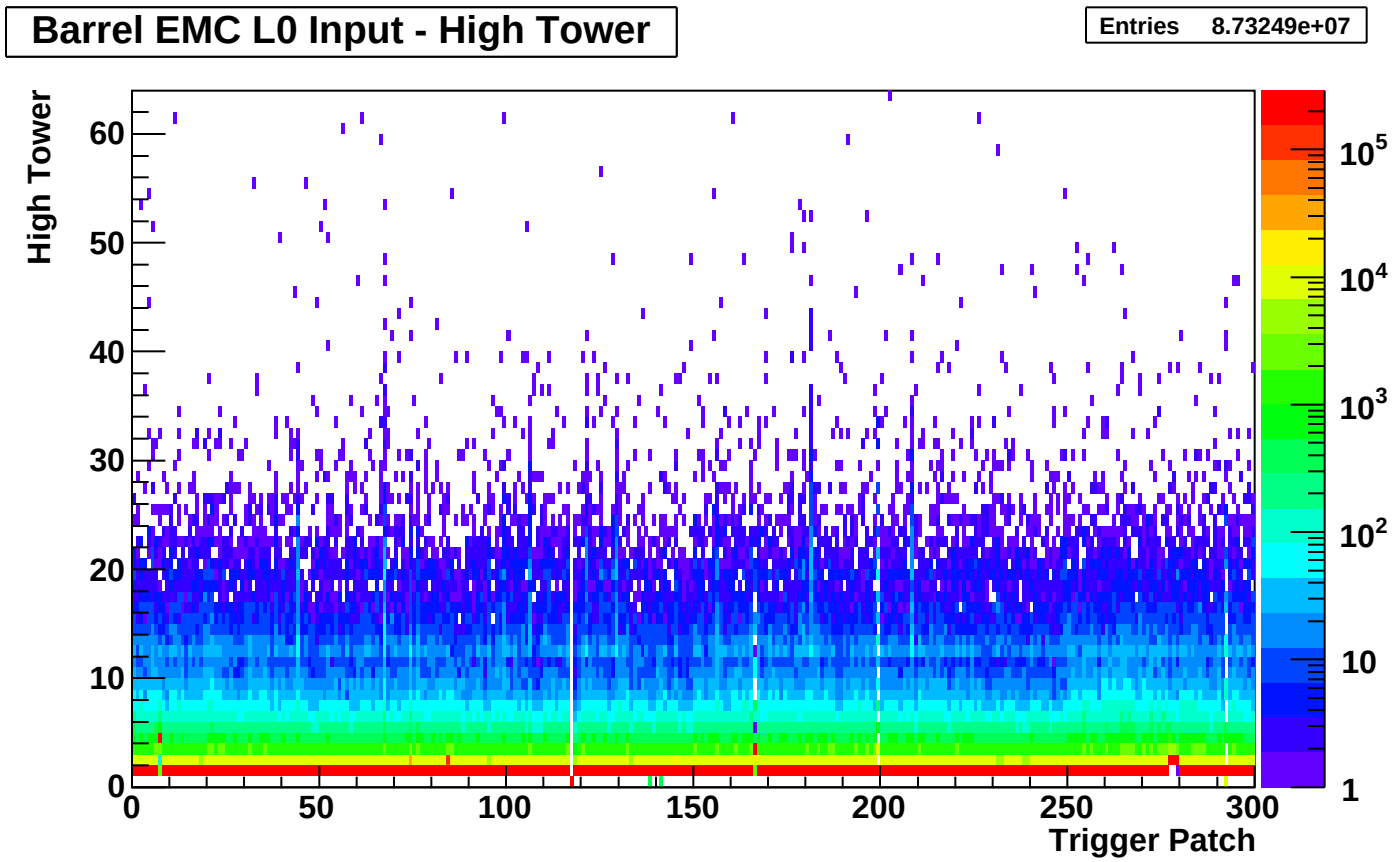

DAQ10k HT bits errors
Entries 7

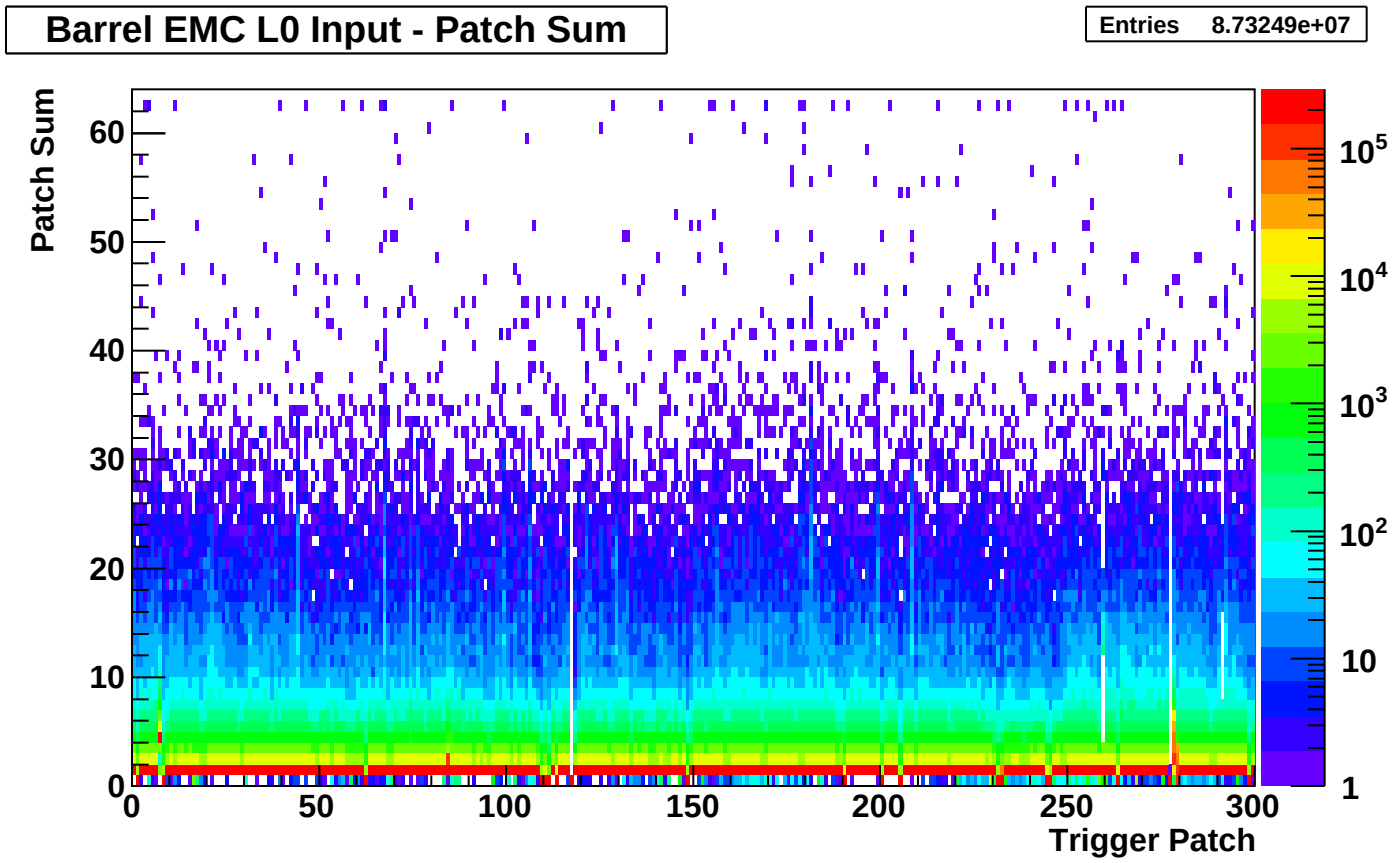

TPC Readout Mask (simulated)
Entries 44124

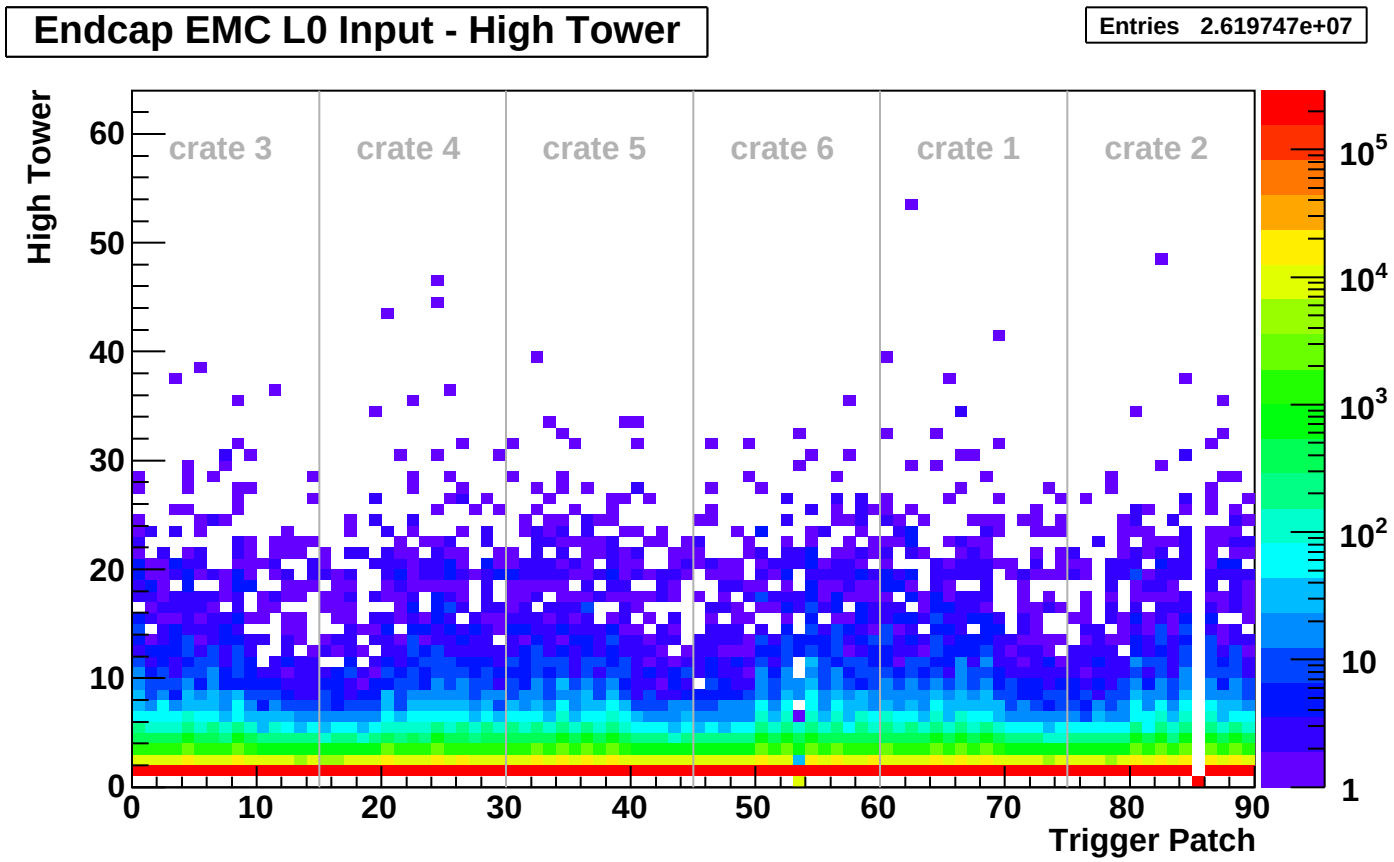

BunchId7Bit (all events)
Entries 291083

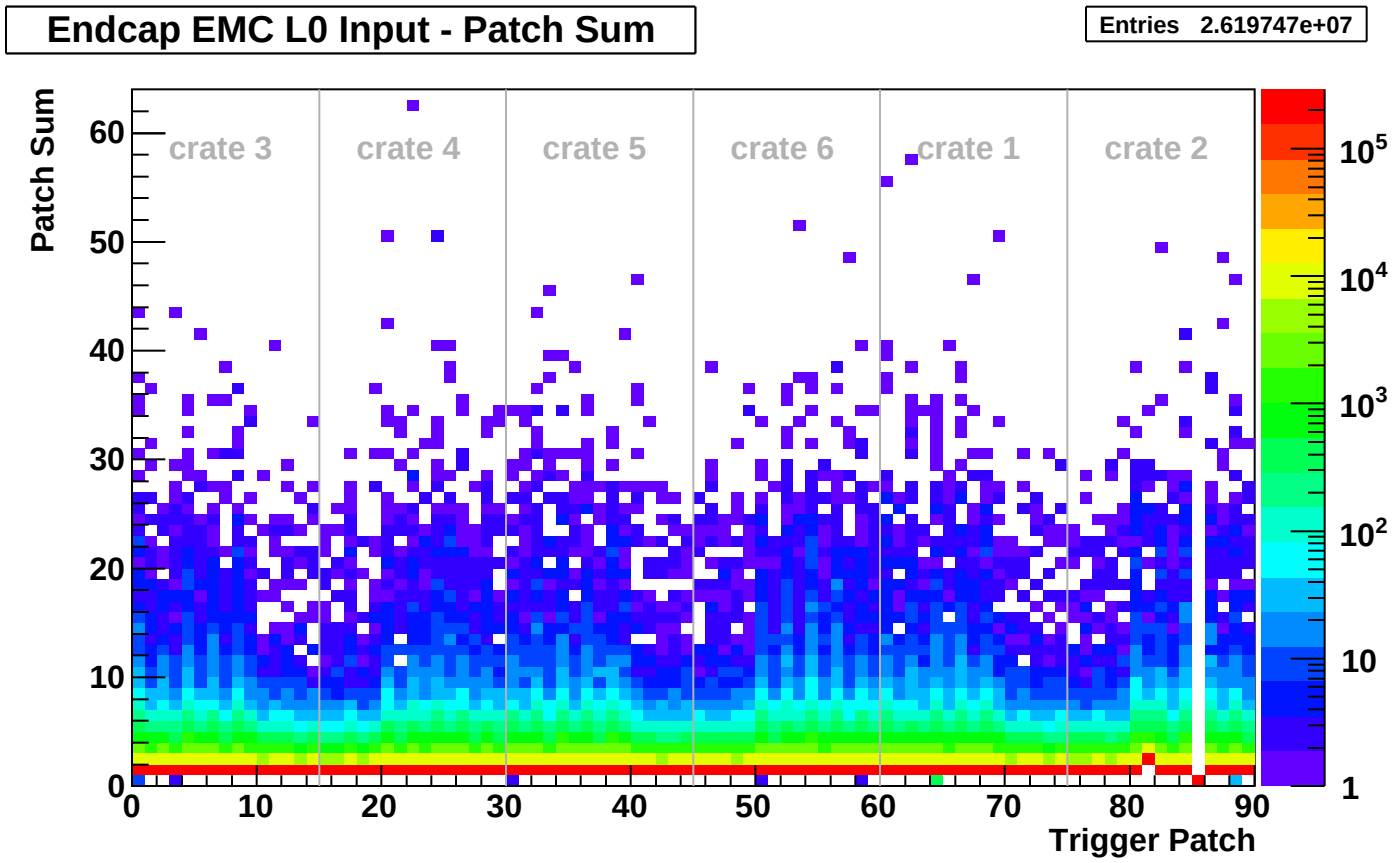

Abort-gaps
Entries 5973

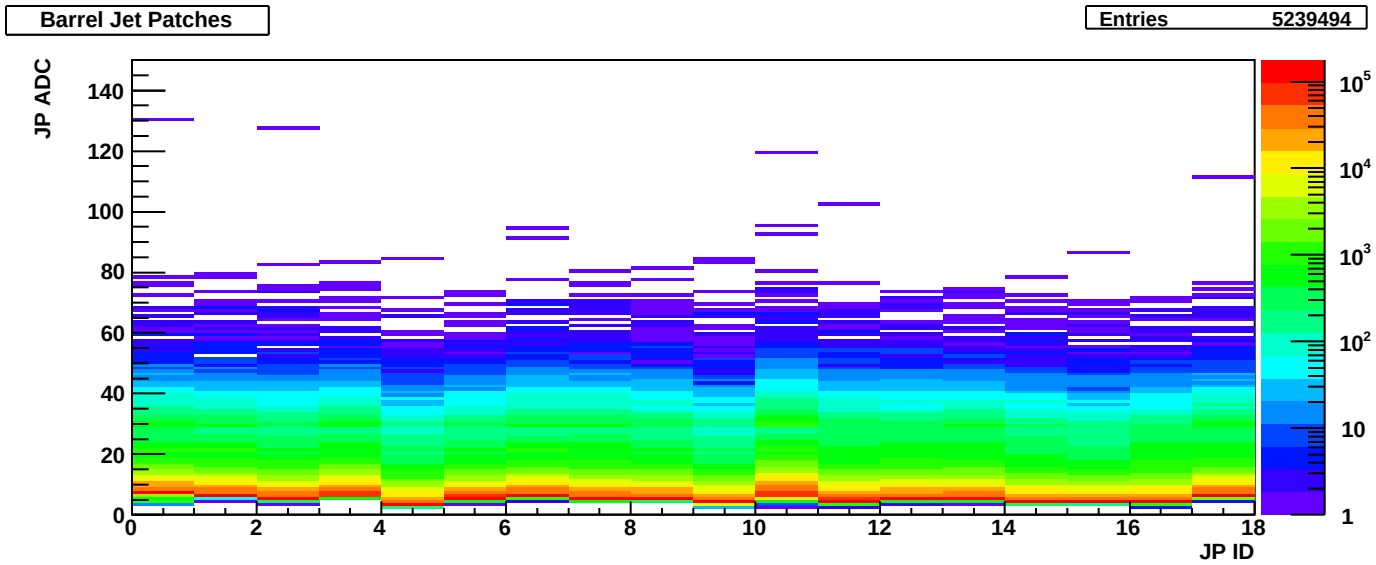

!Yellow-Fill
Entries 9603

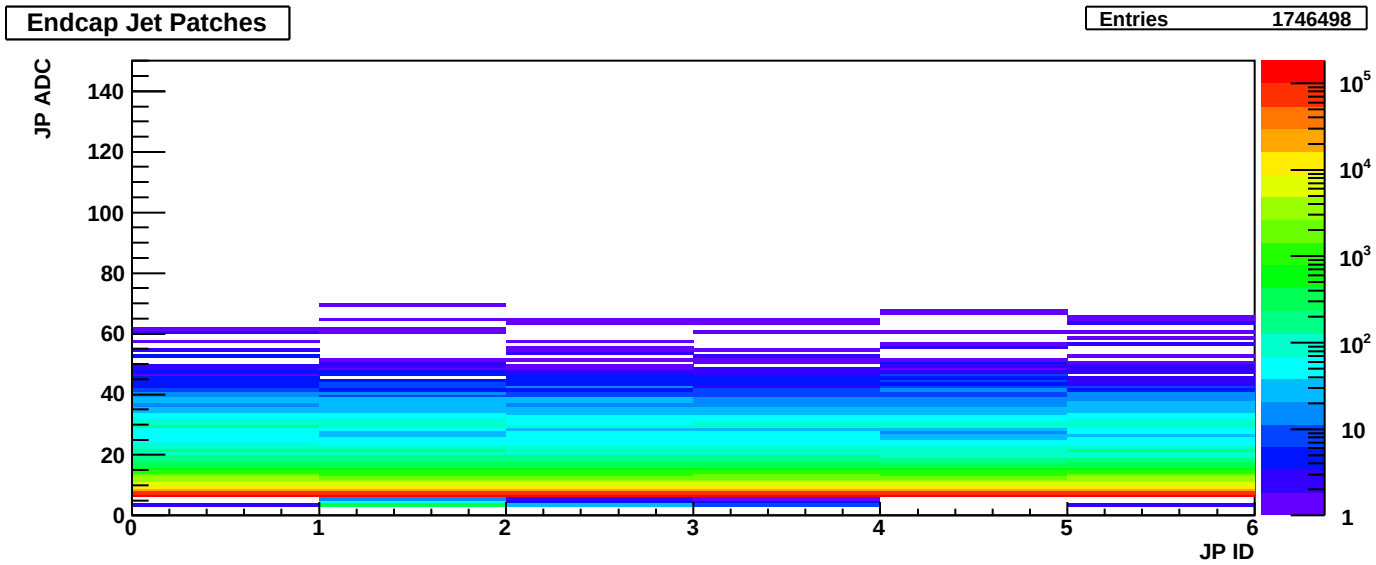

Barrel EMC L0 Input - High Tower
Entries 8.73249e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

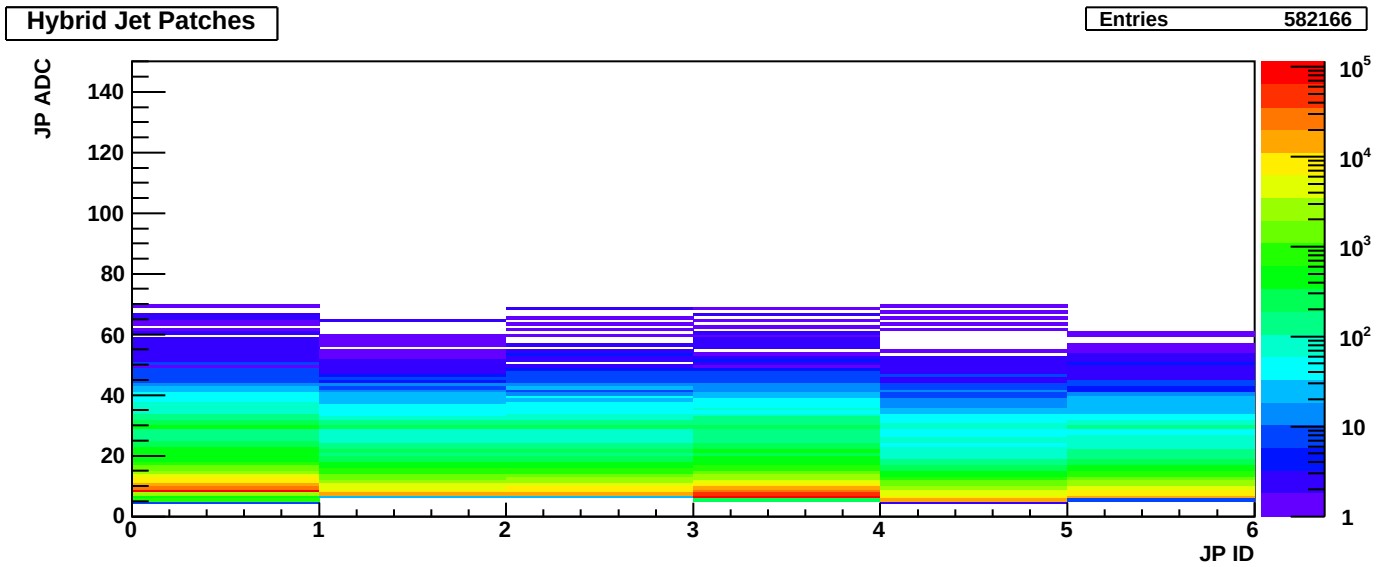

Barrel EMC L0 Input - Patch Sum
Entries 8.73249e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

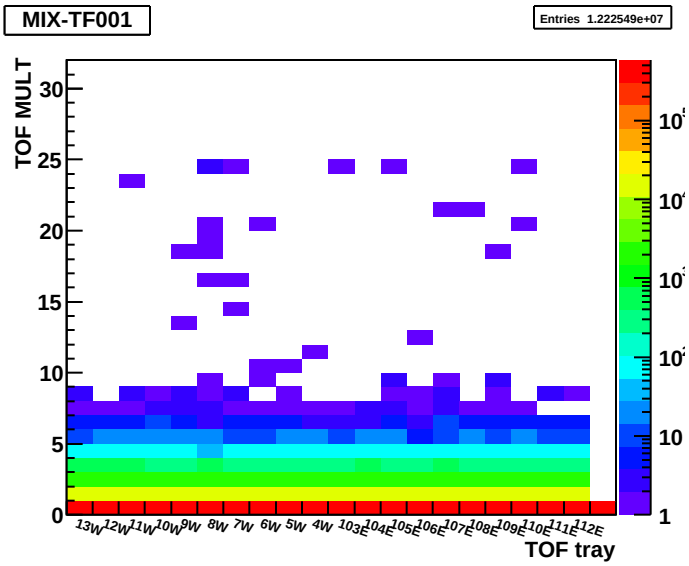

Endcap EMC L0 Input - High Tower
Entries 2.619747e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

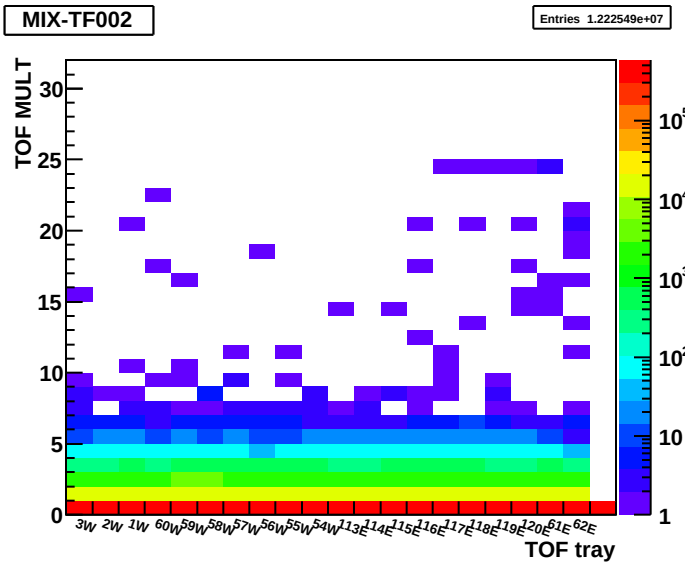

Endcap EMC L0 Input - Patch Sum
Entries 2.619747e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

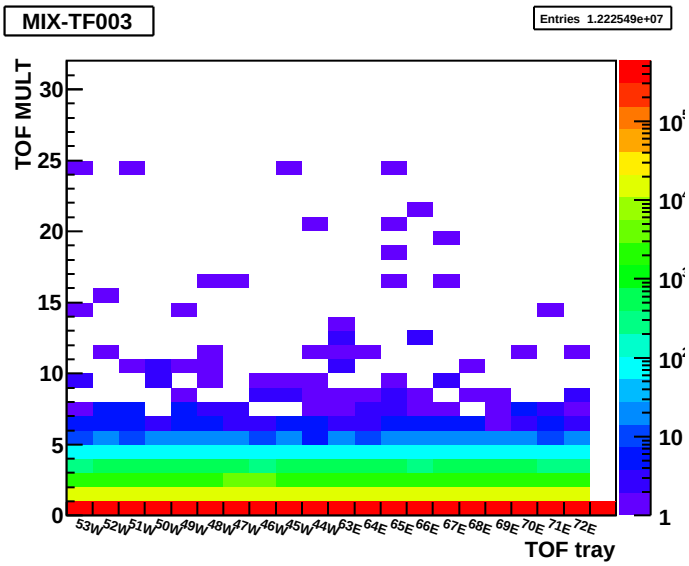

Barrel Jet Patches
Entries 5239494
JP ADC
JP ID

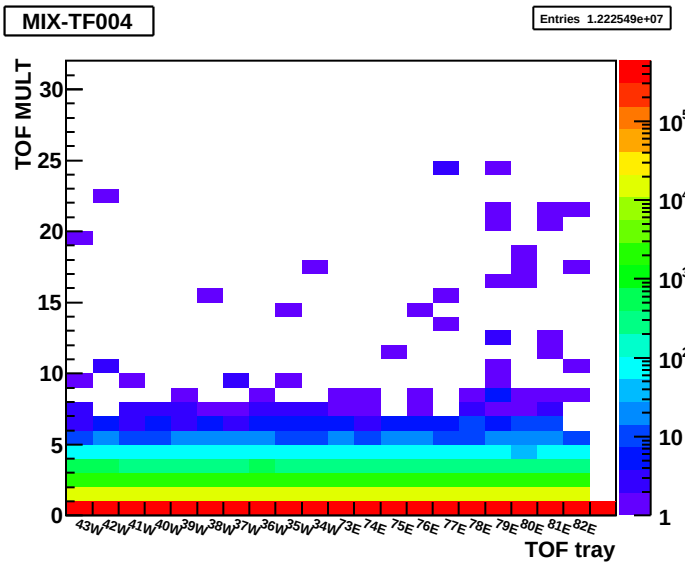

Endcap Jet Patches
Entries 1746498
JP ADC
JP ID

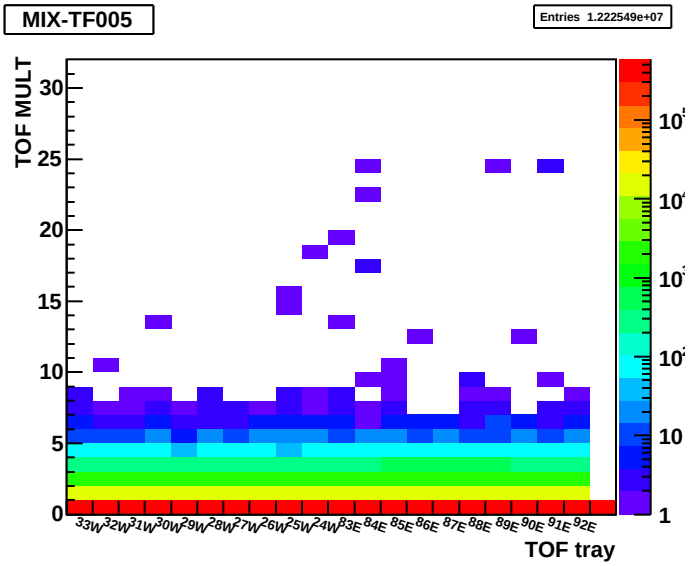

Hybrid Jet Patches
Entries 582166
JP ADC
JP ID

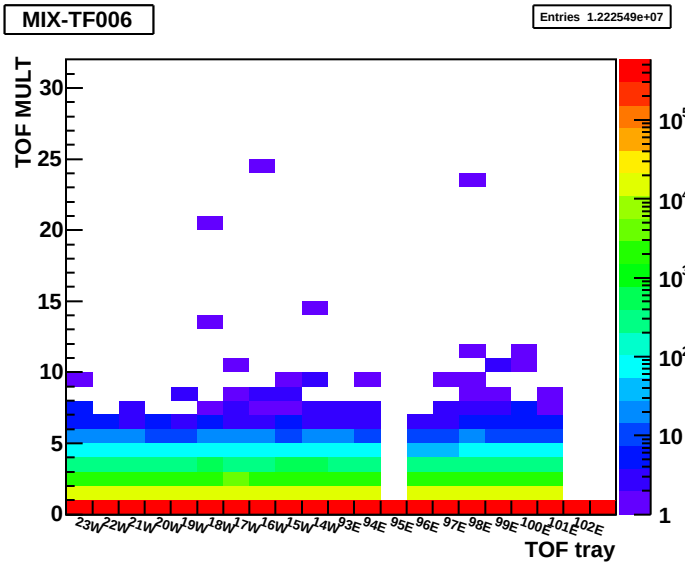

MIX-TF001
Entries 1.222549e+07
TOF MULT
TOF tray

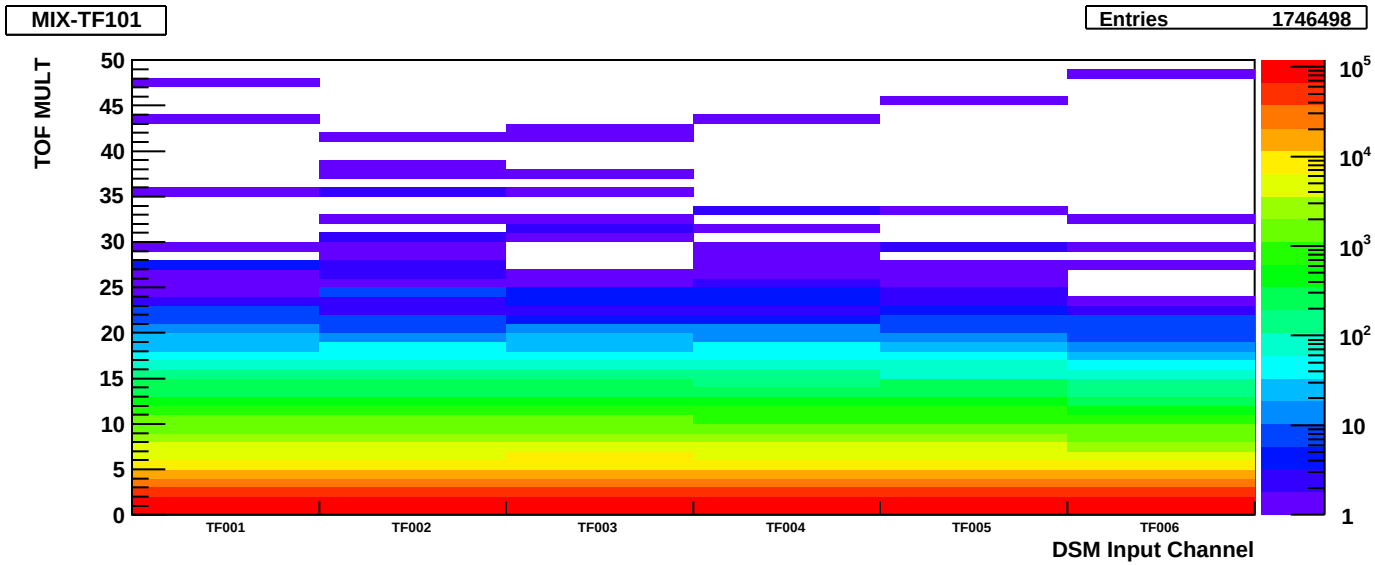

MIX-TF002
Entries 1.222549e+07
TOF MULT
TOF tray

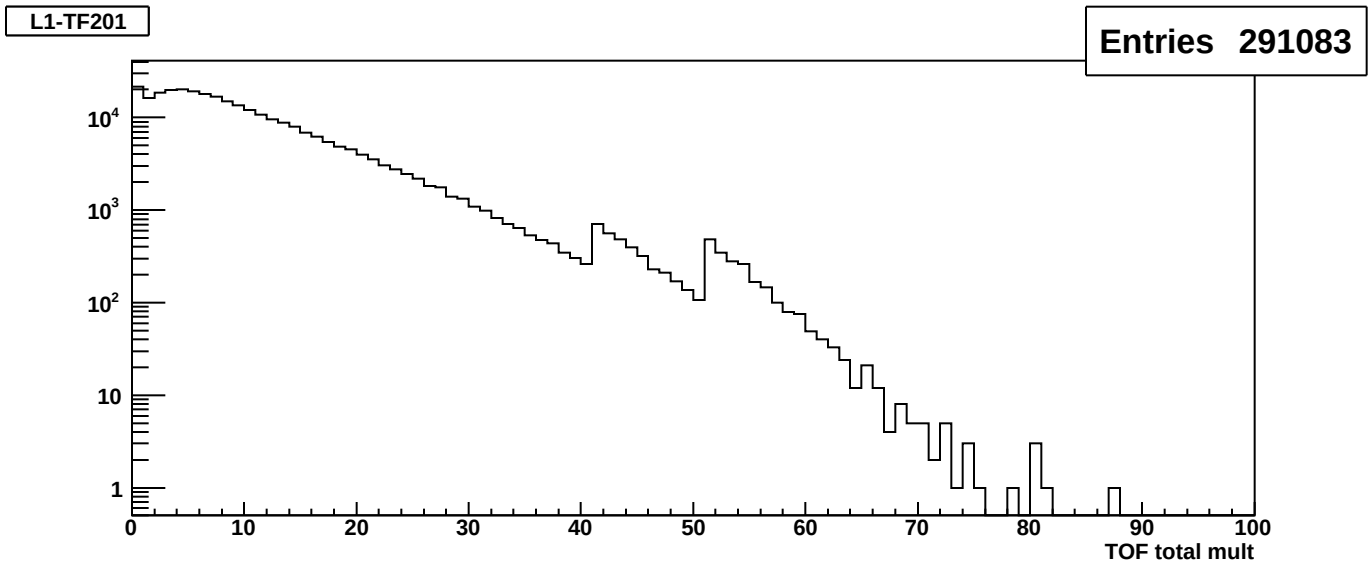

MIX-TF003
Entries 1.222549e+07
TOF MULT
TOF tray

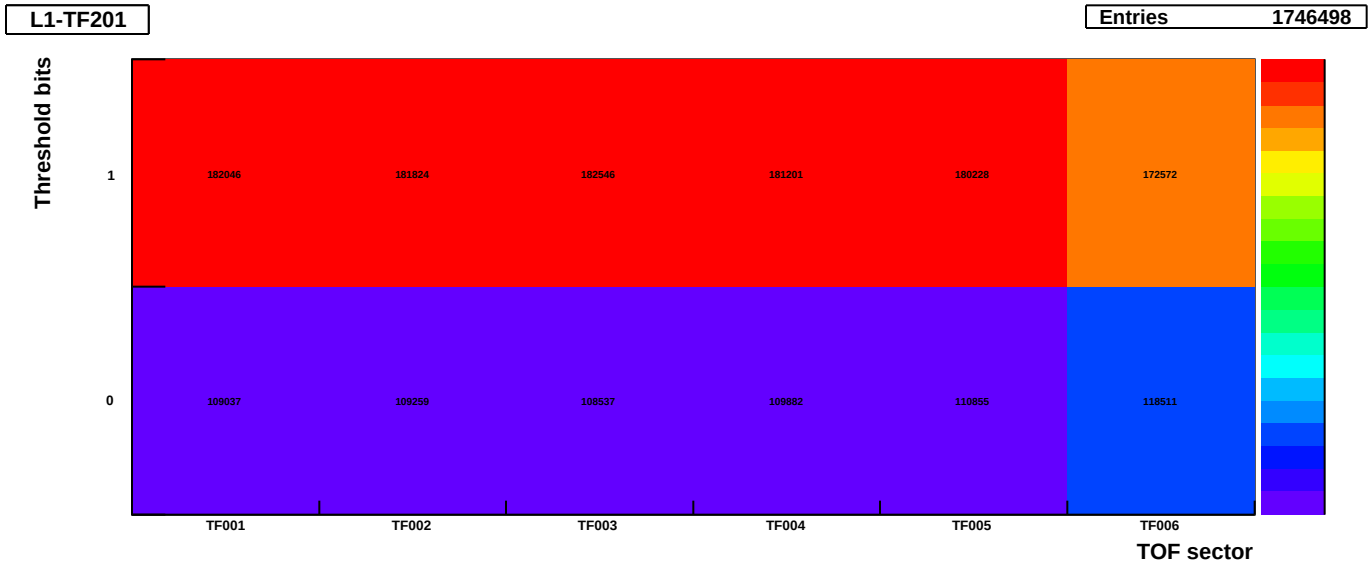

MIX-TF004
Entries 1.222549e+07
TOF MULT
TOF tray

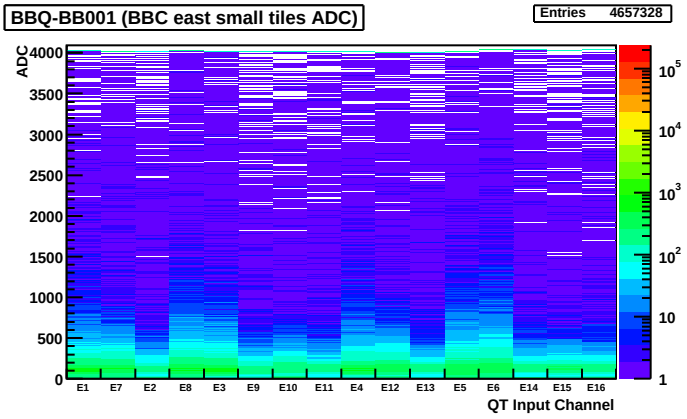

MIX-TF005
Entries 1.222549e+07
TOF MULT
TOF tray

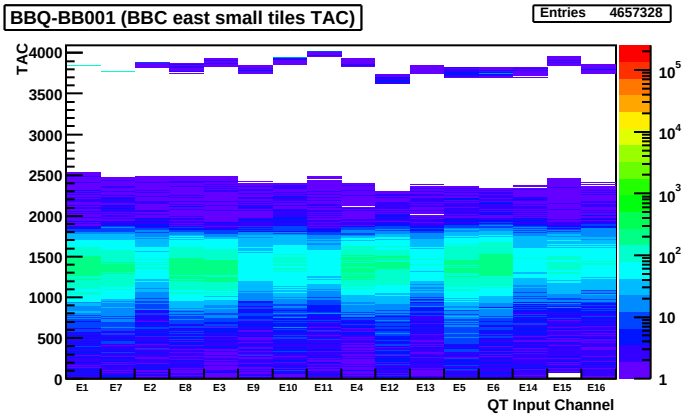

MIX-TF006
Entries 1.222549e+07
TOF MULT
TOF tray

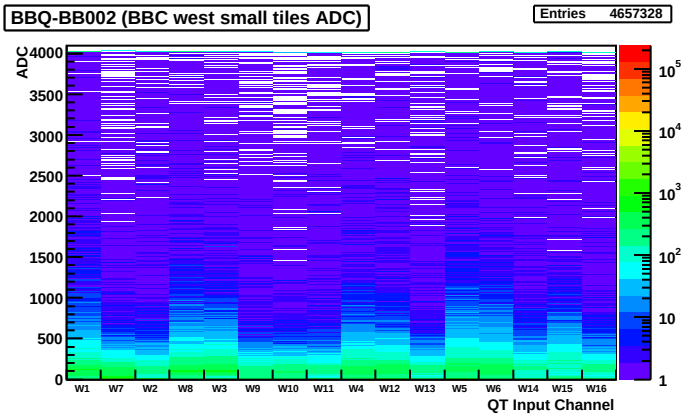

MIX-TF101
Entries 1746498
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

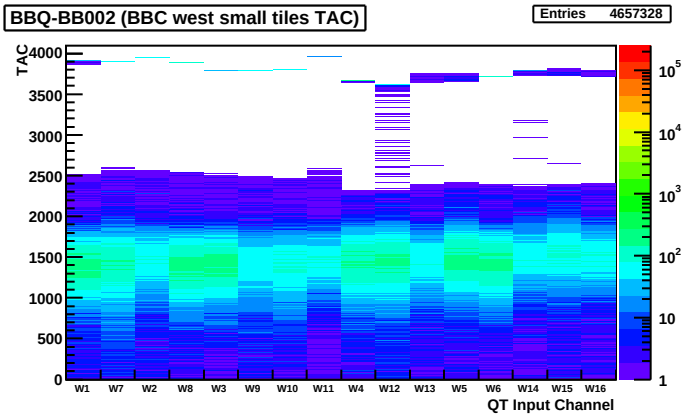

L1-TF201
Entries 291083
TOF total mult

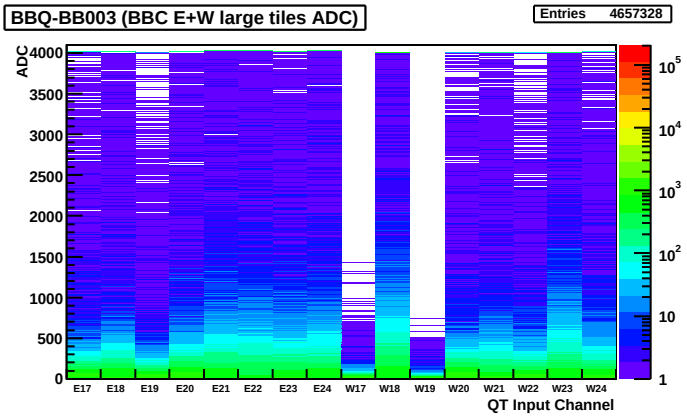

L1-TF201
Entries 1746498
Threshold bits
182046
181824
182546
181201
180228
172572
109037
109259
108537
109882
110855
118511
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

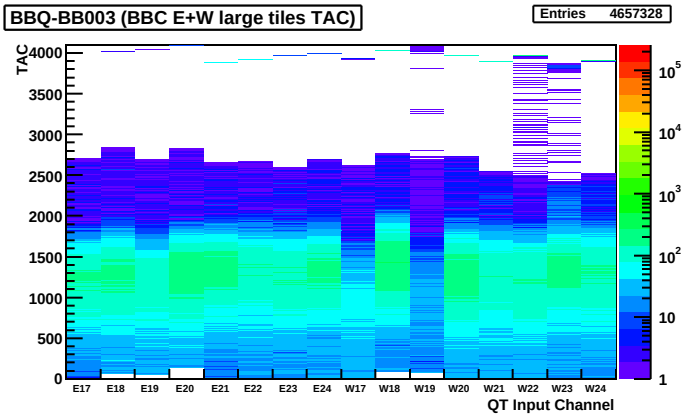

BBQ-BB001 (BBC east small tiles ADC)
Entries 4657328
ADC
QT Input Channel

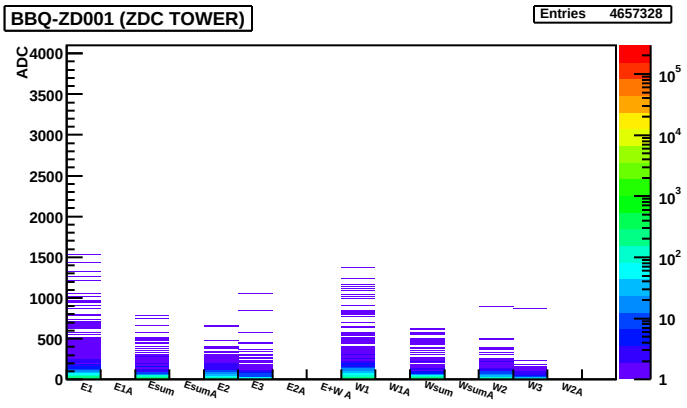

BBQ-BB001 (BBC east small tiles TAC)
Entries 4657328
TAC
QT Input Channel

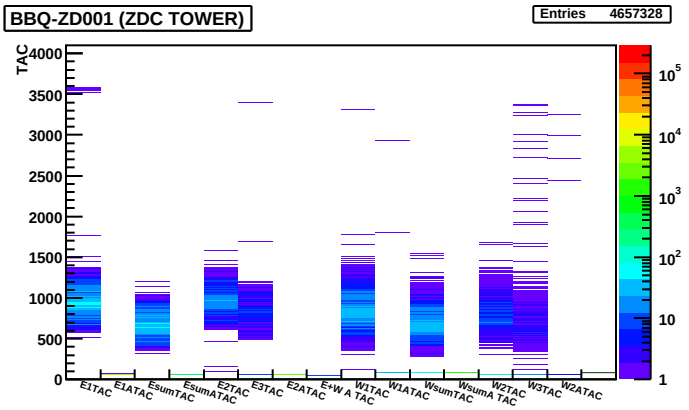

BBQ-BB002 (BBC west small tiles ADC)
Entries 4657328
ADC
QT Input Channel

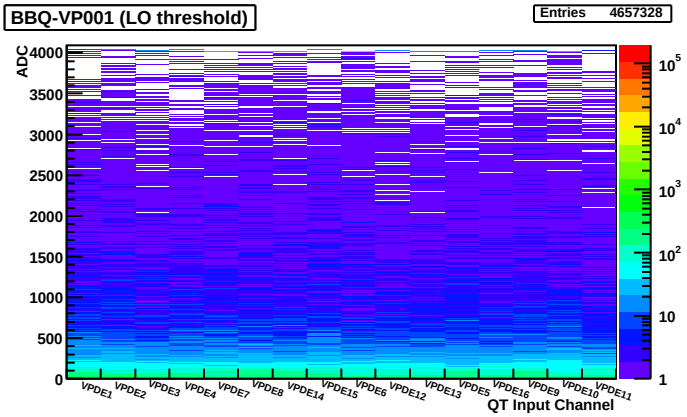

BBQ-BB002 (BBC west small tiles TAC)
Entries 4657328
TAC
QT Input Channel

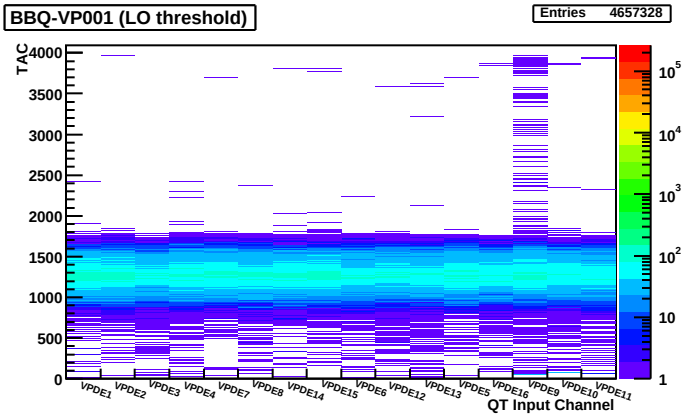

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4657328
ADC
QT Input Channel

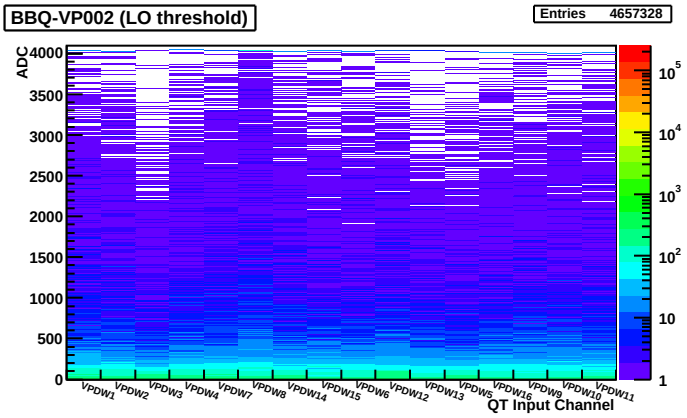

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4657328
TAC
QT Input Channel

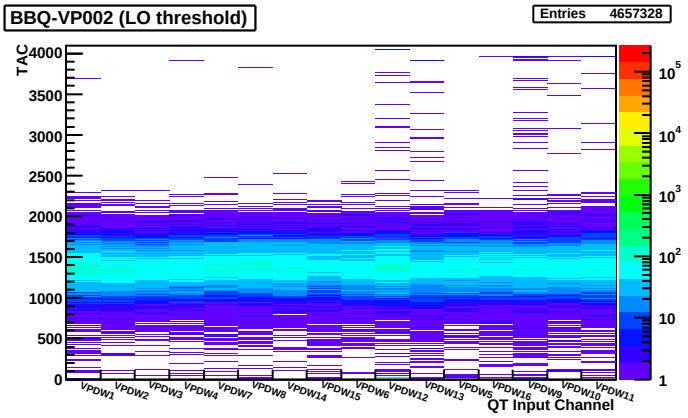

BBQ-ZD001 (ZDC TOWER)
Entries 4657328
ADC

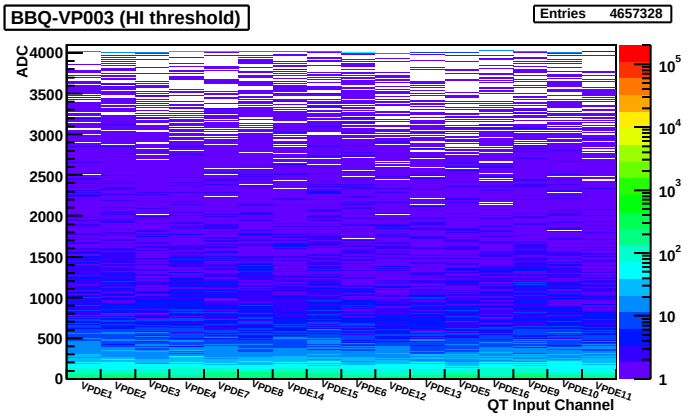

BBQ-ZD001 (ZDC TOWER)
Entries 4657328
TAC

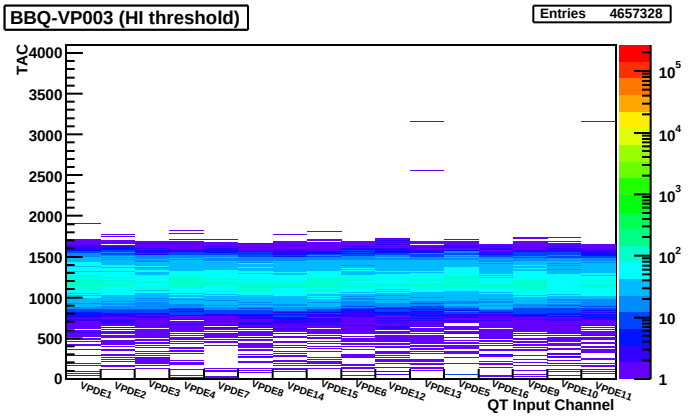

BBQ-VP001 (LO threshold)
Entries 4657328
ADC
QT Input Channel

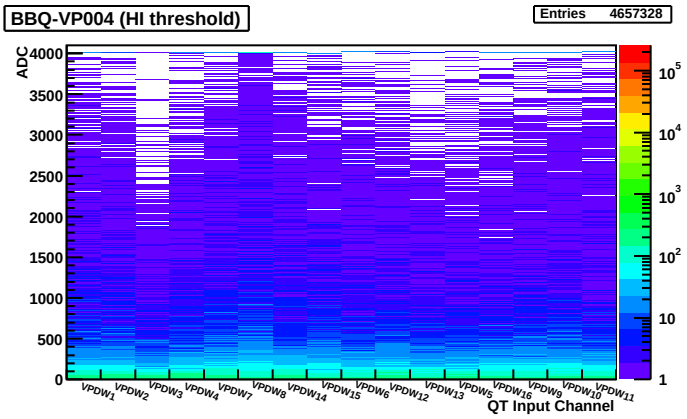

BBQ-VP001 (LO threshold)
Entries 4657328
TAC
QT Input Channel

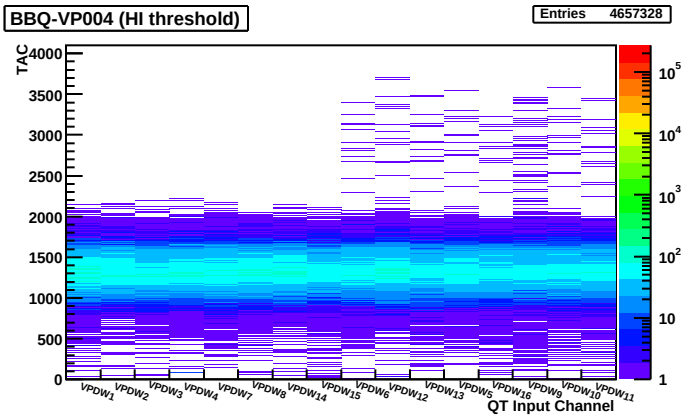

BBQ-VP002 (LO threshold)
Entries 4657328
ADC
QT Input Channel

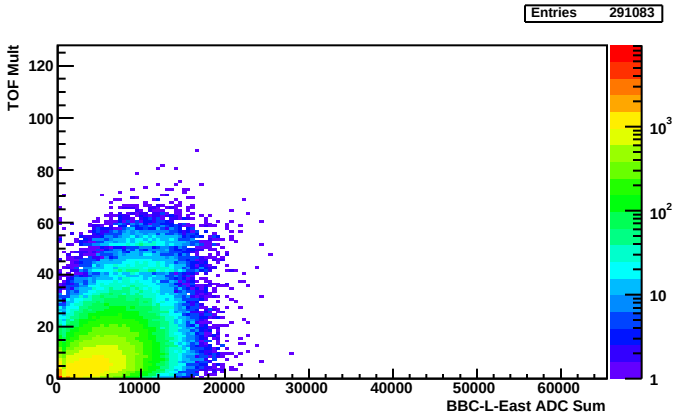

BBQ-VP002 (LO threshold)
Entries 4657328
TAC
QT Input Channel

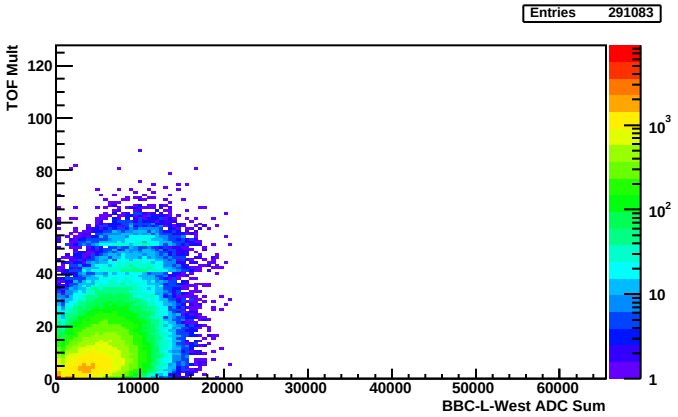

BBQ-VP003 (HI threshold)
Entries 4657328
ADC
QT Input Channel

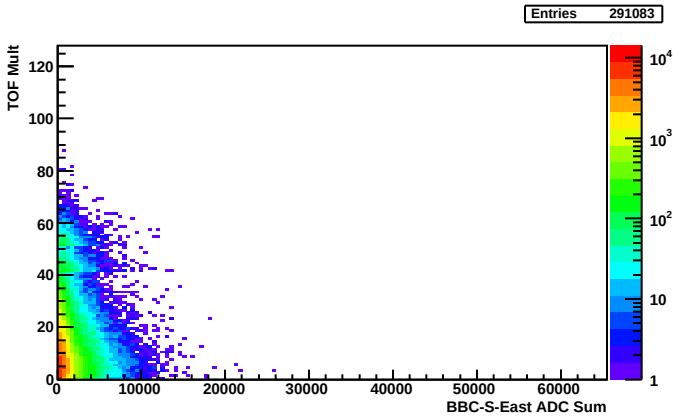

BBQ-VP003 (HI threshold)
Entries 4657328
TAC
QT Input Channel

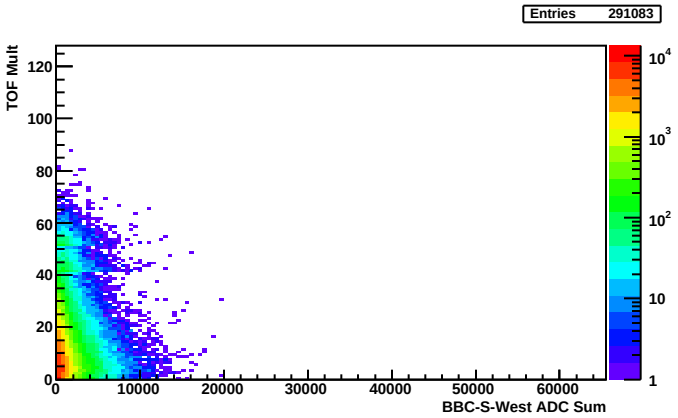

BBQ-VP004 (HI threshold)
Entries 4657328
ADC
QT Input Channel

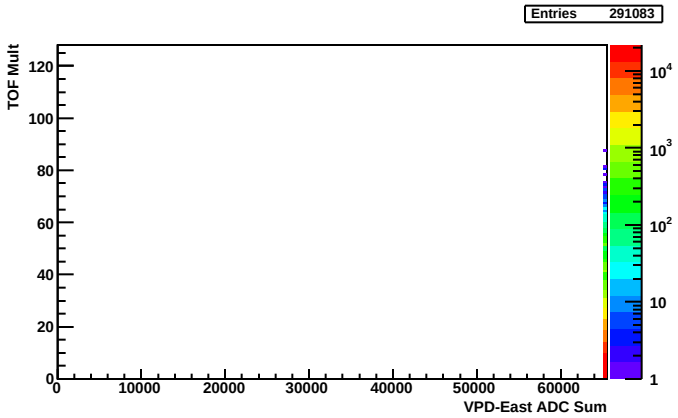

BBQ-VP004 (HI threshold)
Entries 4657328
TAC
QT Input Channel

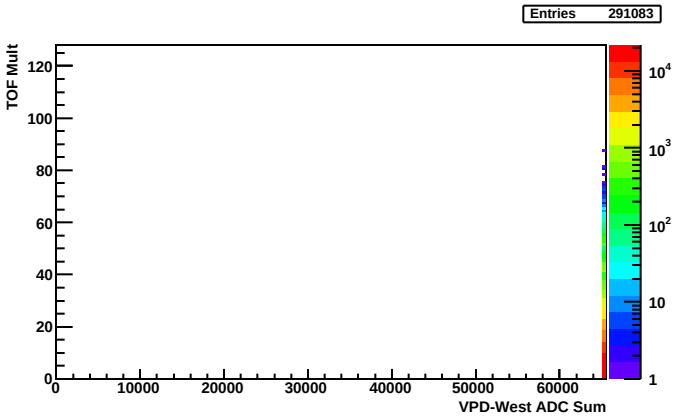

Entries 291083
TOF Mult
BBC-L-East ADC Sum

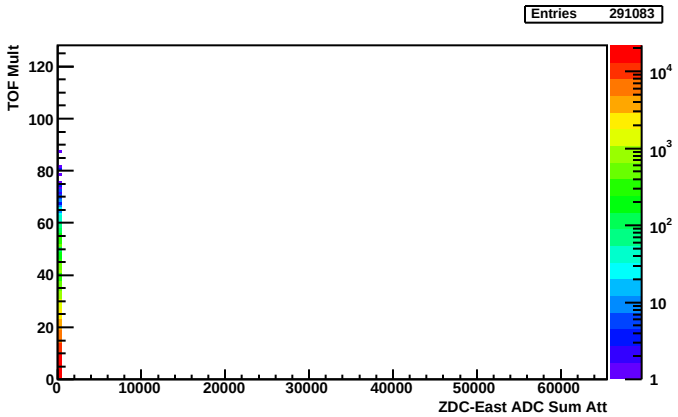

Entries 291083
TOF Mult
BBC-L-West ADC Sum

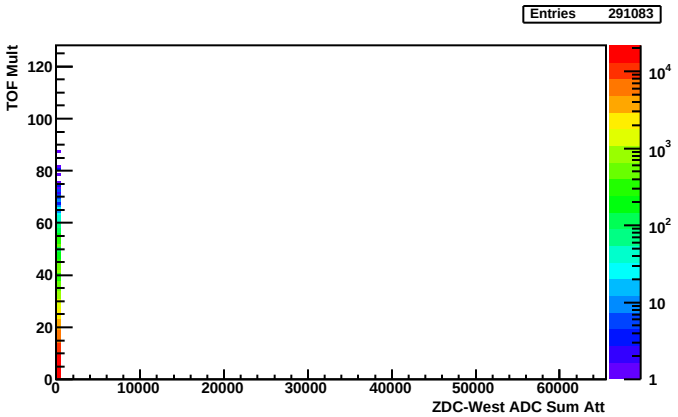

Entries 291083
TOF Mult
BBC-S-East ADC Sum

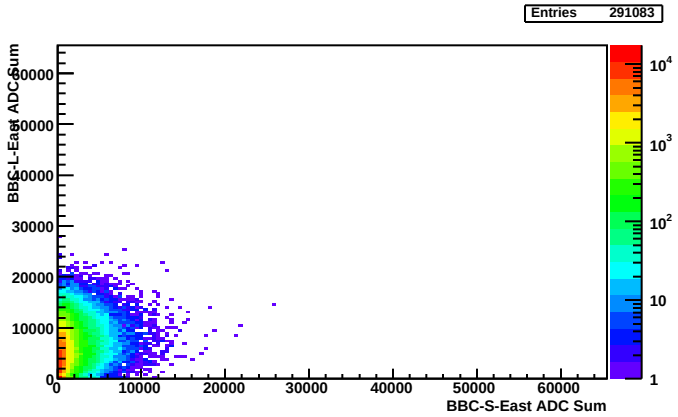

Entries 291083
TOF Mult
BBC-S-West ADC Sum

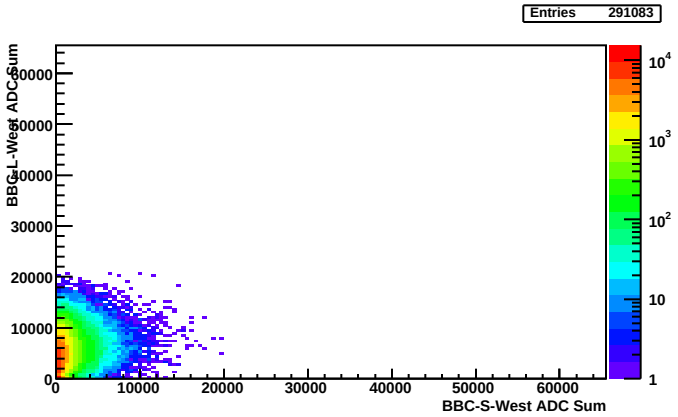

Entries 291083
TOF Mult
VPD-East ADC Sum

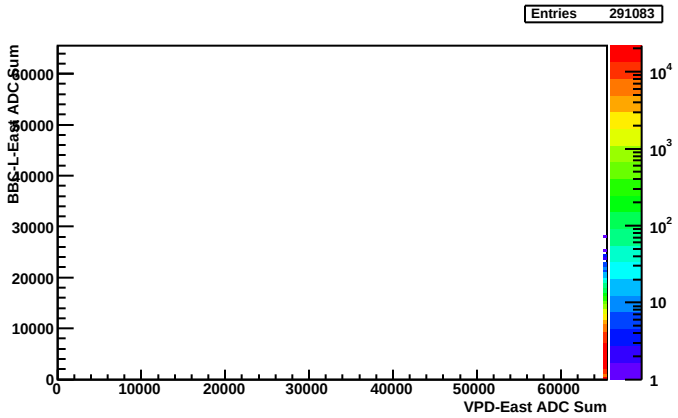

Entries 291083
TOF Mult
VPD-West ADC Sum

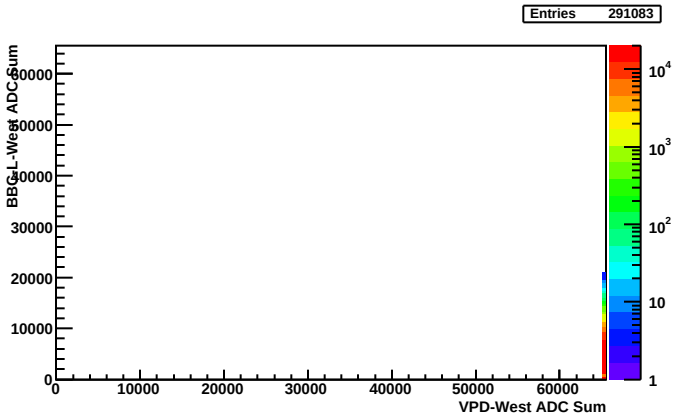

Entries 291083
TOF Mult
ZDC-East ADC Sum Att

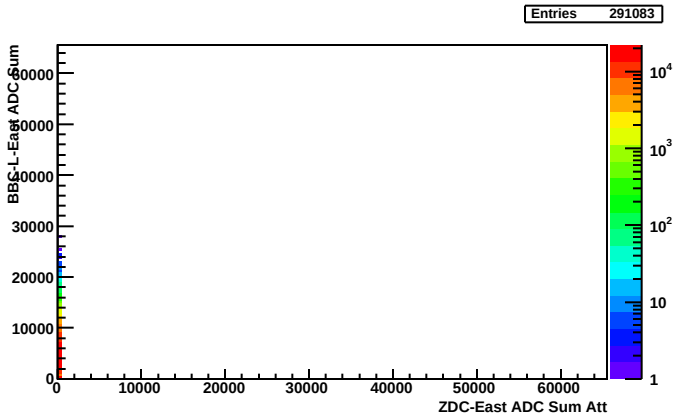

Entries 291083
TOF Mult
ZDC-West ADC Sum Att


Entries 291083
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 291083
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 291083
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 291083
BBC-L-West ADC Sum
VPD-West ADC Sum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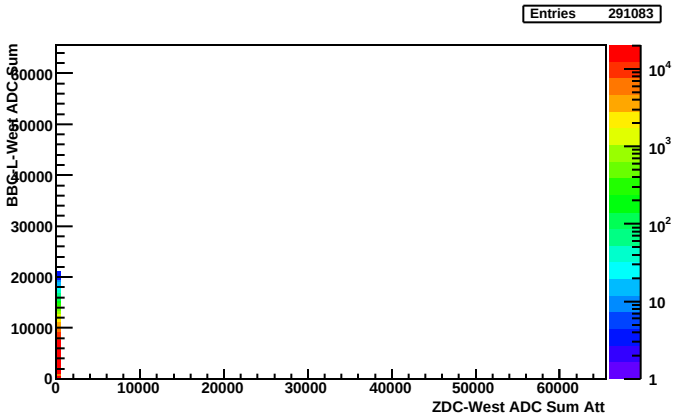
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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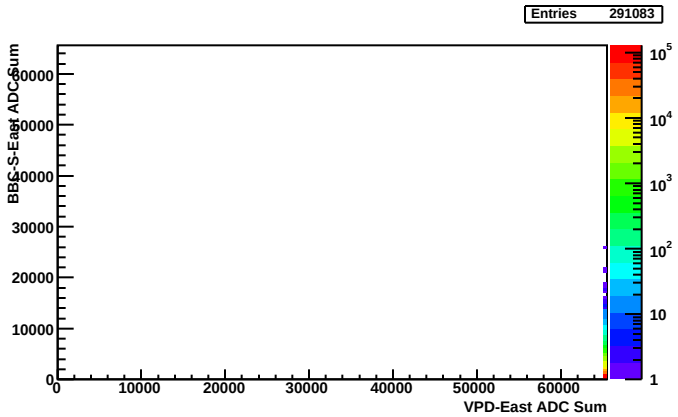
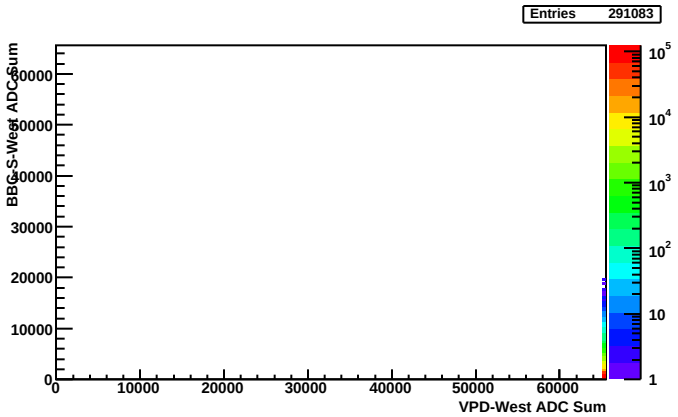
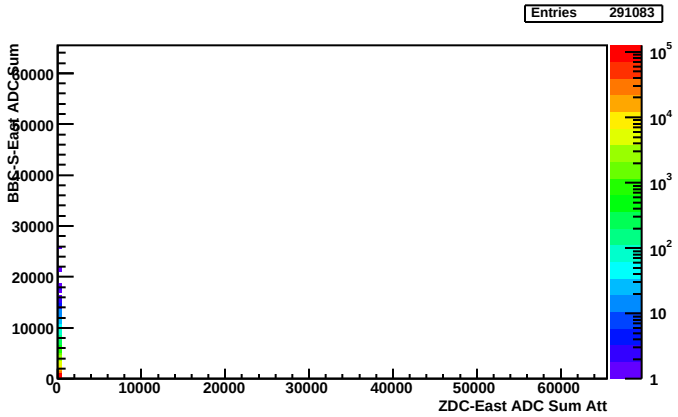
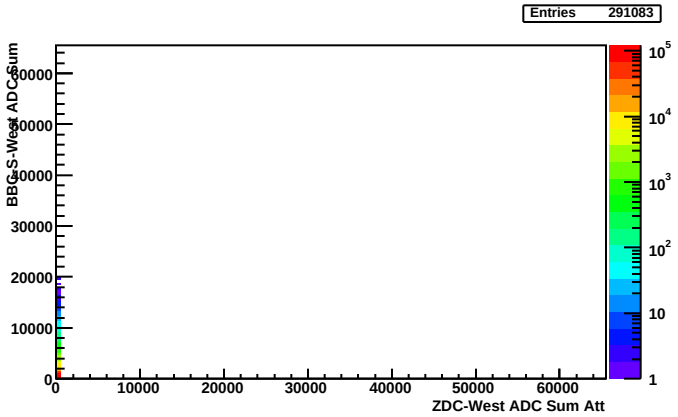
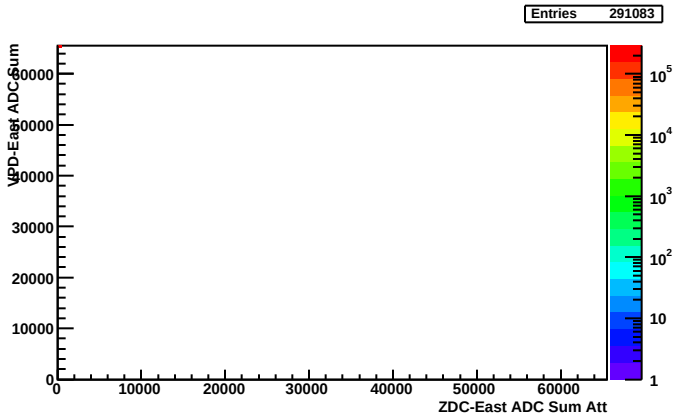
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

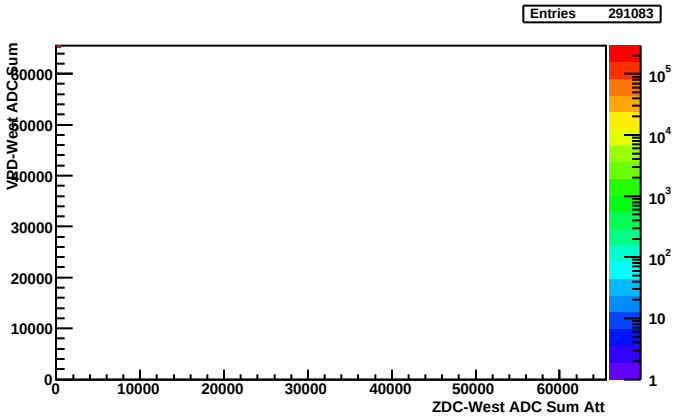

Entries 291083
BBC-S-East ADC Sum
VPD-East ADC Sum

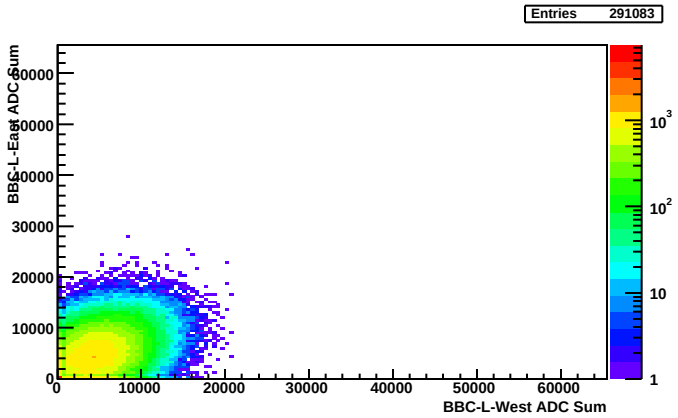

Entries 291083
BBC-S-West ADC Sum
VPD-West ADC Sum

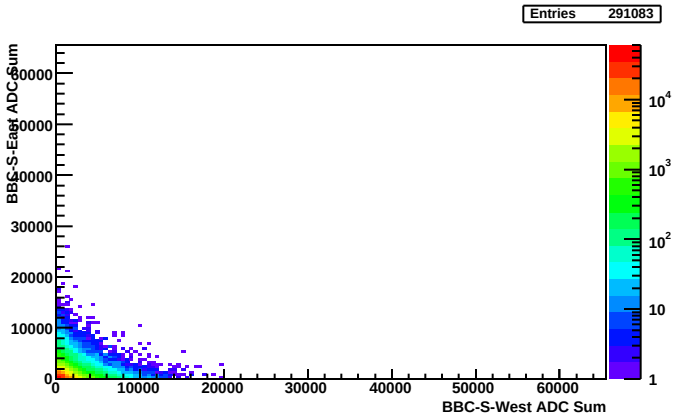

Entries 291083
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

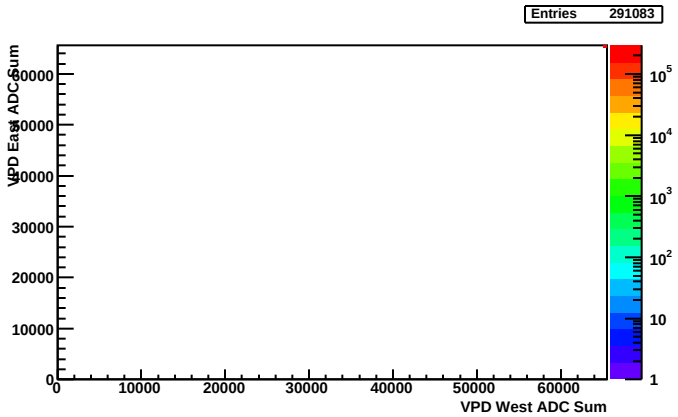

Entries 291083
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

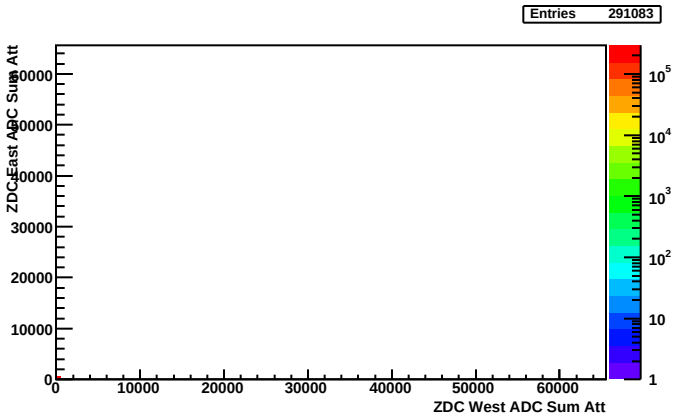

Entries 291083
VPD-East ADC Sum
ZDC-East ADC Sum Att

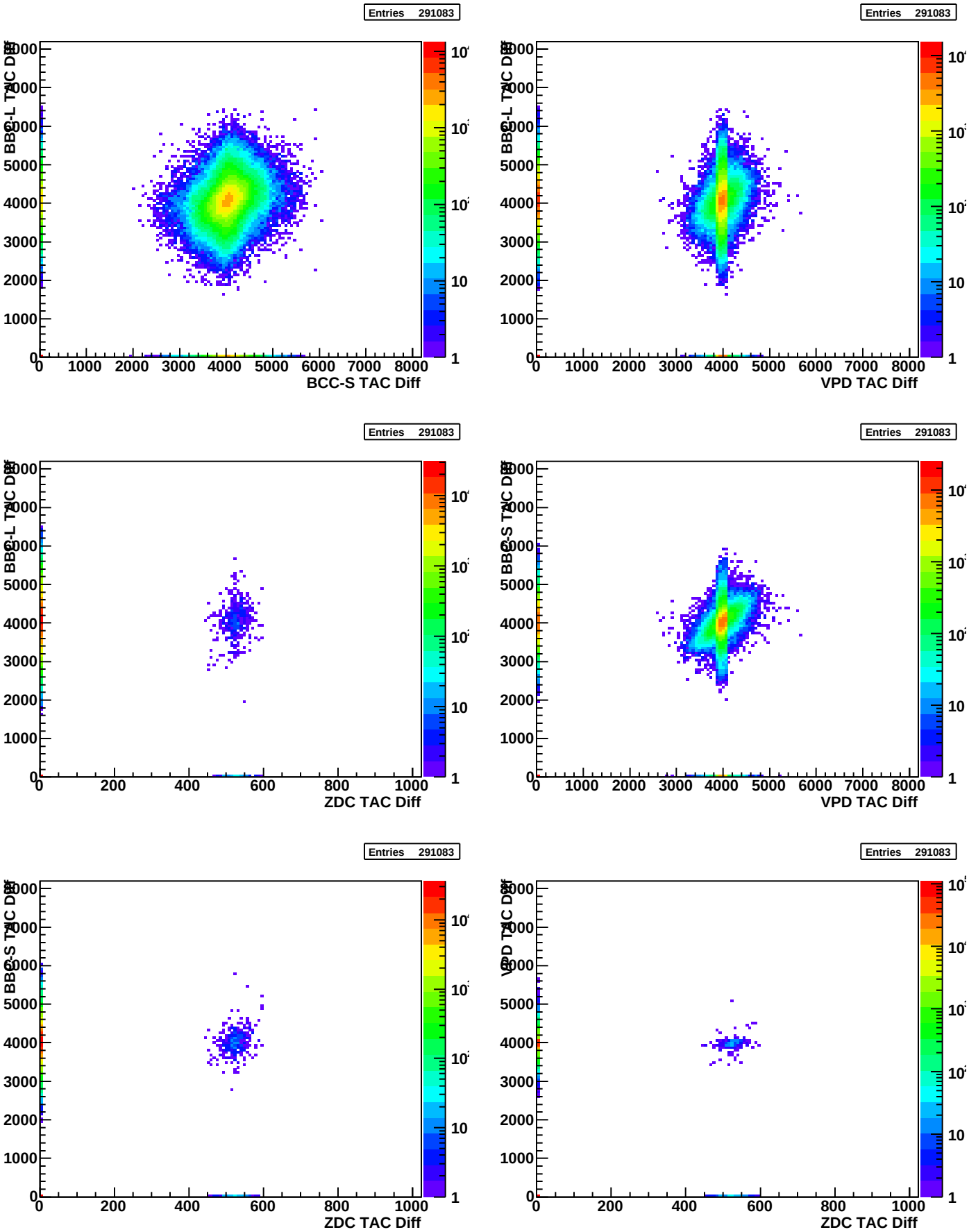

Entries 291083
VPD-West ADC Sum
ZDC-West ADC Sum Att

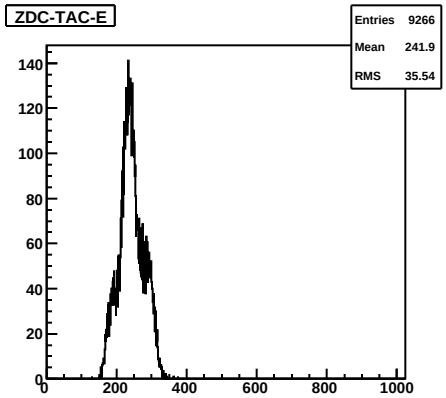

Entries 291083
BBC-L-East ADC Sum
BBC-L-West ADC Sum

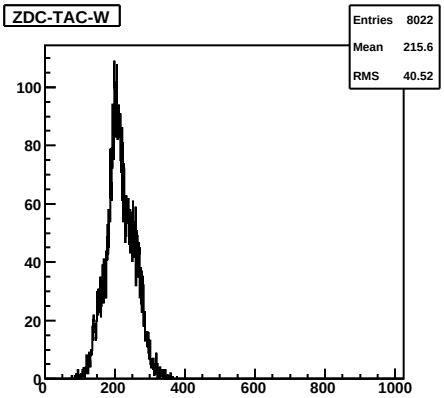

Entries 291083
BBC-S-East ADC Sum
BBC-S-West ADC Sum

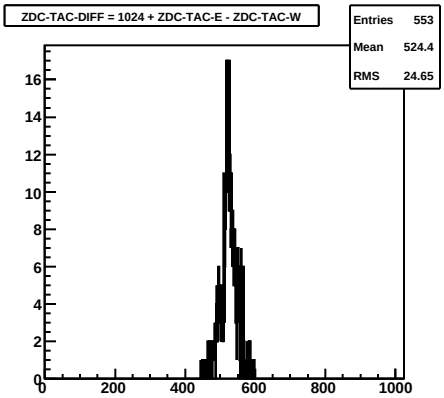

Entries 291083
VPD East ADC Sum
VPD West ADC Sum

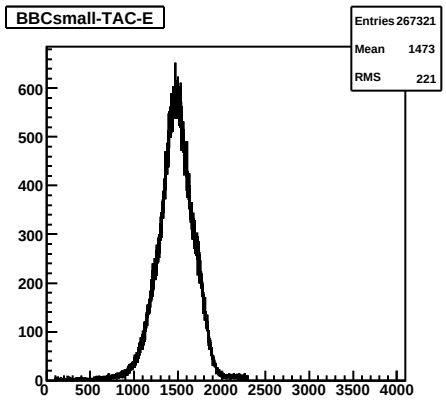

Entries 291083
ZDC East ADC Sum Att
ZDC West ADC Sum Att

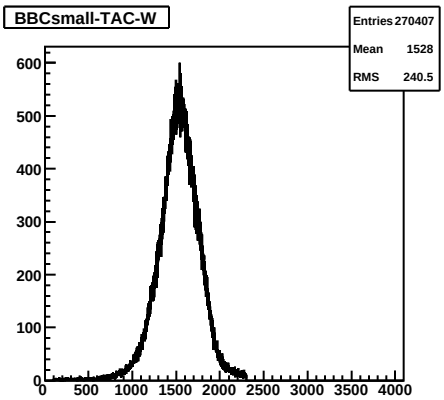

Entries 291083
BBC-L TAC Diff
BCC-S TAC Diff
Entries 291083
BBC-L TAC Diff
VPD TAC Diff
Entries 291083
BBC-L TAC Diff
ZDC TAC Diff
Entries 291083
BBC-S TAC Diff
VPD TAC Diff
Entries 291083
BBC-S TAC Diff
ZDC TAC Diff
Entries 291083
VPD TAC Diff
ZDC TAC Diff

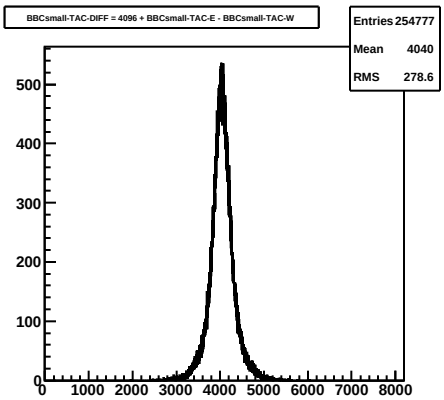

ZDC-TAC-E
Entries 9266
Mean 241.9
RMS 35.54

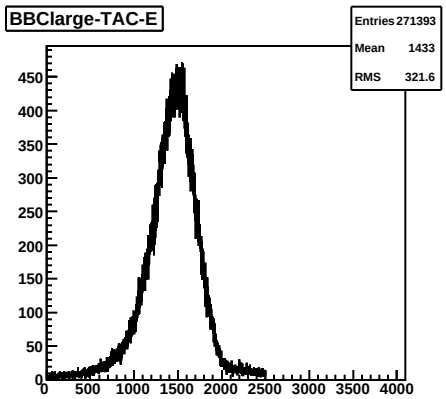

ZDC-TAC-W
Entries 8022
Mean 215.6
RMS 40.52

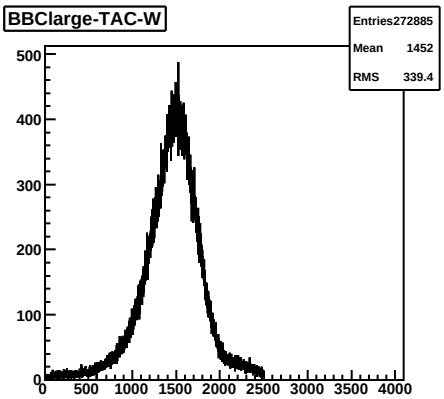

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 553
Mean 524.4
RMS 24.65

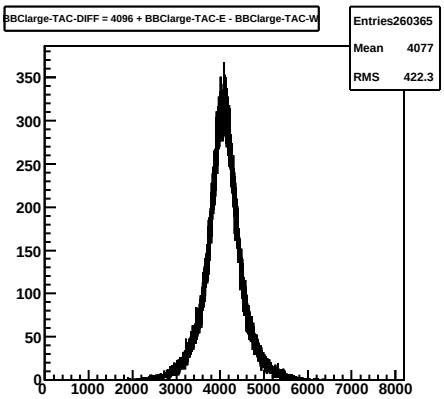

BBCsmall-TAC-E
Entries 267321
Mean 1473
RMS 221

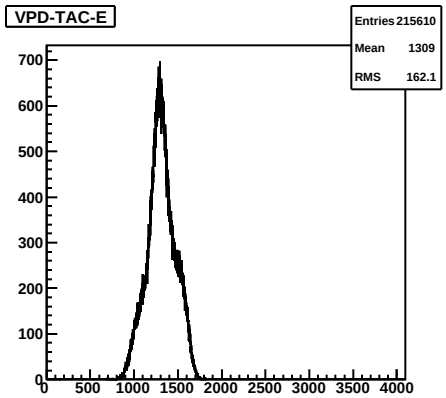

BBCsmall-TAC-W
Entries 270407
Mean 1528
RMS 240.5

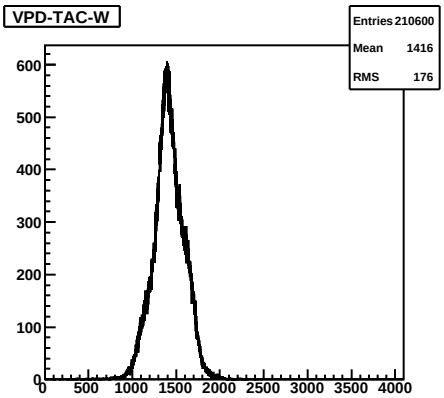

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 254777
Mean 4040
RMS 278.6

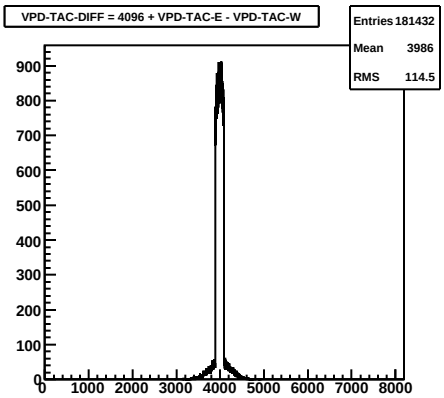

BBClarge-TAC-E
Entries 271393
Mean 1433
RMS 321.6

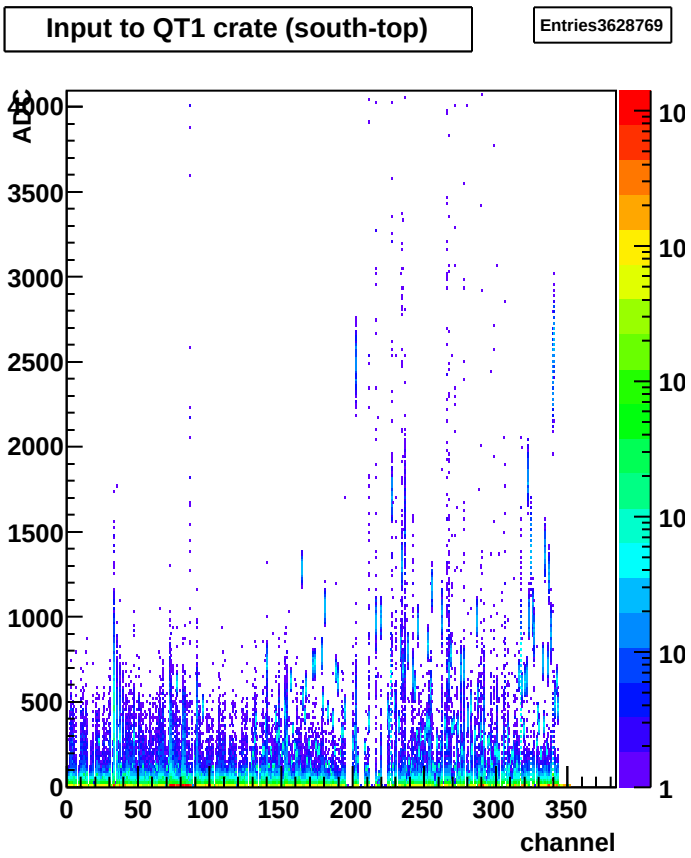

BBClarge-TAC-W
Entries 272885
Mean 1452
RMS 339.4

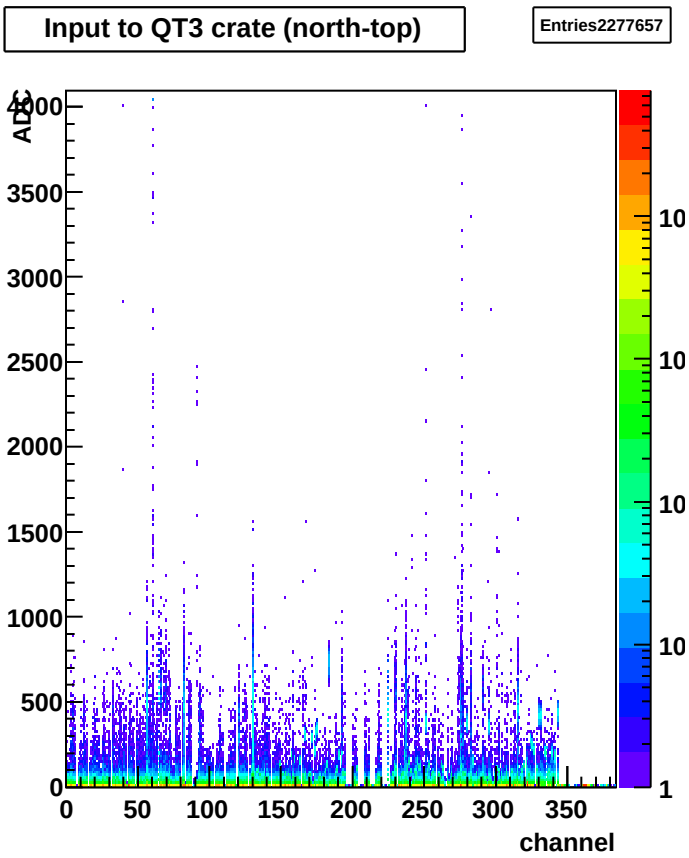

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 260365
Mean 4077
RMS 422.3

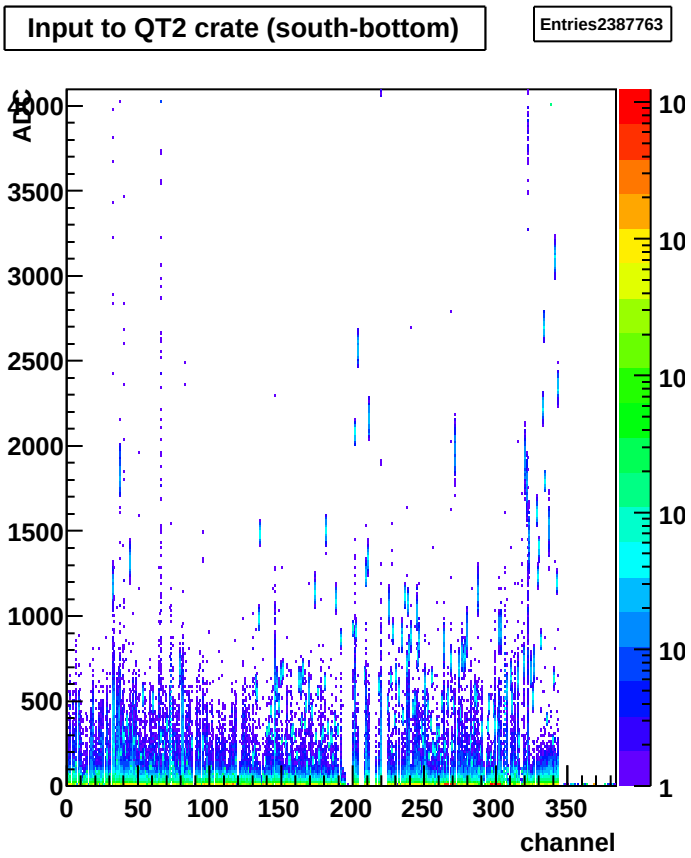

VPD-TAC-E
Entries 215610
Mean 1309
RMS 162.1

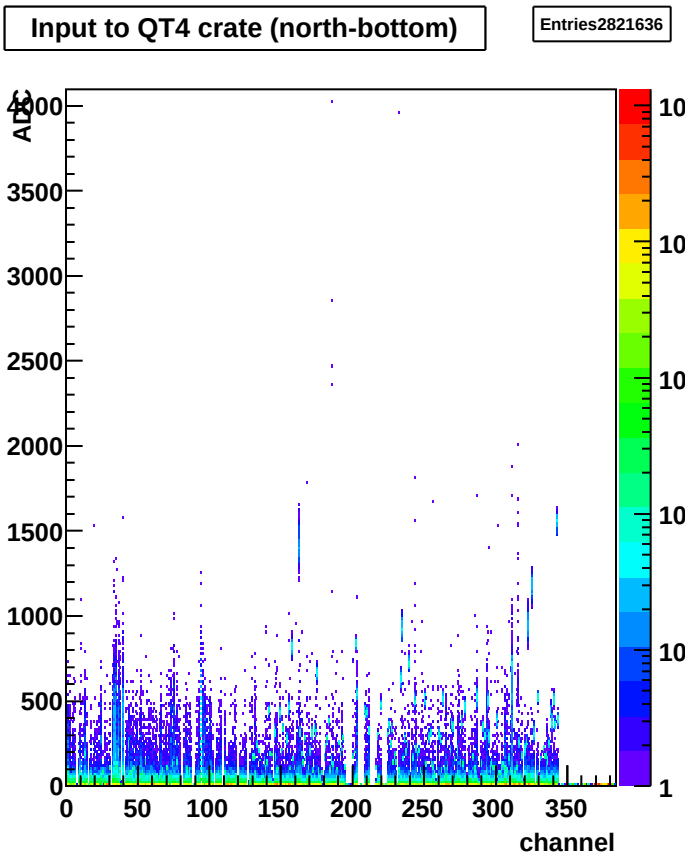

VPD-TAC-W
Entries 210600
Mean 1416
RMS 176

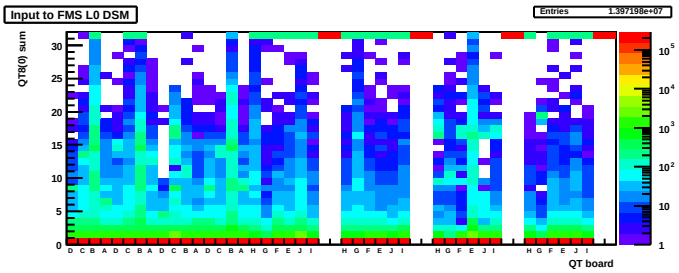

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 181432
Mean 3986
RMS 114.5

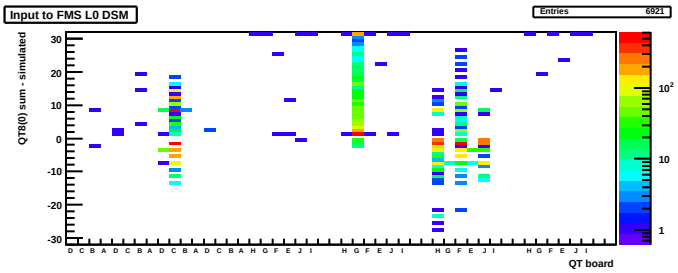

Input to QT1 crate (south-top)
Entries3628769
ADC
channel

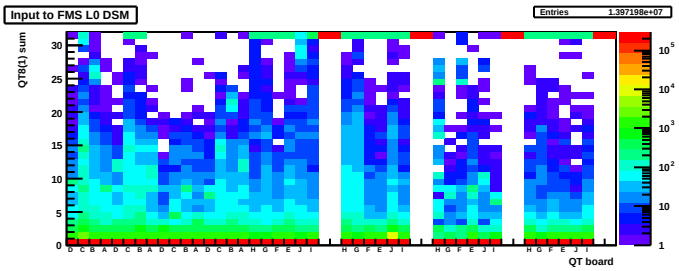

Input to QT3 crate (north-top)
Entries2277657
ADC
channel

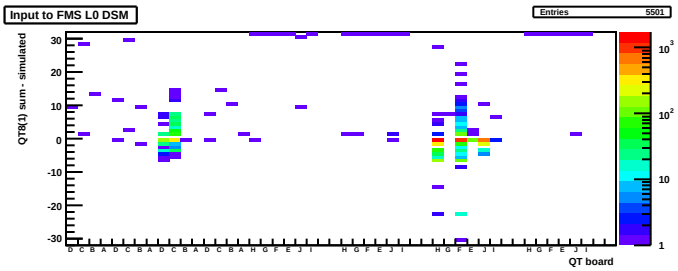

Input to QT2 crate (south-bottom)
Entries2387763
ADC
channel

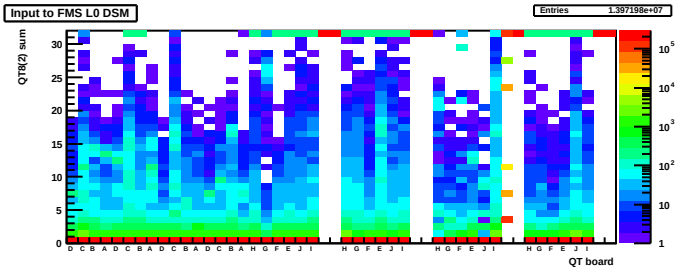

Input to QT4 crate (north-bottom)
Entries2821636
ADC
channel

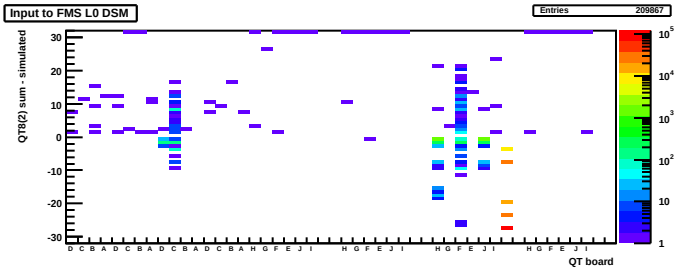

Input to FMS L0 DSM
Entries 1.397198e+07
QT8(0) sum
QT board

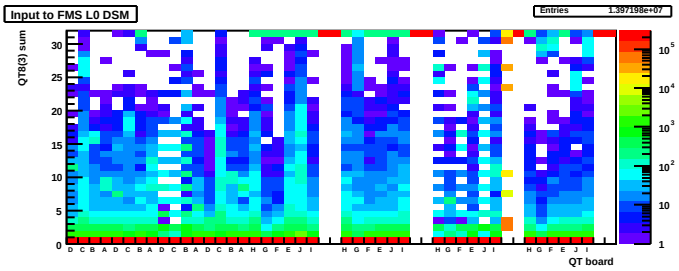

Input to FMS L0 DSM
Entries 6921
QT8(0) sum - simulated
QT board

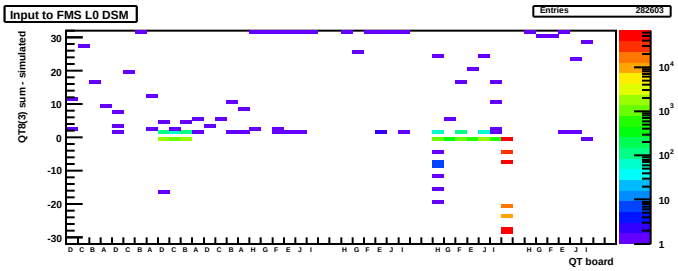

Input to FMS L0 DSM
Entries 1.397198e+07
QT8(1) sum
QT board

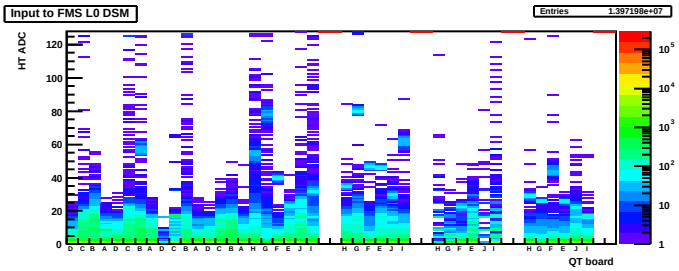

Input to FMS L0 DSM
Entries 5501
QT8(1) sum - simulated
QT board

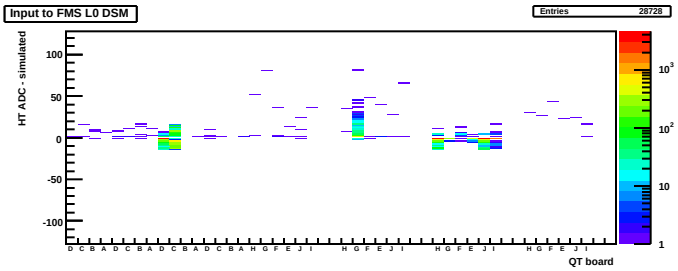

Input to FMS L0 DSM
Entries 1.397198e+07
QT8(2) sum
QT board

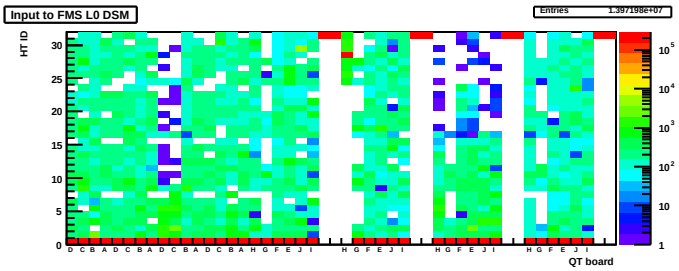

Input to FMS L0 DSM
Entries 209867
QT8(2) sum - simulated
QT board

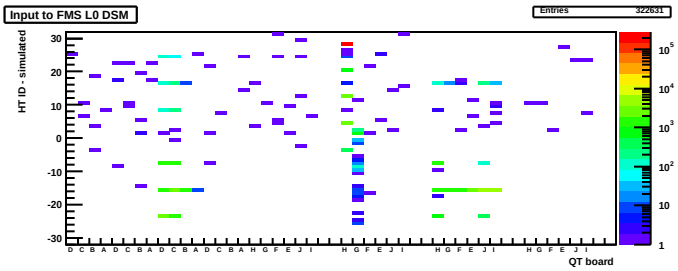

Input to FMS L0 DSM
Entries 1.397198e+07
QT8(3) sum
QT board

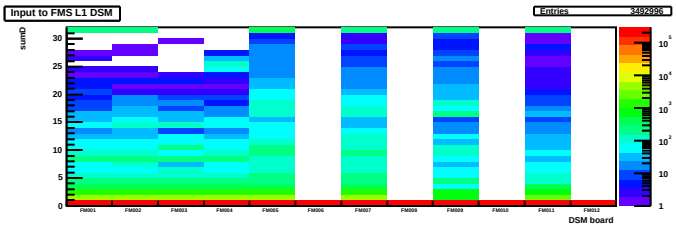

Input to FMS L0 DSM
Entries 282603
QT8(3) sum - simulated
QT board

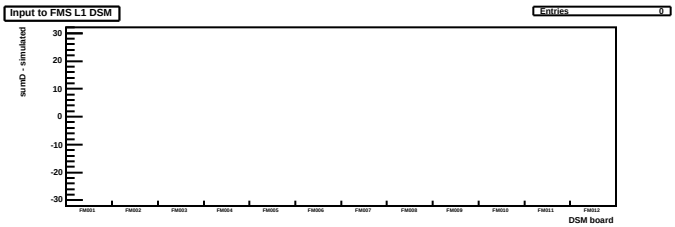

Input to FMS L0 DSM
Entries 1.397198e+07
HT ADC
QT board

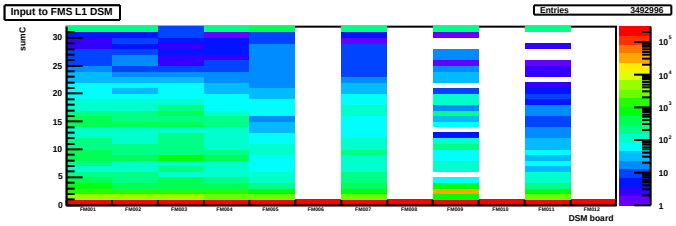

Input to FMS L0 DSM
Entries 28728
HT ADC - simulated
QT board

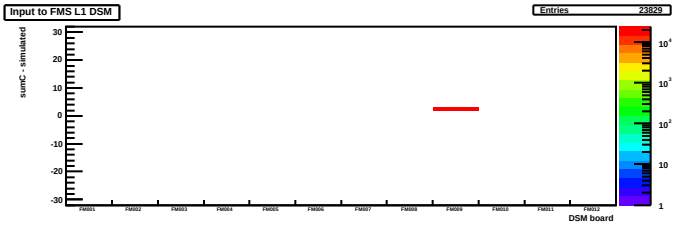

Input to FMS L0 DSM
Entries 1.397198e+07
HT ID
QT board

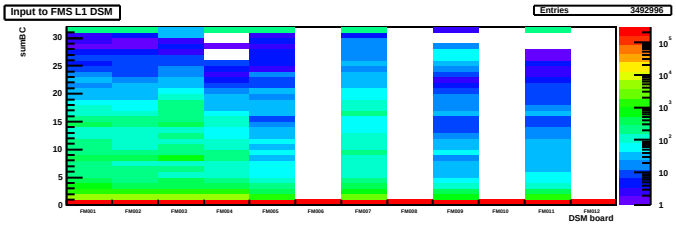

Input to FMS L0 DSM
Entries 322631
HT ID - simulated
QT board

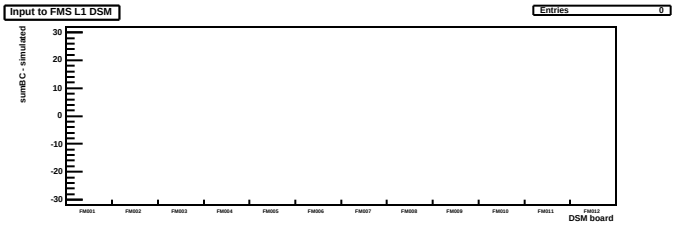

Input to FMS L1 DSM
Entries 3492996
sumD
DSM board

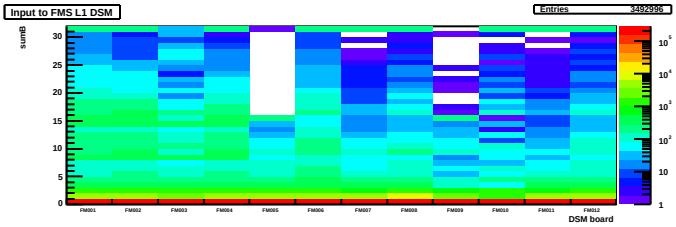

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

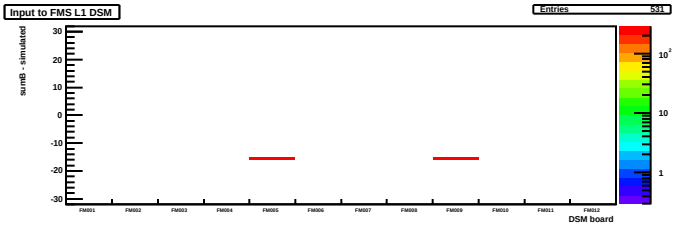

Input to FMS L1 DSM
Entries 3492996
sumC
DSM board

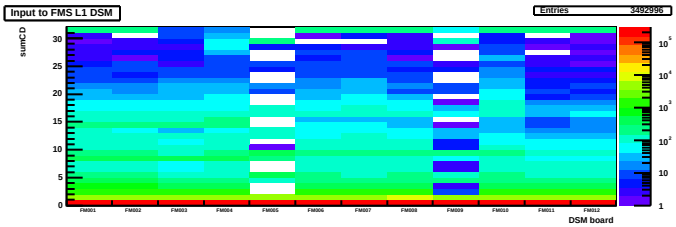

Input to FMS L1 DSM
Entries 23829
sumC - simulated
DSM board

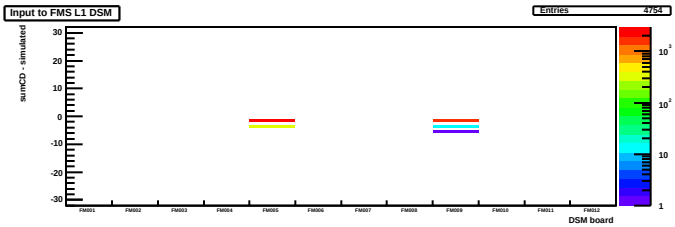

Input to FMS L1 DSM
Entries 3492996
sumBC
DSM board

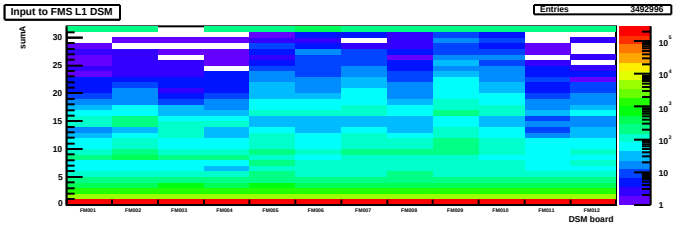

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

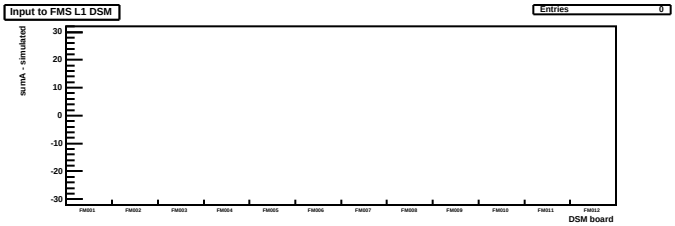

Input to FMS L1 DSM
Entries 3492996
sumB
DSM board

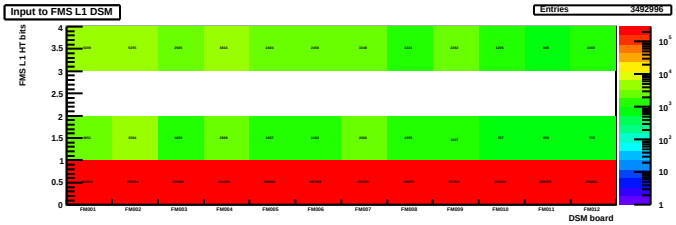

Input to FMS L1 DSM
Entries 531
sumB - simulated
DSM board

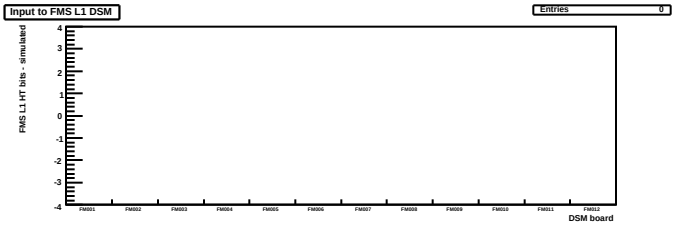

Input to FMS L1 DSM
Entries 3492996
sumCD
DSM board

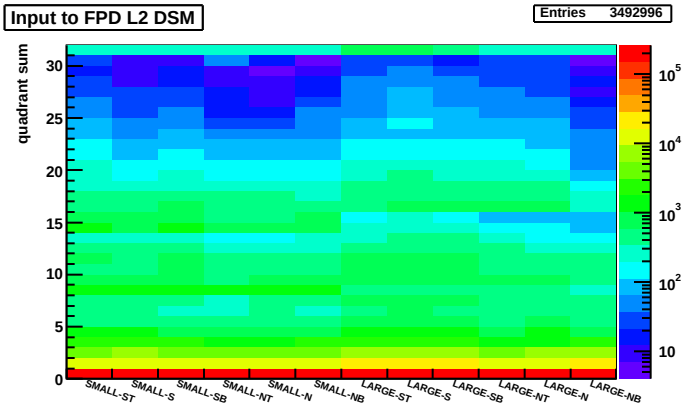

Input to FMS L1 DSM
Entries 4754
sumCD - simulated
DSM board

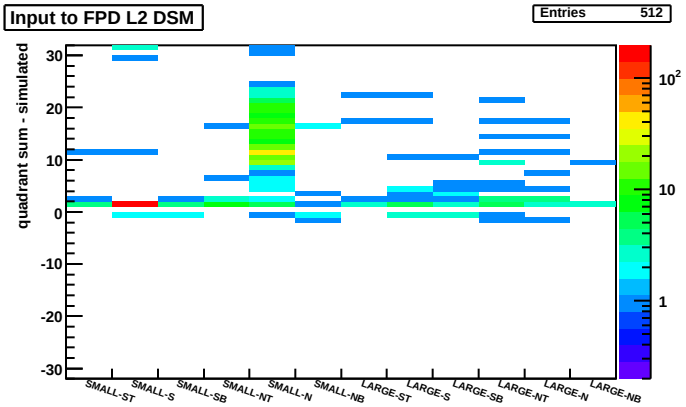

Input to FMS L1 DSM
Entries 3492996
sumA
DSM board

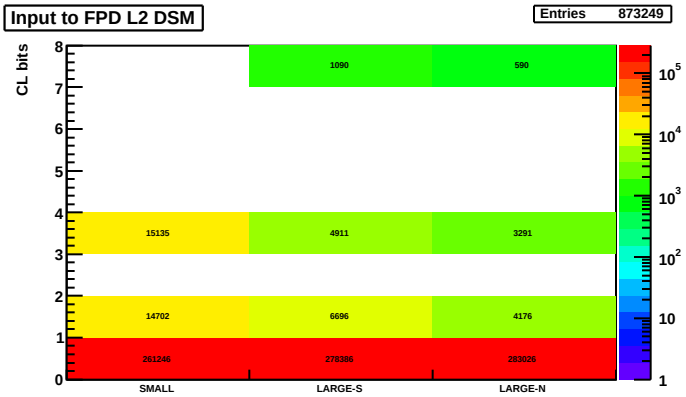

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

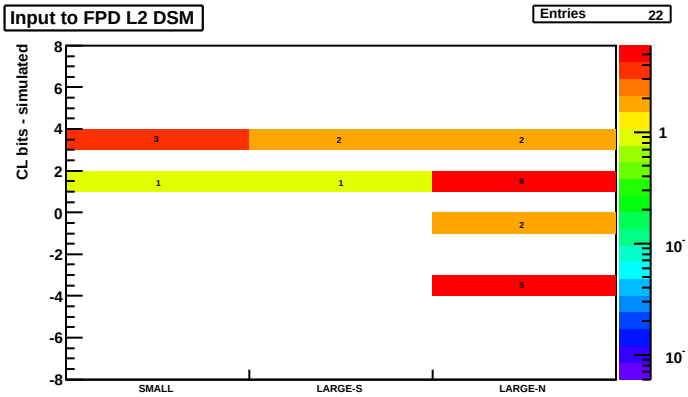

Input to FMS L1 DSM
Entries 3492996
FMS L1 HT bits
DSM board

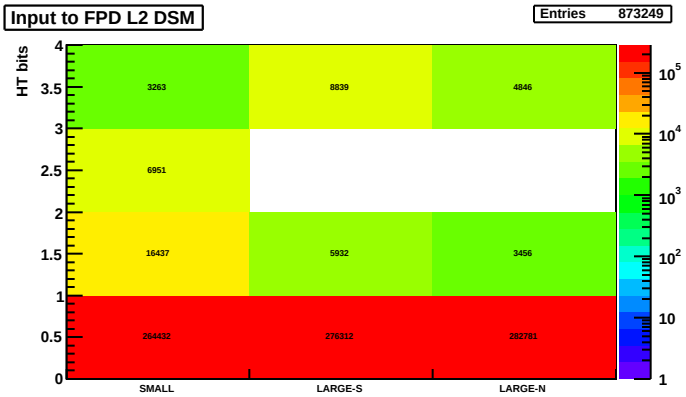

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

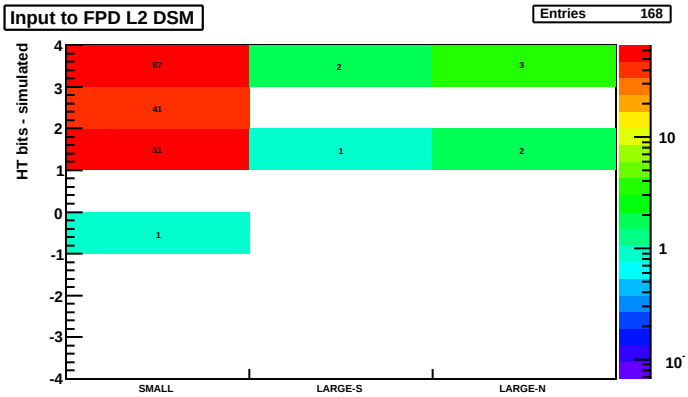

Input to FPD L2 DSM
Entries 3492996
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

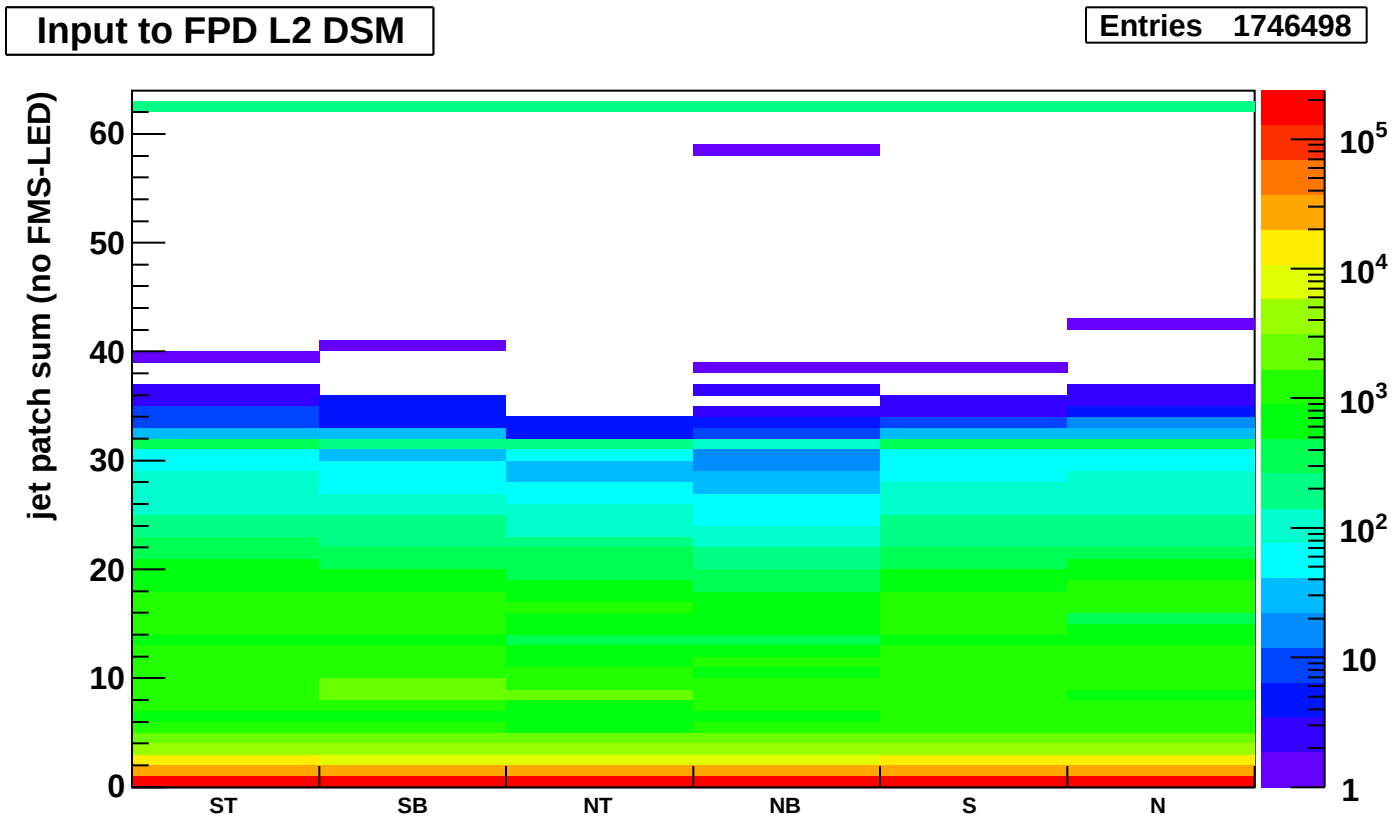

Input to FPD L2 DSM
Entries 512
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

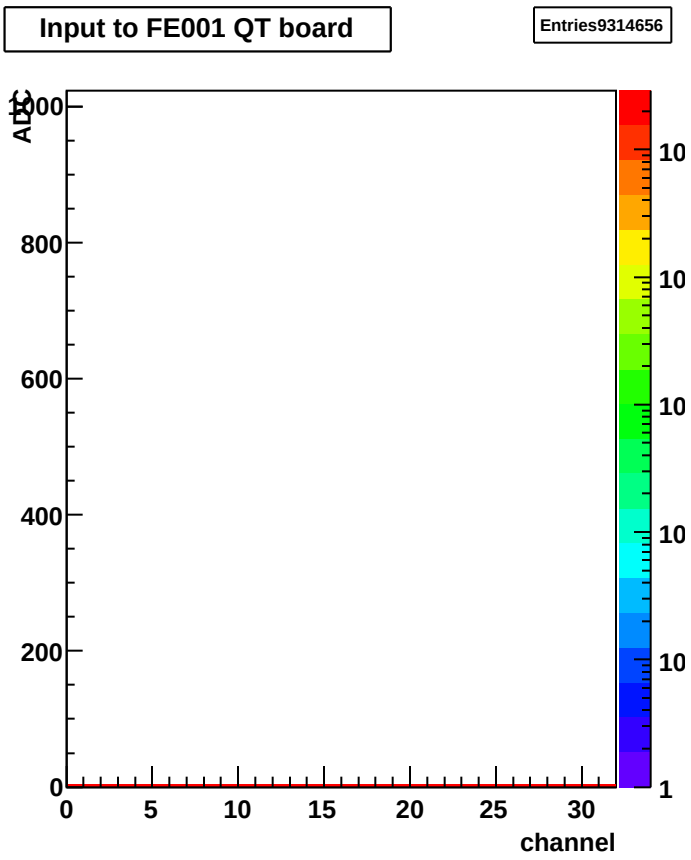

Input to FPD L2 DSM
Entries 873249
CL bits
1090
590
15135
4911
3291
14702
6696
4176
261246
278386
283026
SMALL
LARGE-S
LARGE-N

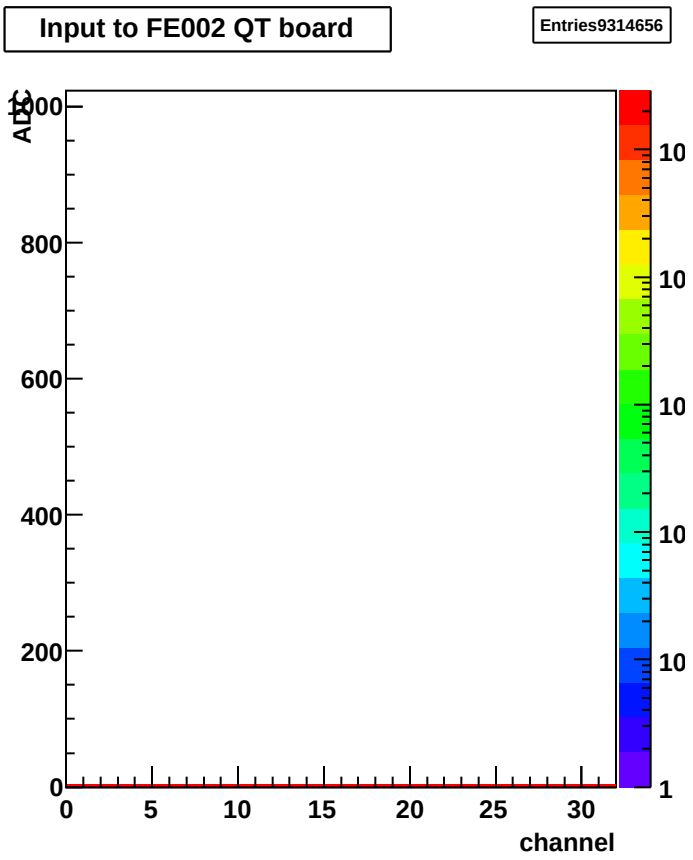

Input to FPD L2 DSM
Entries 22
CL bits - simulated
3
2
2
1
1
6
2
5
SMALL
LARGE-S
LARGE-N

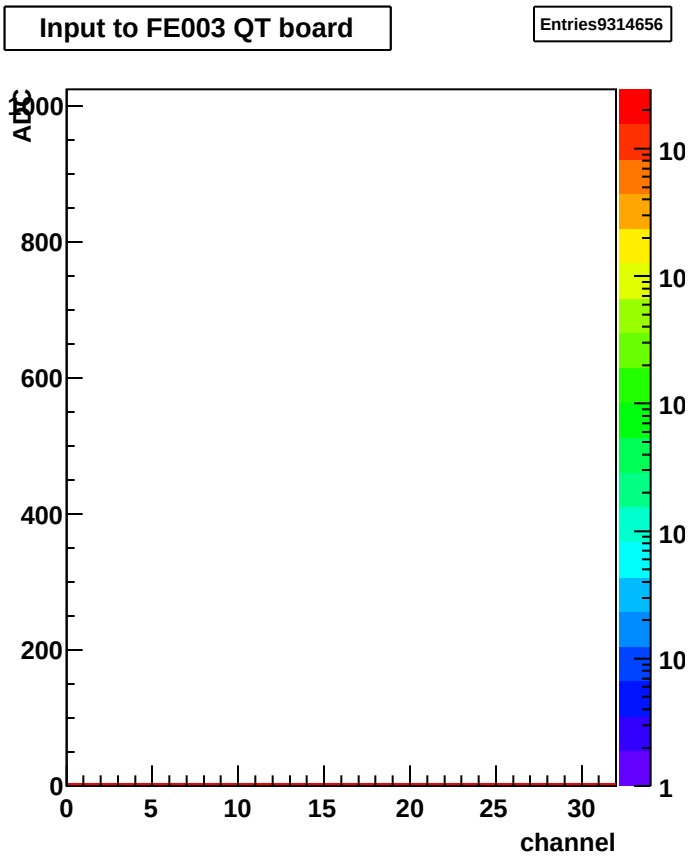

Input to FPD L2 DSM
Entries 873249
HT bits
3263
8839
4846
6951
16437
5932
3456
264432
276312
282781
SMALL
LARGE-S
LARGE-N

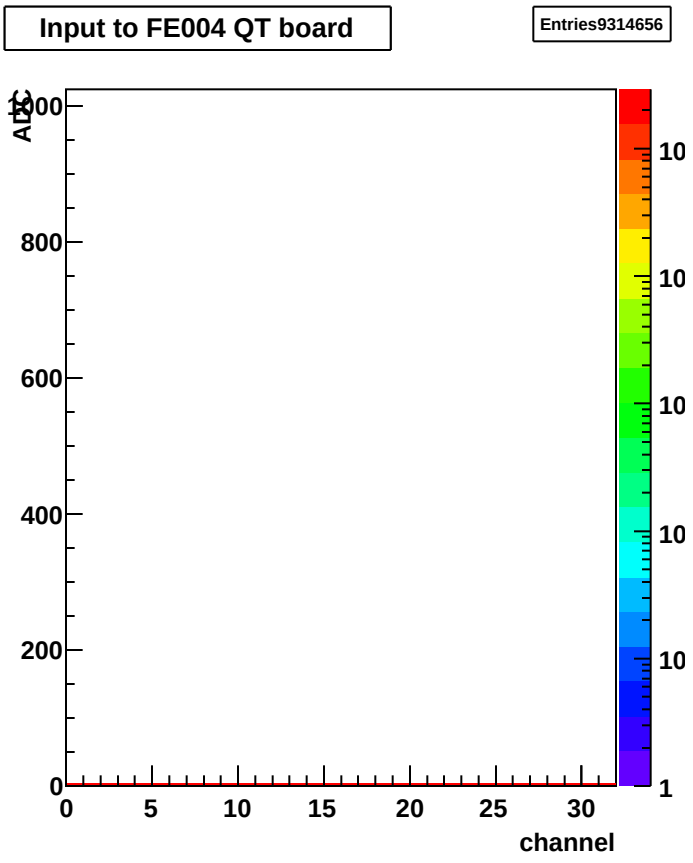

Input to FPD L2 DSM
Entries 168
HT bits - simulated
67
2
3
41
51
1
2
1
SMALL
LARGE-S
LARGE-N

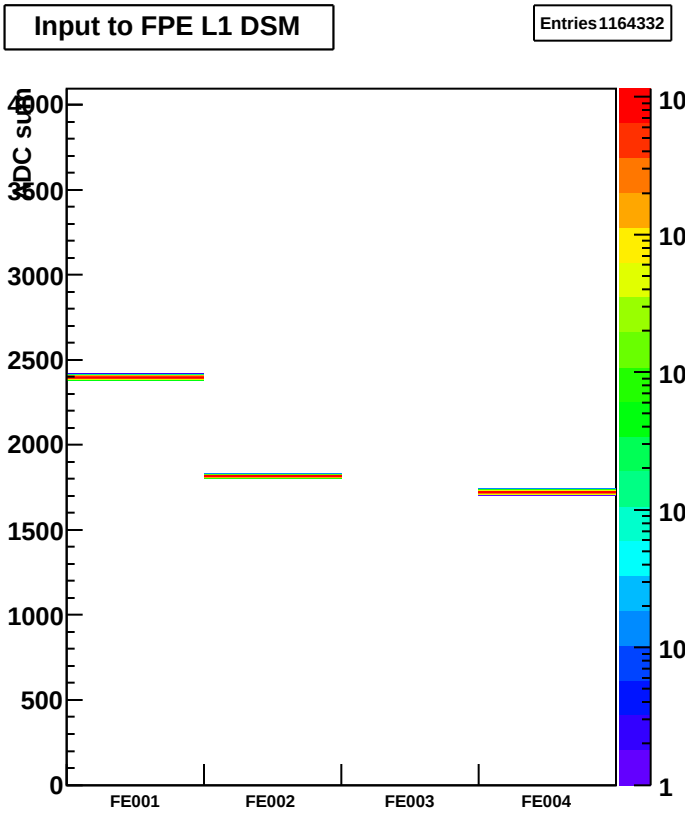

Input to FPD L2 DSM
Entries 1746498
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries9314656
ADC
channel

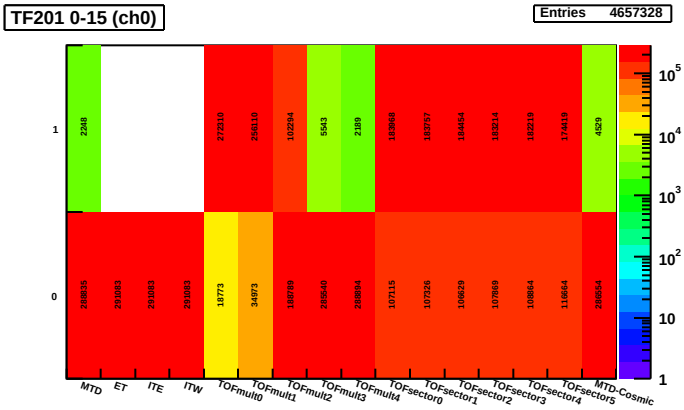

Input to FE002 QT board
Entries9314656
ADC
channel


Input to FE003 QT board
Entries9314656
ADC
channel

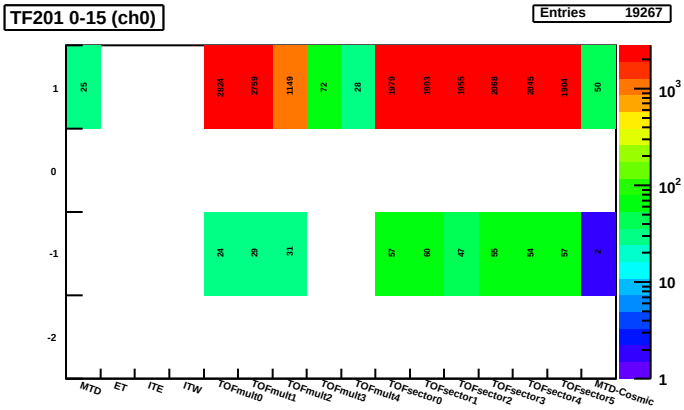

Input to FE004 QT board
Entries9314656
ADC
channel


Input to FPE L1 DSM
Entries 1164332
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

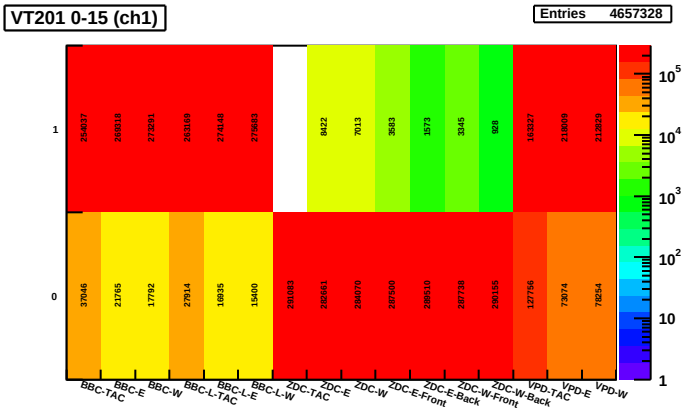
TF201 0-15 (ch0)



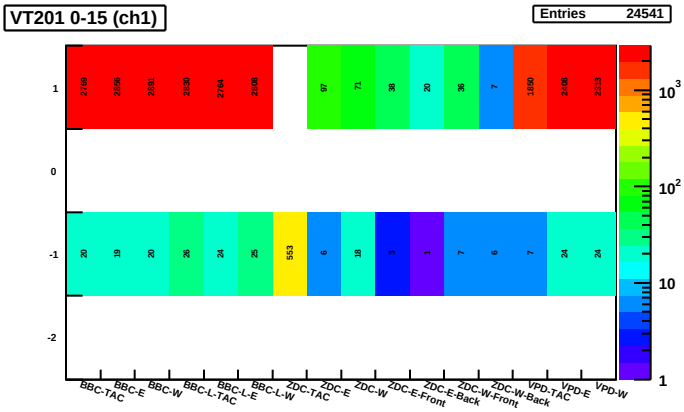
TF201 0-15 (ch0)



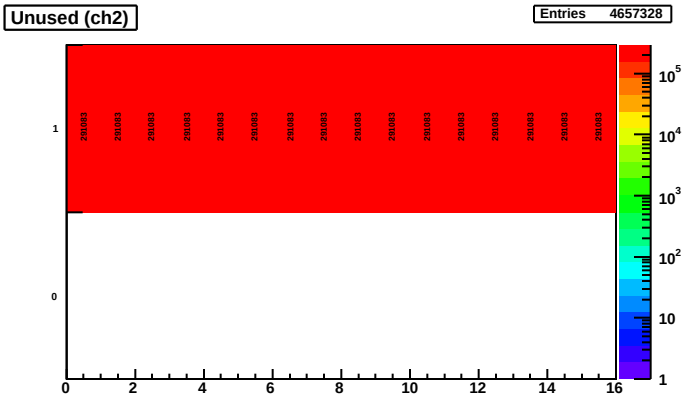
VT201 0-15 (ch1)

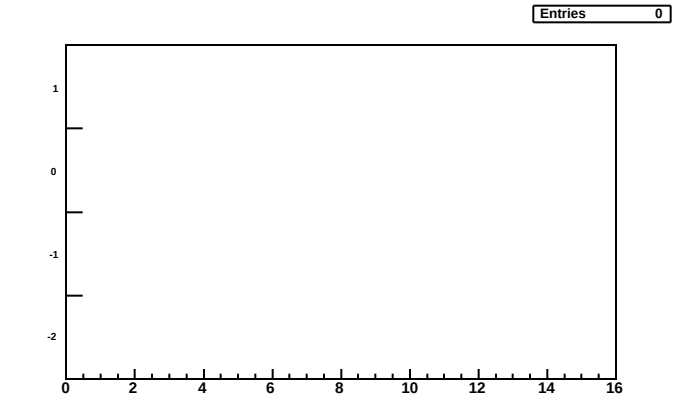


VT201 0-15 (ch1)

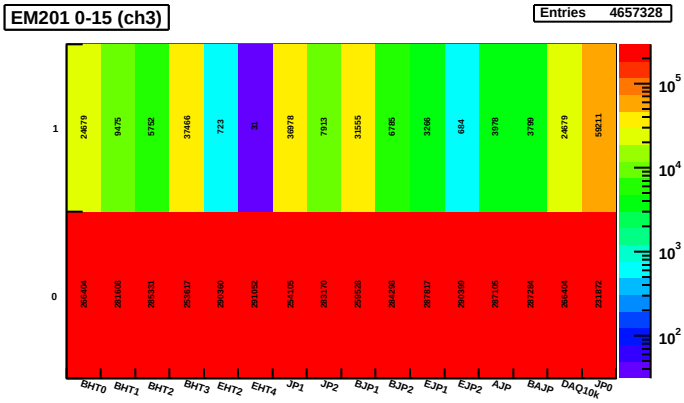


Unused (ch2)





EM201 0-15 (ch3)



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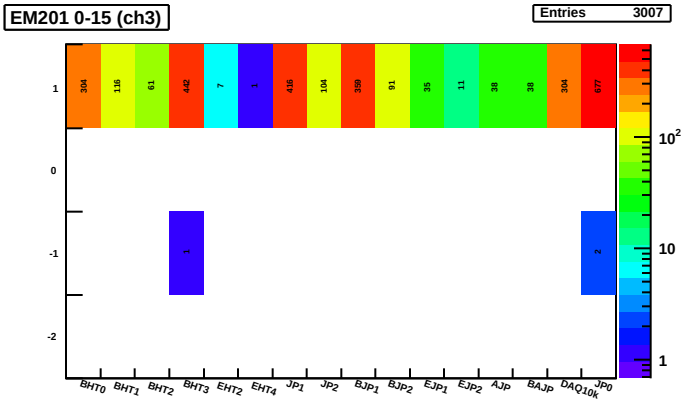
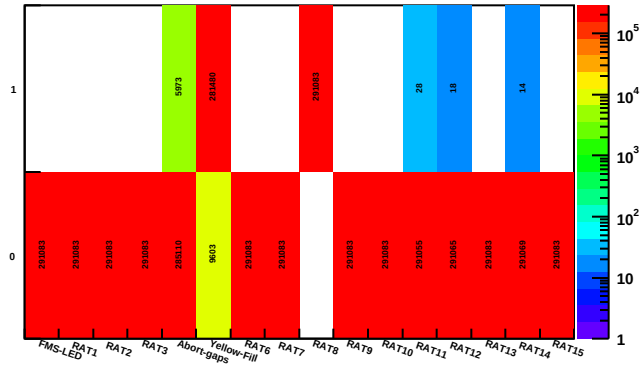
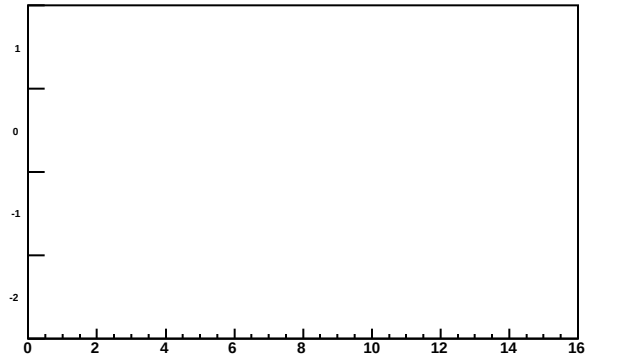
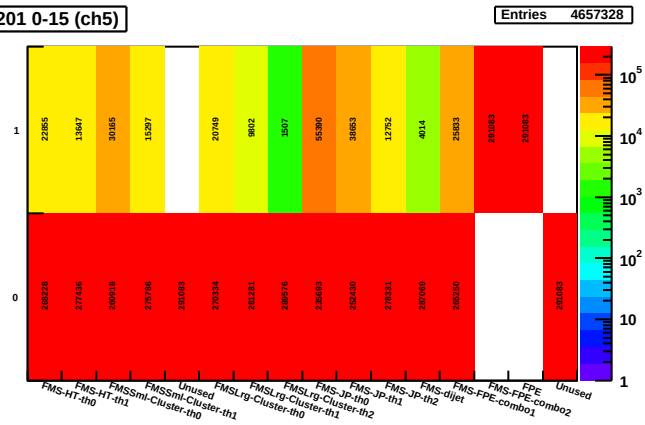


RAT board (ch4)





FP201 0-15 (ch5)



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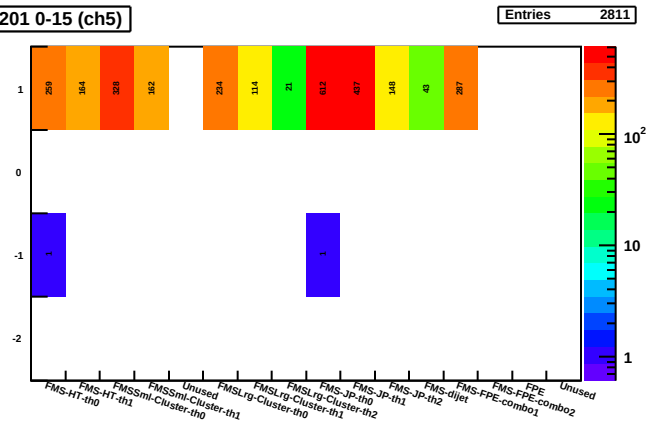

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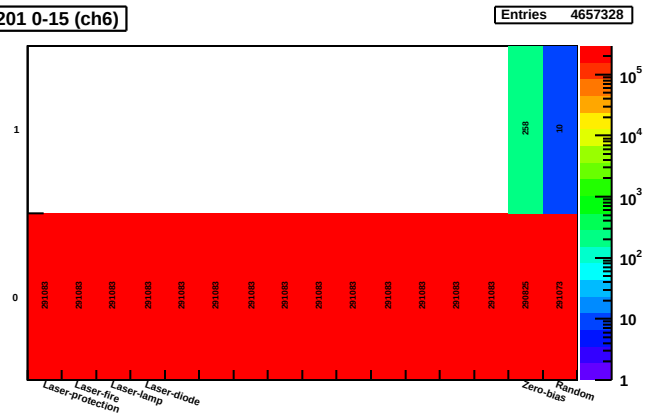

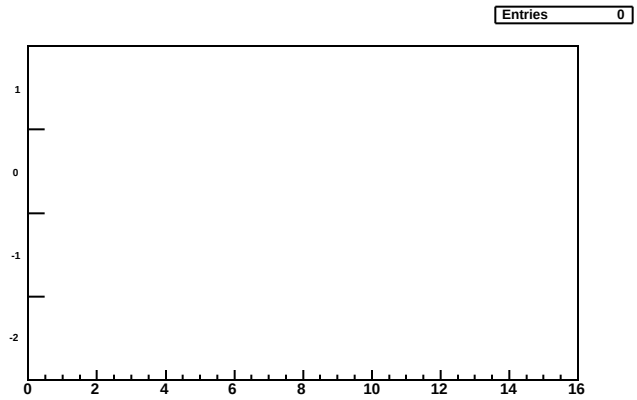
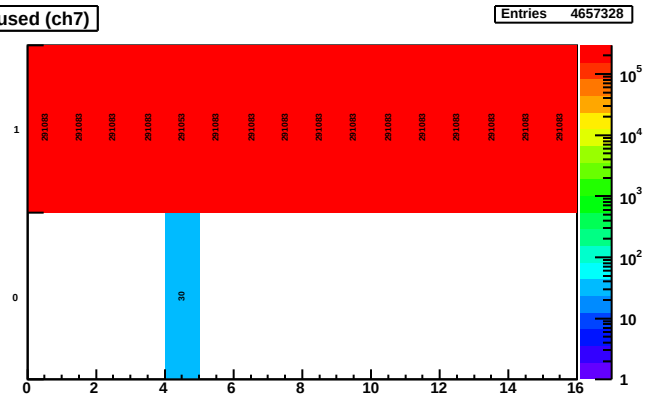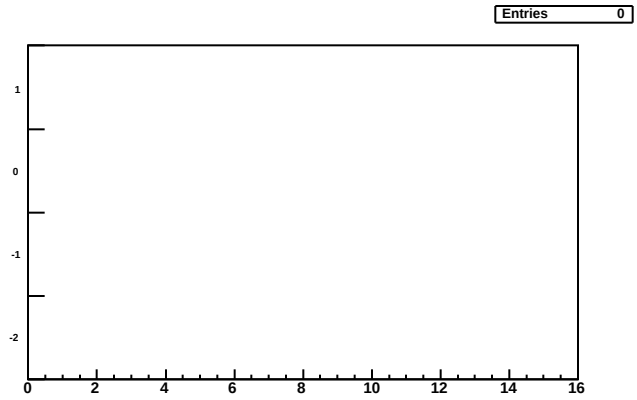
Unused (ch7)