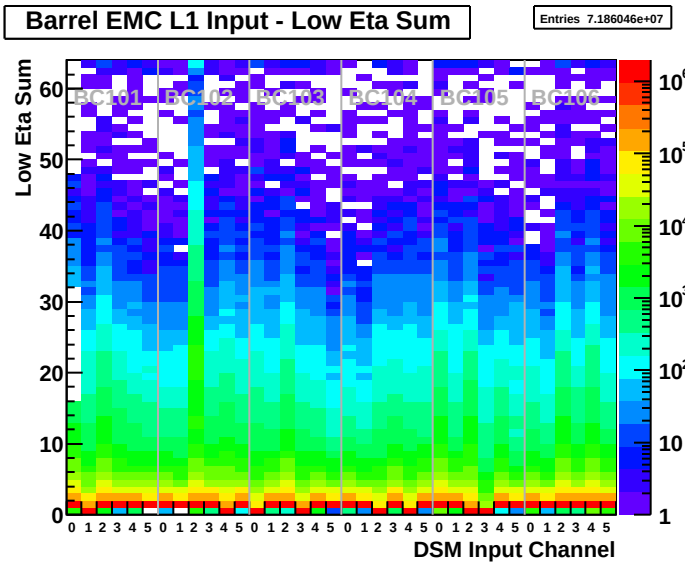
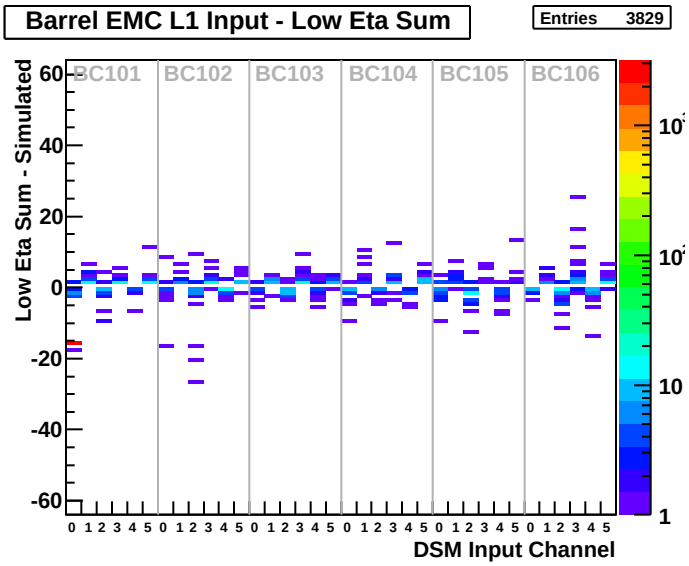
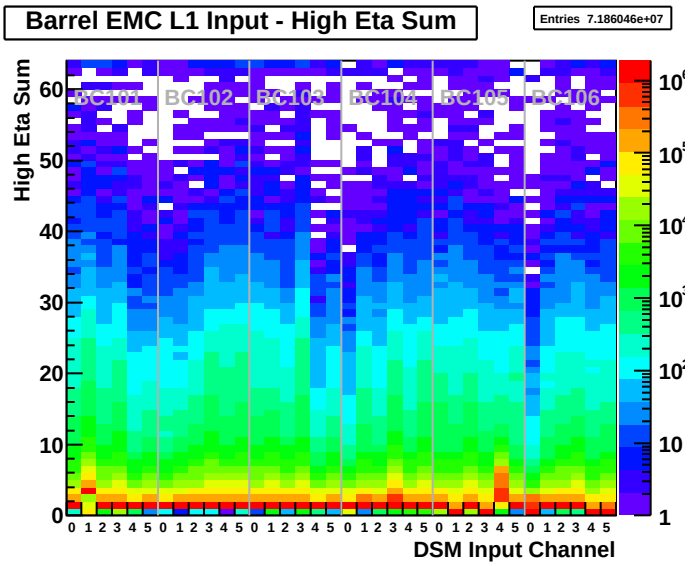
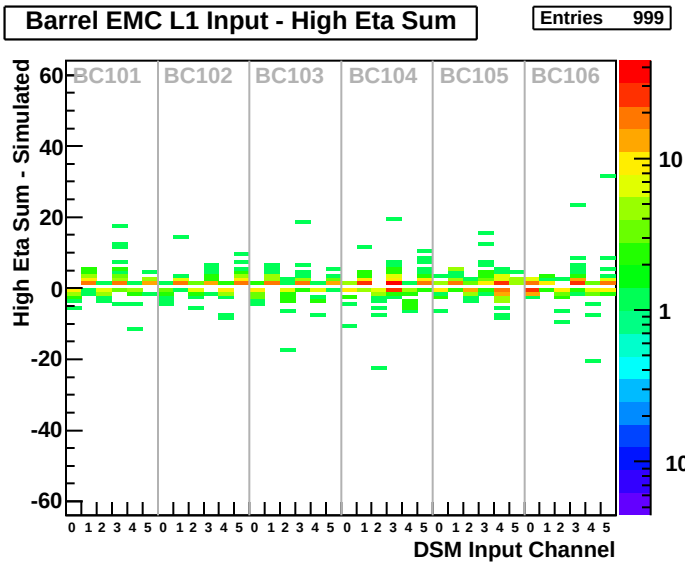
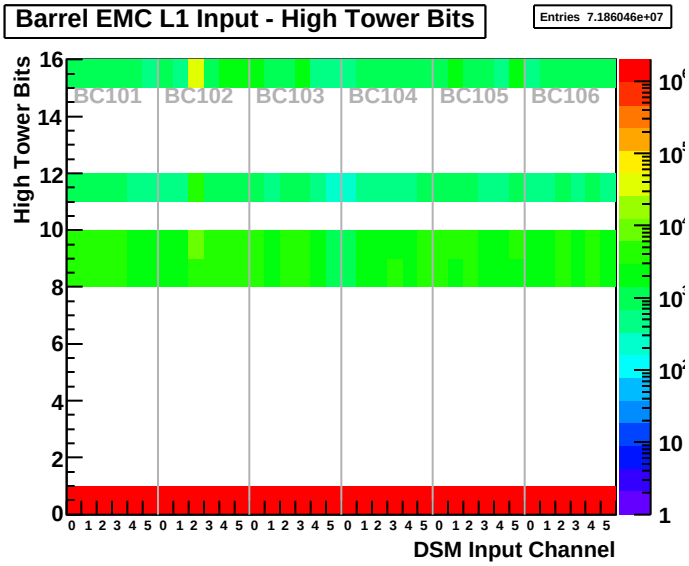
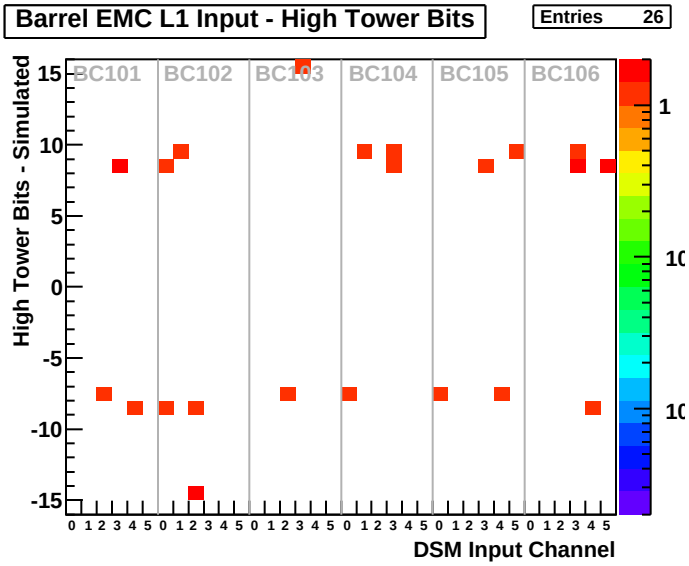

Barrel EMC L1 Input - Low Eta Sum
Entries 7.186046e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

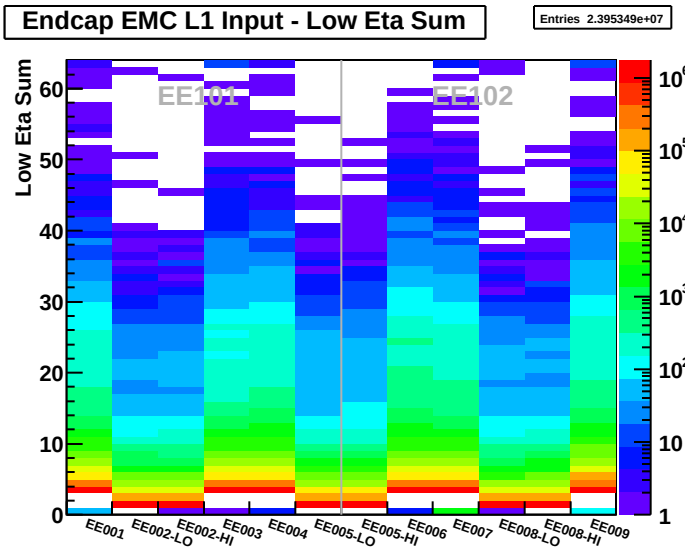

Barrel EMC L1 Input - Low Eta Sum
Entries 3829
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

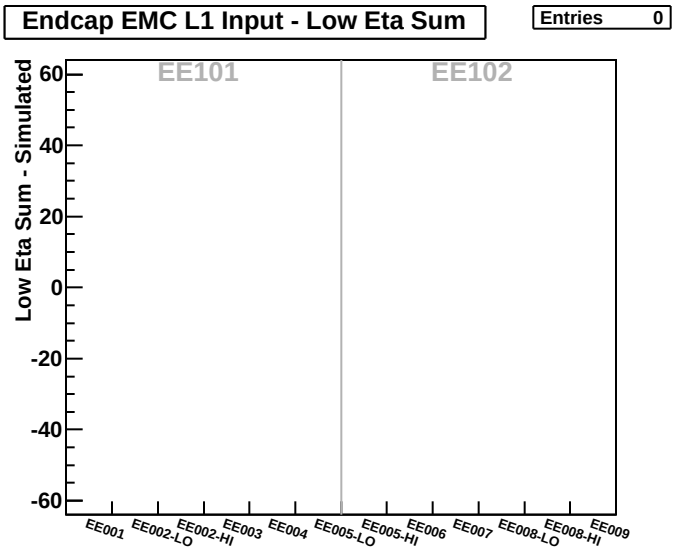

Barrel EMC L1 Input - High Eta Sum
Entries 7.186046e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

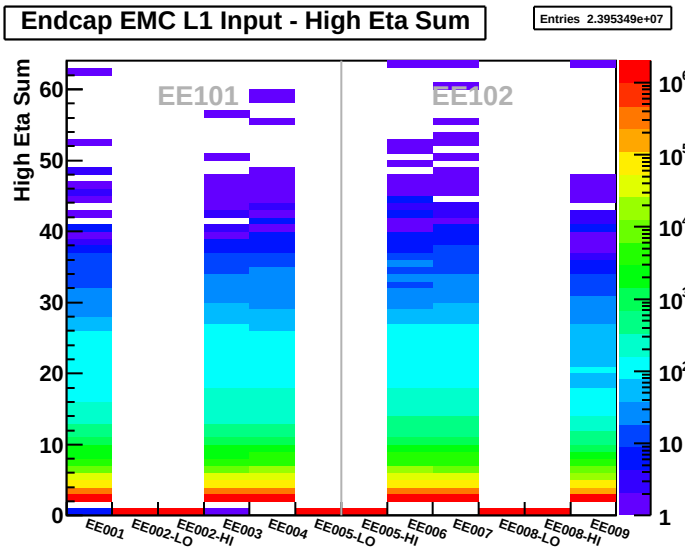

Barrel EMC L1 Input - High Eta Sum
Entries 999
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

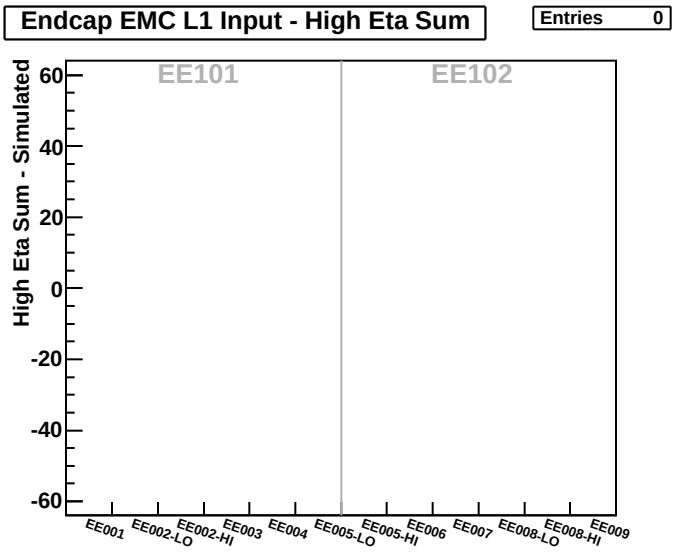

Barrel EMC L1 Input - High Tower Bits
Entries 7.186046e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

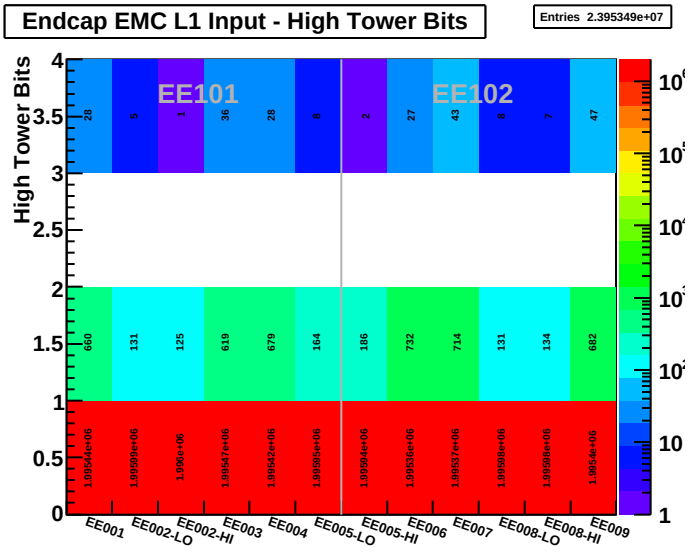

Barrel EMC L1 Input - High Tower Bits
Entries 26
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

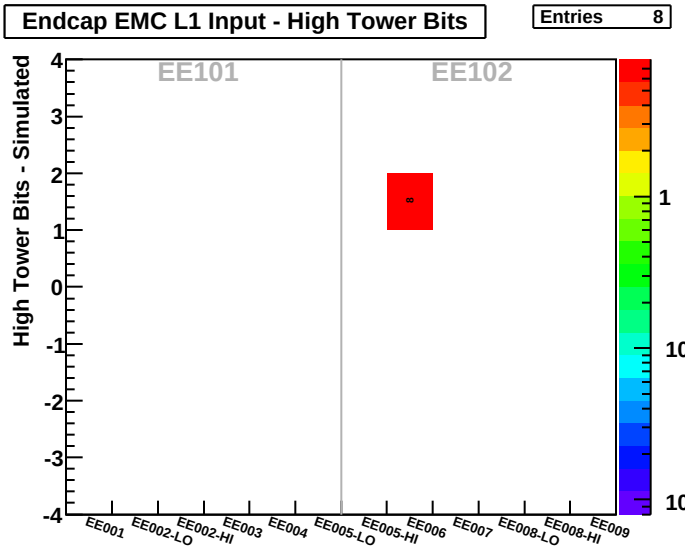

Endcap EMC L1 Input - Low Eta Sum
Entries 2.395349e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

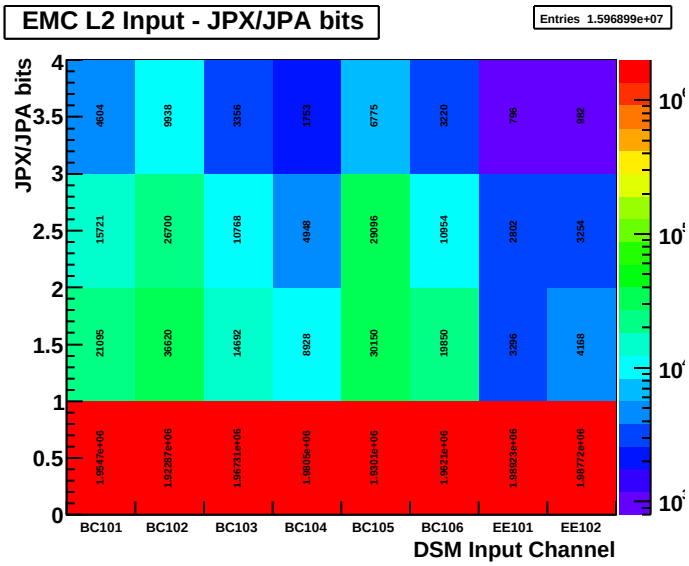

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

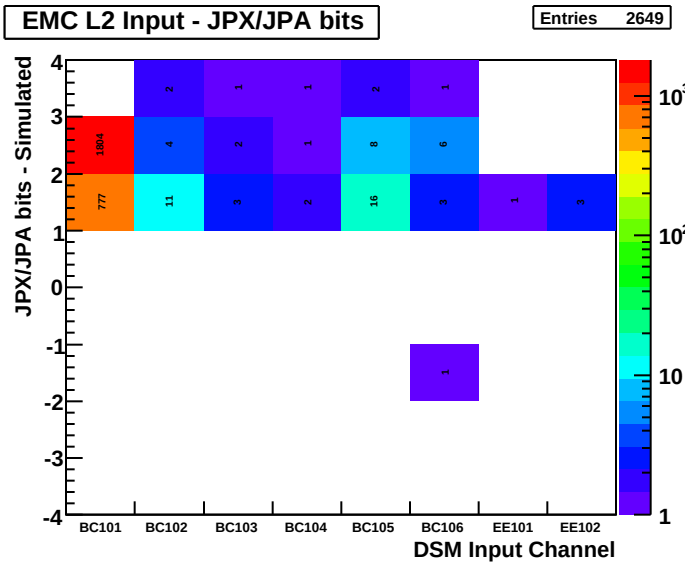

Endcap EMC L1 Input - High Eta Sum
Entries 2.395349e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

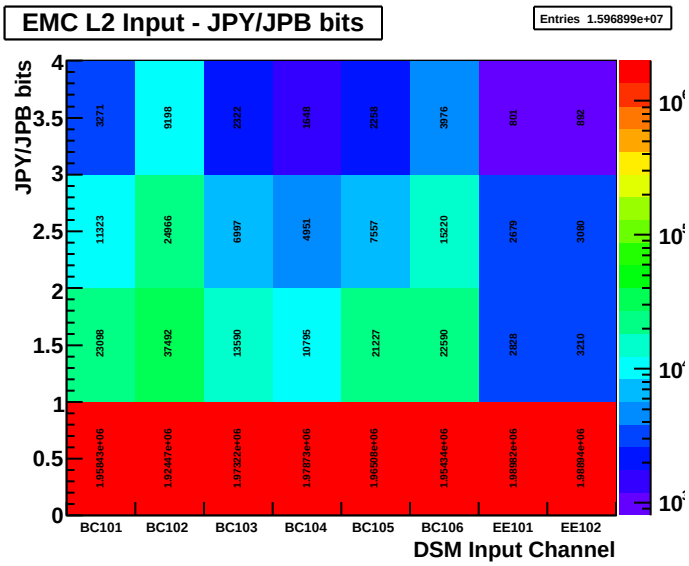

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

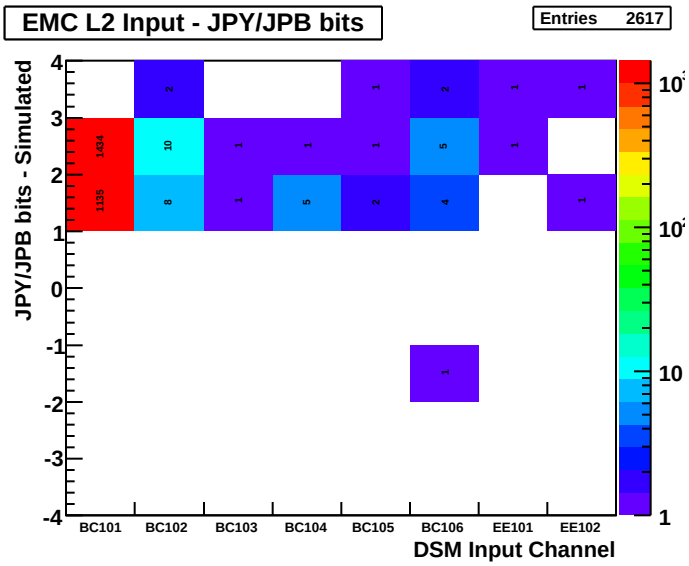

Endcap EMC L1 Input - High Tower Bits
Entries 2.395349e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

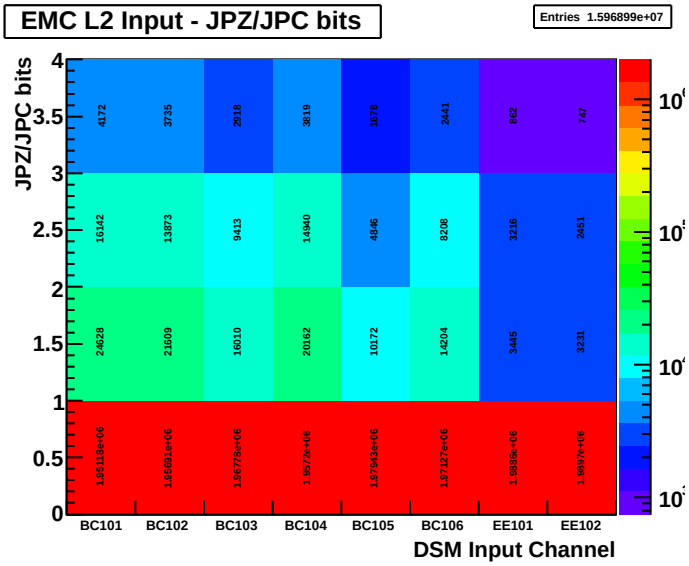

Endcap EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

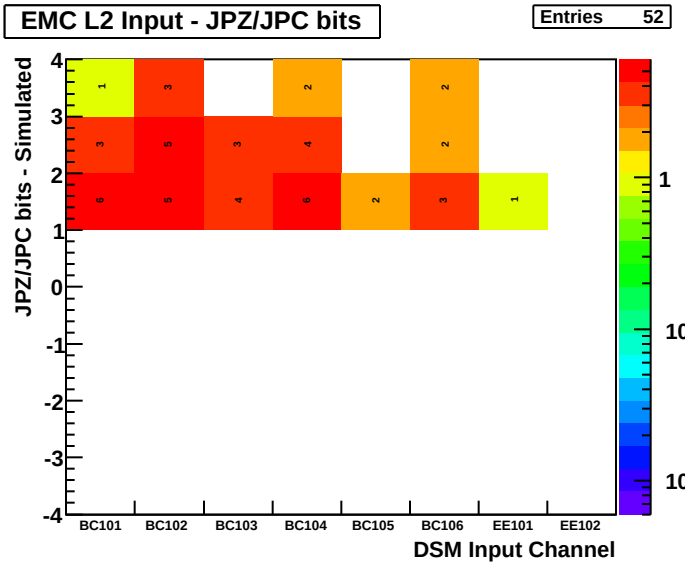

EMC L2 Input - JPX/JPA bits
Entries 1.596899e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

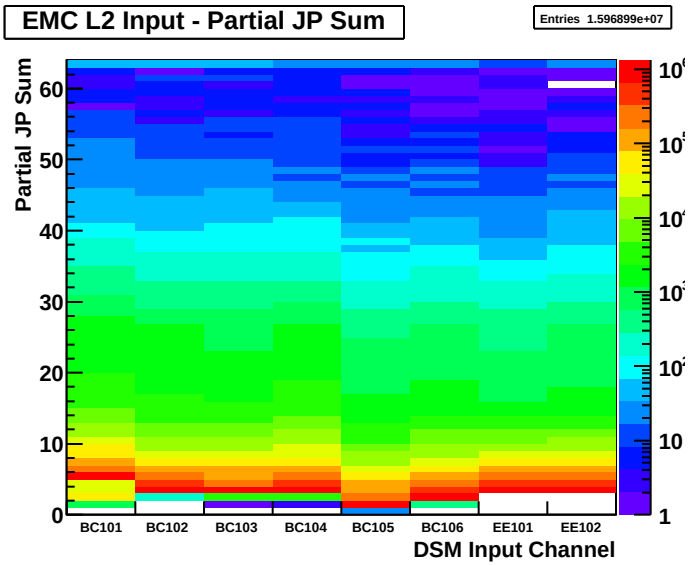

EMC L2 Input - JPX/JPA bits
Entries 2649
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

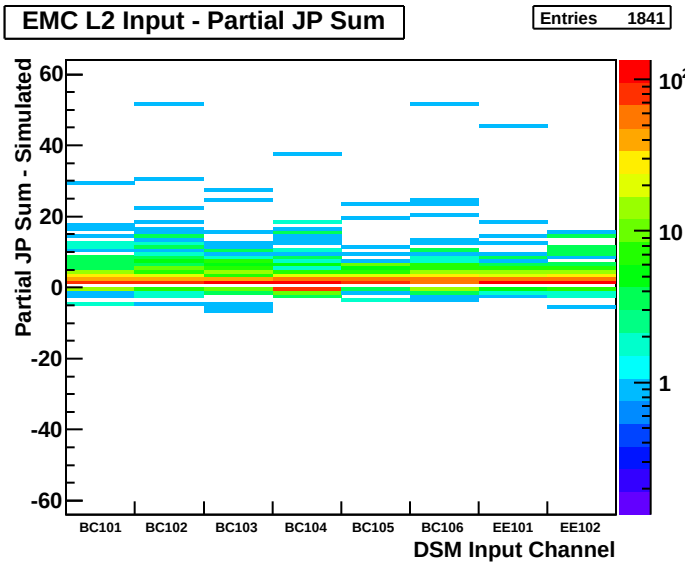

EMC L2 Input - JPY/JPB bits
Entries 1.596899e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

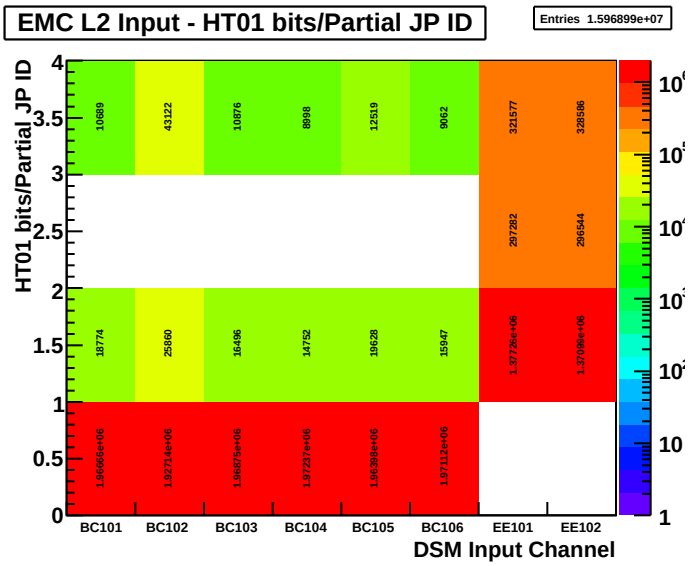

EMC L2 Input - JPY/JPB bits
Entries 2617
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

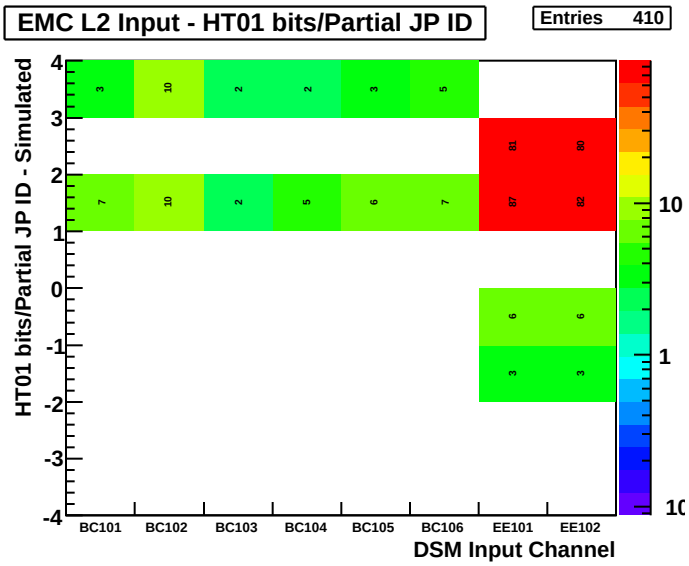

EMC L2 Input - JPZ/JPC bits
Entries 1.596899e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

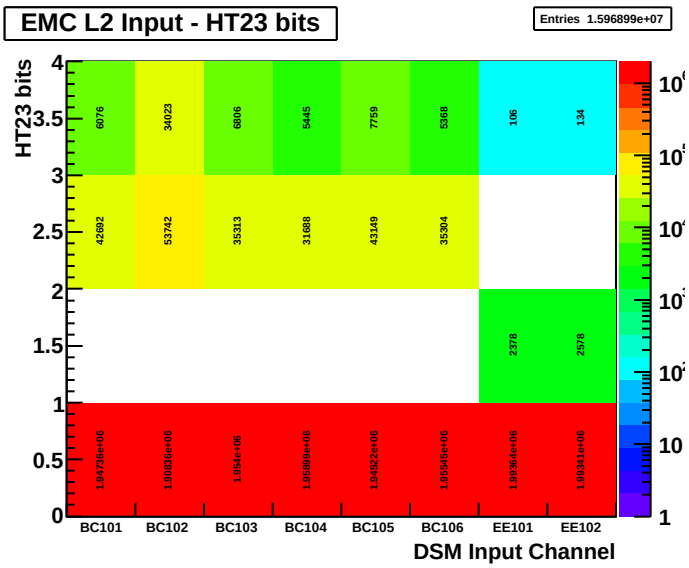

EMC L2 Input - JPZ/JPC bits
Entries 52
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

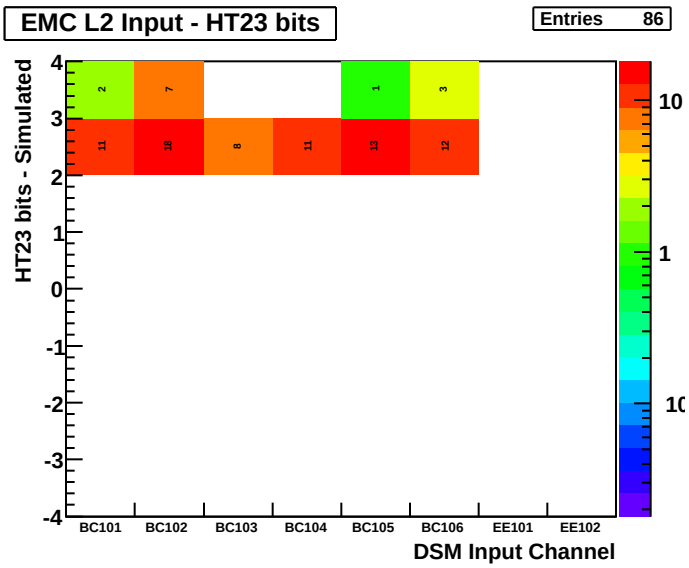

EMC L2 Input - Partial JP Sum
Entries 1.596899e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

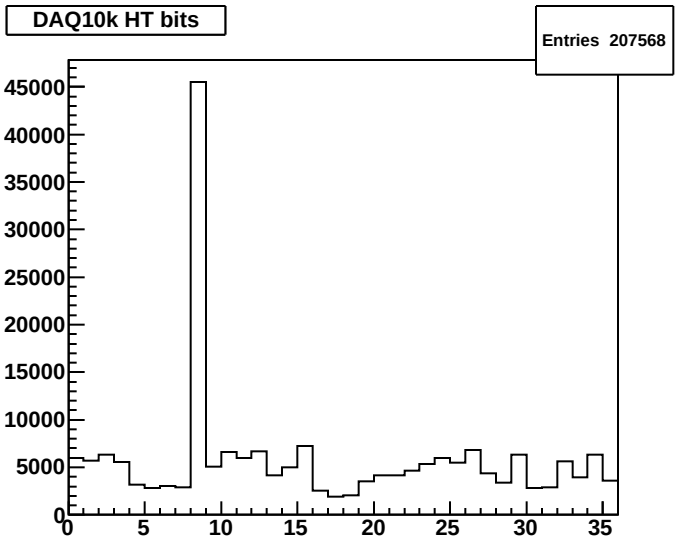

EMC L2 Input - Partial JP Sum
Entries 1841
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

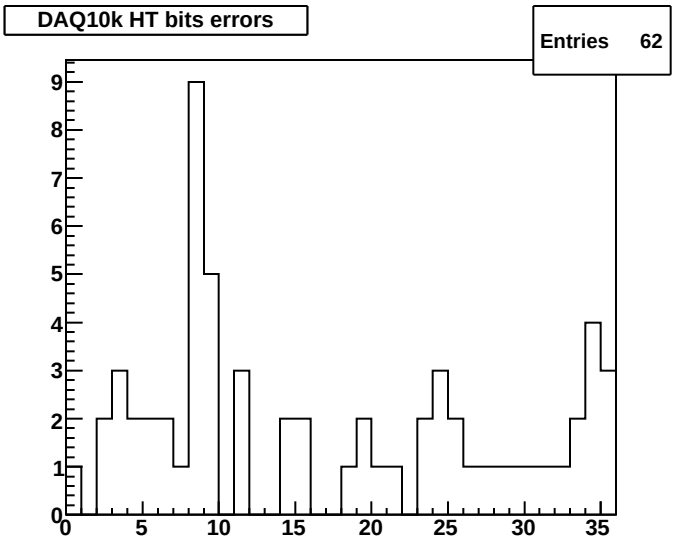

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.596899e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

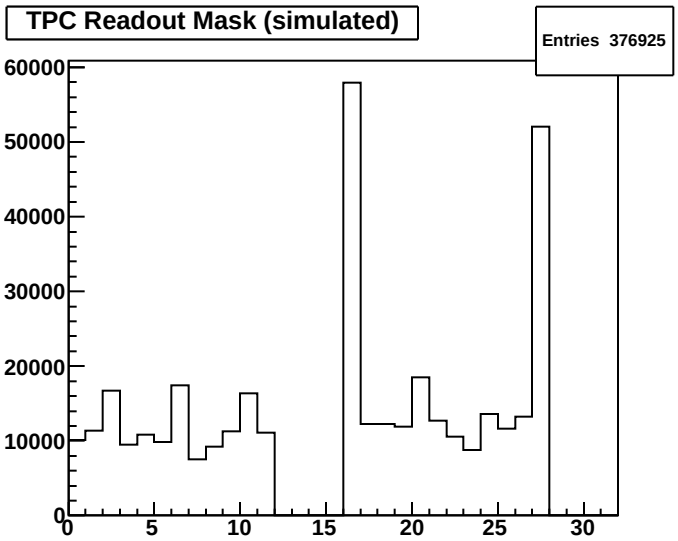

EMC L2 Input - HT01 bits/Partial JP ID
Entries 410
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

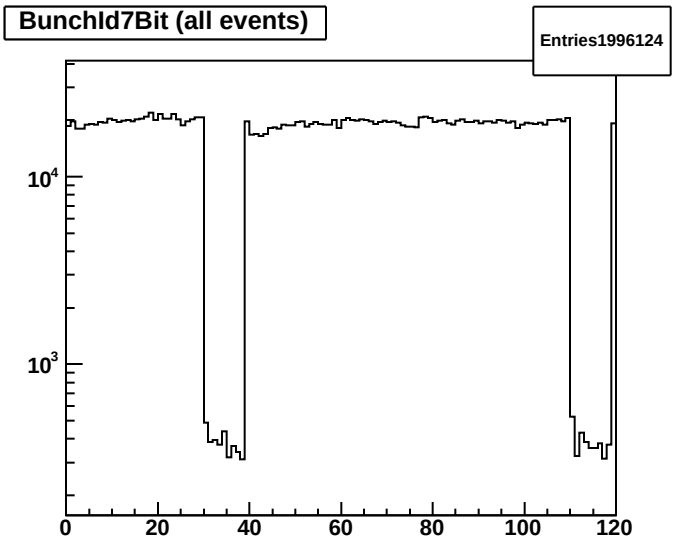

EMC L2 Input - HT23 bits
Entries 1.596899e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

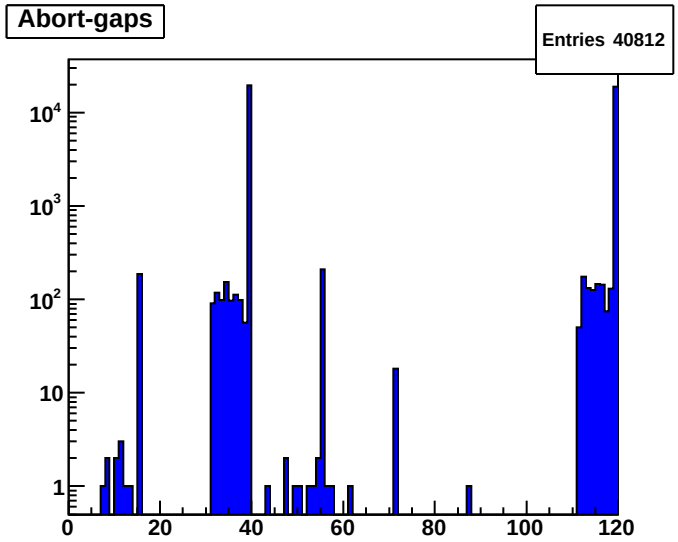

EMC L2 Input - HT23 bits
Entries 86
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

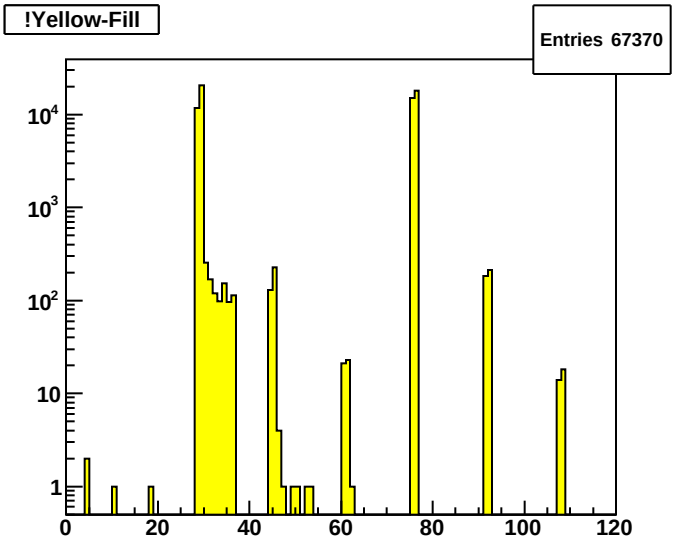

DAQ10k HT bits
Entries 207568

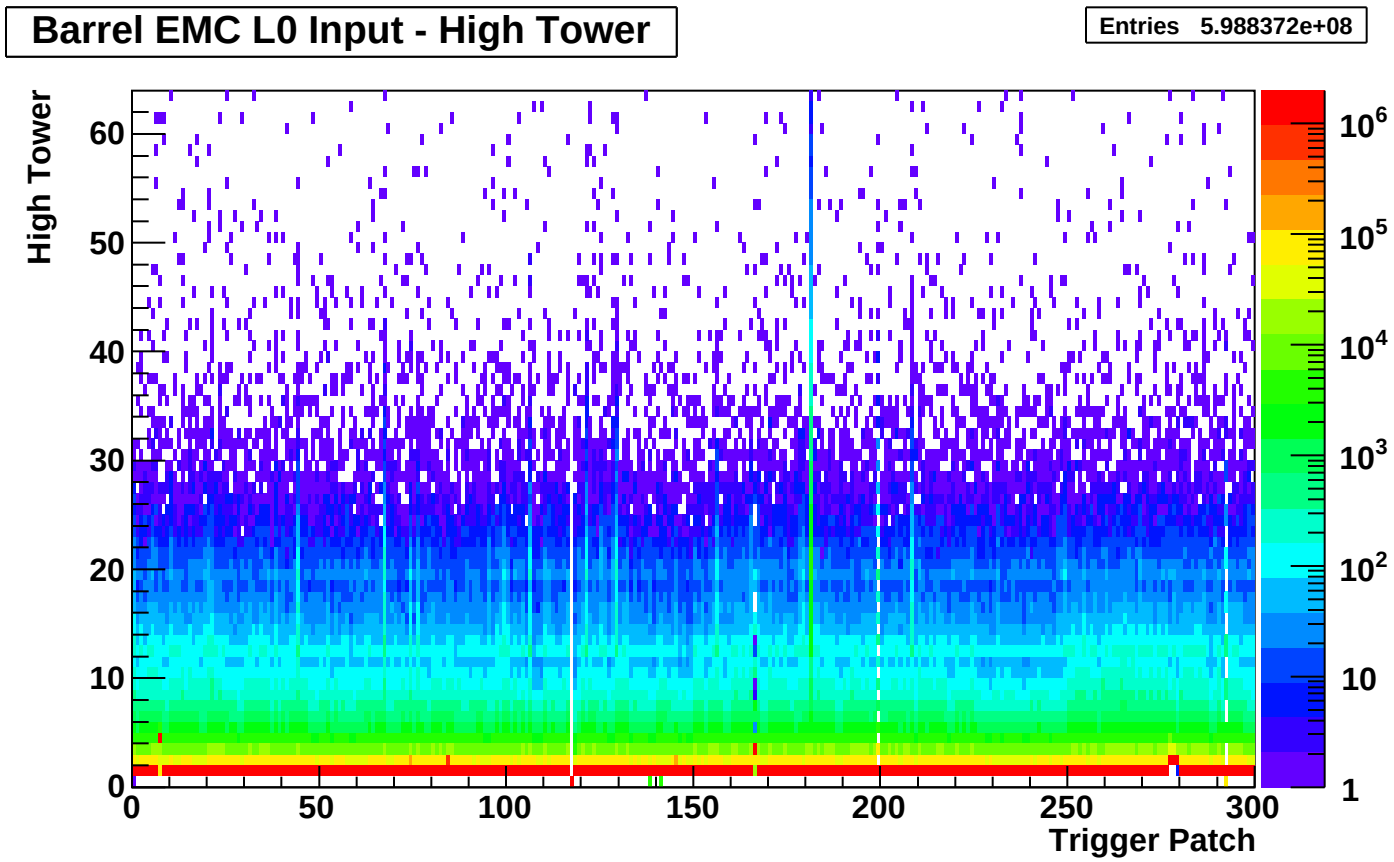

DAQ10k HT bits errors
Entries 62

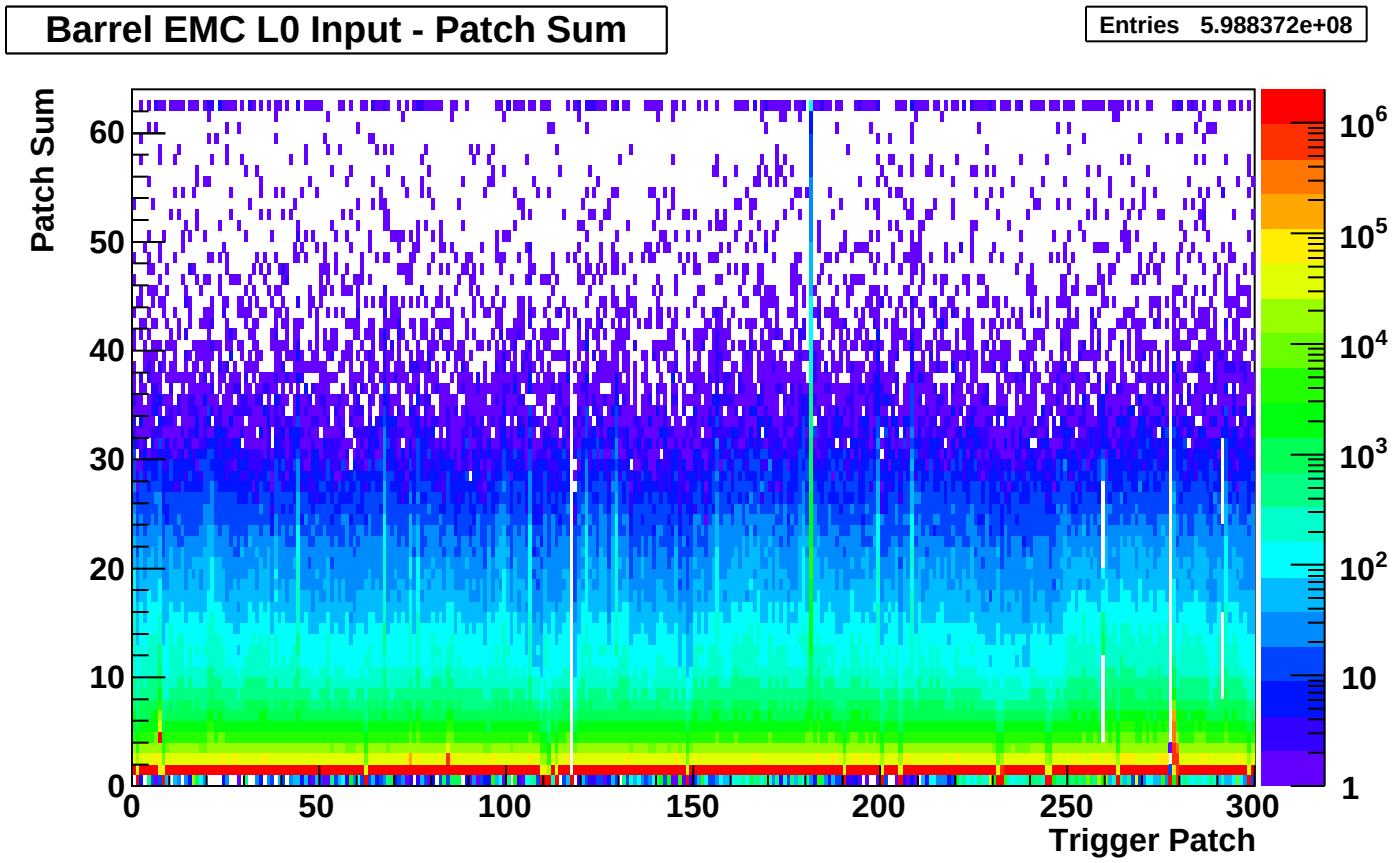

TPC Readout Mask (simulated)
Entries 376925

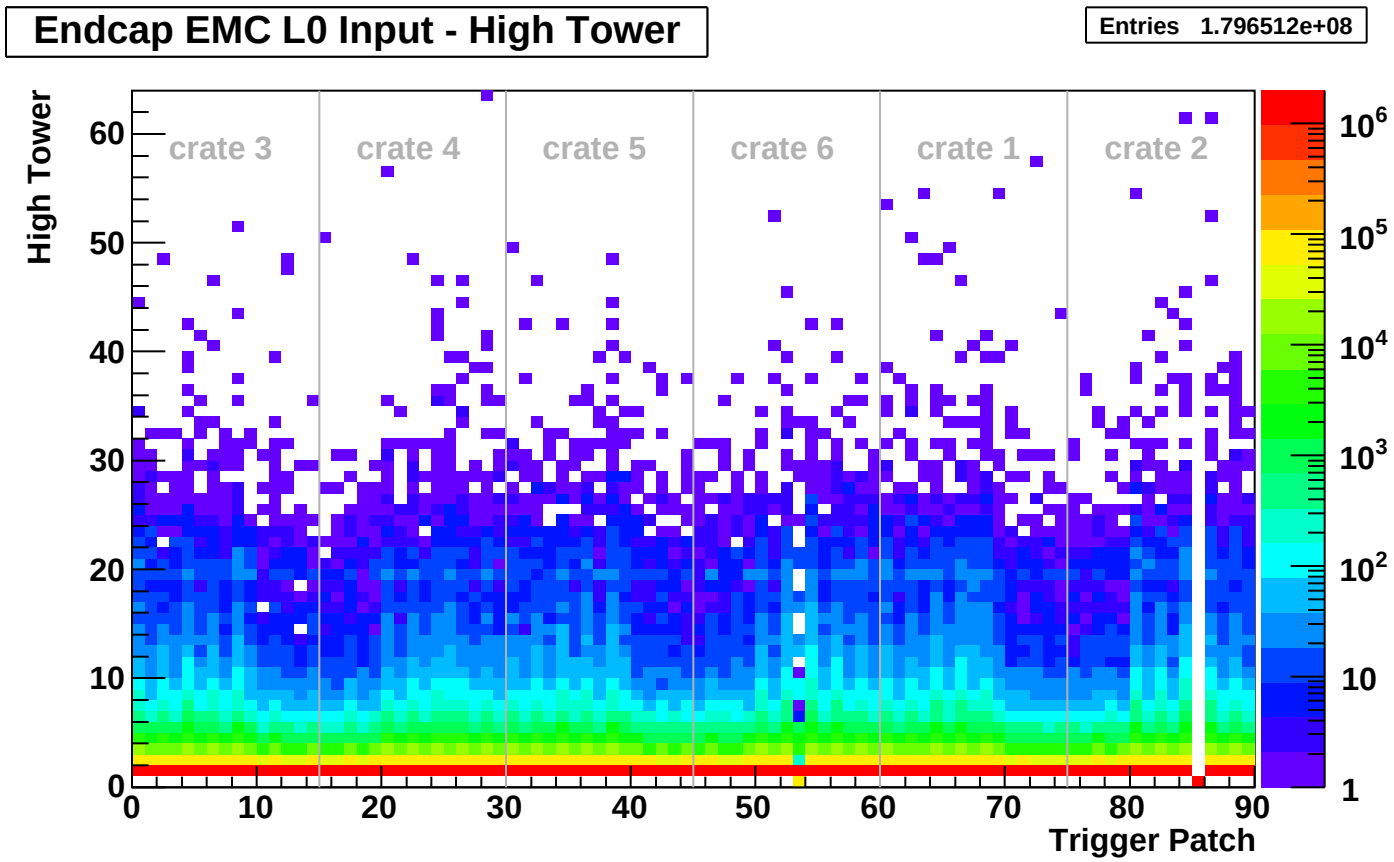

BunchId7Bit (all events)
Entries1996124

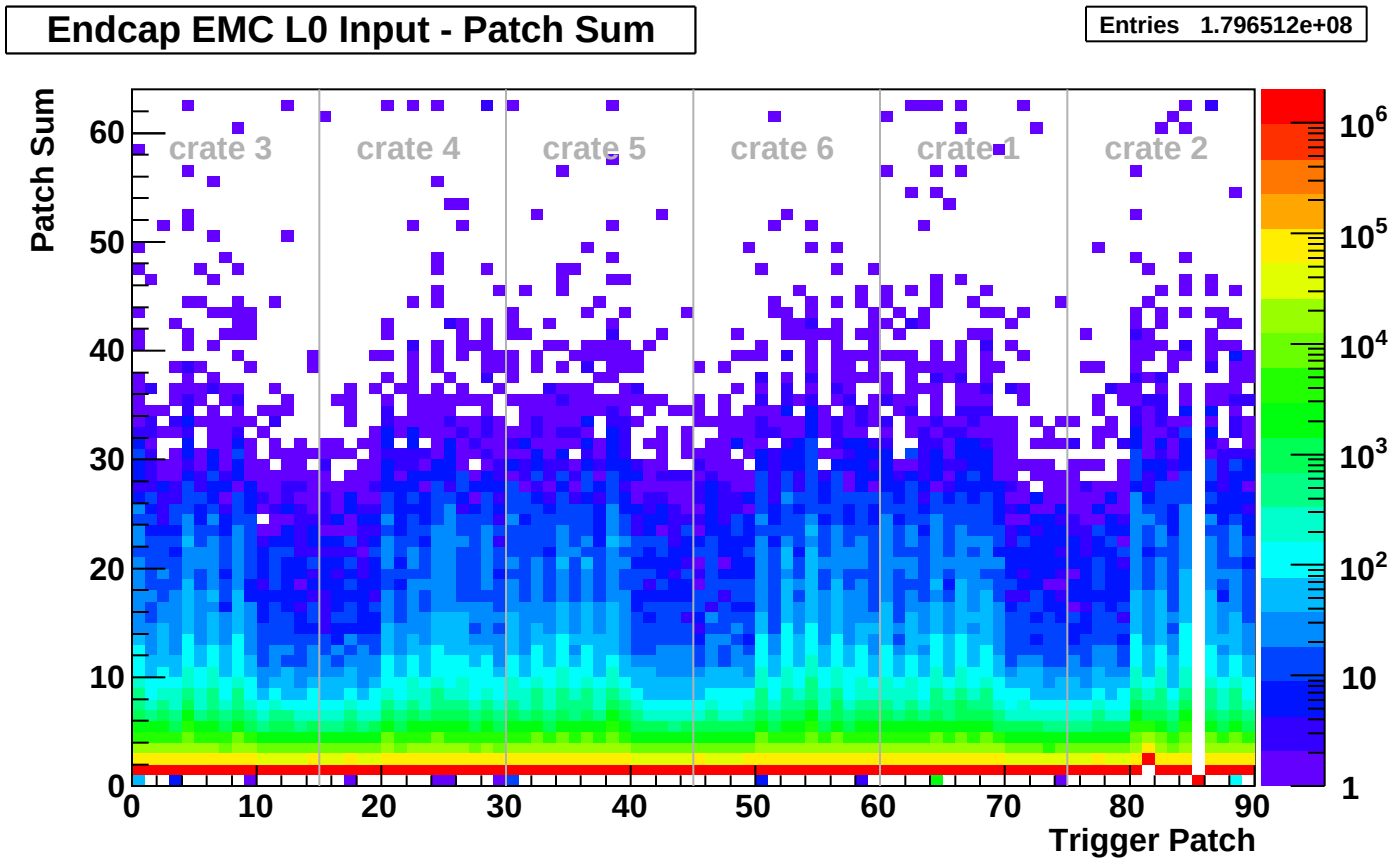

Abort-gaps
Entries 40812

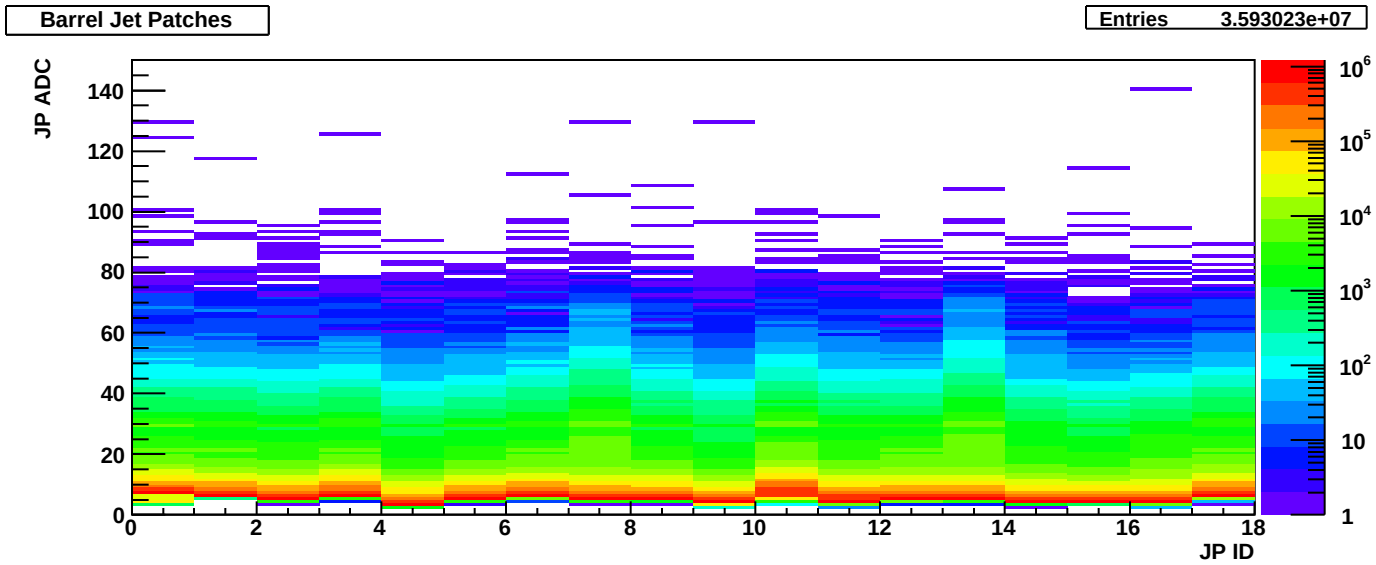

!Yellow-Fill
Entries 67370

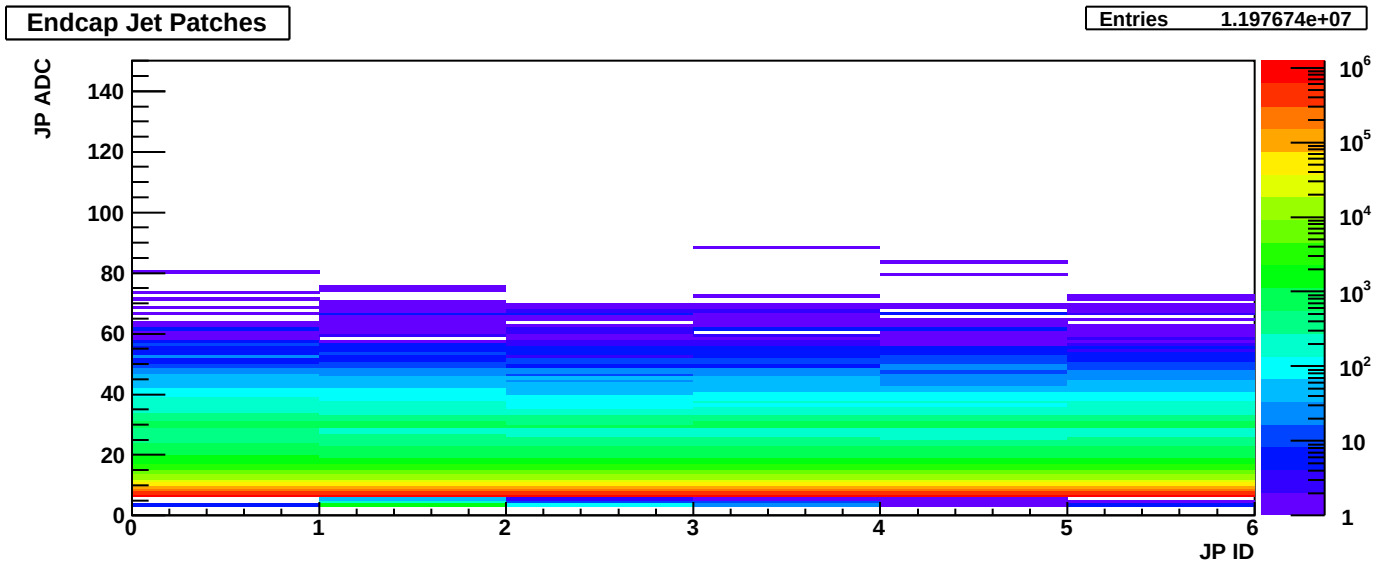

Barrel EMC L0 Input - High Tower
Entries 5.988372e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

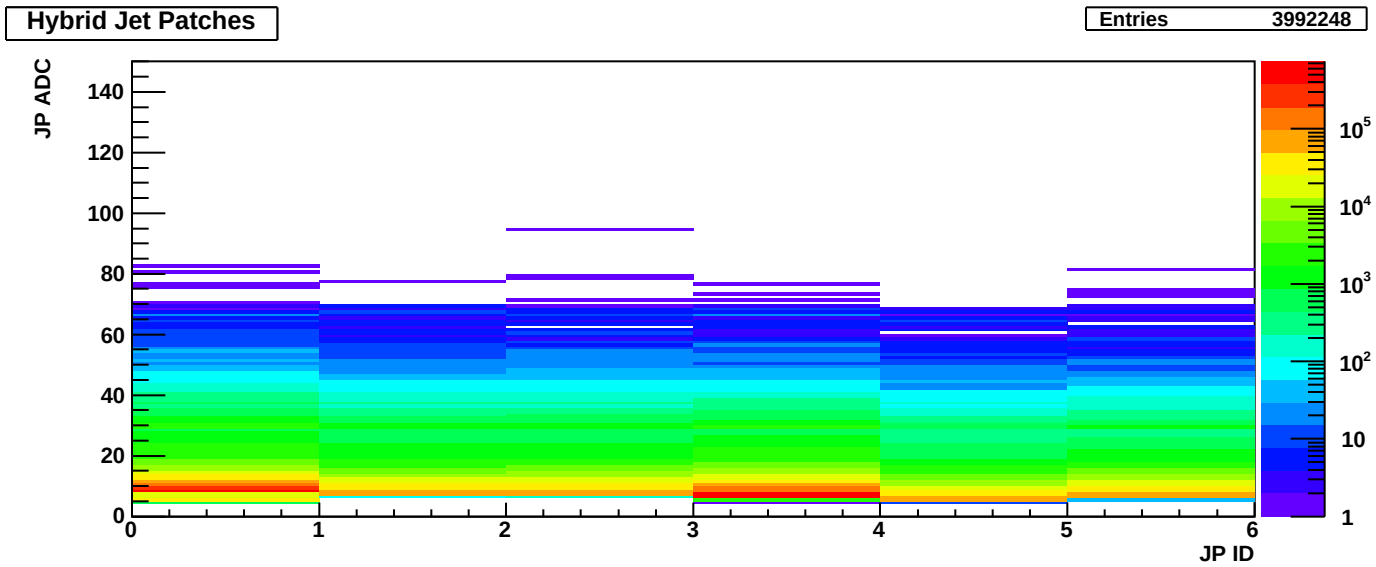

Barrel EMC L0 Input - Patch Sum
Entries 5.988372e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

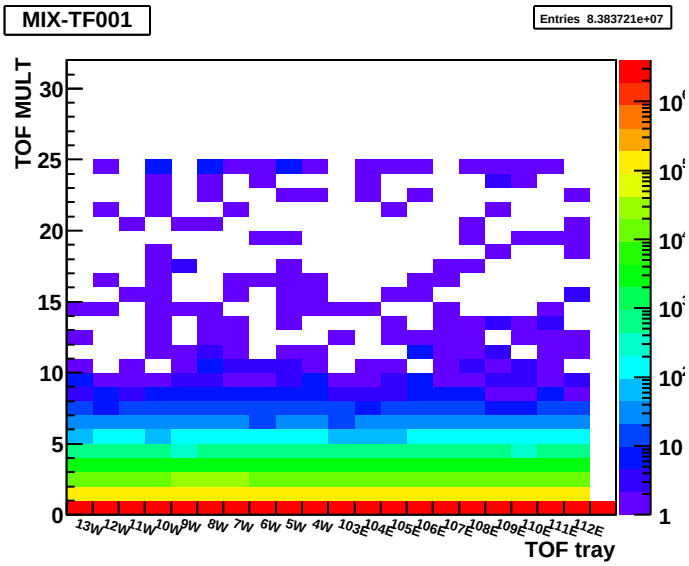

Endcap EMC L0 Input - High Tower
Entries 1.796512e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

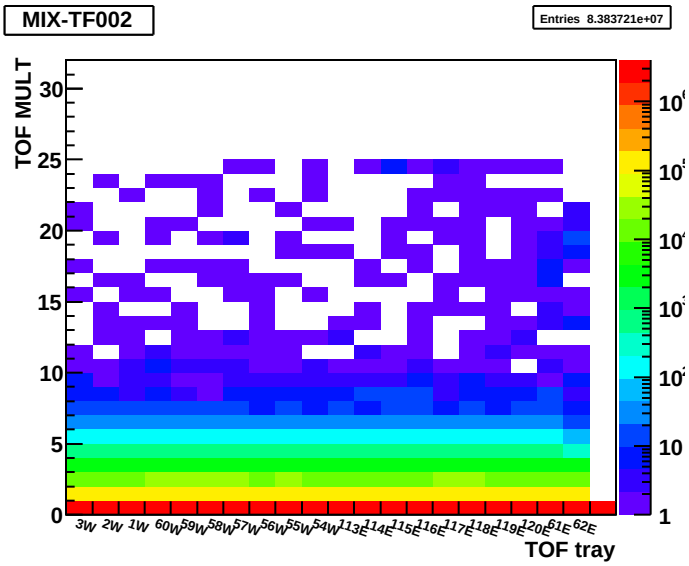

Endcap EMC L0 Input - Patch Sum
Entries 1.796512e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

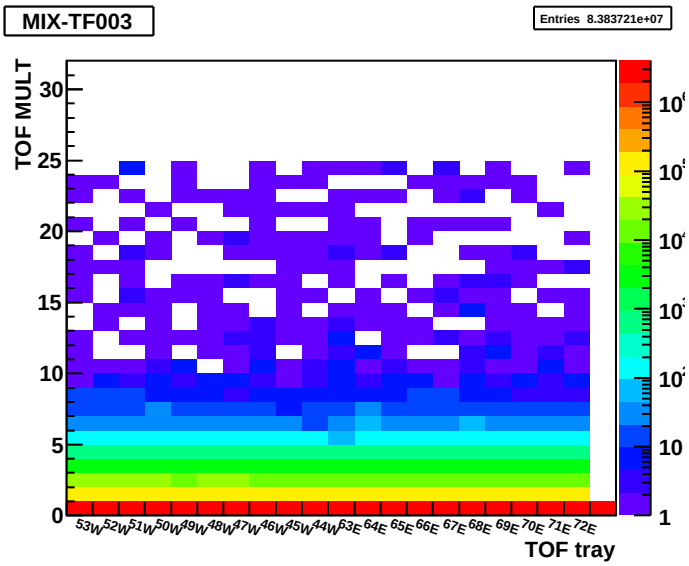

Barrel Jet Patches
Entries 3.593023e+07
JP ADC
JP ID

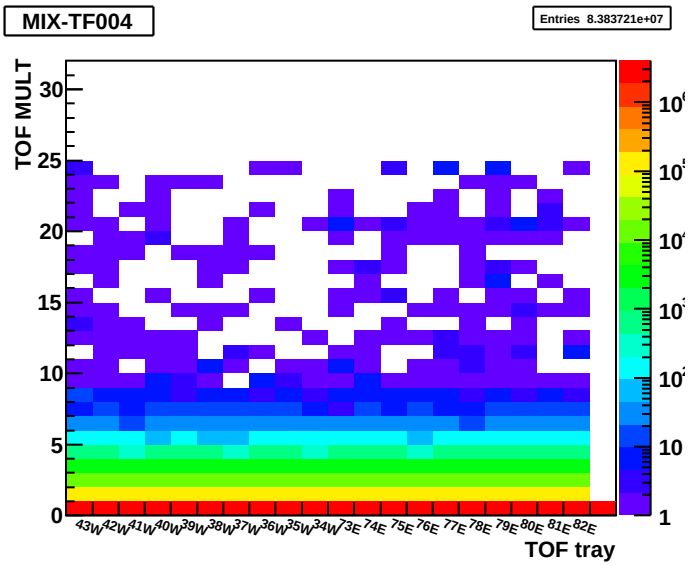

Endcap Jet Patches
Entries 1.197674e+07
JP ADC
JP ID

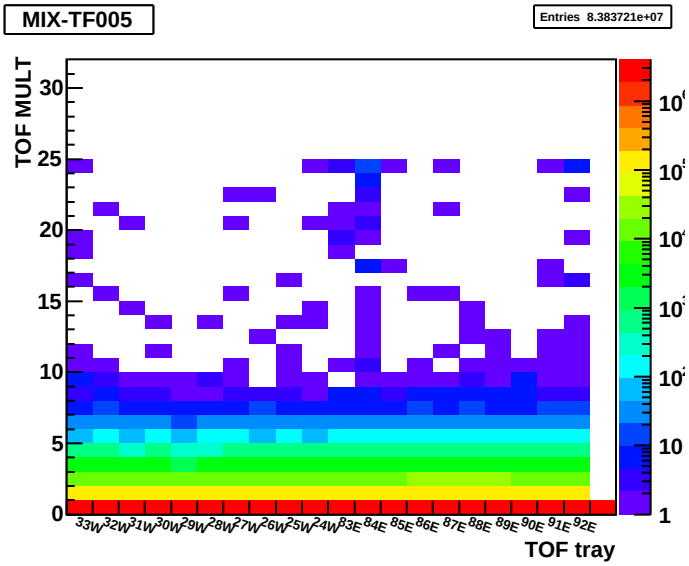

Hybrid Jet Patches
Entries 3992248
JP ADC
JP ID

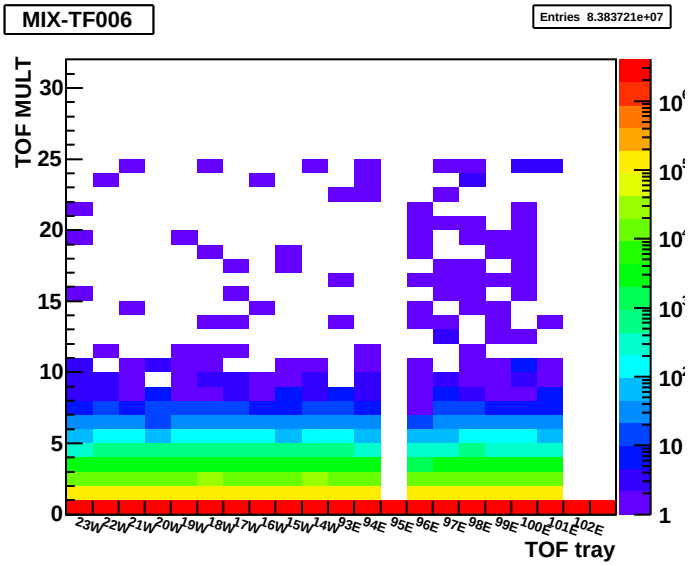

MIX-TF001
Entries 8.383721e+07
TOF MULT
TOF tray

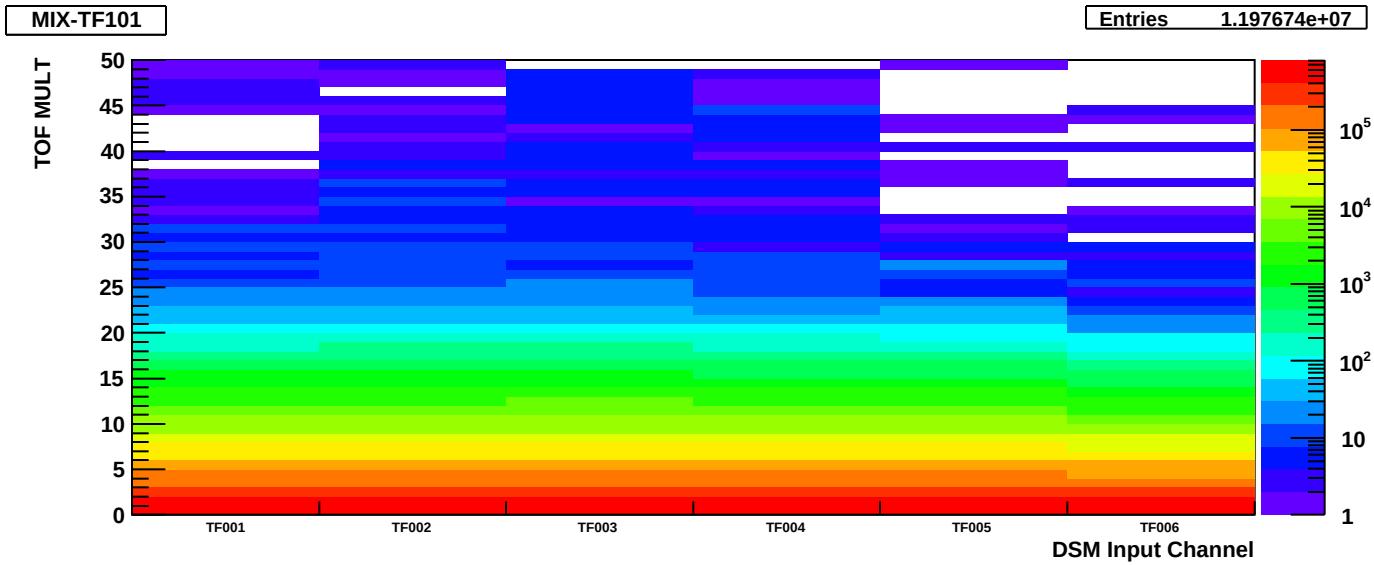

MIX-TF002
Entries 8.383721e+07
TOF MULT
TOF tray

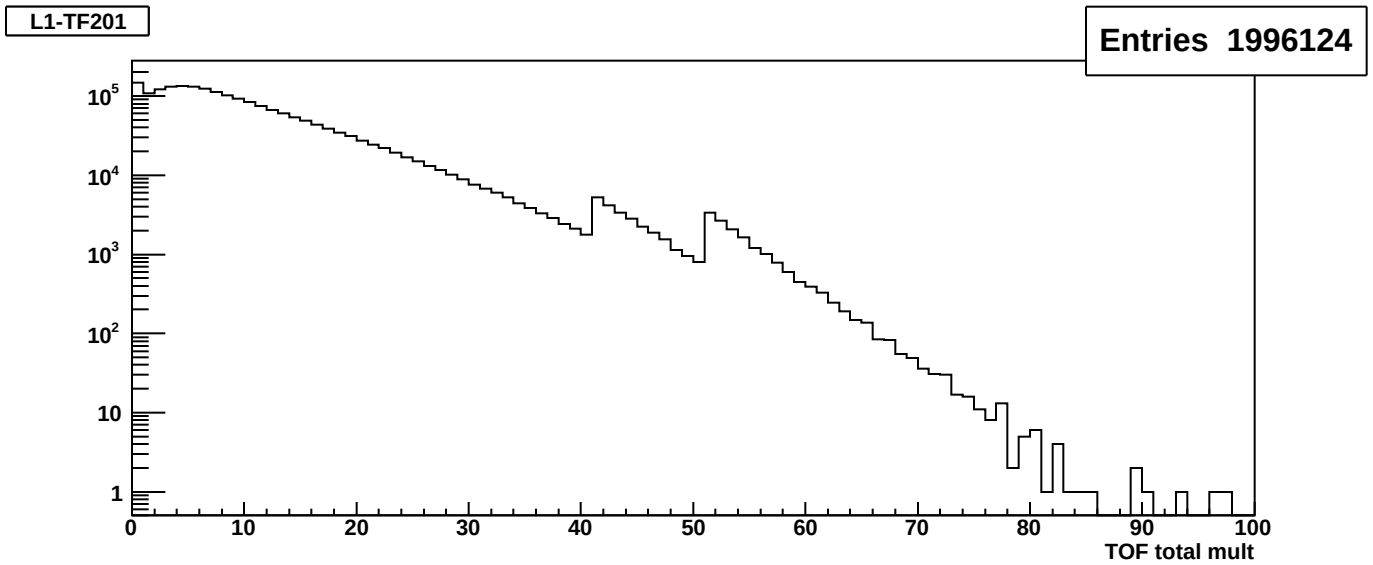

MIX-TF003
Entries 8.383721e+07
TOF MULT
TOF tray

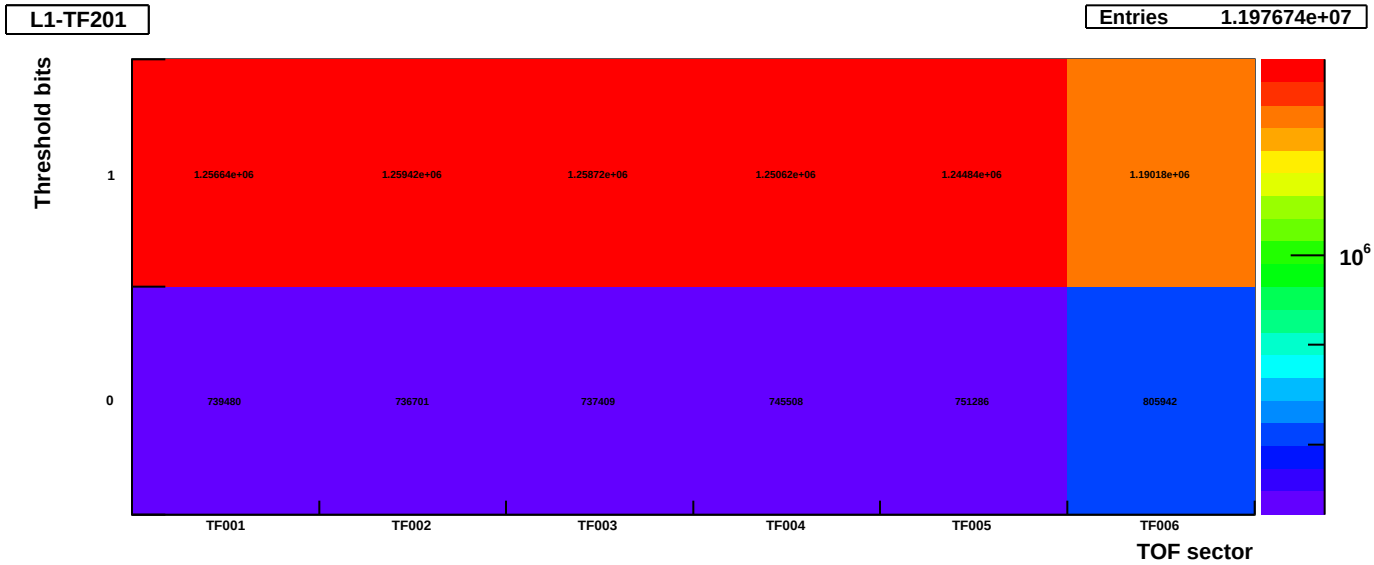

MIX-TF004
Entries 8.383721e+07
TOF MULT
TOF tray

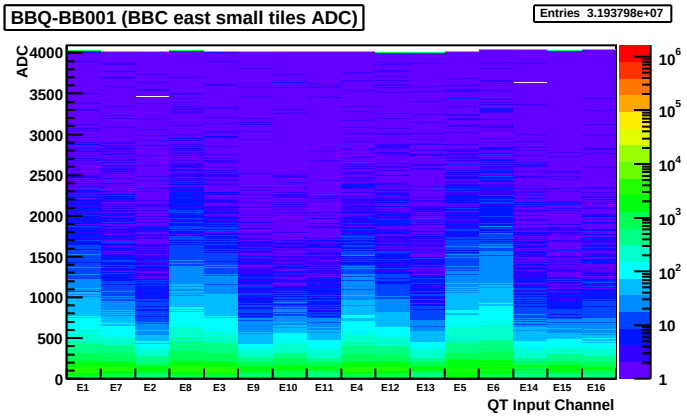

MIX-TF005
Entries 8.383721e+07
TOF MULT
TOF tray

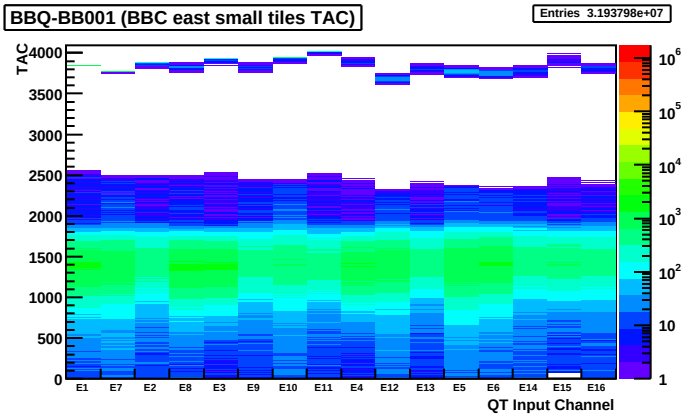

MIX-TF006
Entries 8.383721e+07
TOF MULT
TOF tray

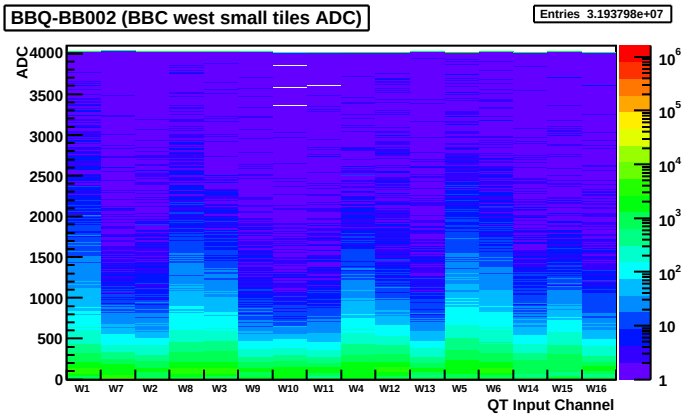

MIX-TF101
Entries 1.197674e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

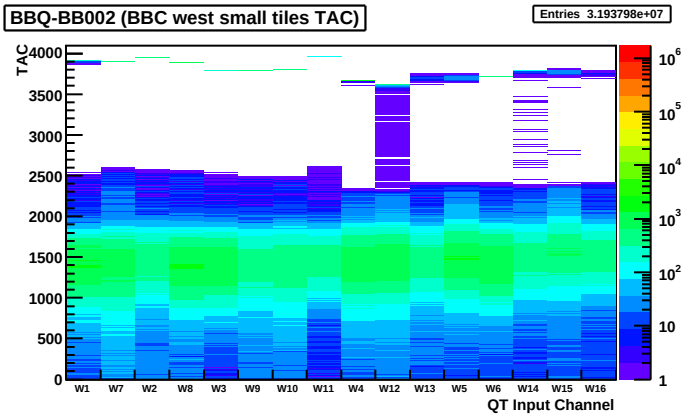

L1-TF201
Entries 1996124
TOF total mult

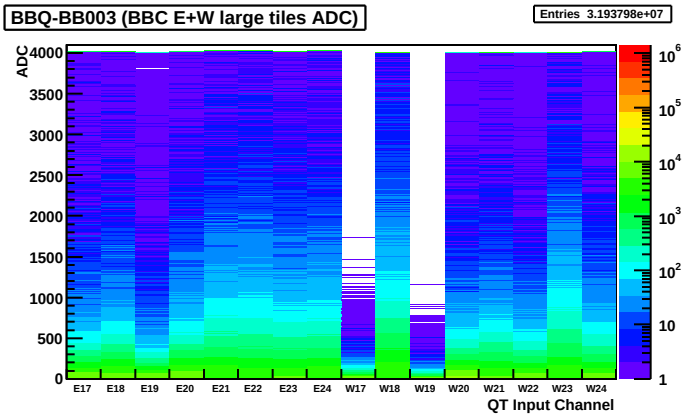

L1-TF201
Entries 1.197674e+07
Threshold bits
1.25664e+06
1.25942e+06
1.25872e+06
1.25062e+06
1.24484e+06
1.19018e+06
739480
736701
737409
745508
751286
805942
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

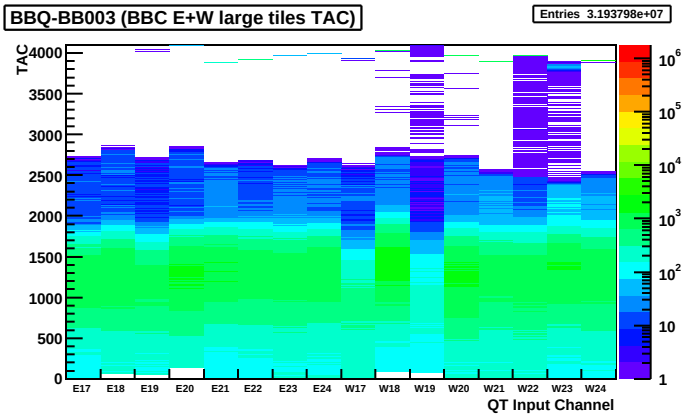

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.193798e+07
ADC
QT Input Channel

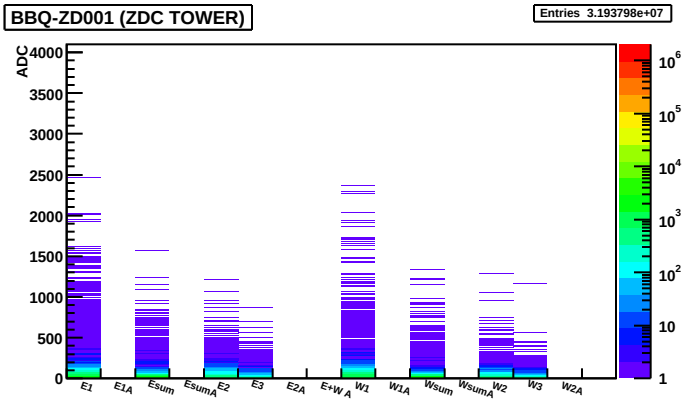

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.193798e+07
TAC
QT Input Channel

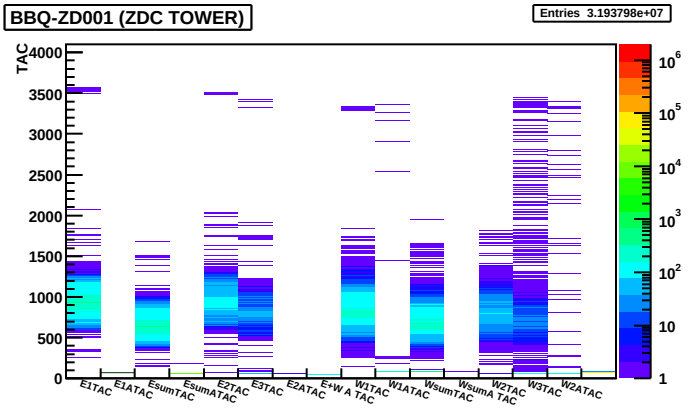

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.193798e+07
ADC
QT Input Channel

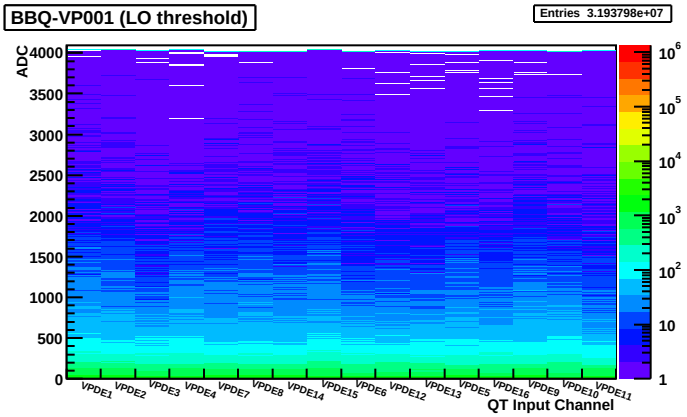

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.193798e+07
TAC
QT Input Channel

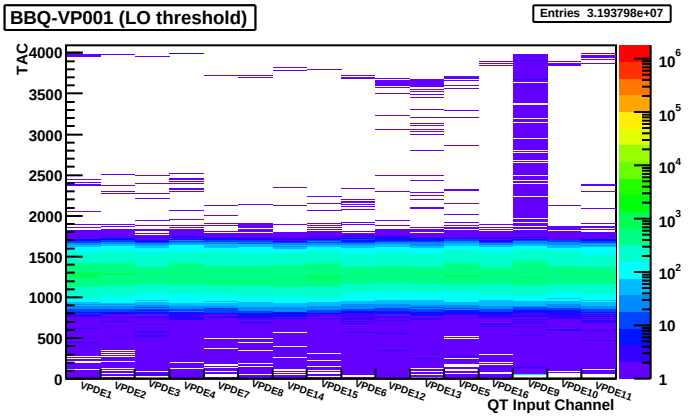

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.193798e+07
ADC
QT Input Channel

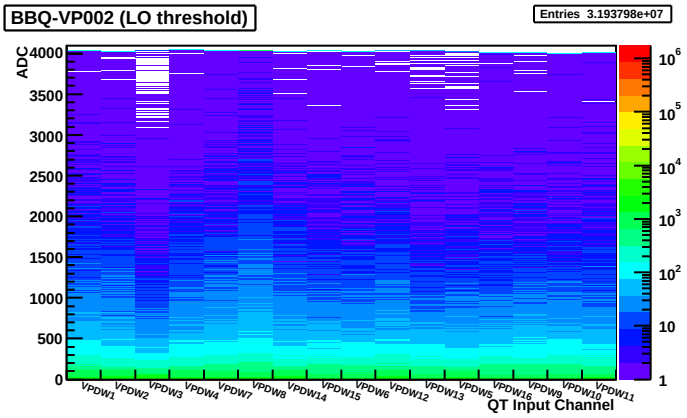

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.193798e+07
TAC
QT Input Channel

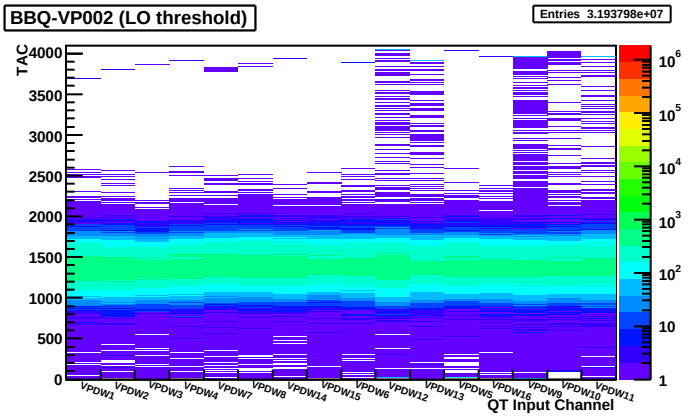

BBQ-ZD001 (ZDC TOWER)
Entries 3.193798e+07
ADC

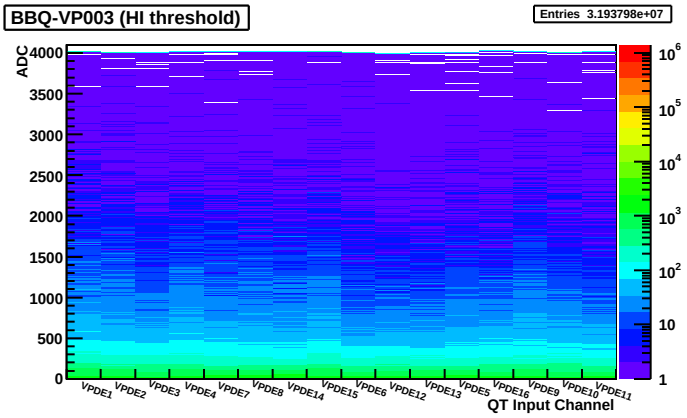

BBQ-ZD001 (ZDC TOWER)
Entries 3.193798e+07
TAC

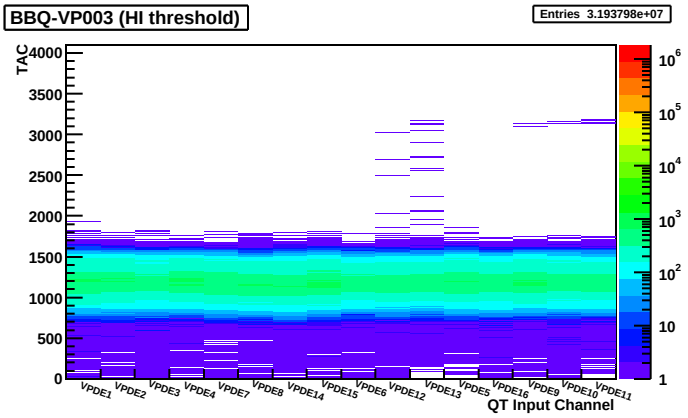

BBQ-VP001 (LO threshold)
Entries 3.193798e+07
ADC
QT Input Channel

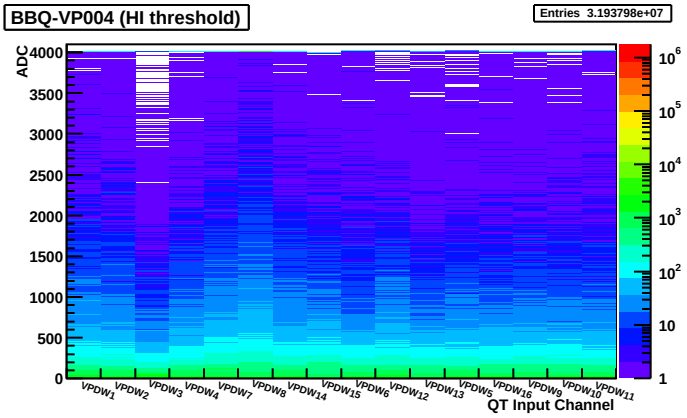

BBQ-VP001 (LO threshold)
Entries 3.193798e+07
TAC
QT Input Channel

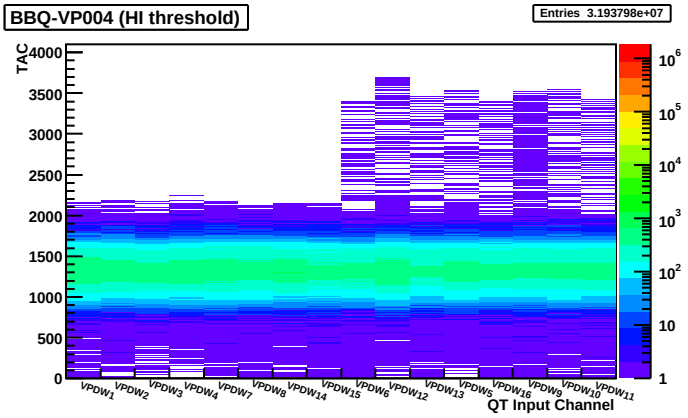

BBQ-VP002 (LO threshold)
Entries 3.193798e+07
ADC
QT Input Channel

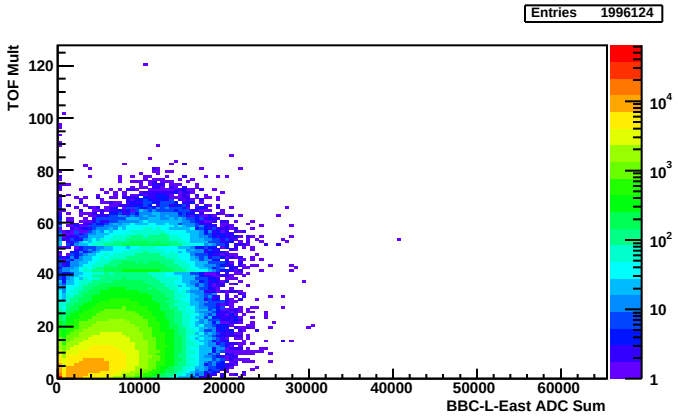

BBQ-VP002 (LO threshold)
Entries 3.193798e+07
TAC
QT Input Channel

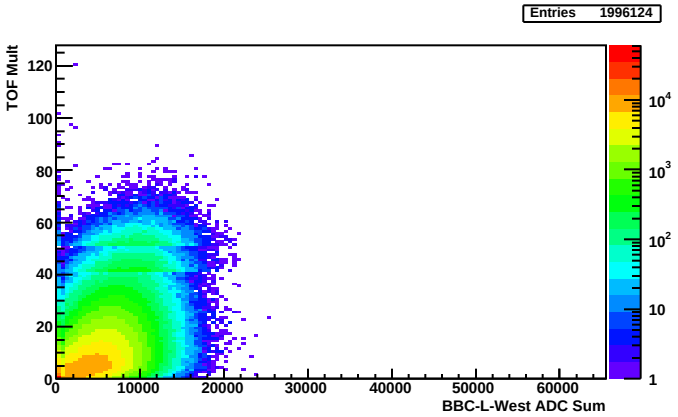

BBQ-VP003 (HI threshold)
Entries 3.193798e+07
ADC
QT Input Channel

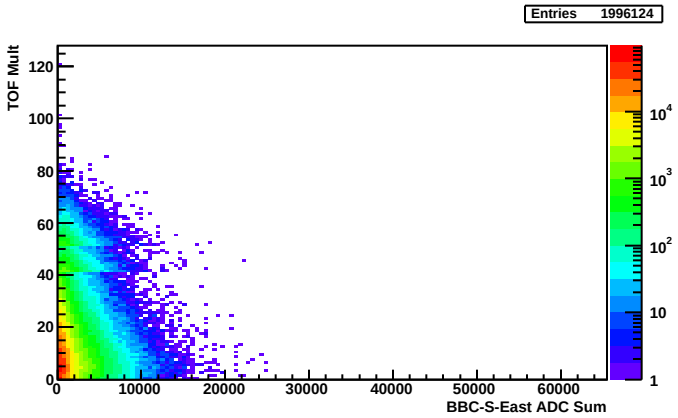

BBQ-VP003 (HI threshold)
Entries 3.193798e+07
TAC
QT Input Channel

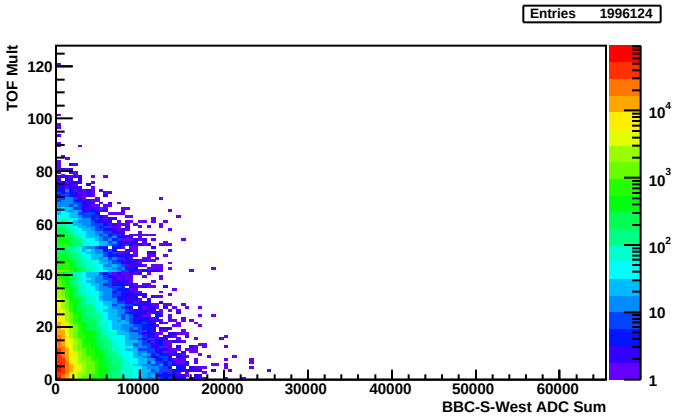

BBQ-VP004 (HI threshold)
Entries 3.193798e+07
ADC
QT Input Channel

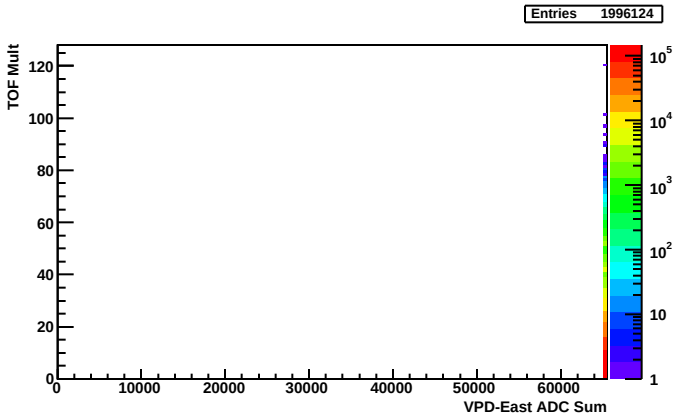

BBQ-VP004 (HI threshold)
Entries 3.193798e+07
TAC
QT Input Channel

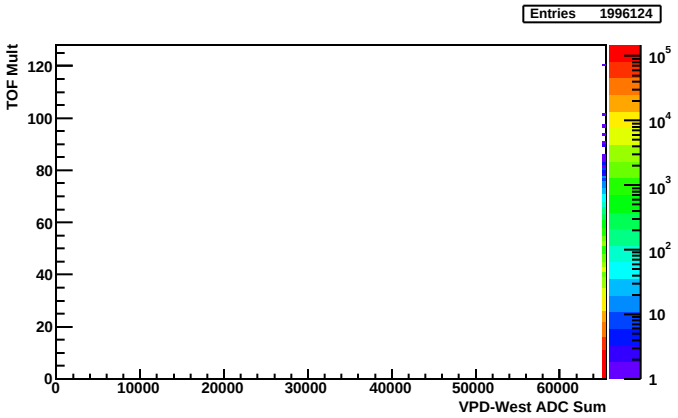

Entries 1996124
TOF Mult
BBC-L-East ADC Sum

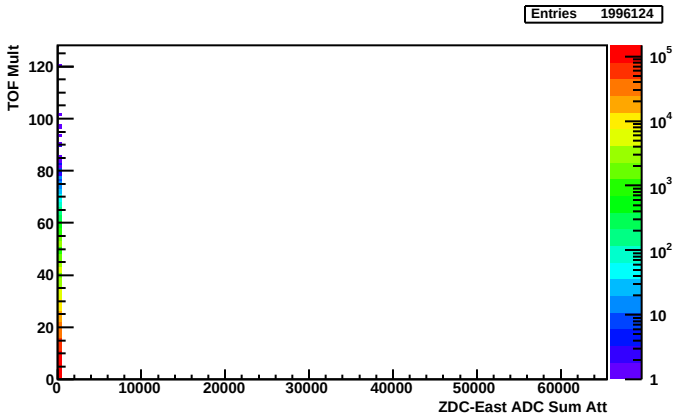

Entries 1996124
TOF Mult
BBC-L-West ADC Sum

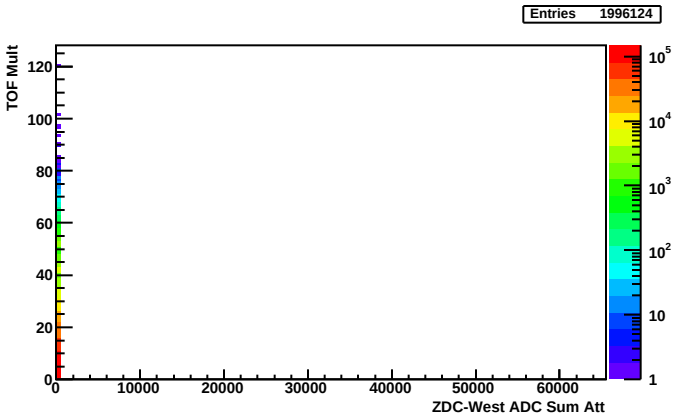

Entries 1996124
TOF Mult
BBC-S-East ADC Sum

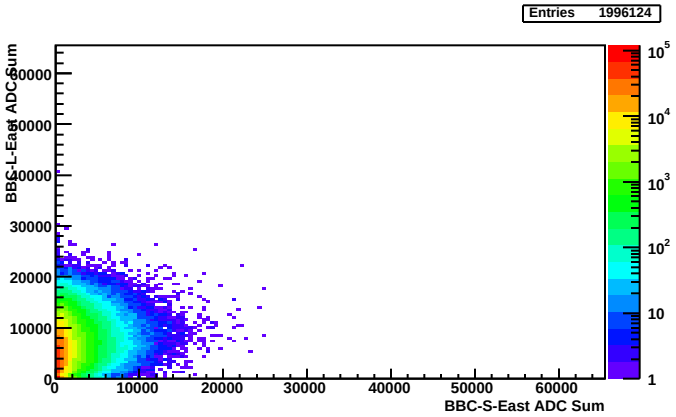

Entries 1996124
TOF Mult
BBC-S-West ADC Sum

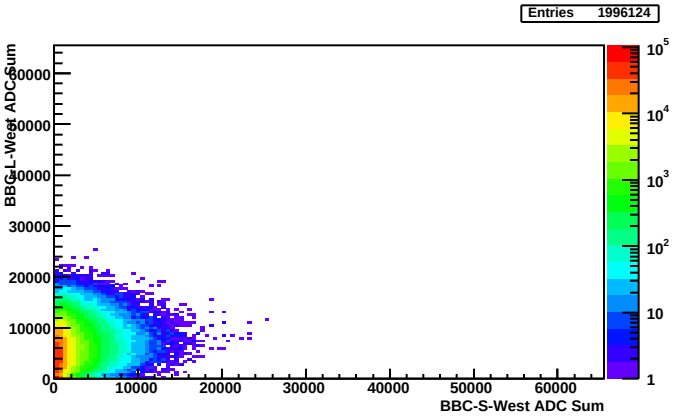

Entries 1996124
TOF Mult
VPD-East ADC Sum

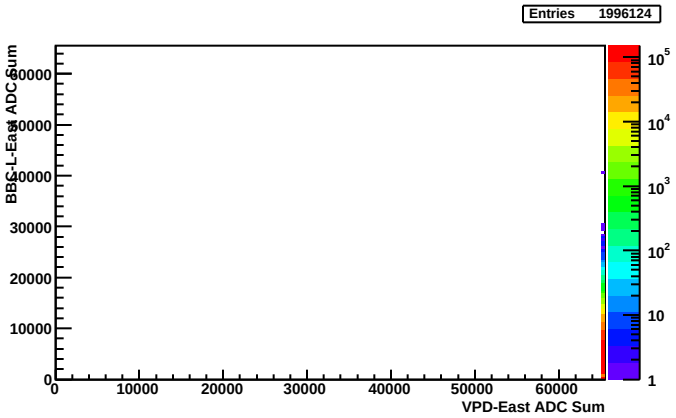

Entries 1996124
TOF Mult
VPD-West ADC Sum

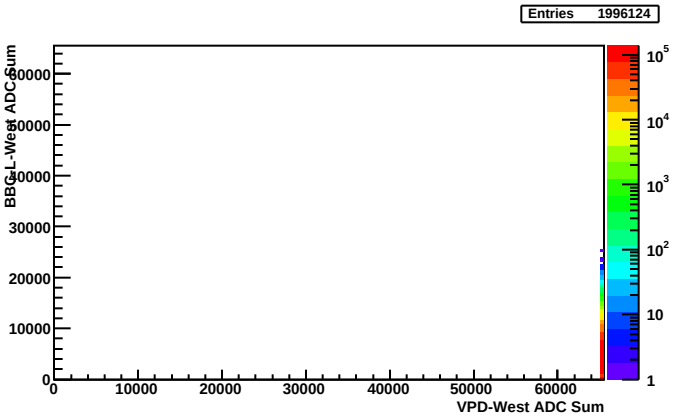

Entries 1996124
TOF Mult
ZDC-East ADC Sum Att


Entries 1996124
TOF Mult
ZDC-West ADC Sum Att


Entries 1996124
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1996124
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1996124
BBC-L-East ADC Sum
VPD-East ADC Sum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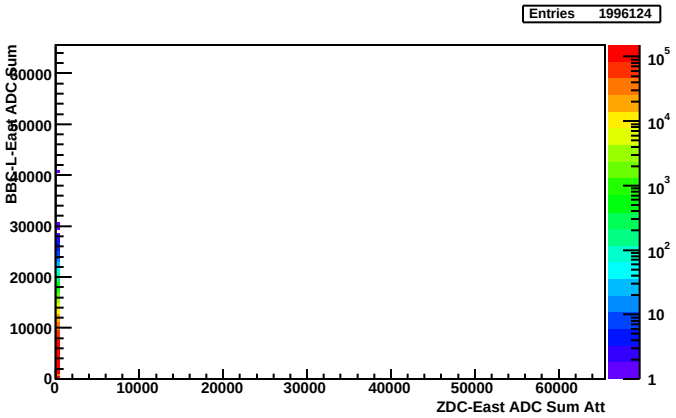
BBC-L-West ADC Sum
VPD-West ADC Sum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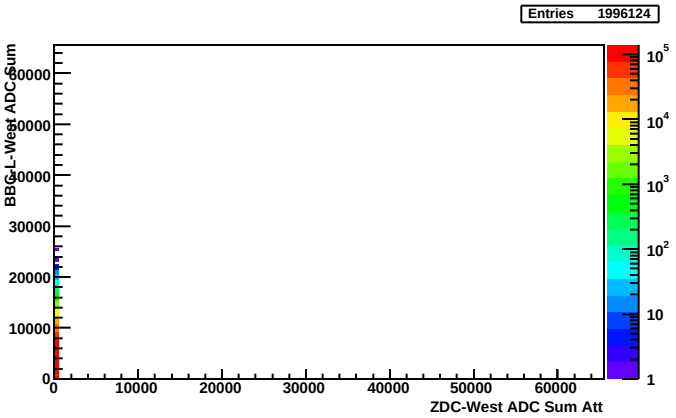
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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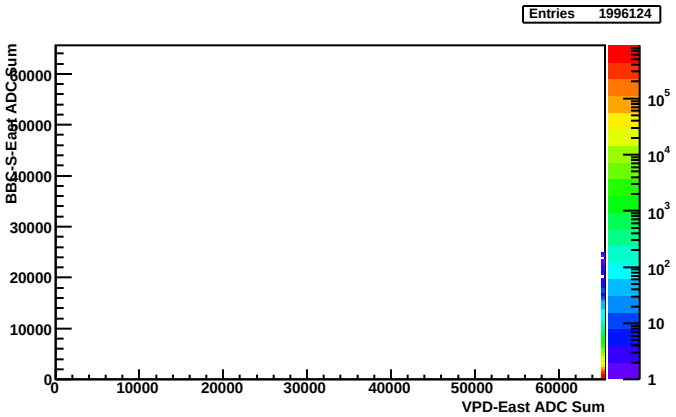
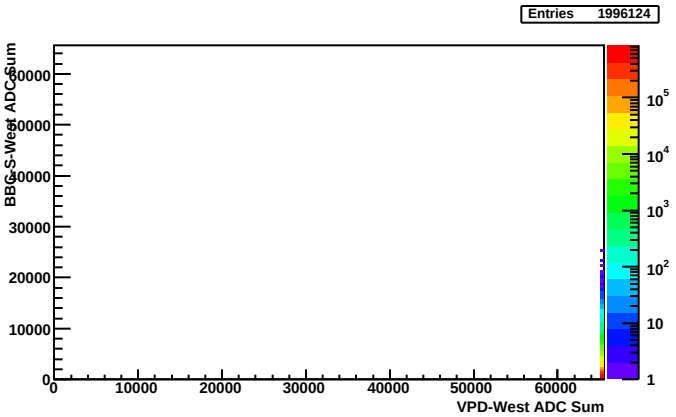
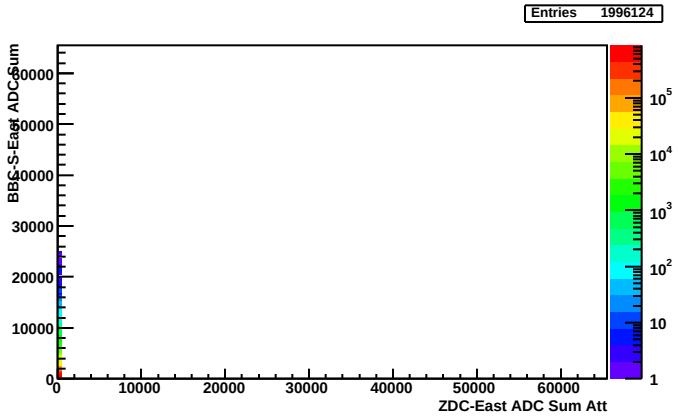
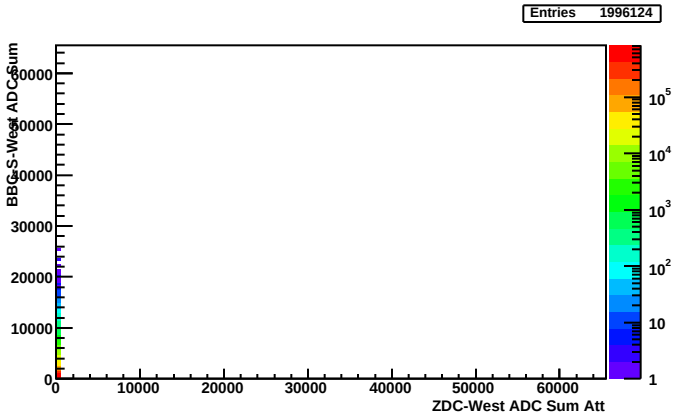
ZDC-West ADC Sum Att


Entries 1996124
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1996124
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1996124
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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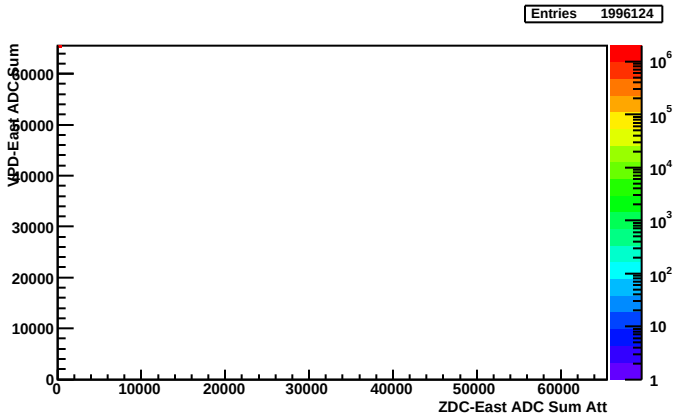
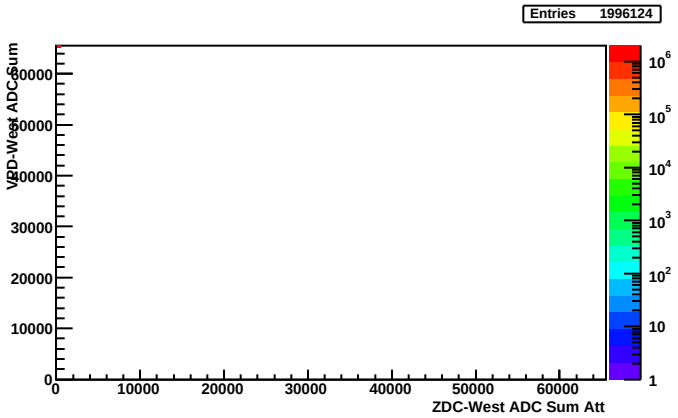
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1996124
VPD-East ADC Sum
ZDC-East ADC Sum Att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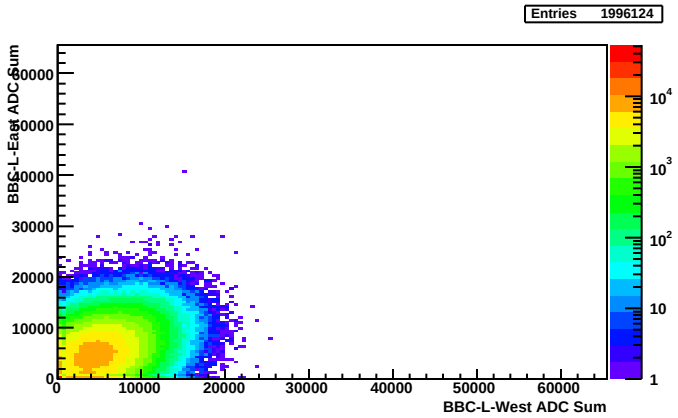
VPD-West ADC Sum
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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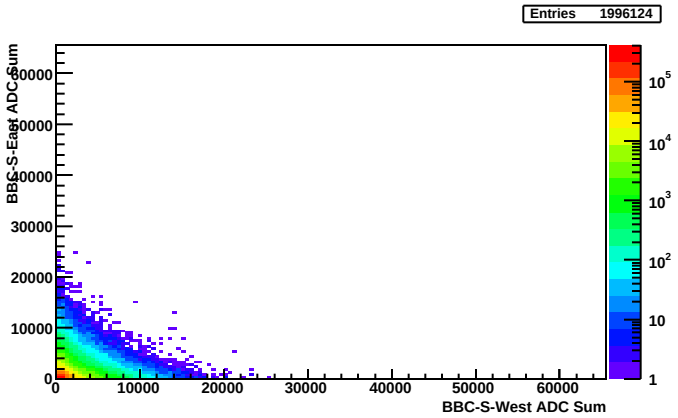
Entries 1996124
BBC-S-East ADC Sum
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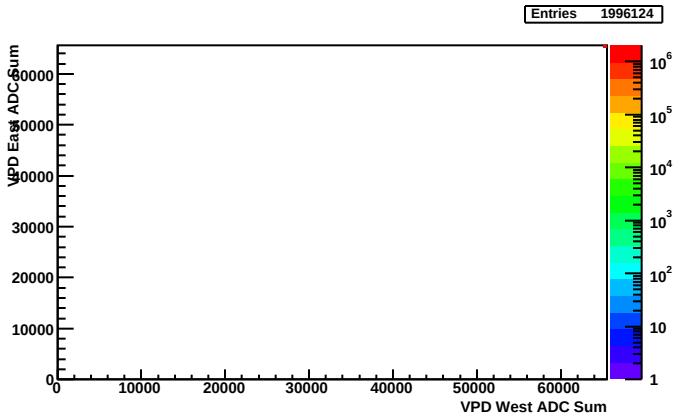
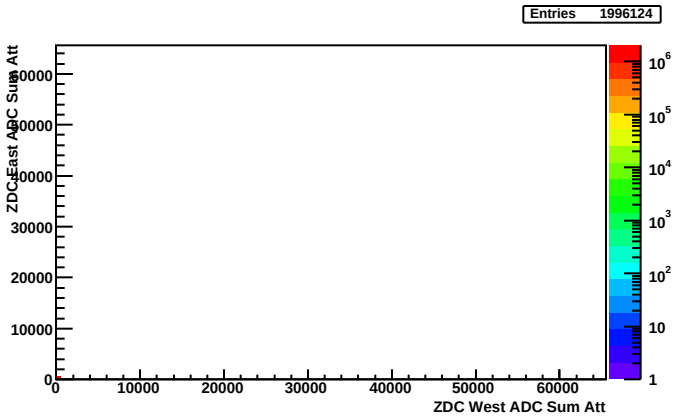
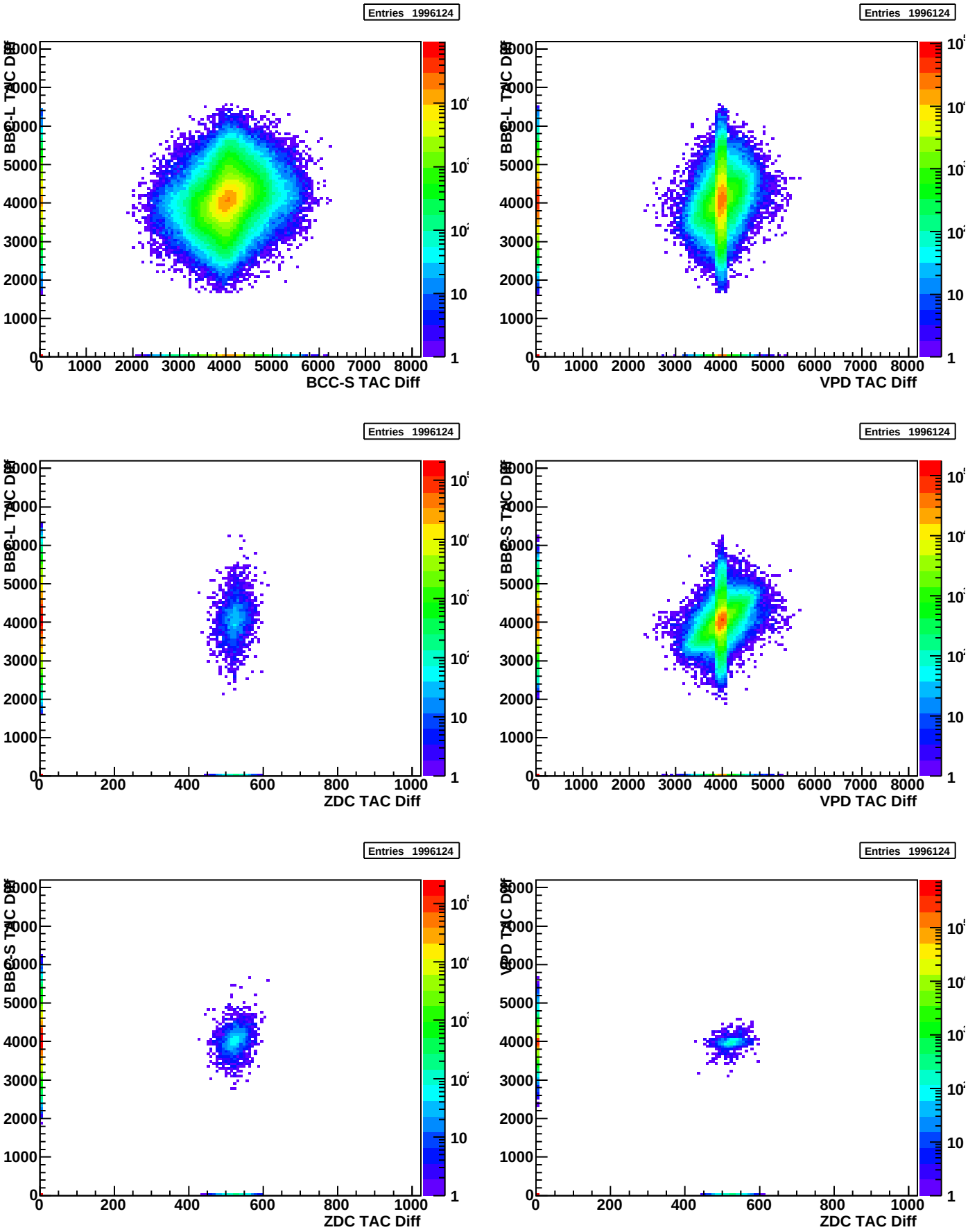
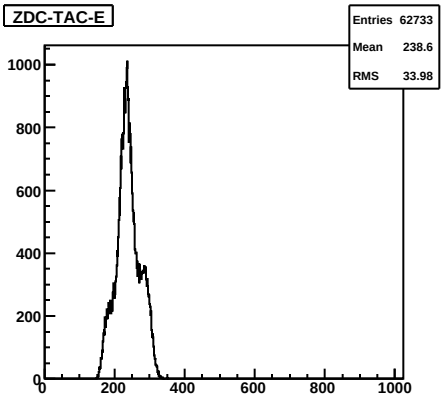
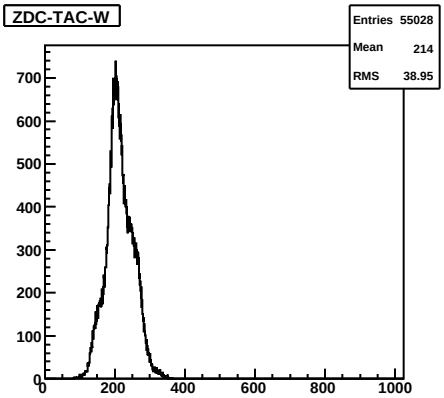
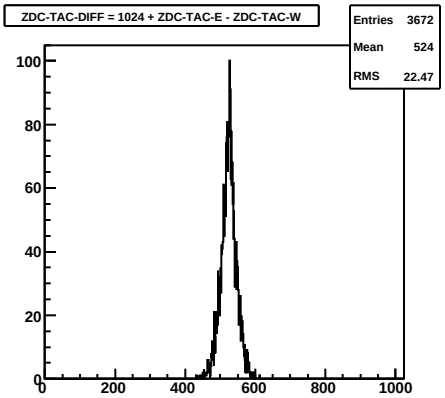

Entries 1996124
VPD East ADC Sum
VPD West ADC Sum

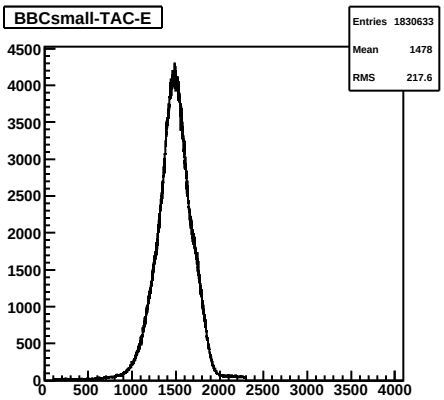

Entries 1996124
ZDC East ADC Sum Att
ZDC West ADC Sum Att

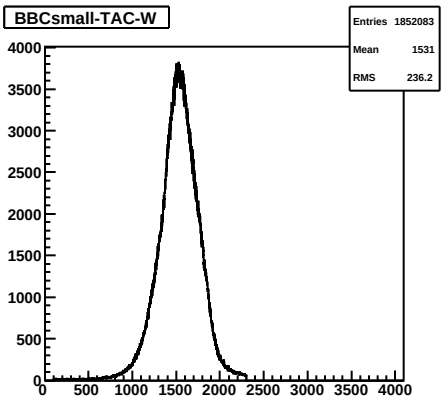

Entries 1996124
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1996124
BBC-L TAC Diff
VPD TAC Diff
Entries 1996124
BBC-L TAC Diff
ZDC TAC Diff
Entries 1996124
BBC-S TAC Diff
VPD TAC Diff
Entries 1996124
BBC-S TAC Diff
ZDC TAC Diff
Entries 1996124
VPD TAC Diff
ZDC TAC Diff

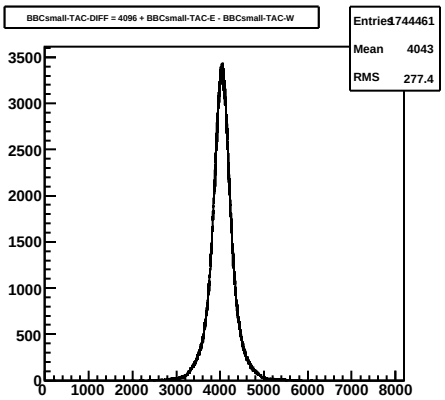

ZDC-TAC-E
Entries 62733
Mean 238.6
RMS 33.98

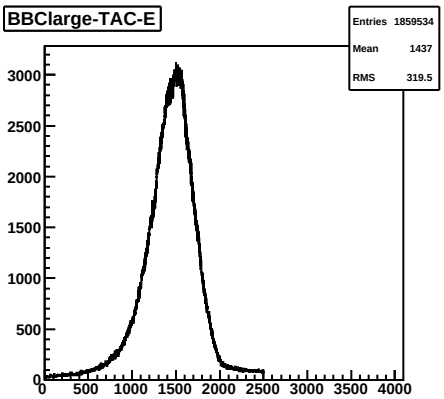

ZDC-TAC-W
Entries 55028
Mean 214
RMS 38.95

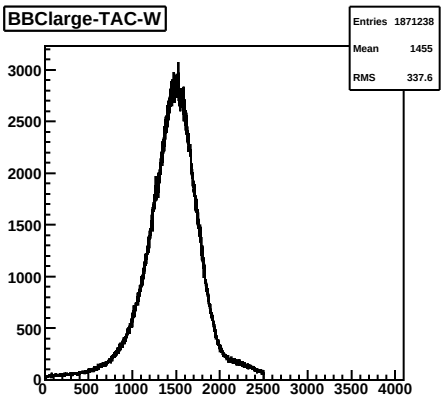

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3672
Mean 524
RMS 22.47

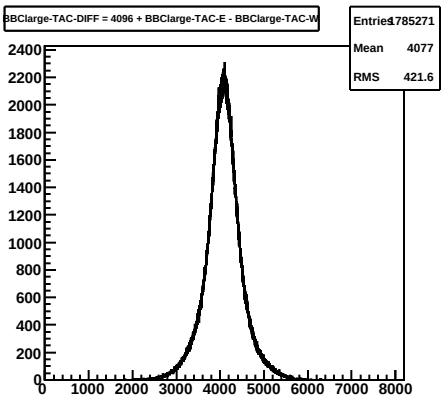

BBCsmall-TAC-E
Entries 1830633
Mean 1478
RMS 217.6

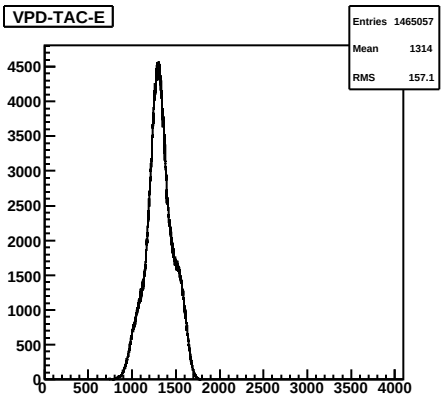

BBCsmall-TAC-W
Entries 1852083
Mean 1531
RMS 236.2

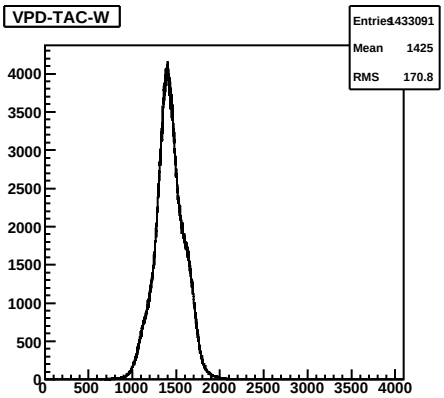

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1744461
Mean 4043
RMS 277.4

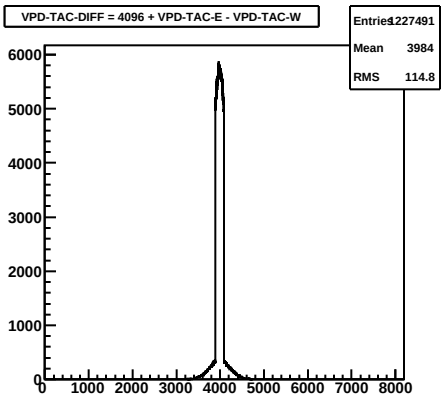

BBClarge-TAC-E
Entries 1859534
Mean 1437
RMS 319.5

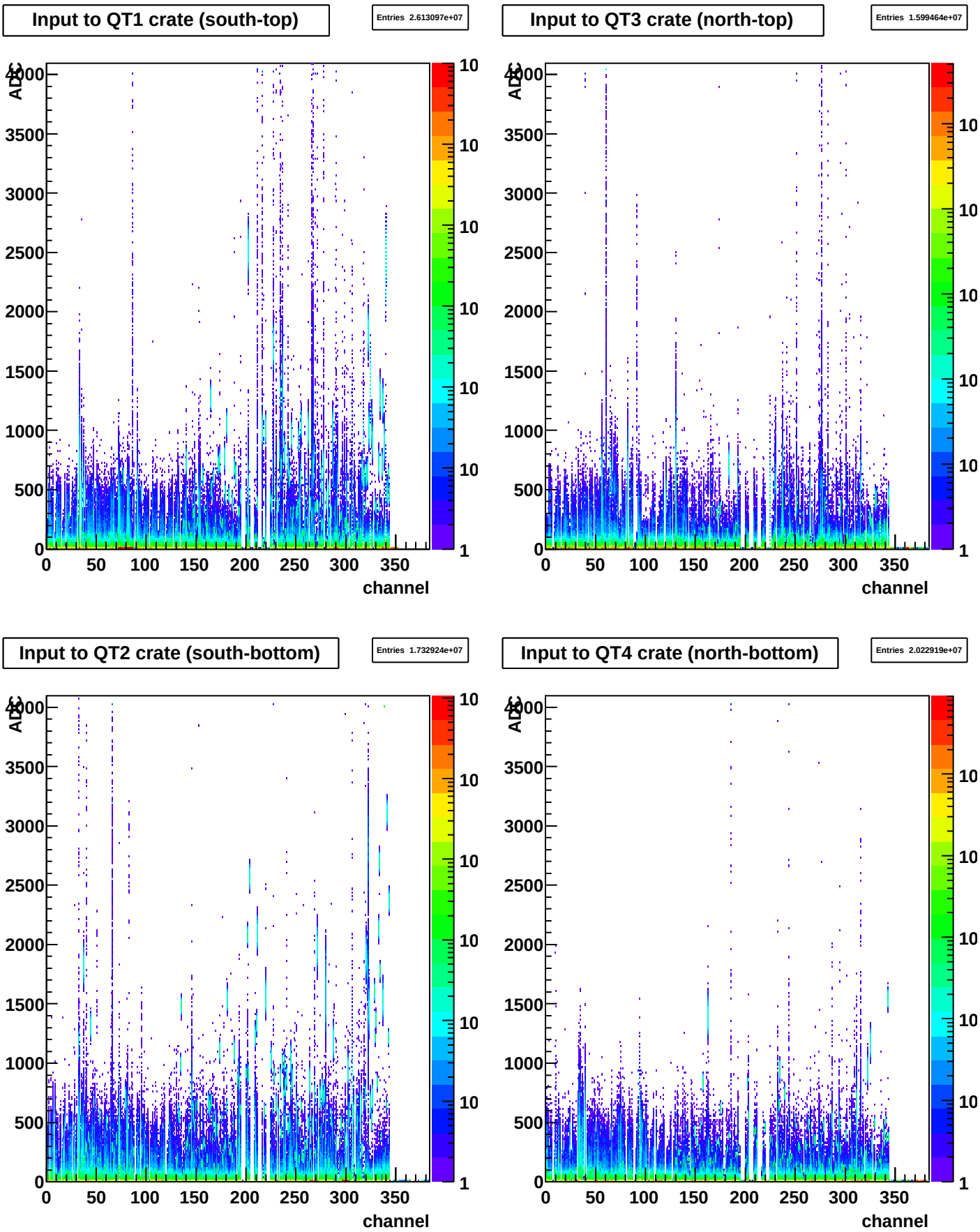

BBClarge-TAC-W
Entries 1871238
Mean 1455
RMS 337.6

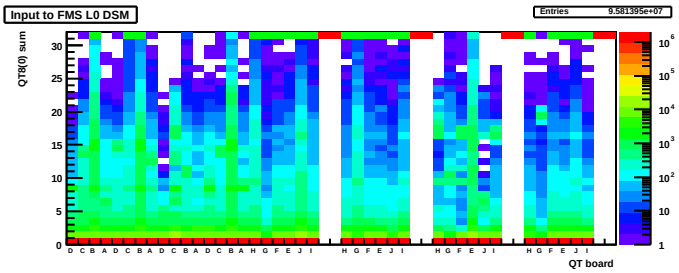

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1785271
Mean 4077
RMS 421.6

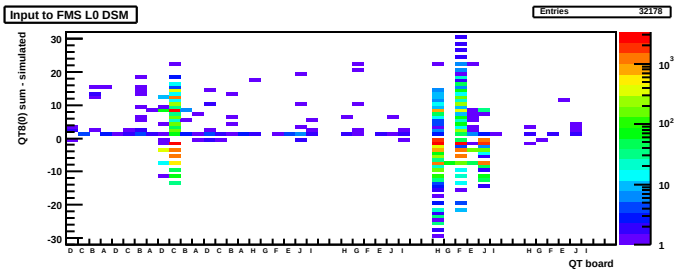

VPD-TAC-E
Entries 1465057
Mean 1314
RMS 157.1

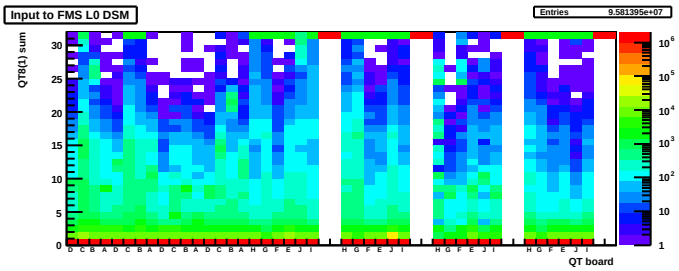

VPD-TAC-W
Entries 1433091
Mean 1425
RMS 170.8

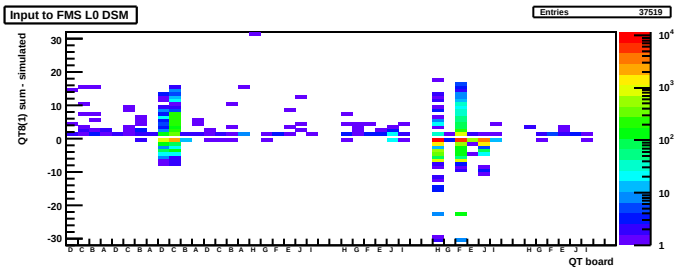

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1227491
Mean 3984
RMS 114.8

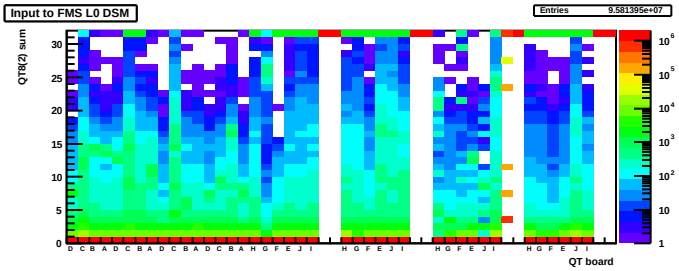

Input to QT1 crate (south-top)
Entries 2.613097e+07
Input to QT3 crate (north-top)
Entries 1.599464e+07
Input to QT2 crate (south-bottom)
Entries 1.732924e+07
Input to QT4 crate (north-bottom)
Entries 2.022919e+07
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

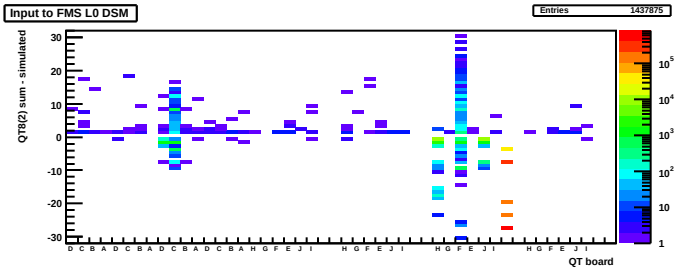

Input to FMS L0 DSM
Entries 9.581395e+07
QT8(0) sum
QT board

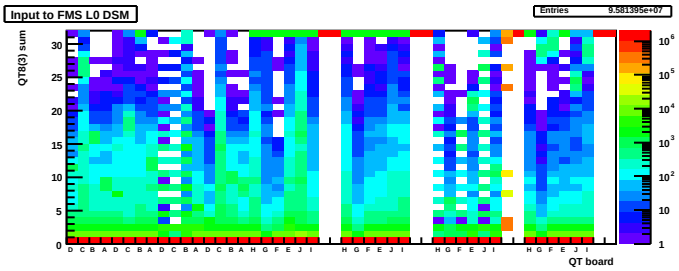

Input to FMS L0 DSM
Entries 32178
QT8(0) sum - simulated
QT board

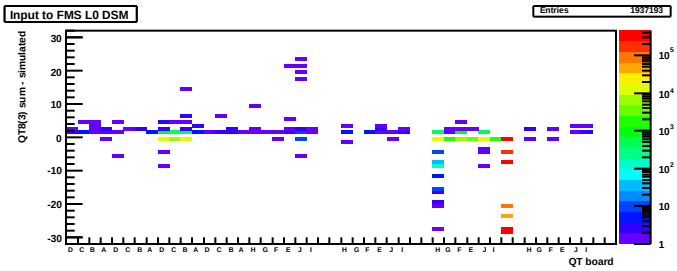

Input to FMS L0 DSM
Entries 9.581395e+07
QT8(1) sum
QT board

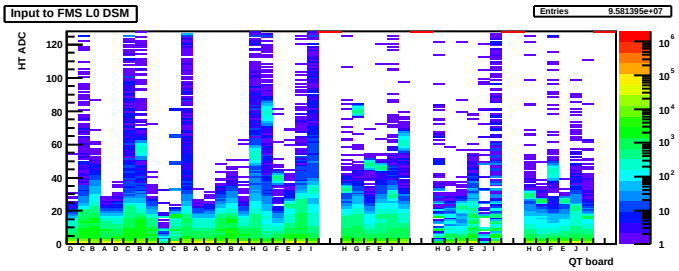

Input to FMS L0 DSM
Entries 37519
QT8(1) sum - simulated
QT board

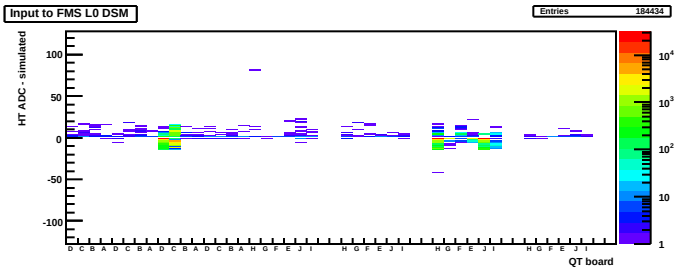

Input to FMS L0 DSM
Entries 9.581395e+07
QT8(2) sum
QT board

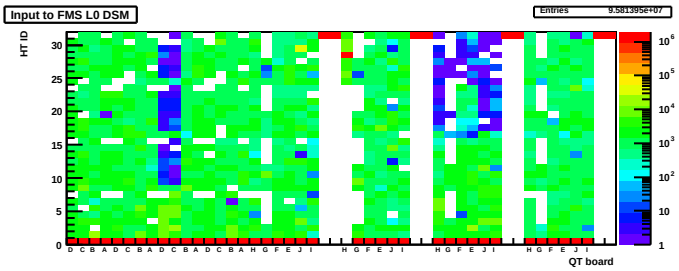

Input to FMS L0 DSM
Entries 1437875
QT8(2) sum - simulated
QT board

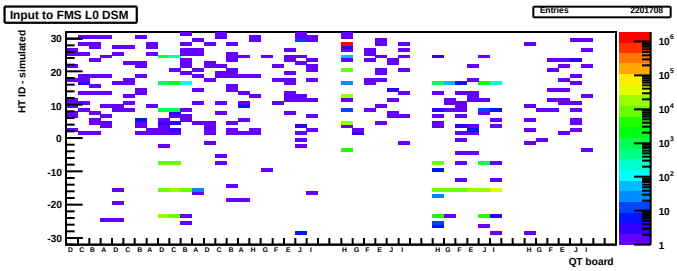

Input to FMS L0 DSM
Entries 9.581395e+07
QT8(3) sum
QT board

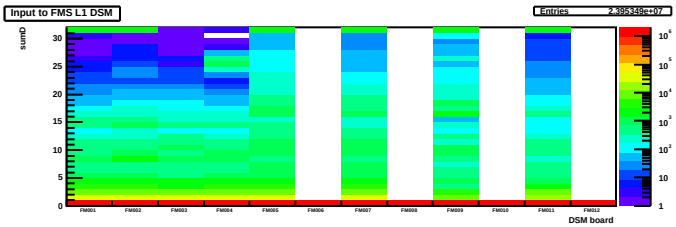

Input to FMS L0 DSM
Entries 1937193
QT8(3) sum - simulated
QT board

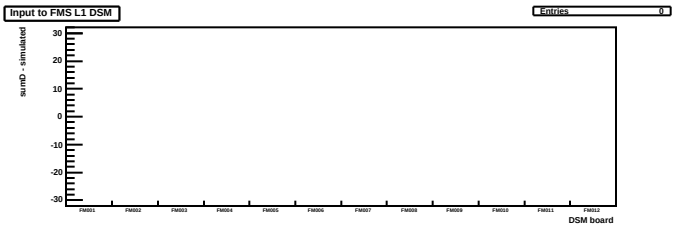

Input to FMS L0 DSM
Entries 9.581395e+07
HT ADC
QT board

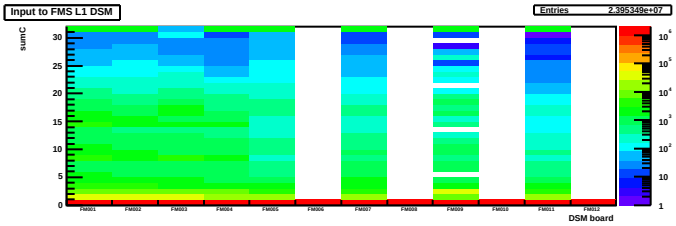

Input to FMS L0 DSM
Entries 184434
HT ADC - simulated
QT board

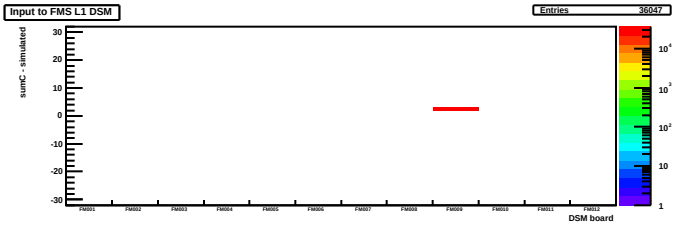

Input to FMS L0 DSM
Entries 9.581395e+07
HT ID
QT board

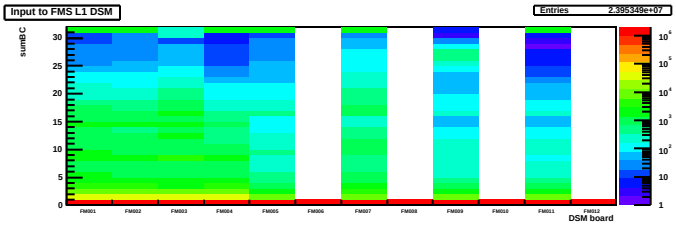

Input to FMS L0 DSM
Entries 2201708
HT ID - simulated
QT board

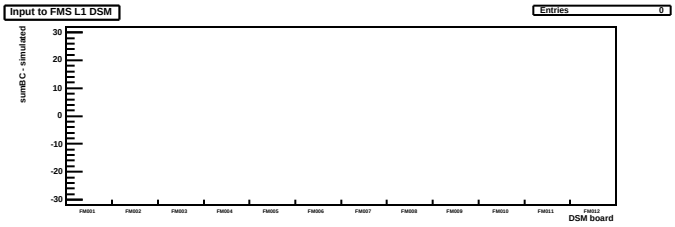

Input to FMS L1 DSM
Entries 2.395349e+07
sumD
DSM board

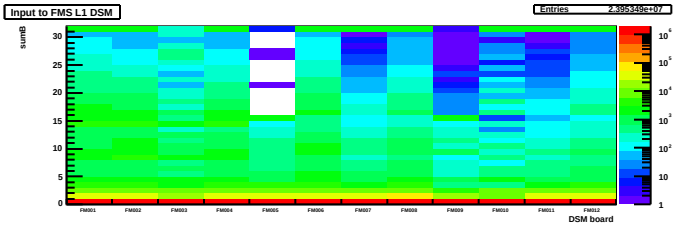

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

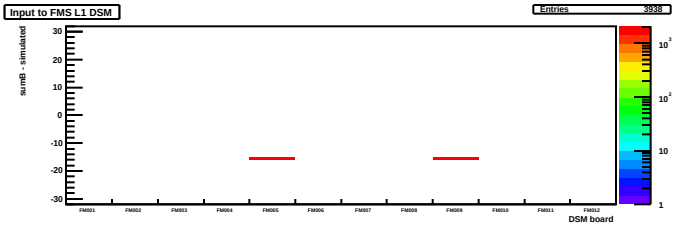

Input to FMS L1 DSM
Entries 2.395349e+07
sumC
DSM board

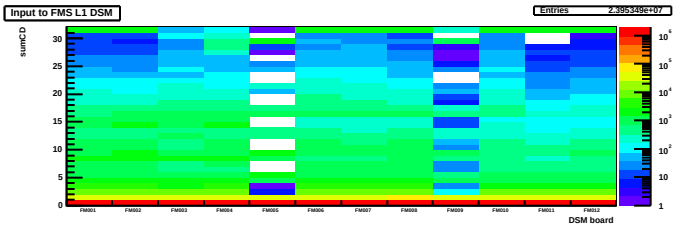

Input to FMS L1 DSM
Entries 36047
sumC - simulated
DSM board

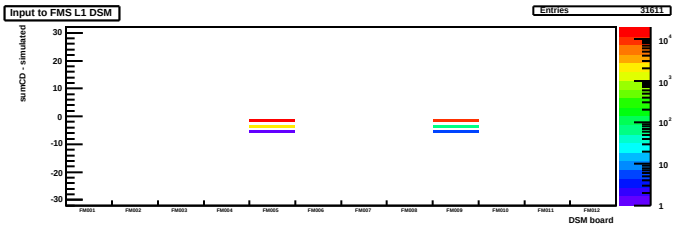

Input to FMS L1 DSM
Entries 2.395349e+07
sumBC
DSM board

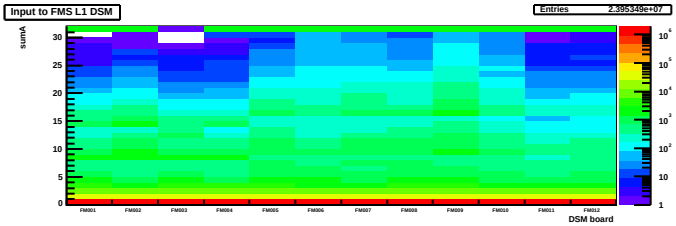

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

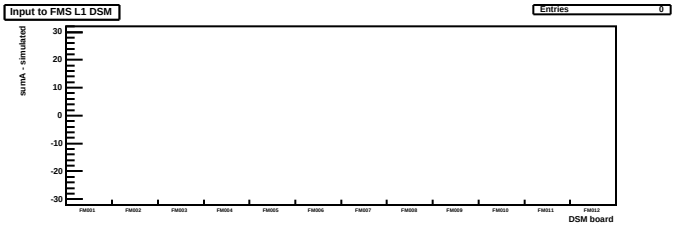

Input to FMS L1 DSM
Entries 2.395349e+07
sumB
DSM board

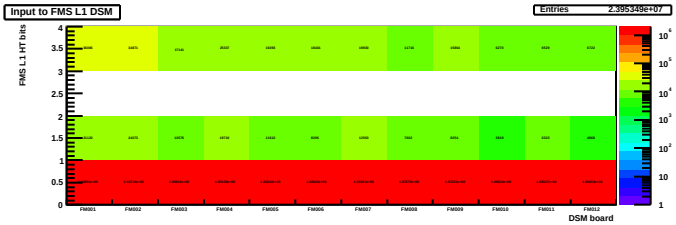

Input to FMS L1 DSM
Entries 3938
sumB - simulated
DSM board

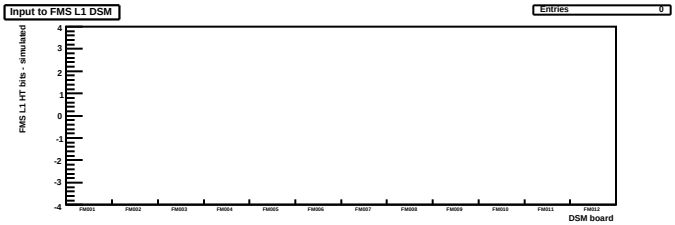

Input to FMS L1 DSM
Entries 2.395349e+07
sumCD
DSM board

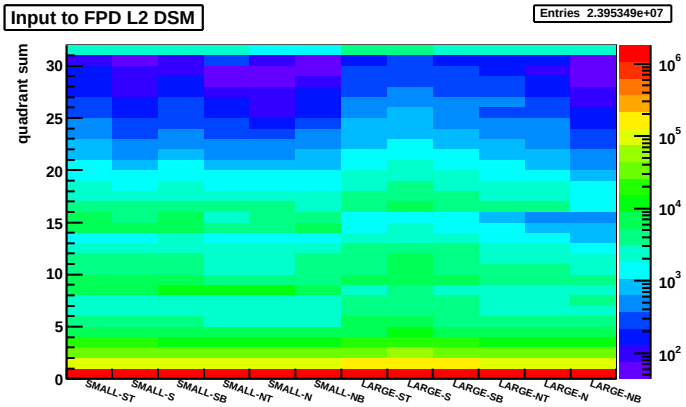

Input to FMS L1 DSM
Entries 31611
sumCD - simulated
DSM board

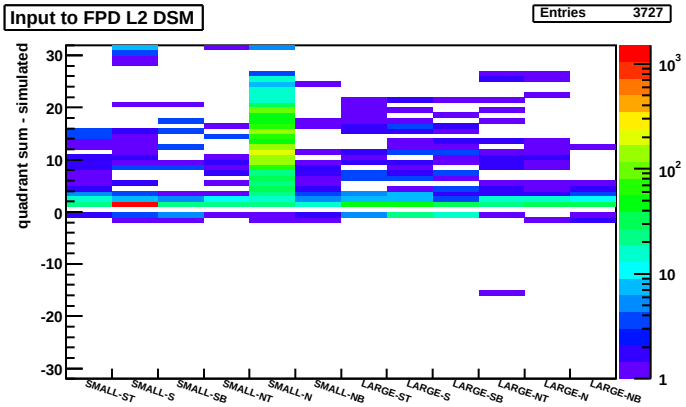

Input to FMS L1 DSM
Entries 2.395349e+07
sumA
DSM board

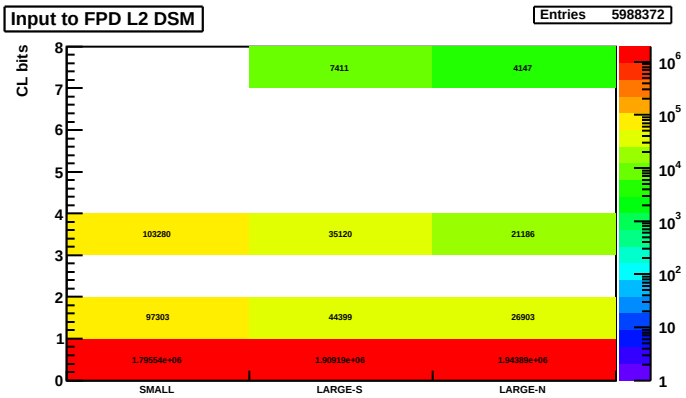

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

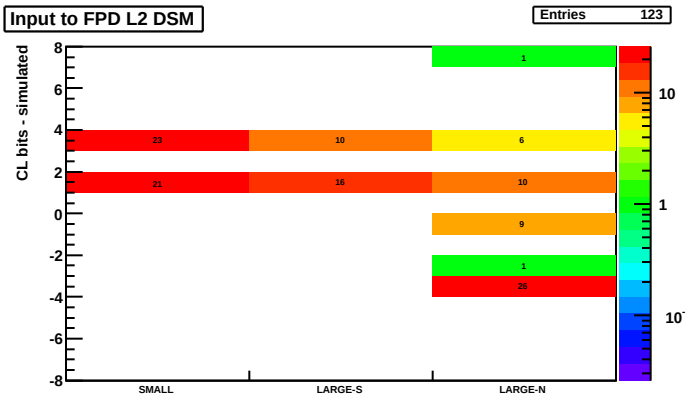

Input to FMS L1 DSM
Entries 2.395349e+07
FMS L1 HT bits
DSM board

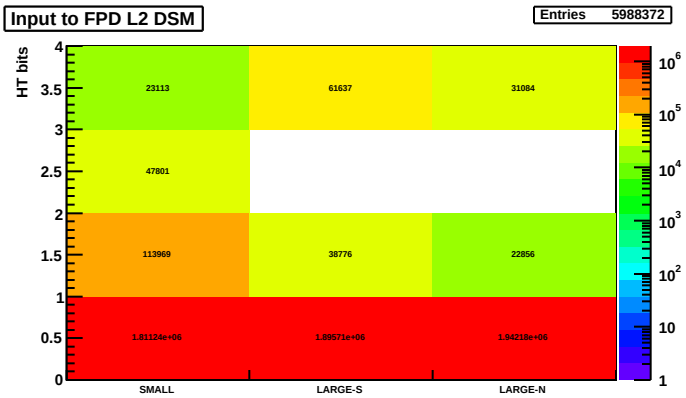

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

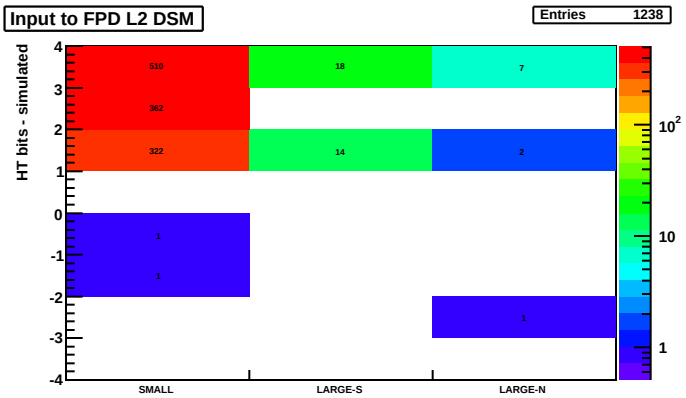

Input to FPD L2 DSM
Entries 2.395349e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

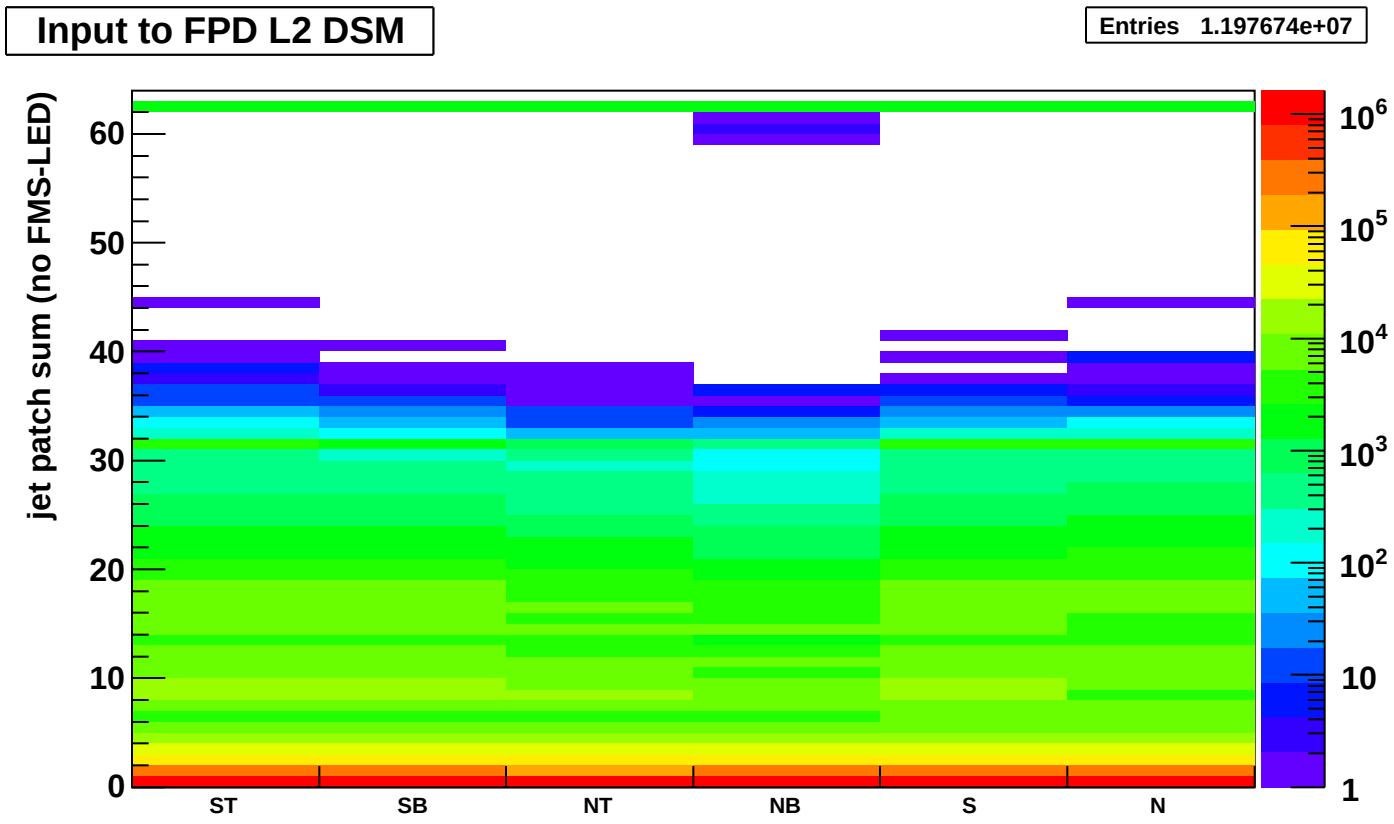

Input to FPD L2 DSM
Entries 3727
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

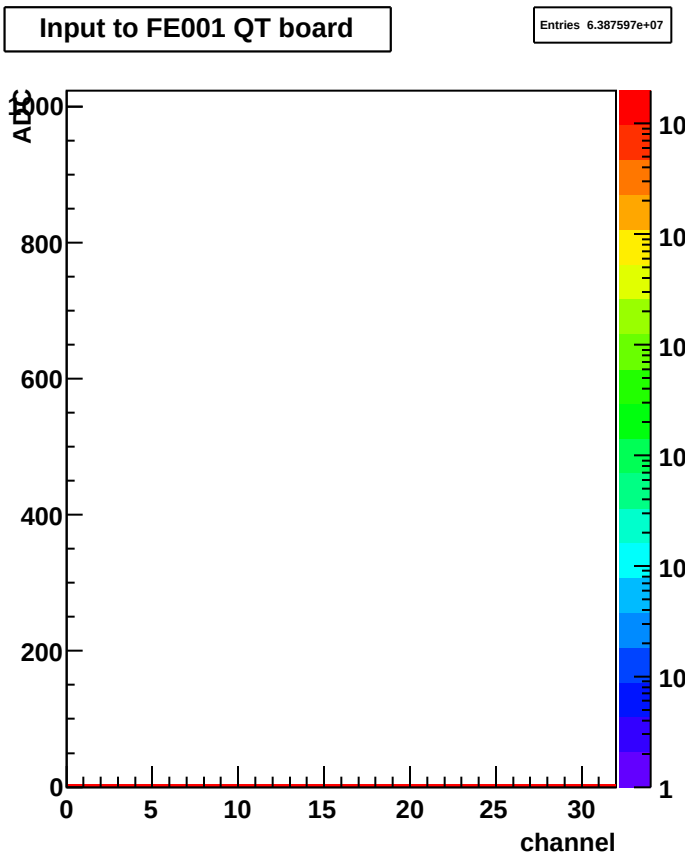

Input to FPD L2 DSM
Entries 5988372
CL bits
7411
4147
103280
35120
21186
97303
44399
26903
1.79554e+06
1.90919e+06
1.94389e+06
SMALL
LARGE-S
LARGE-N

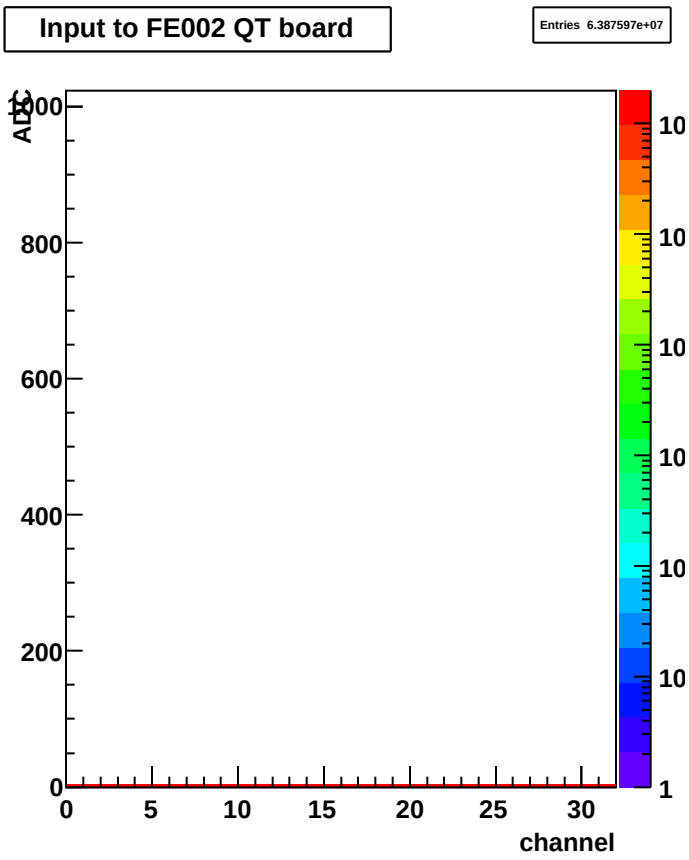

Input to FPD L2 DSM
Entries 123
CL bits - simulated
1
23
10
6
21
16
10
9
1
26
SMALL
LARGE-S
LARGE-N

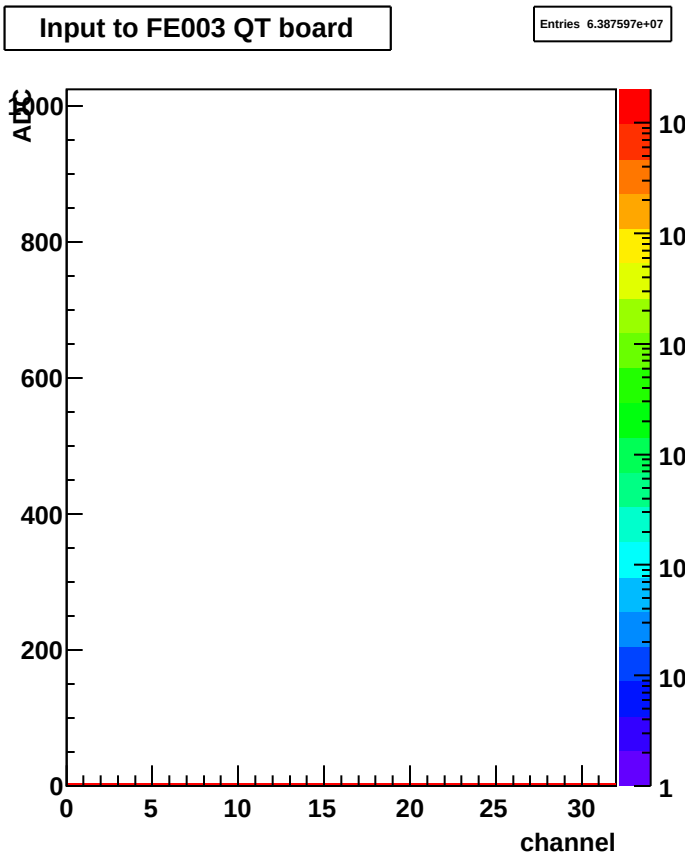

Input to FPD L2 DSM
Entries 5988372
HT bits
23113
61637
31084
47801
113969
38776
22856
1.81124e+06
1.89571e+06
1.94218e+06
SMALL
LARGE-S
LARGE-N

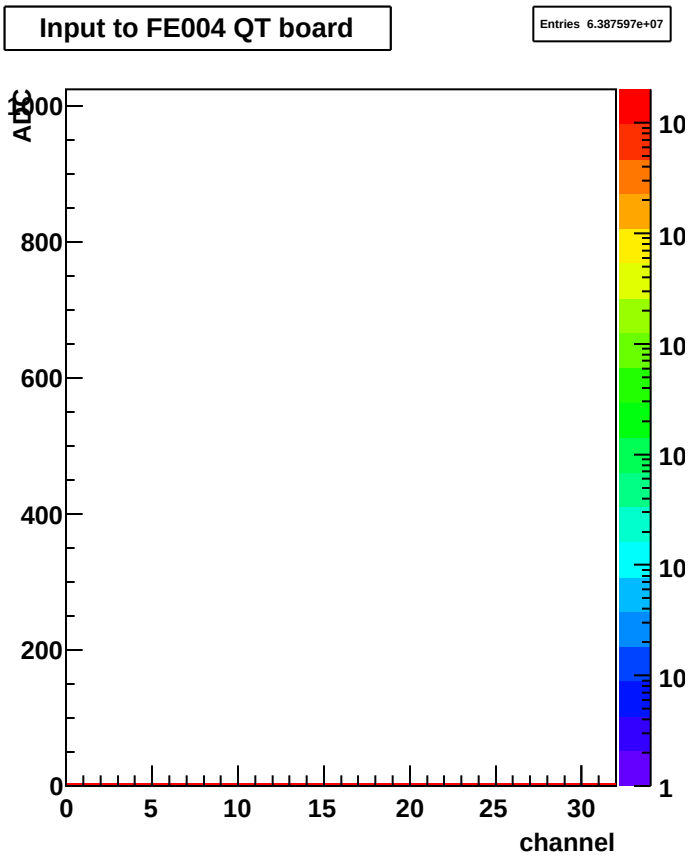

Input to FPD L2 DSM
Entries 1238
HT bits - simulated
510
18
7
362
322
14
2
1
1
1
SMALL
LARGE-S
LARGE-N

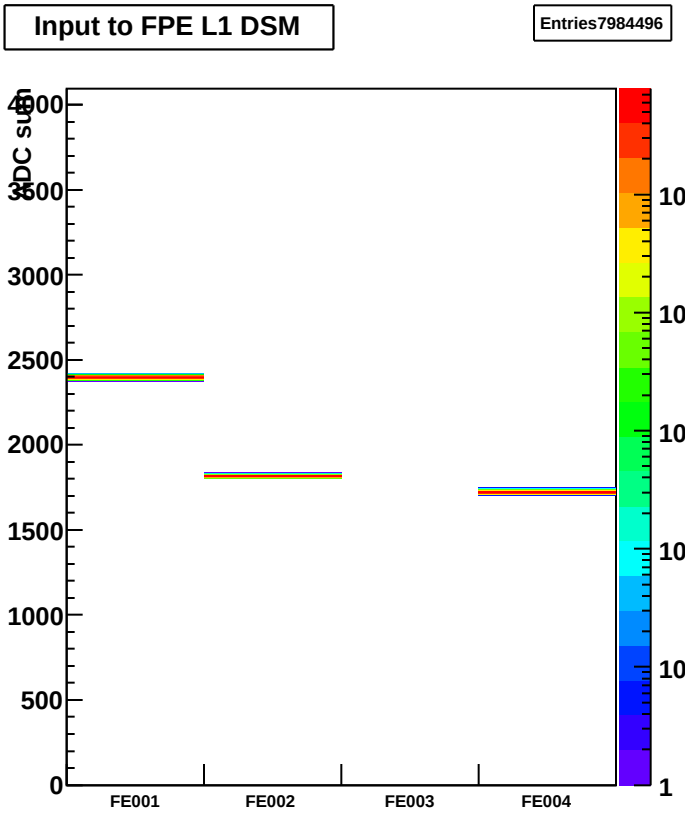

Input to FPD L2 DSM
Entries 1.197674e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 6.387597e+07
ADC
channel

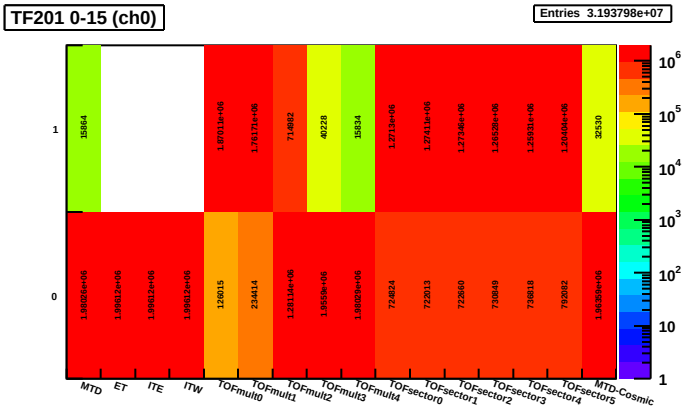

Input to FE002 QT board
Entries 6.387597e+07
ADC
channel


Input to FE003 QT board
Entries 6.387597e+07
ADC
channel

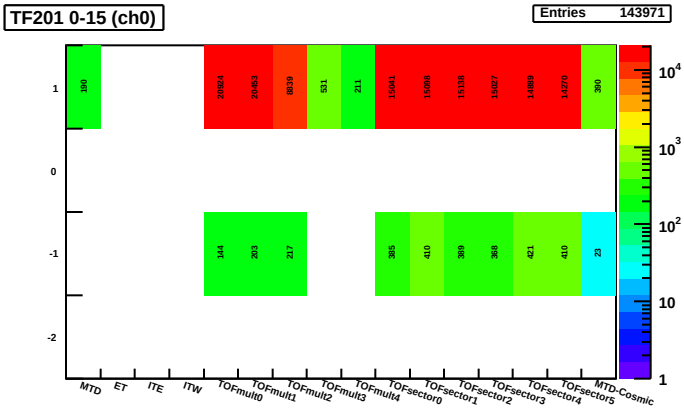

Input to FE004 QT board
Entries 6.387597e+07
ADC
channel


Input to FPE L1 DSM
Entries7984496
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

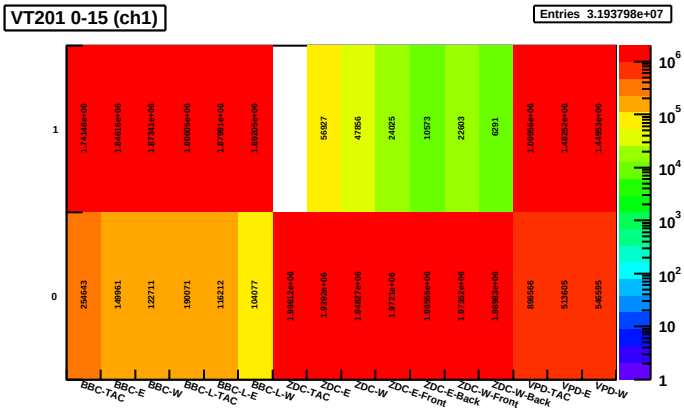
TF201 0-15 (ch0)



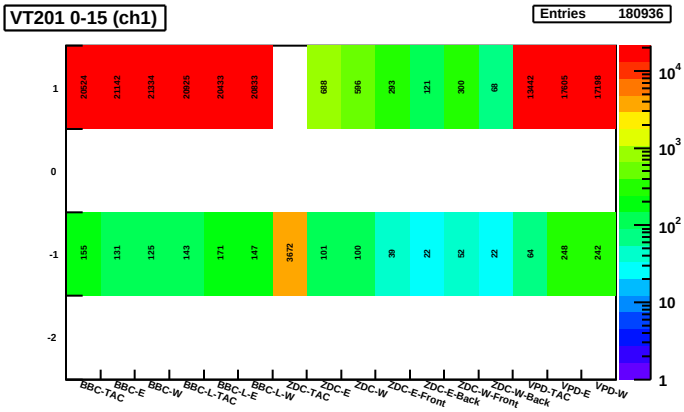
TF201 0-15 (ch0)



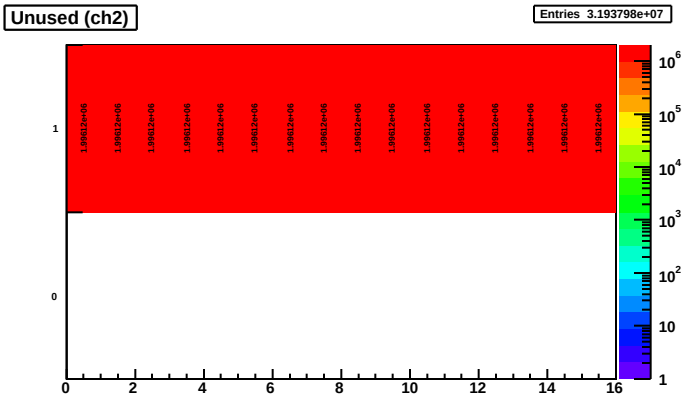
VT201 0-15 (ch1)

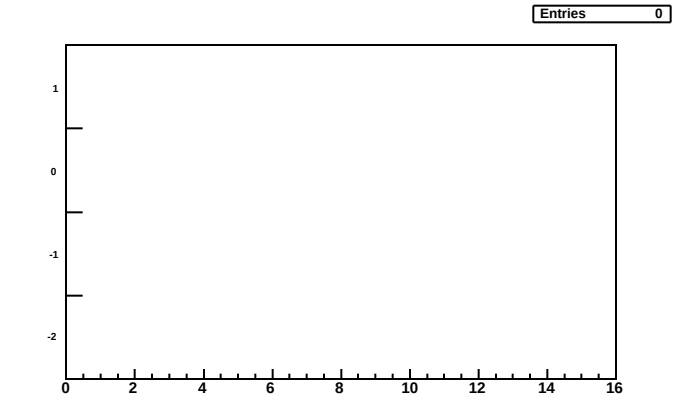


VT201 0-15 (ch1)

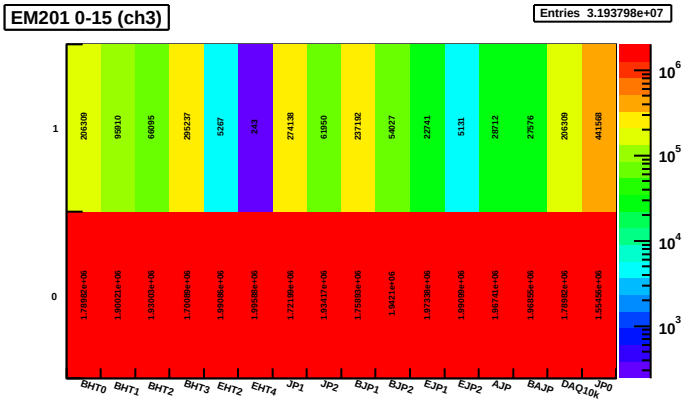


Unused (ch2)

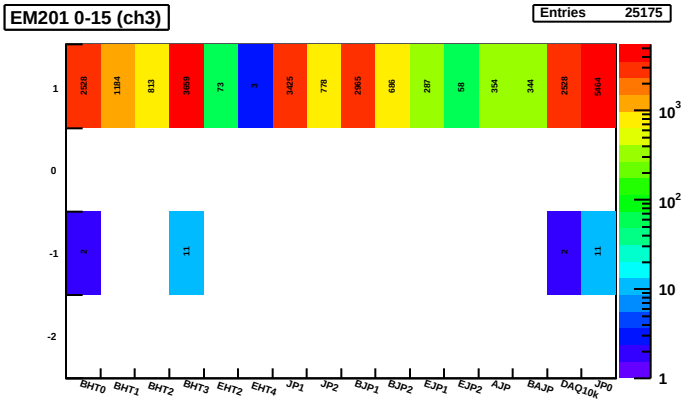




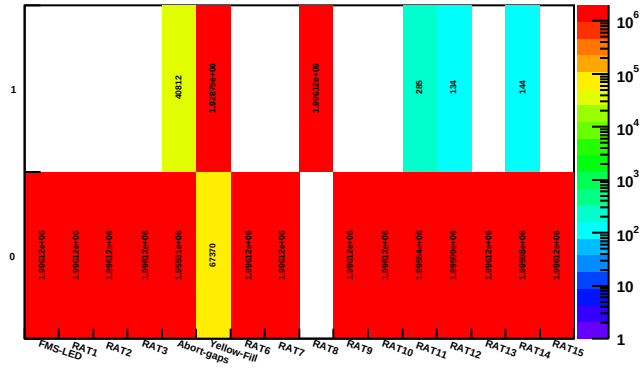
EM201 0-15 (ch3)

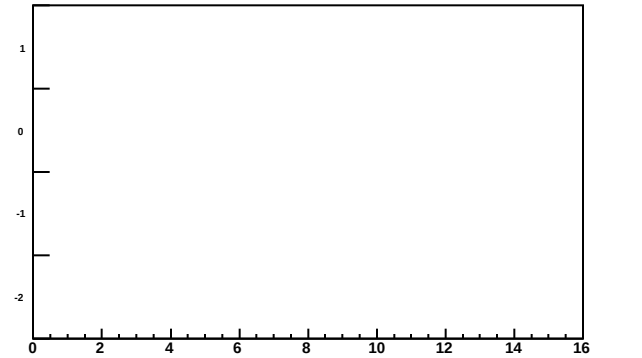


EM201 0-15 (ch3)

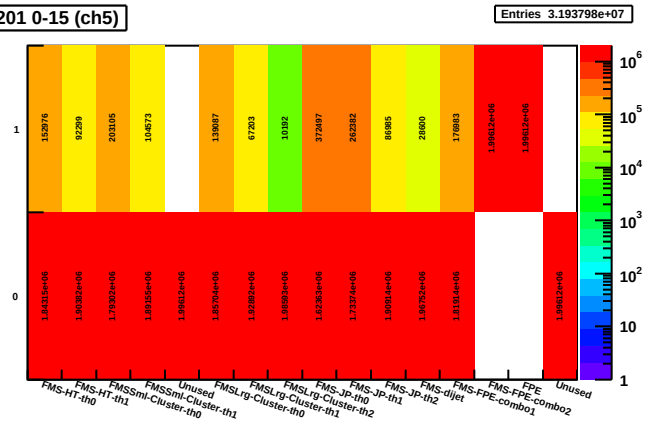


RAT board (ch4)





FP201 0-15 (ch5)



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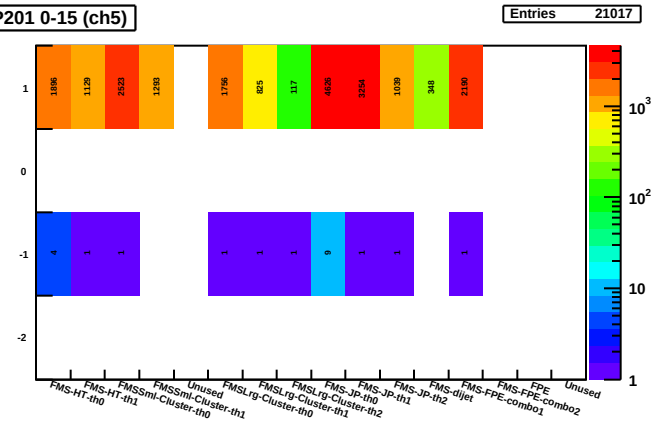
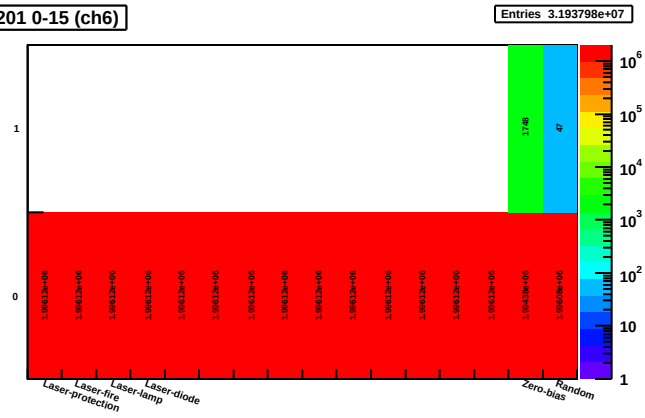
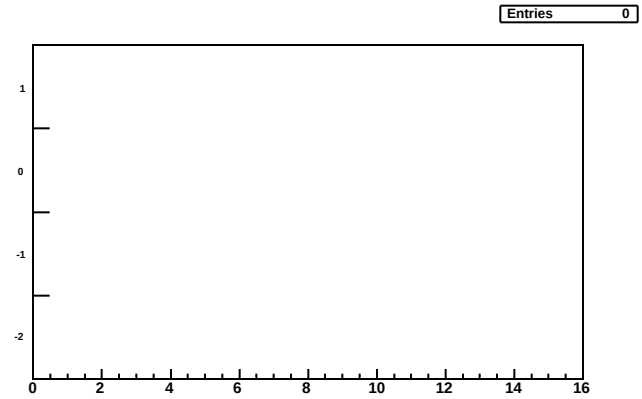
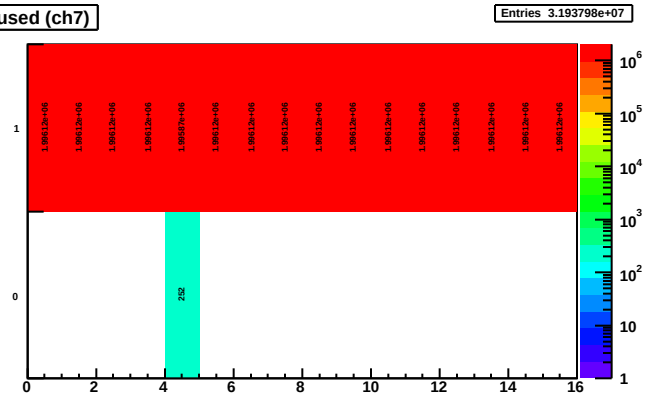
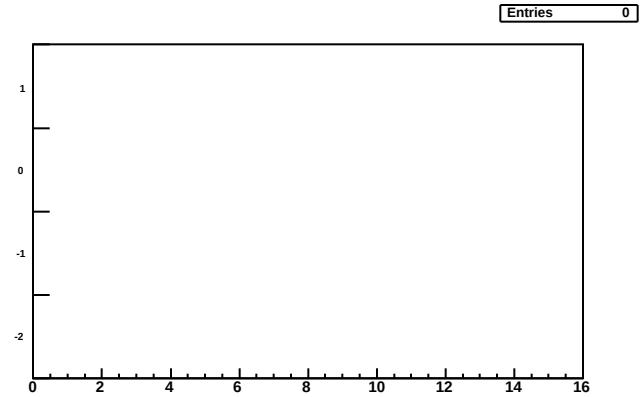


ST201 0-15 (ch6)





Unused (ch7)