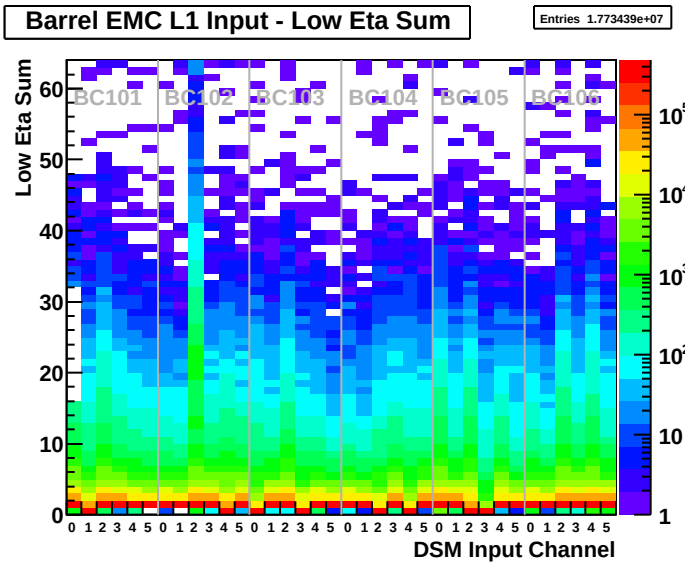
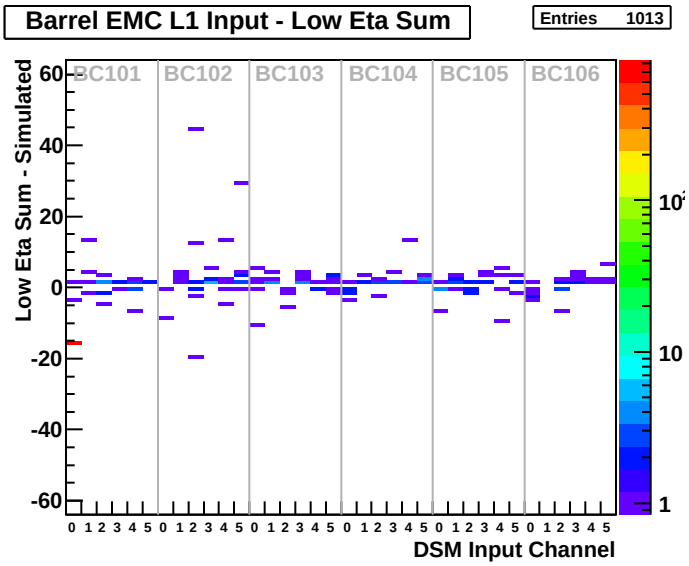
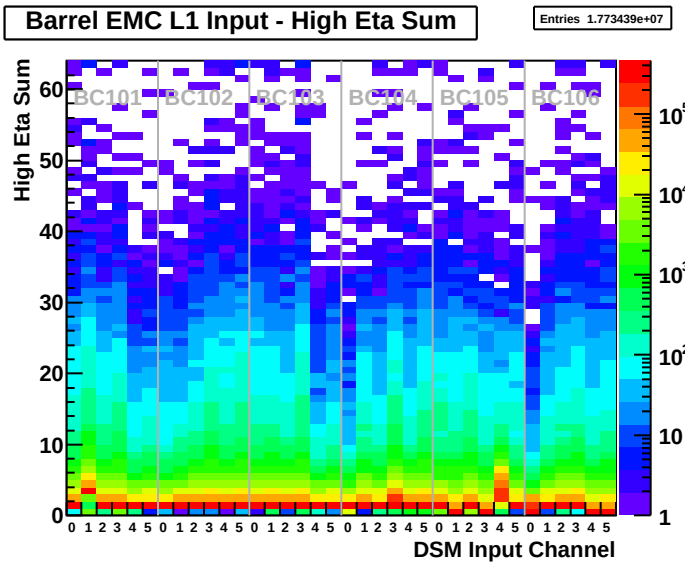
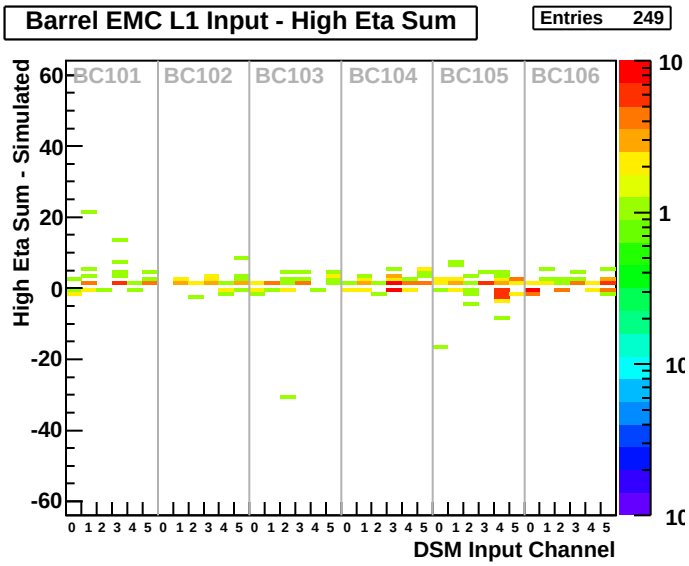
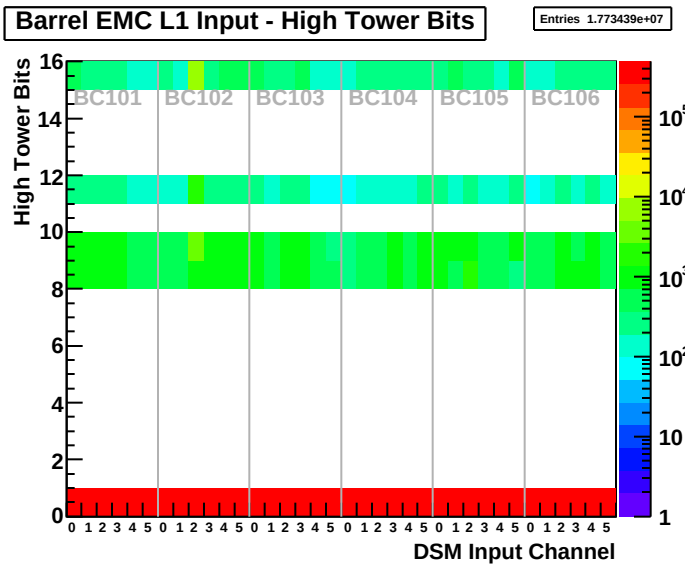
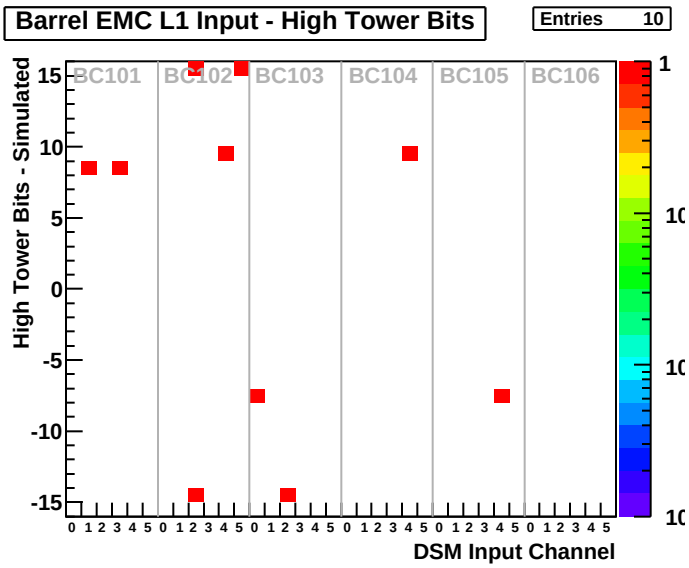

Barrel EMC L1 Input - Low Eta Sum
Entries 1.773439e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

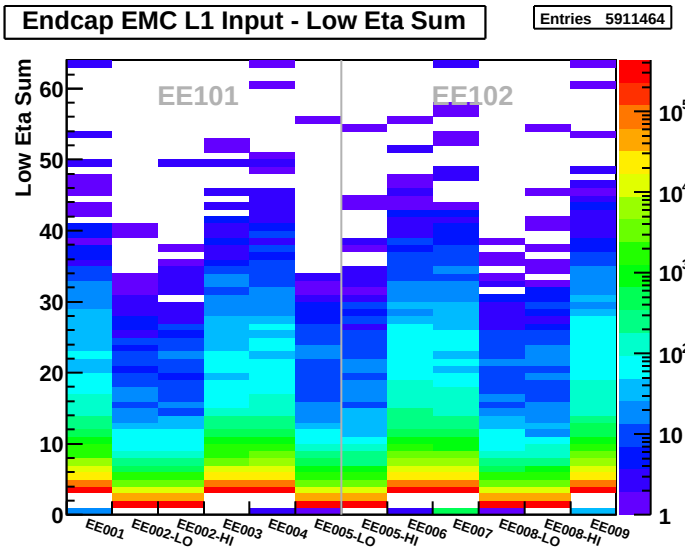

Barrel EMC L1 Input - Low Eta Sum
Entries 1013
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

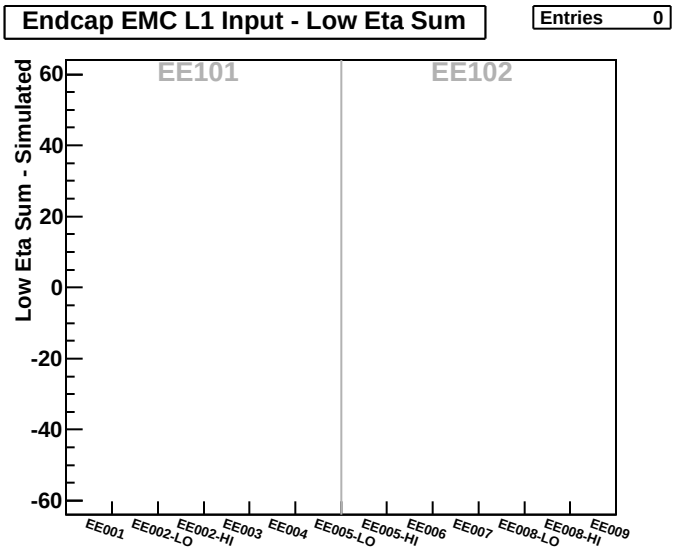

Barrel EMC L1 Input - High Eta Sum
Entries 1.773439e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

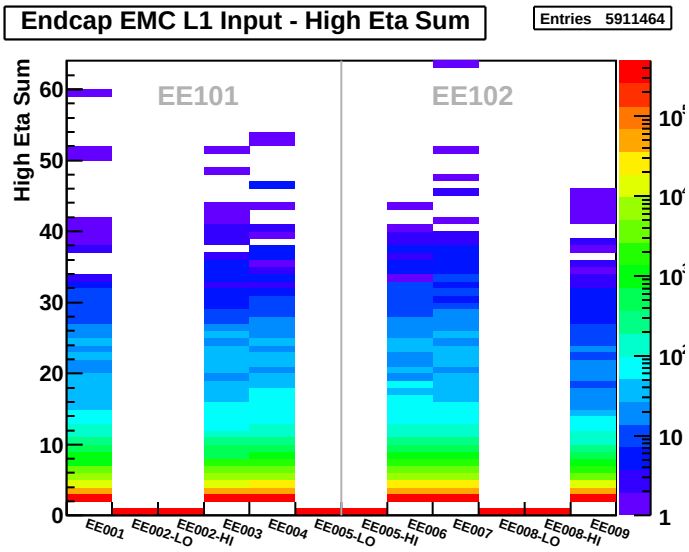

Barrel EMC L1 Input - High Eta Sum
Entries 249
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

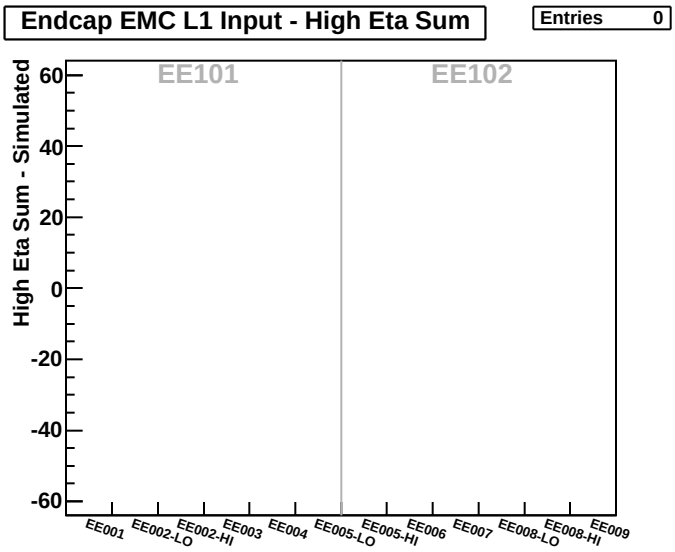

Barrel EMC L1 Input - High Tower Bits
Entries 1.773439e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

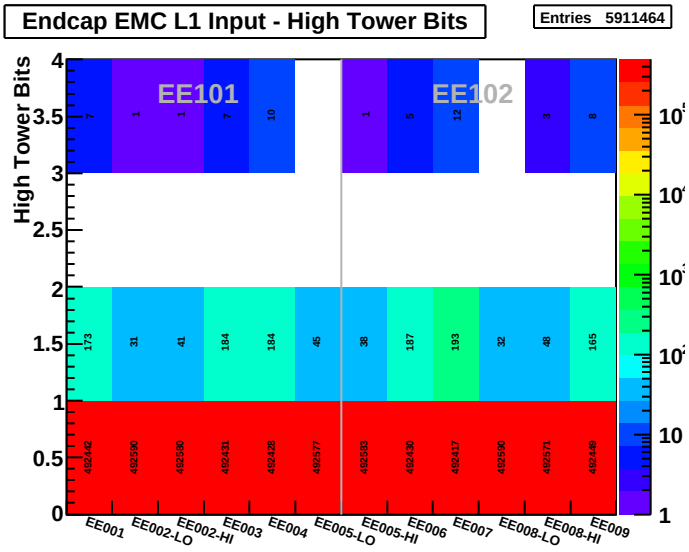

Barrel EMC L1 Input - High Tower Bits
Entries 10
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

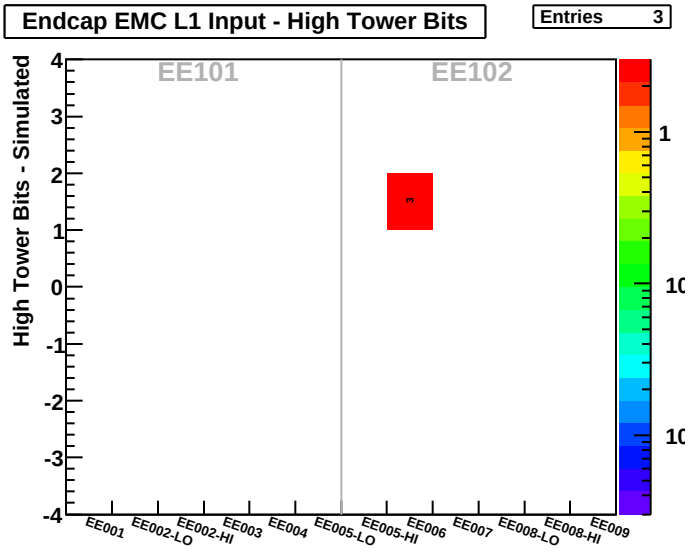

Endcap EMC L1 Input - Low Eta Sum
Entries 5911464
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

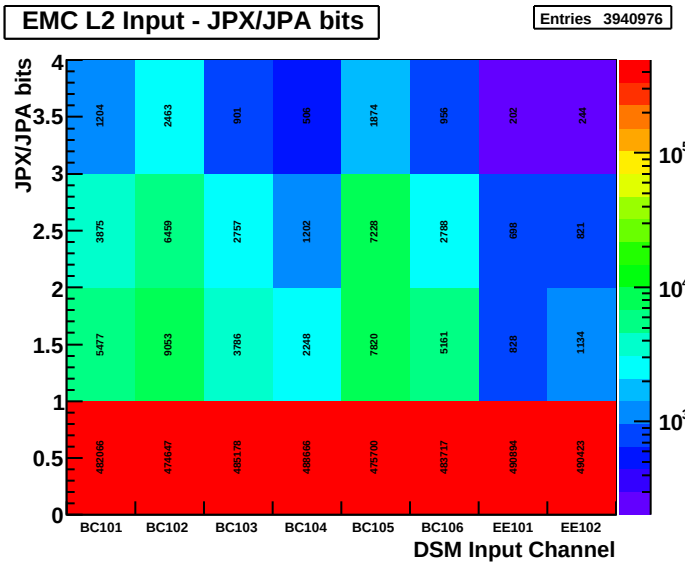

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

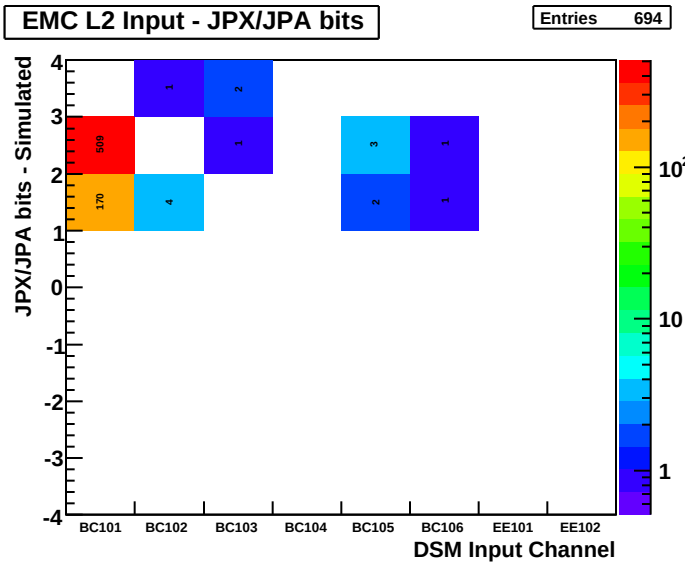

Endcap EMC L1 Input - High Eta Sum
Entries 5911464
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

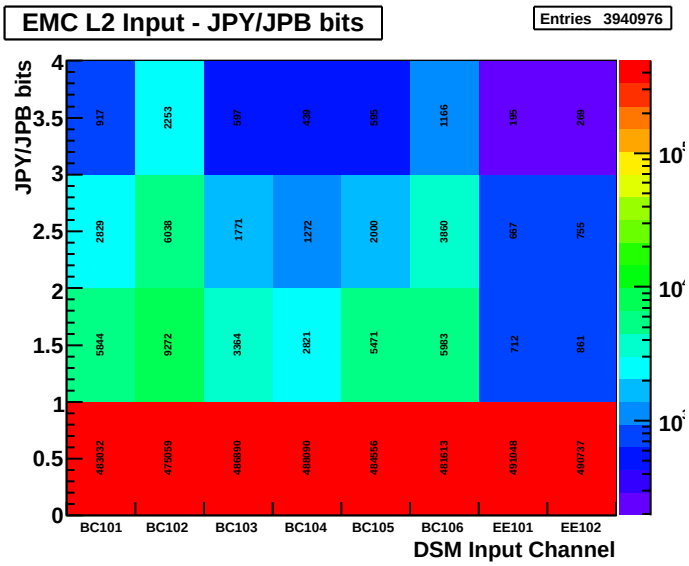

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

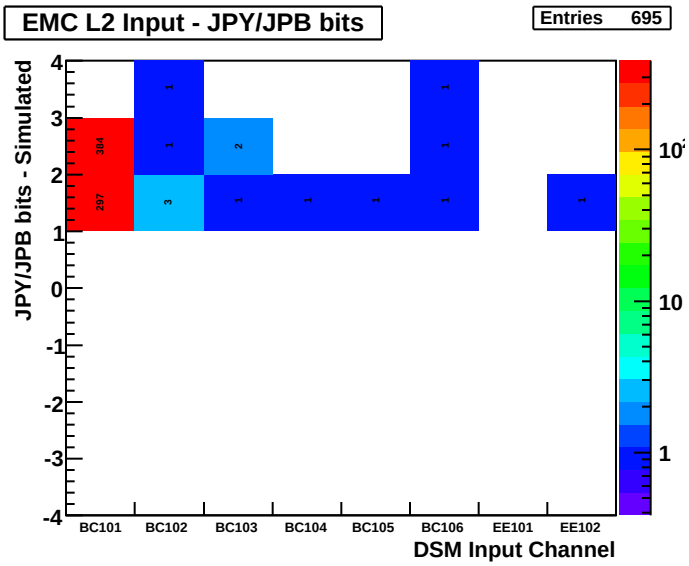

Endcap EMC L1 Input - High Tower Bits
Entries 5911464
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

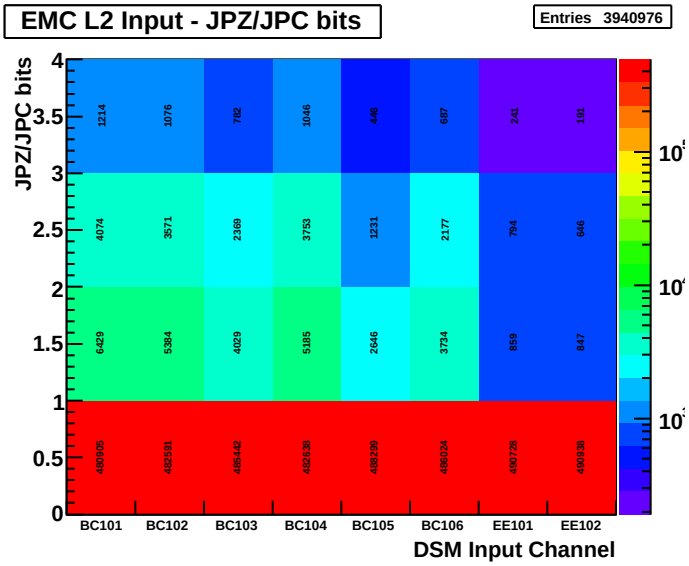

Endcap EMC L1 Input - High Tower Bits
Entries 3
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

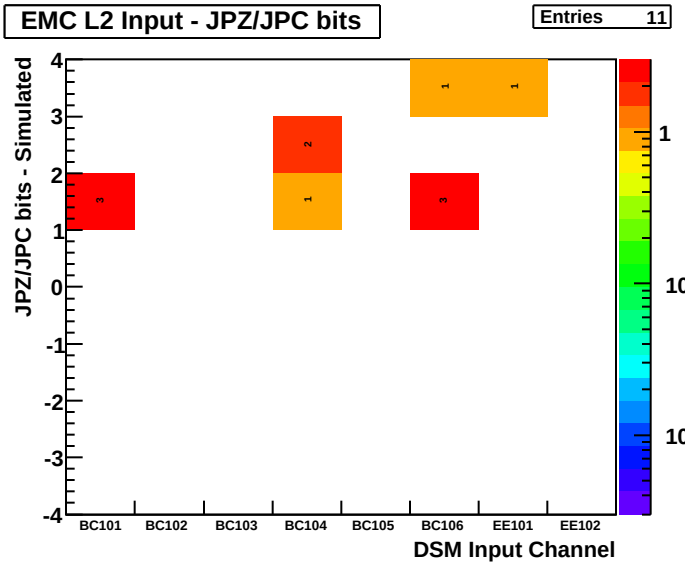

EMC L2 Input - JPX/JPA bits
Entries 3940976
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

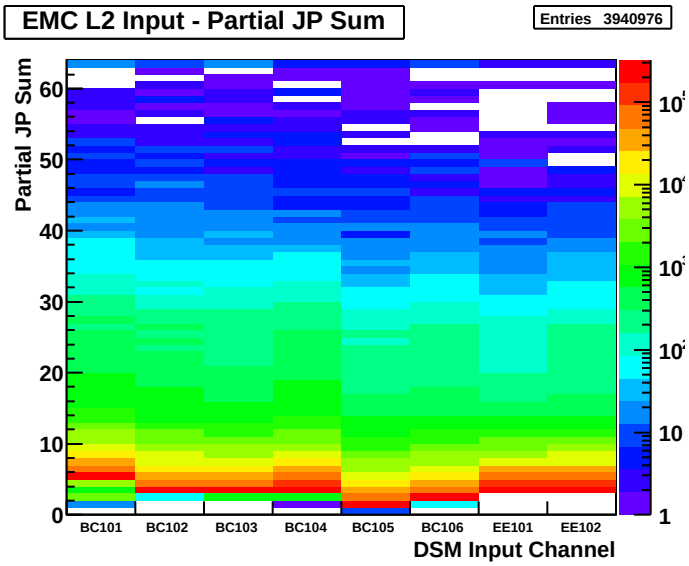

EMC L2 Input - JPX/JPA bits
Entries 694
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

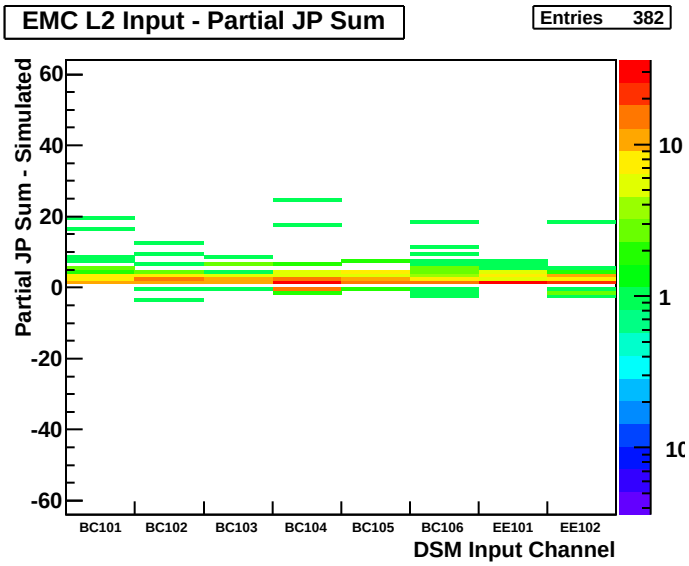

EMC L2 Input - JPY/JPB bits
Entries 3940976
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

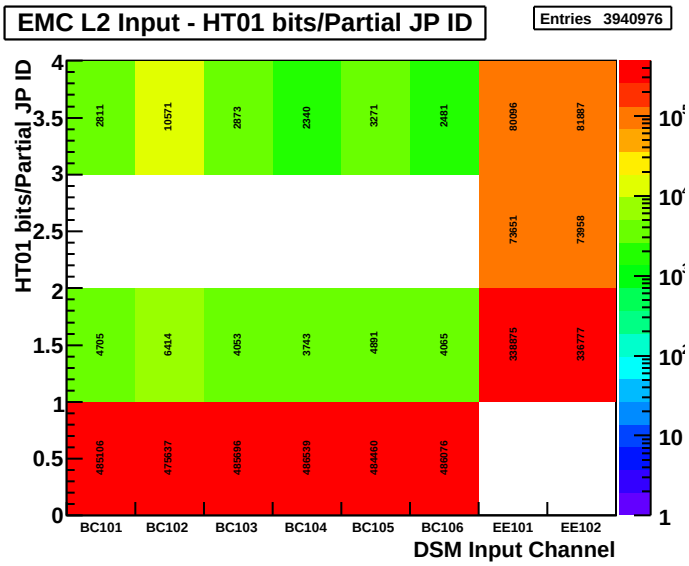

EMC L2 Input - JPY/JPB bits
Entries 695
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

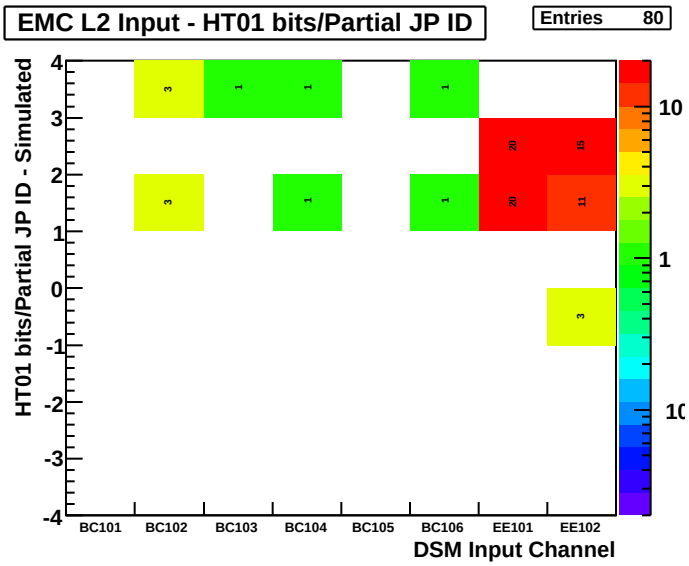

EMC L2 Input - JPZ/JPC bits
Entries 3940976
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

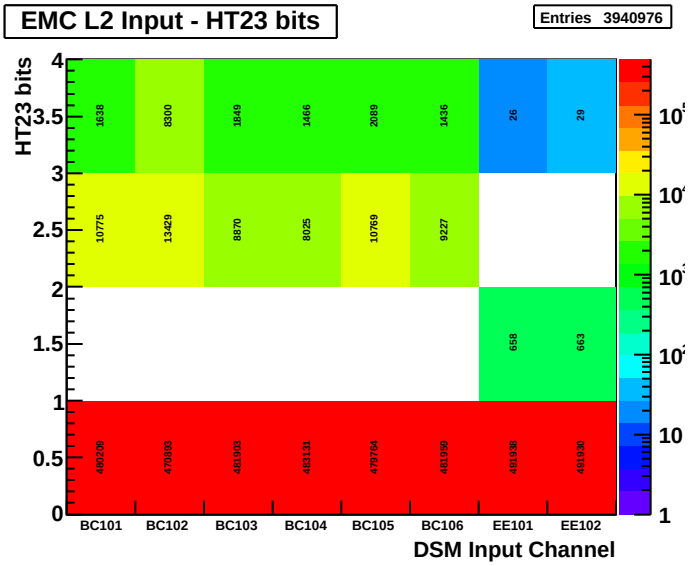

EMC L2 Input - JPZ/JPC bits
Entries 11
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

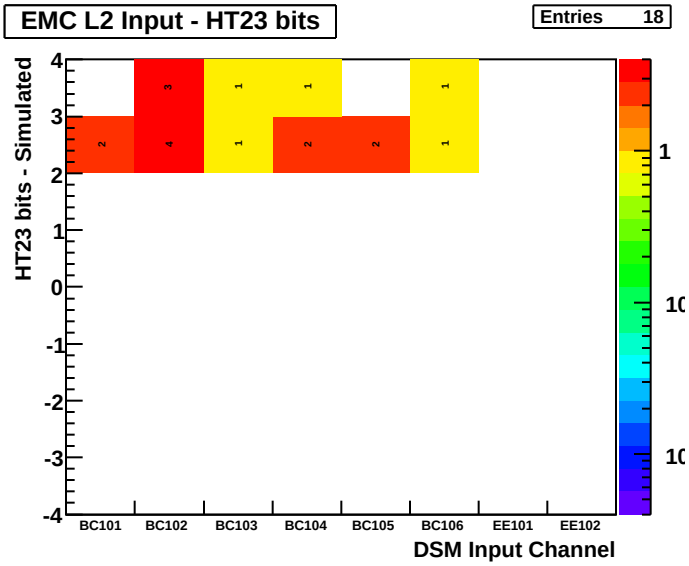

EMC L2 Input - Partial JP Sum
Entries 3940976
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

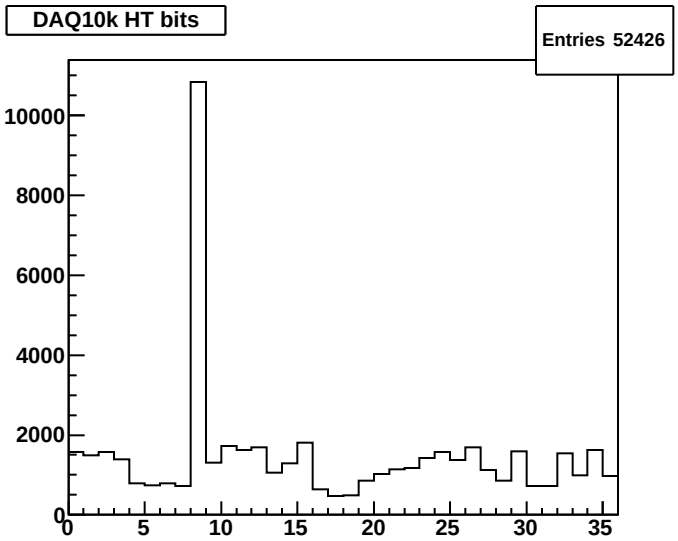

EMC L2 Input - Partial JP Sum
Entries 382
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

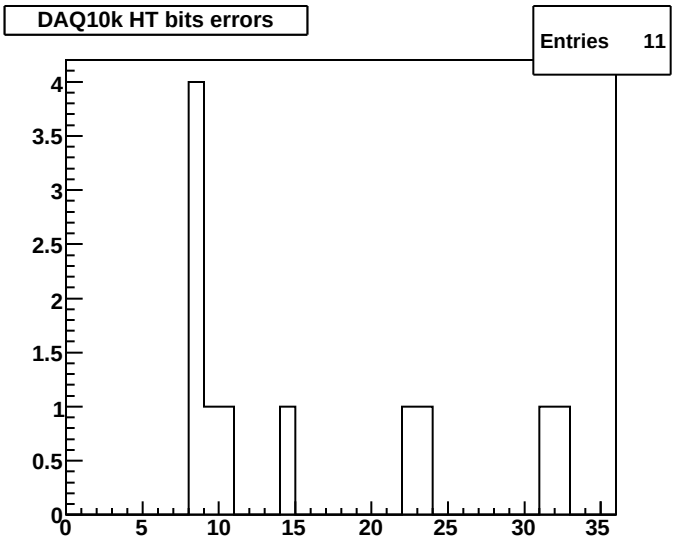

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3940976
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

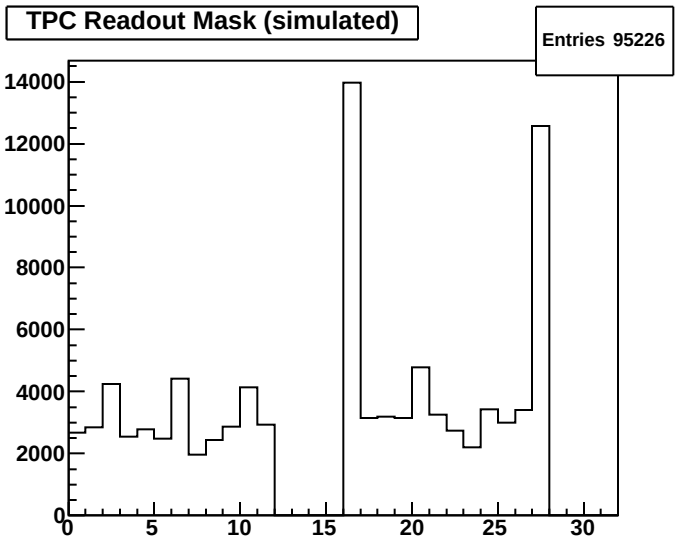

EMC L2 Input - HT01 bits/Partial JP ID
Entries 80
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

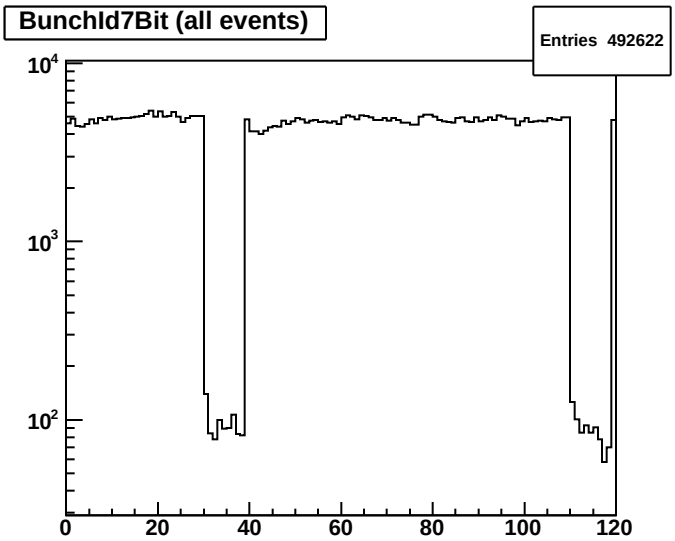

EMC L2 Input - HT23 bits
Entries 3940976
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

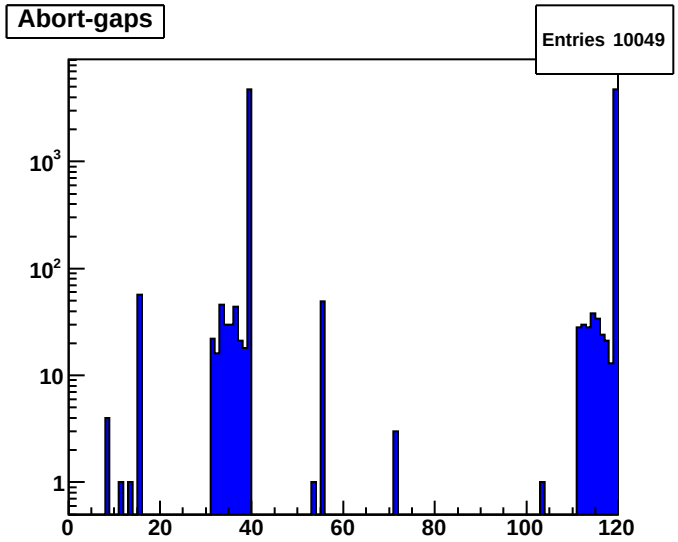

EMC L2 Input - HT23 bits
Entries 18
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

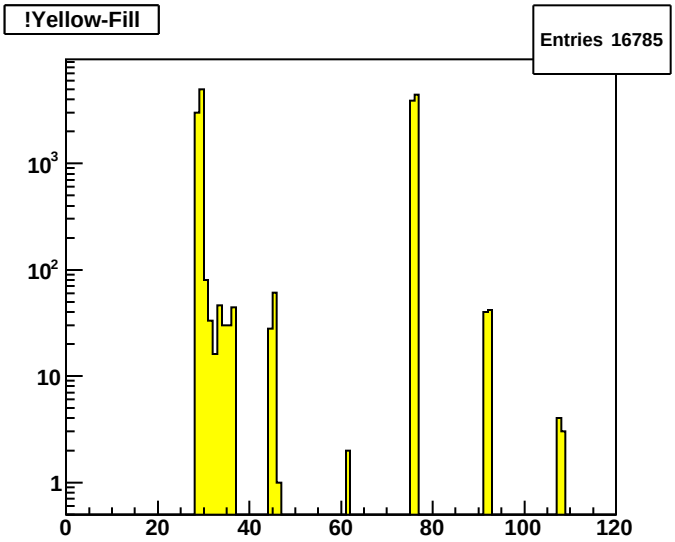

DAQ10k HT bits
Entries 52426

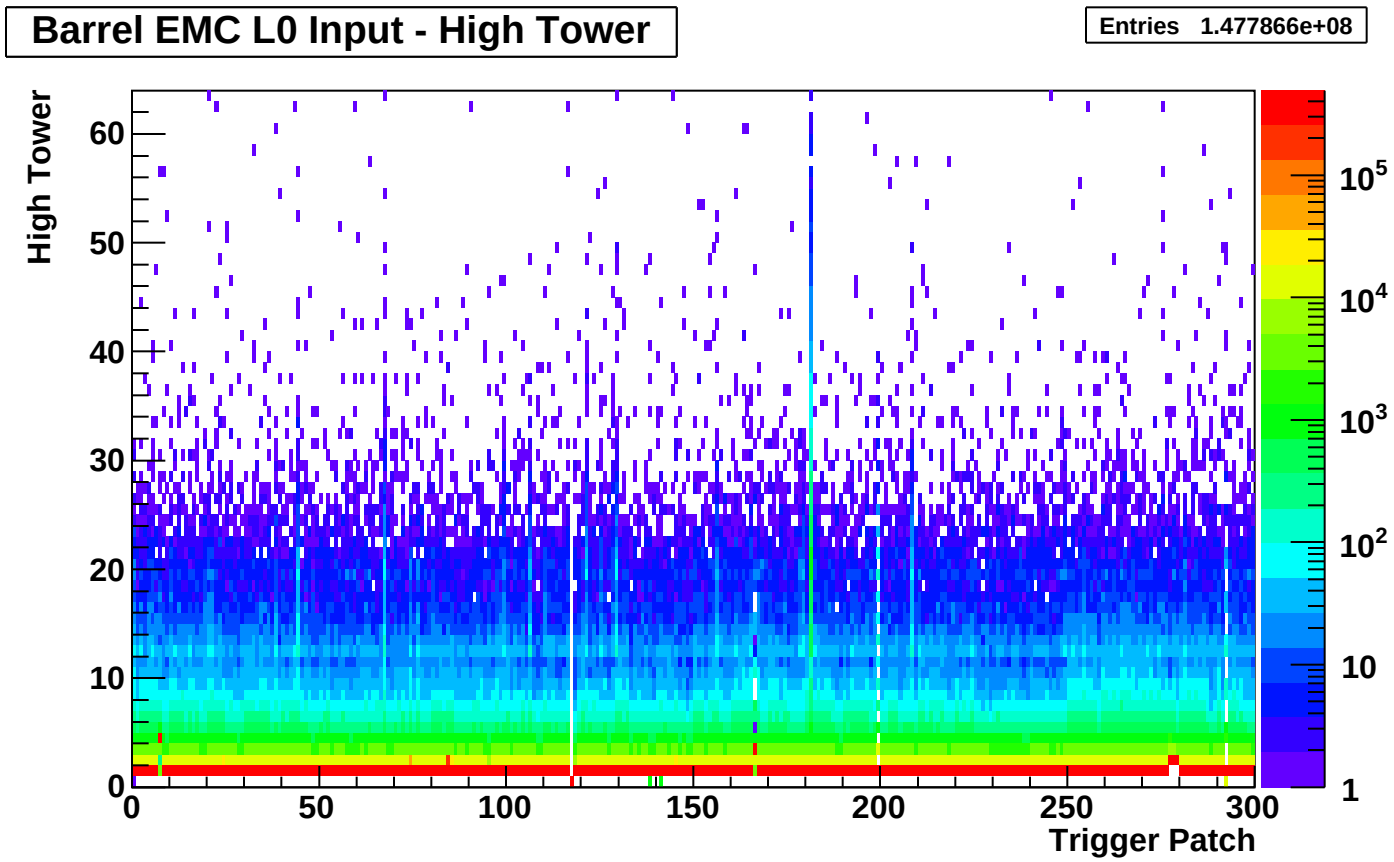

DAQ10k HT bits errors
Entries 11

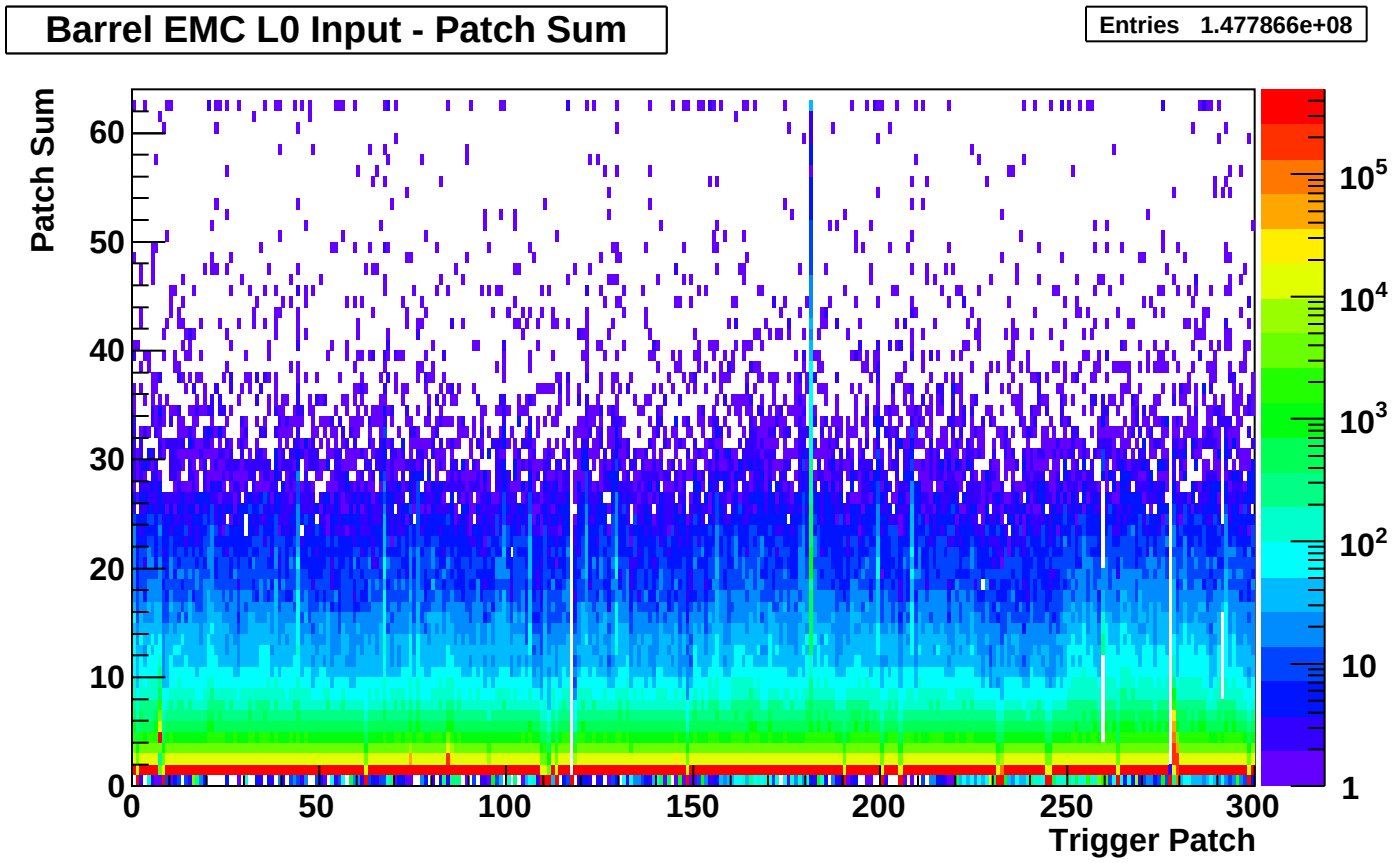

TPC Readout Mask (simulated)
Entries 95226

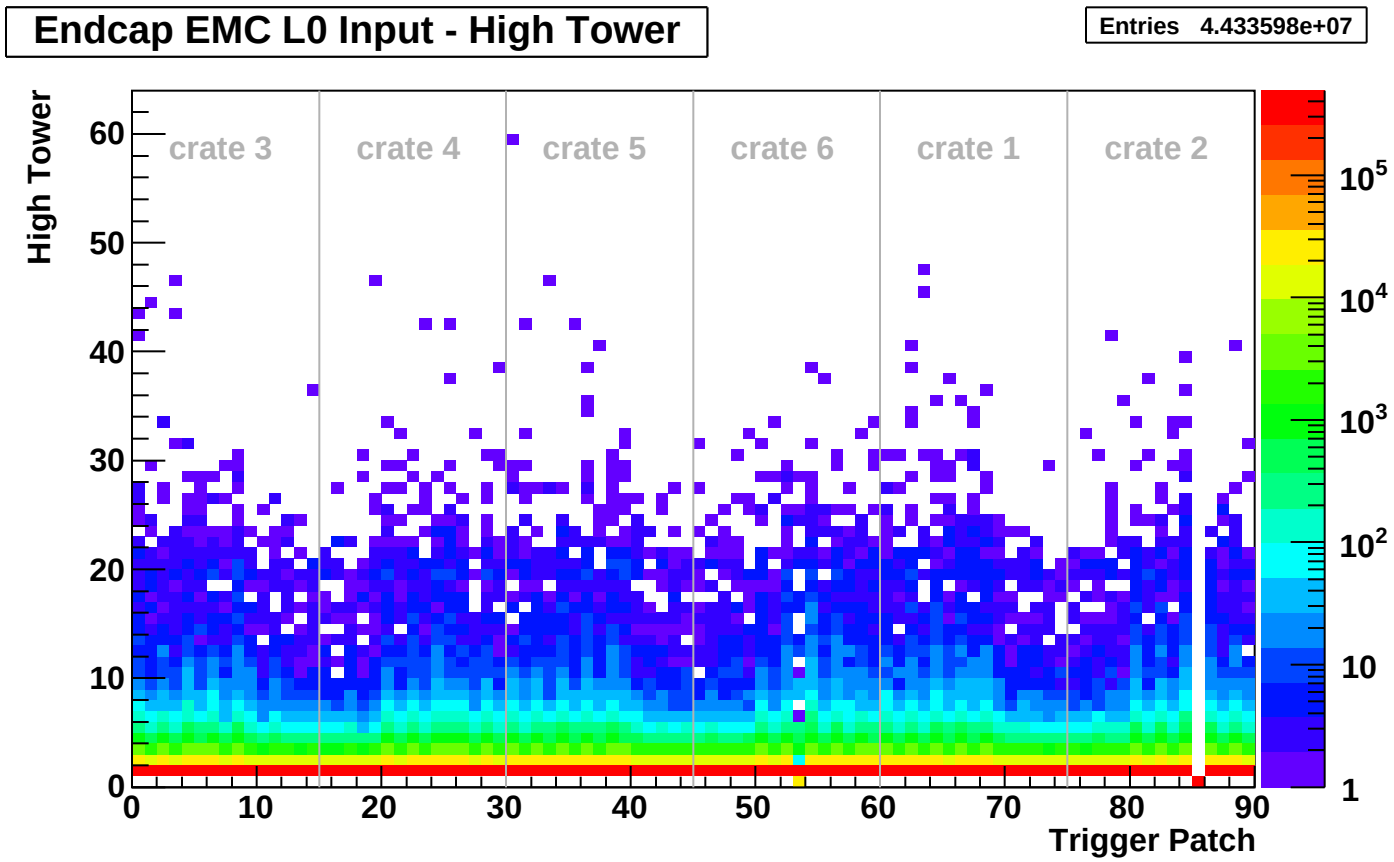

BunchId7Bit (all events)
Entries 492622

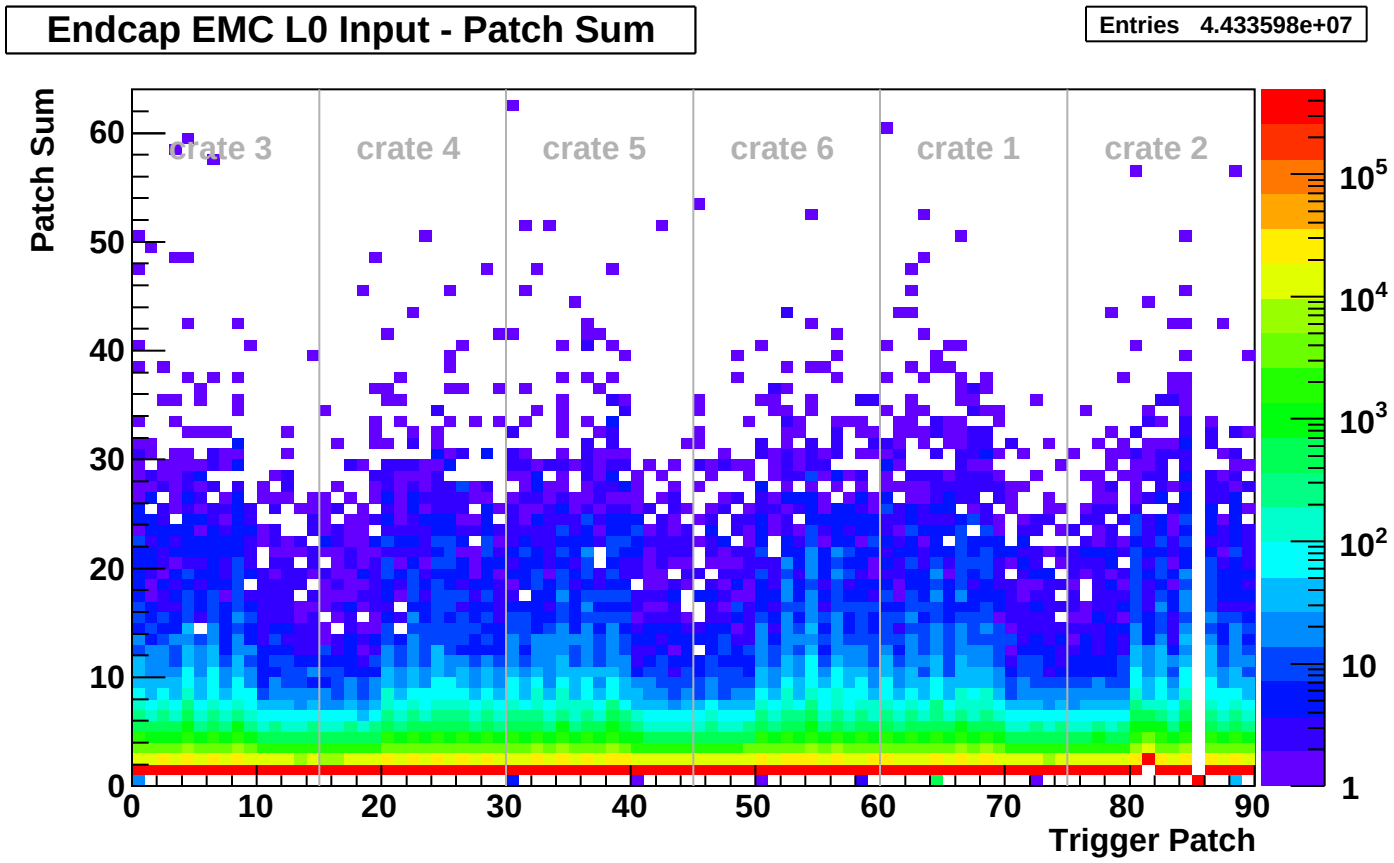

Abort-gaps
Entries 10049

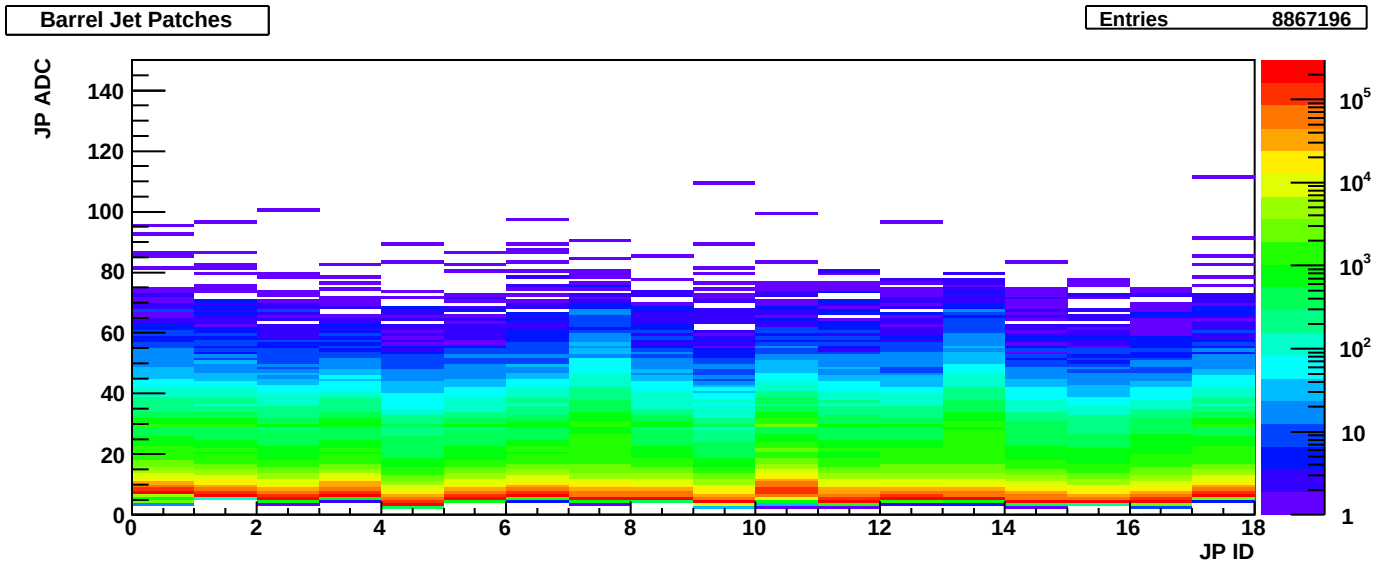

!Yellow-Fill
Entries 16785

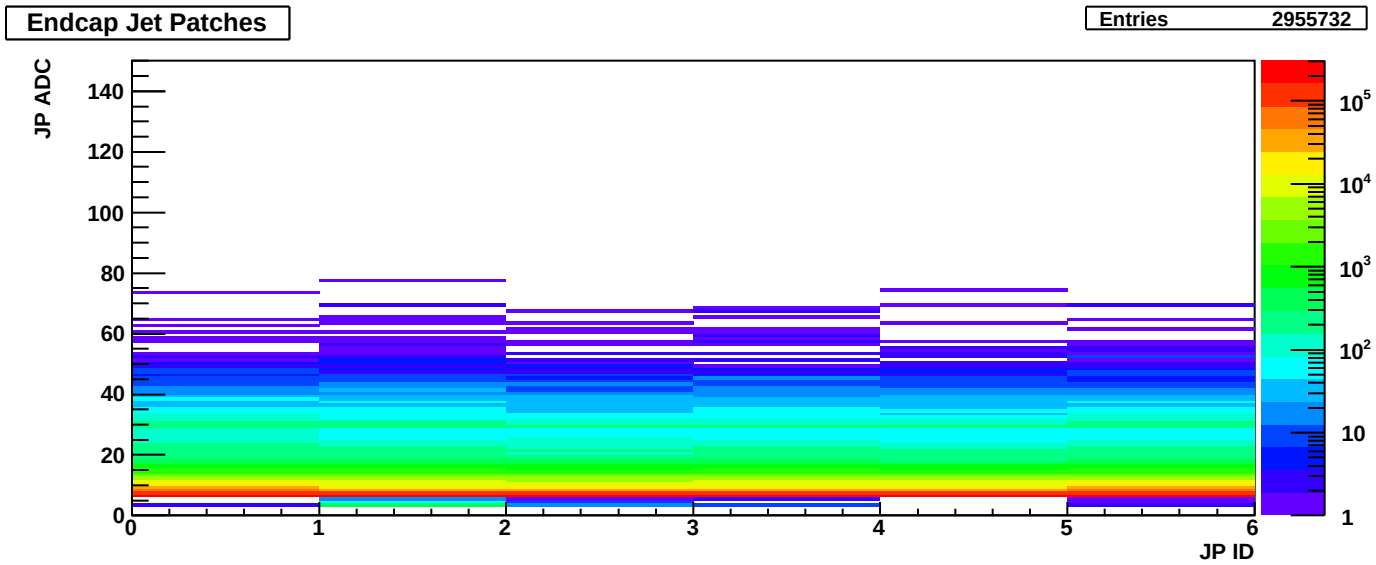

Barrel EMC L0 Input - High Tower
Entries 1.477866e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

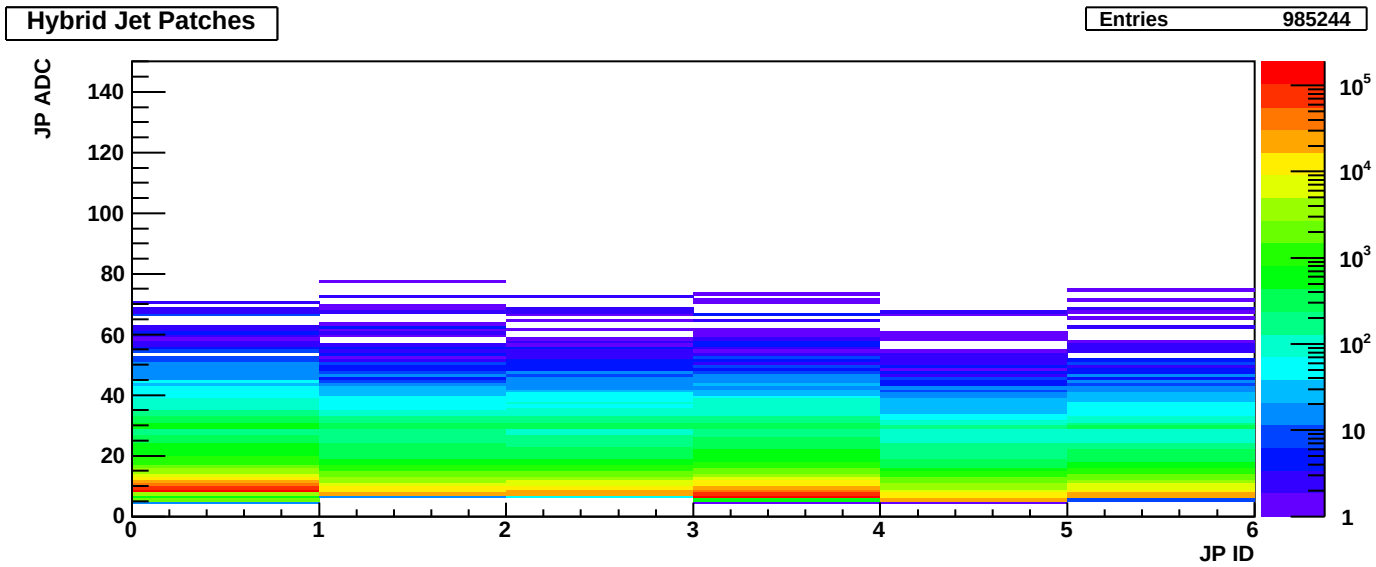

Barrel EMC L0 Input - Patch Sum
Entries 1.477866e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

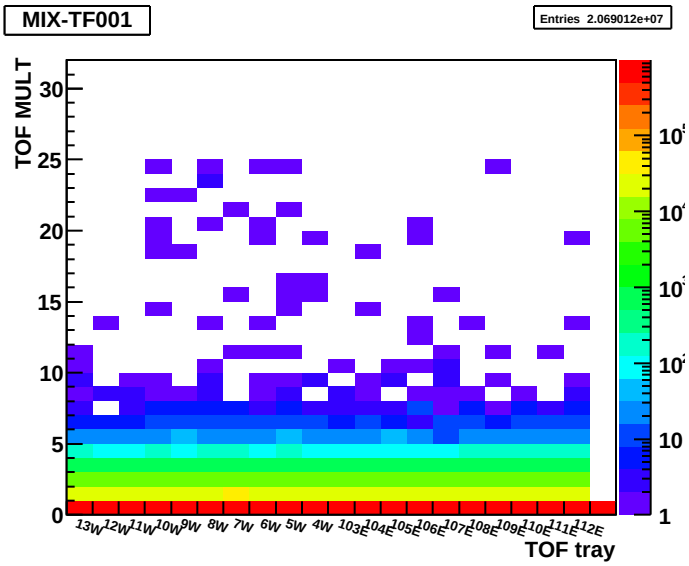

Endcap EMC L0 Input - High Tower
Entries 4.433598e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

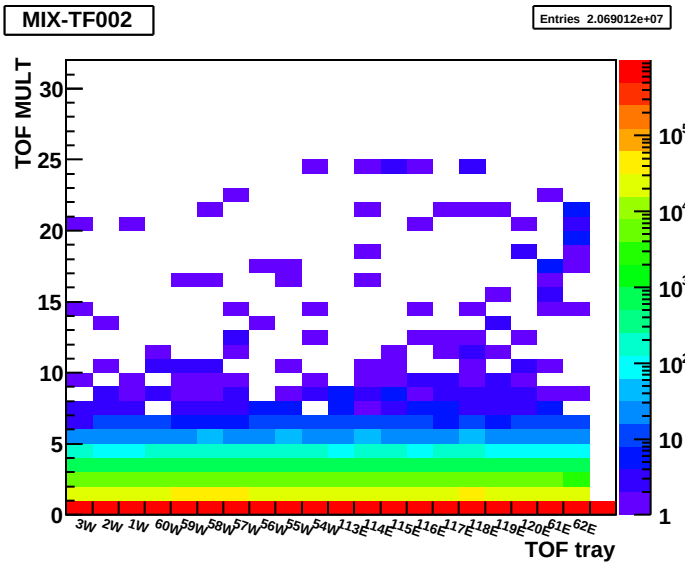

Endcap EMC L0 Input - Patch Sum
Entries 4.433598e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

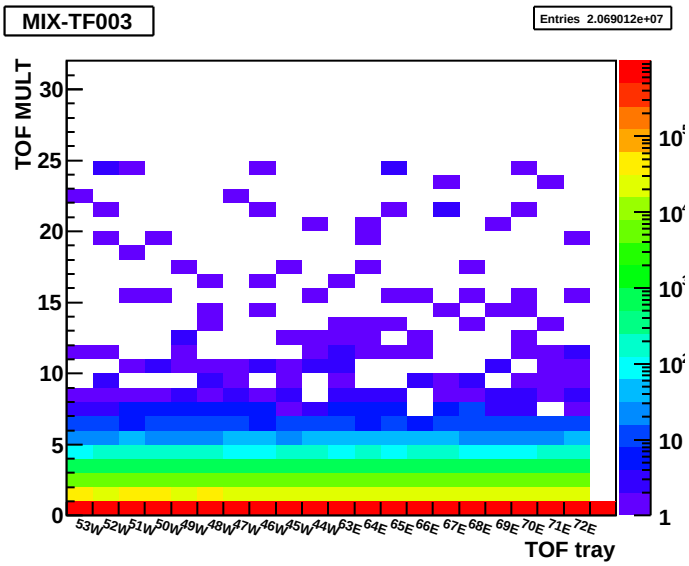

Barrel Jet Patches
Entries 8867196
JP ADC
JP ID

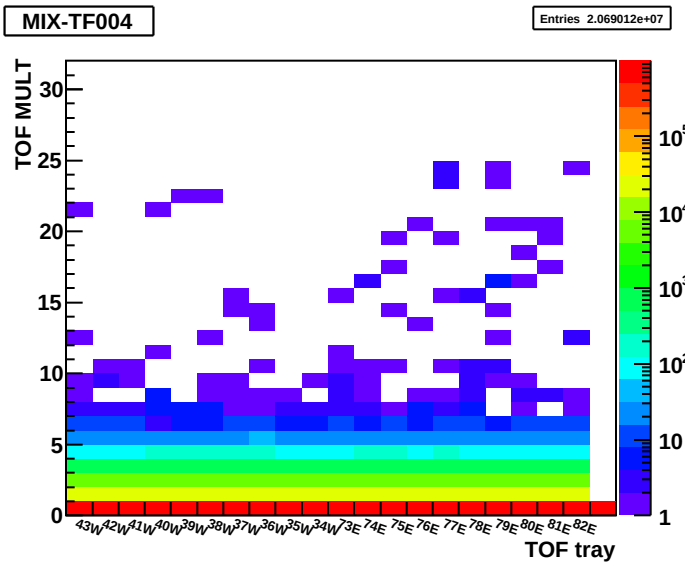

Endcap Jet Patches
Entries 2955732
JP ADC
JP ID

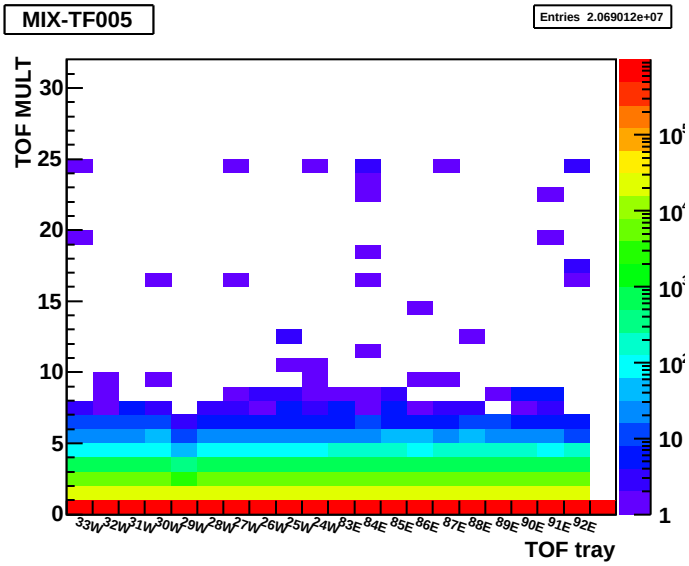

Hybrid Jet Patches
Entries 985244
JP ADC
JP ID

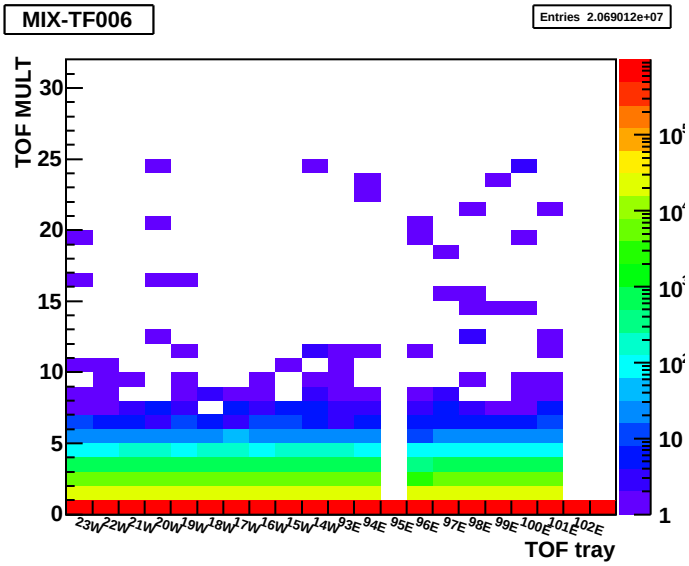

MIX-TF001
Entries 2.069012e+07
TOF MULT
TOF tray

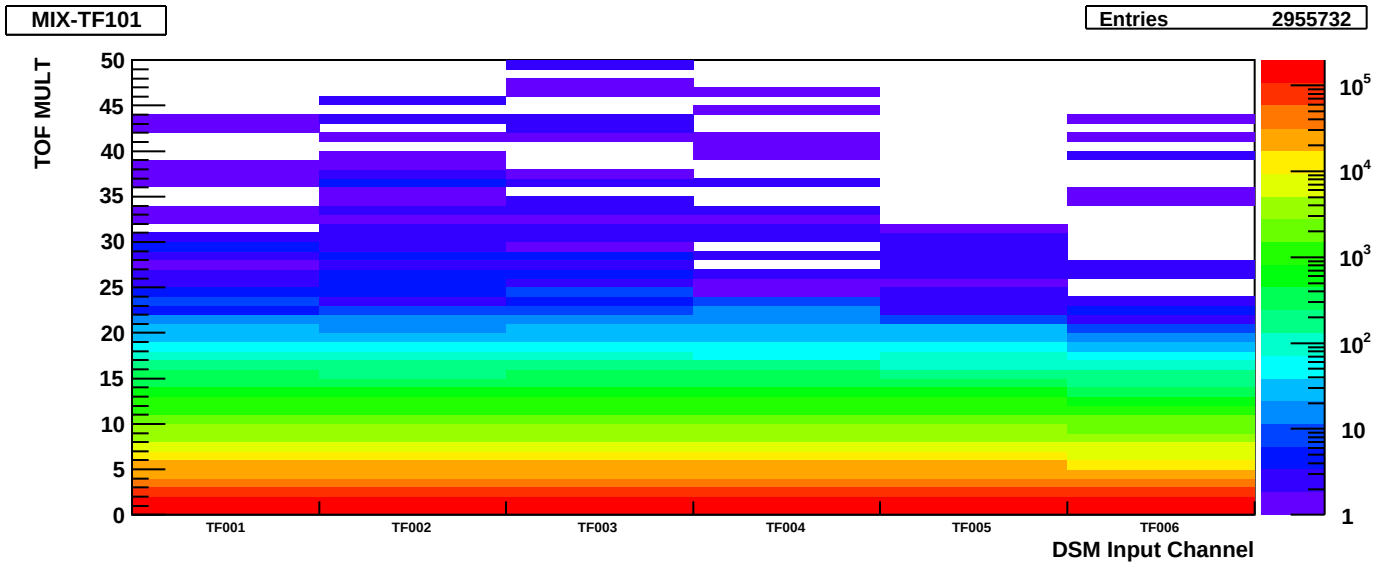

MIX-TF002
Entries 2.069012e+07
TOF MULT
TOF tray

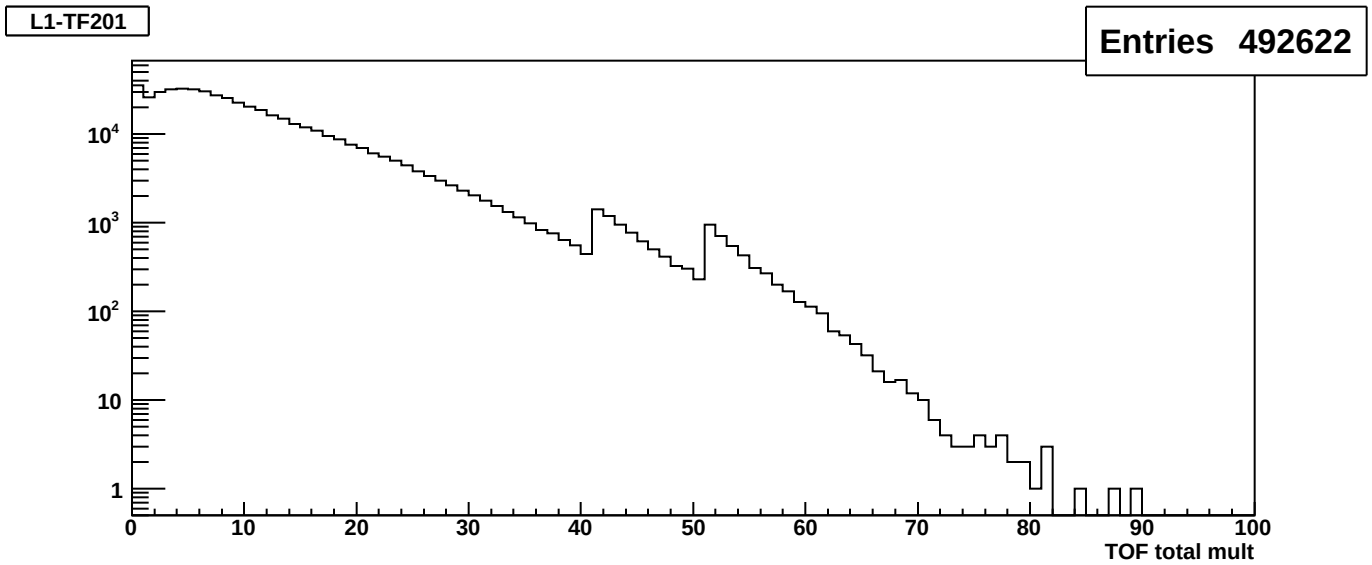

MIX-TF003
Entries 2.069012e+07
TOF MULT
TOF tray

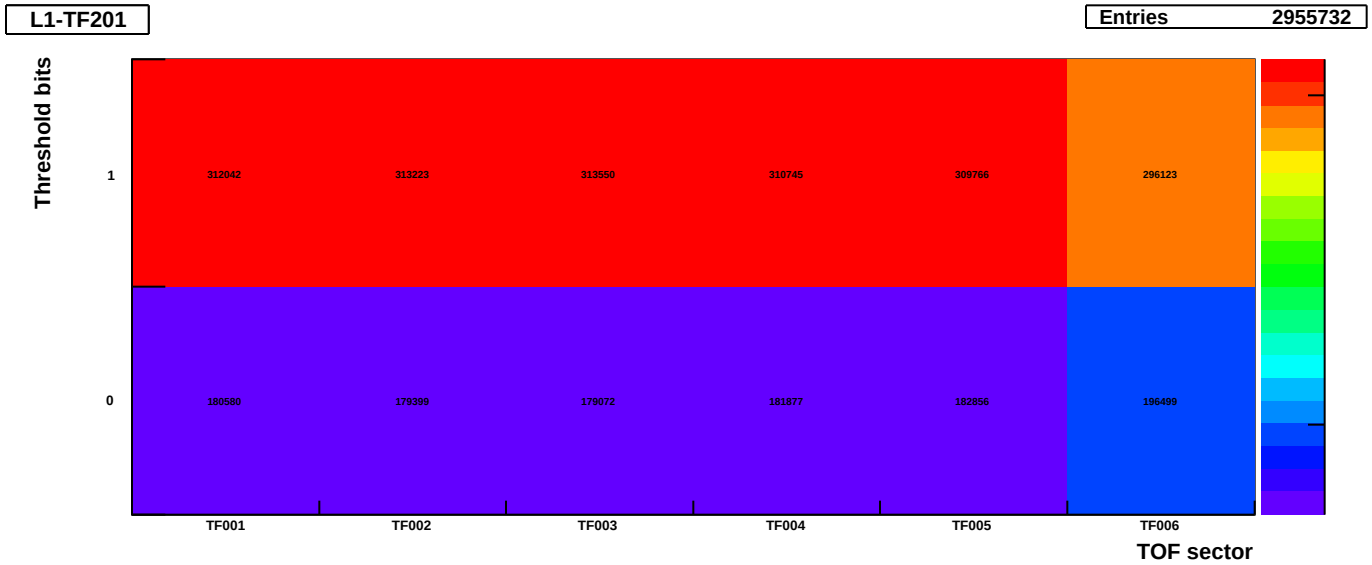

MIX-TF004
Entries 2.069012e+07
TOF MULT
TOF tray

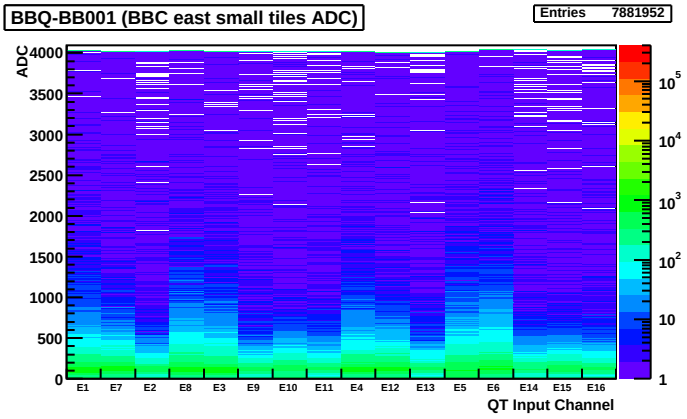

MIX-TF005
Entries 2.069012e+07
TOF MULT
TOF tray

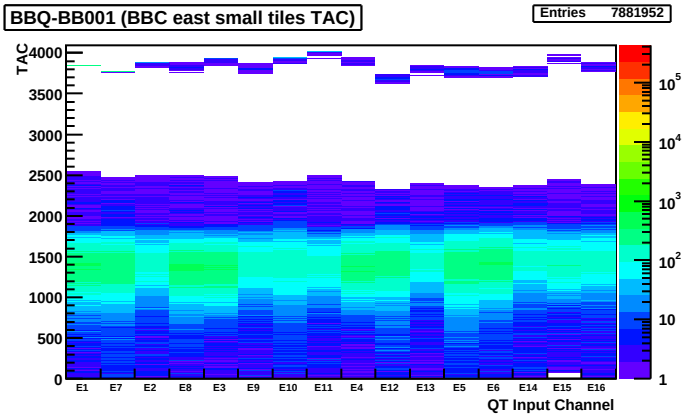

MIX-TF006
Entries 2.069012e+07
TOF MULT
TOF tray

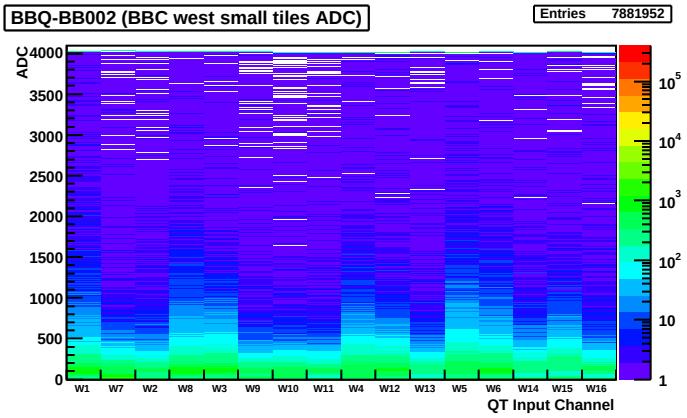

MIX-TF101
Entries 2955732
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

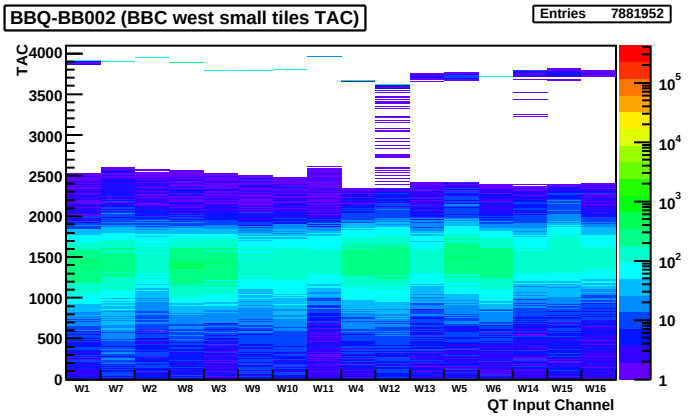

L1-TF201
Entries 492622
TOF total mult

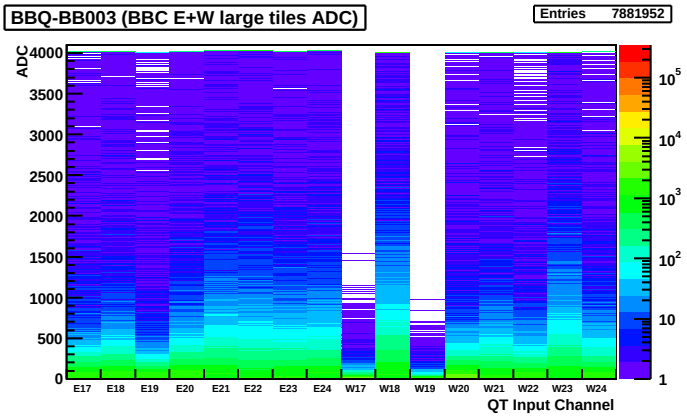

L1-TF201
Entries 2955732
Threshold bits
312042
313223
313550
310745
309766
296123
180580
179399
179072
181877
182856
196499
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

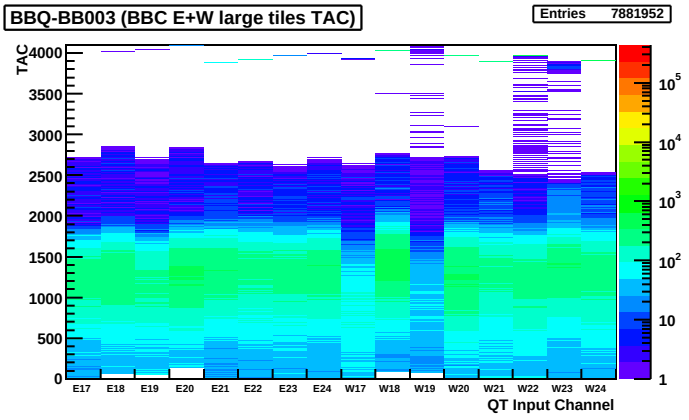

BBQ-BB001 (BBC east small tiles ADC)
Entries 7881952
ADC
QT Input Channel

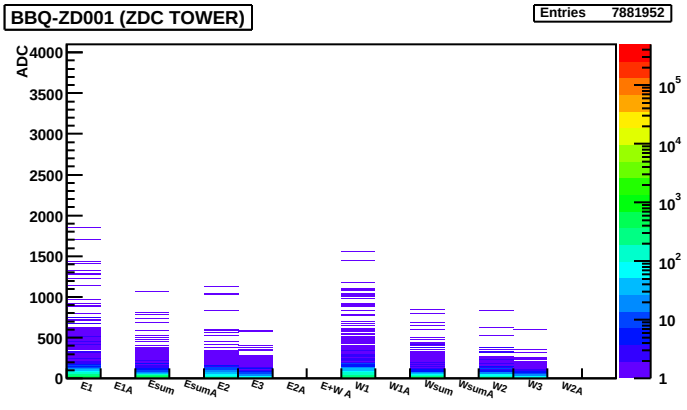

BBQ-BB001 (BBC east small tiles TAC)
Entries 7881952
TAC
QT Input Channel

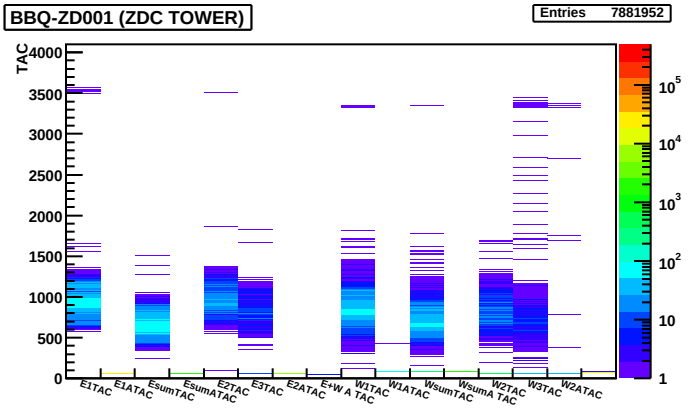

BBQ-BB002 (BBC west small tiles ADC)
Entries 7881952
ADC
QT Input Channel

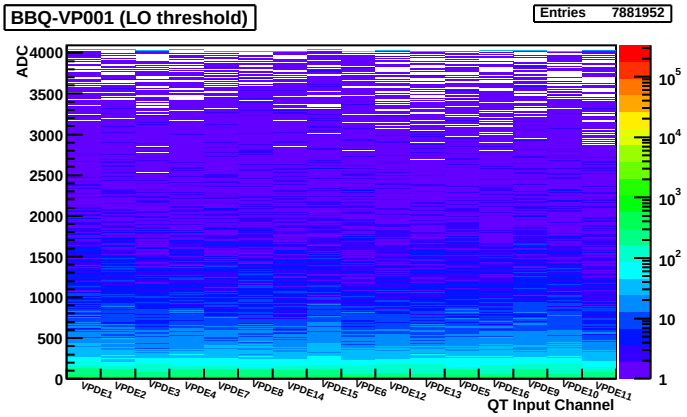

BBQ-BB002 (BBC west small tiles TAC)
Entries 7881952
TAC
QT Input Channel

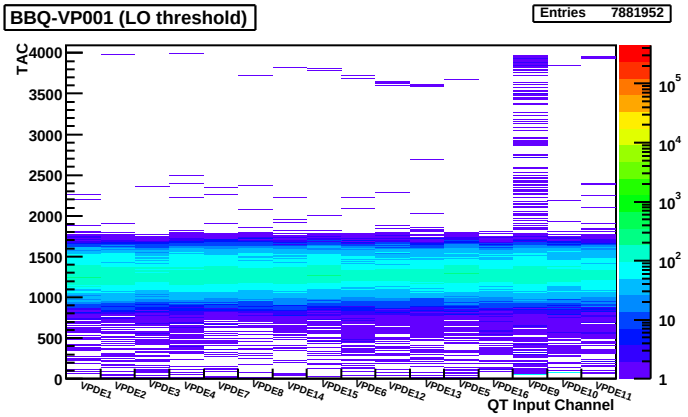

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7881952
ADC
QT Input Channel

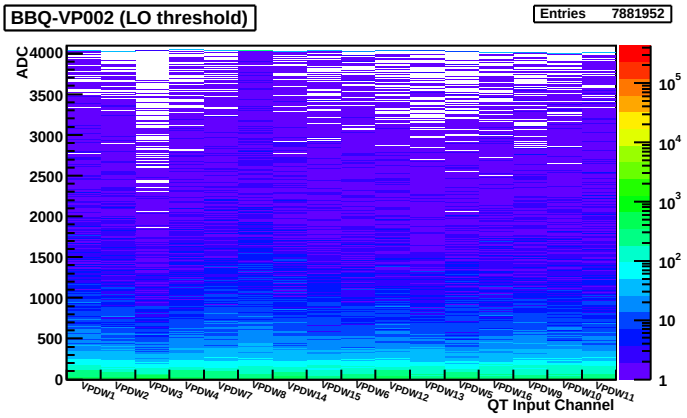

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7881952
TAC
QT Input Channel

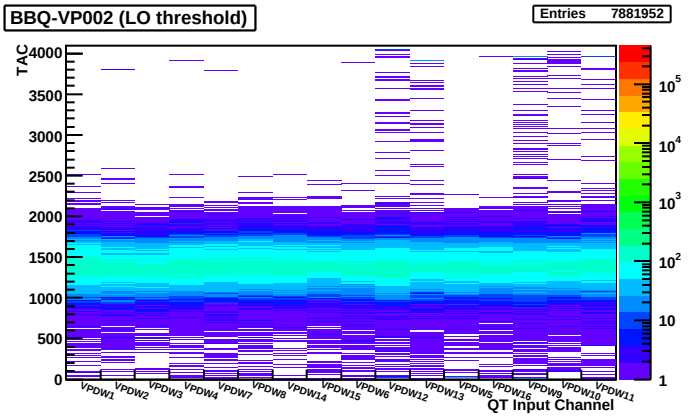

BBQ-ZD001 (ZDC TOWER)
Entries 7881952
ADC

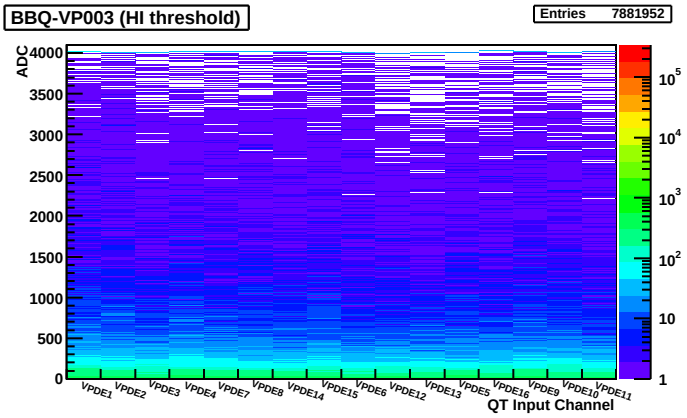

BBQ-ZD001 (ZDC TOWER)
Entries 7881952
TAC

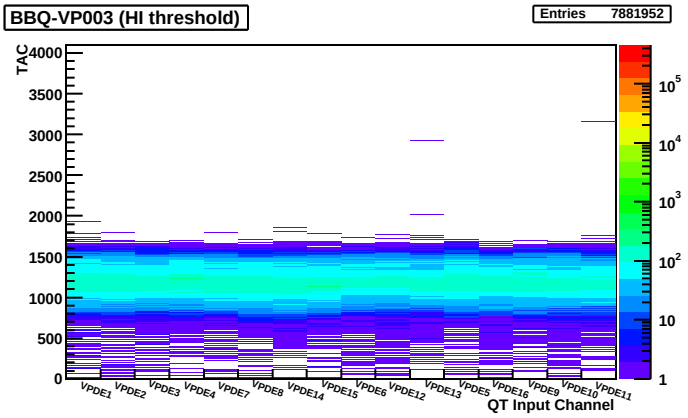

BBQ-VP001 (LO threshold)
Entries 7881952
ADC
QT Input Channel

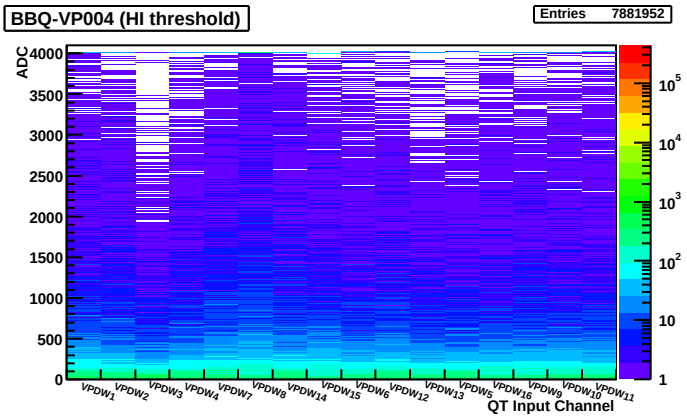

BBQ-VP001 (LO threshold)
Entries 7881952
TAC
QT Input Channel

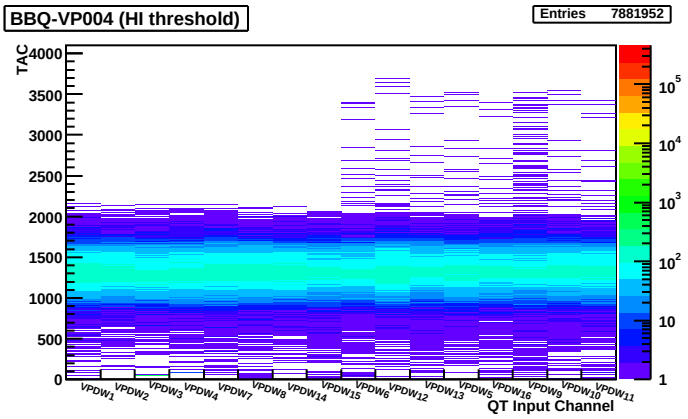

BBQ-VP002 (LO threshold)
Entries 7881952
ADC
QT Input Channel

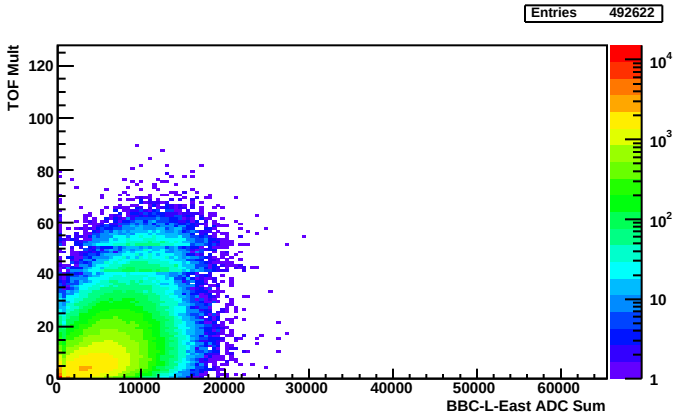

BBQ-VP002 (LO threshold)
Entries 7881952
TAC
QT Input Channel

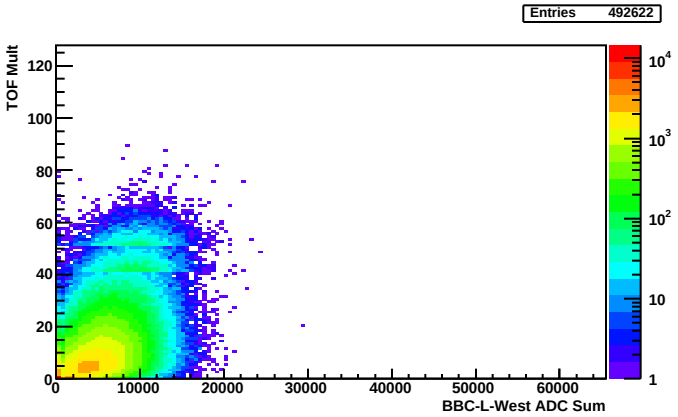

BBQ-VP003 (HI threshold)
Entries 7881952
ADC
QT Input Channel

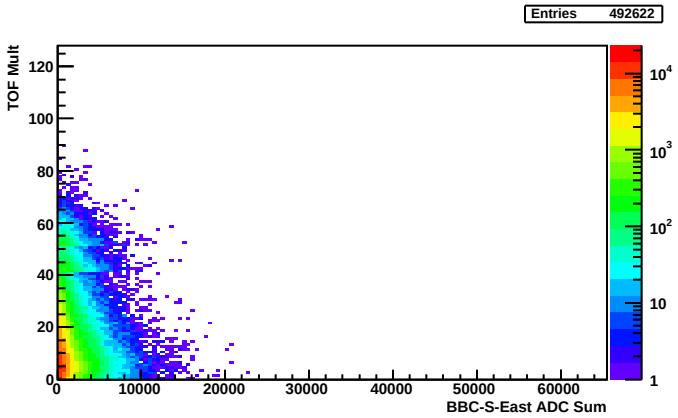

BBQ-VP003 (HI threshold)
Entries 7881952
TAC
QT Input Channel

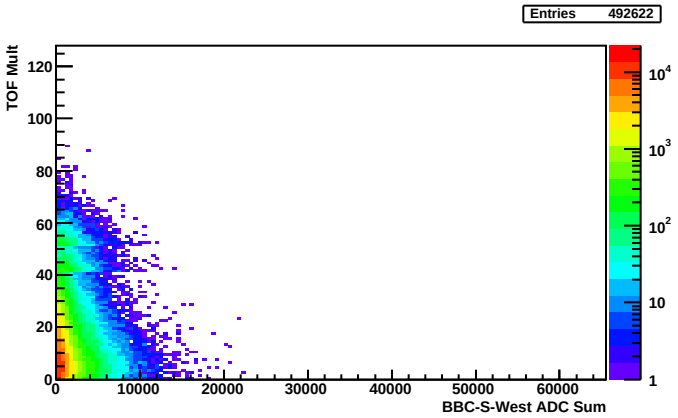

BBQ-VP004 (HI threshold)
Entries 7881952
ADC
QT Input Channel

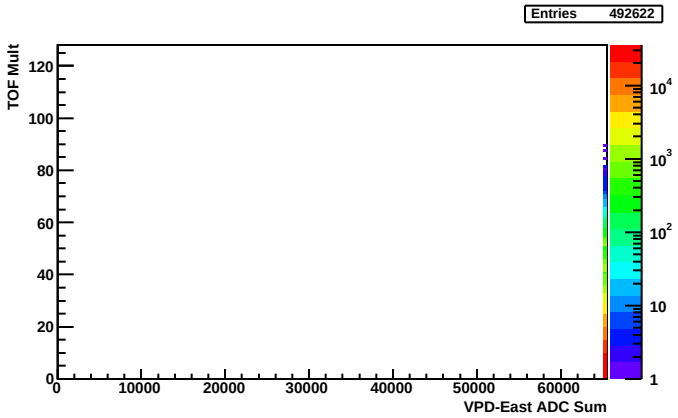

BBQ-VP004 (HI threshold)
Entries 7881952
TAC
QT Input Channel

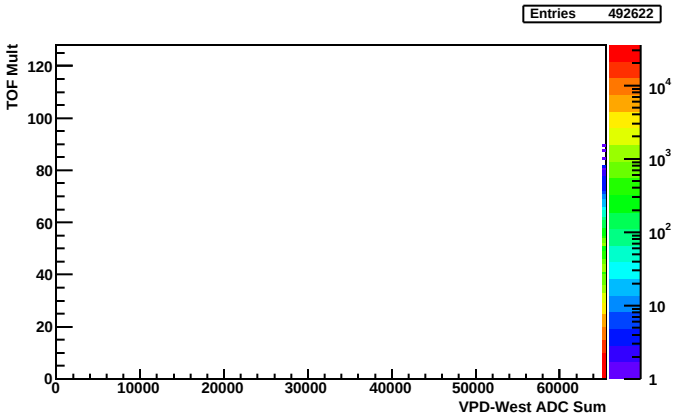

Entries 492622
TOF Mult
BBC-L-East ADC Sum

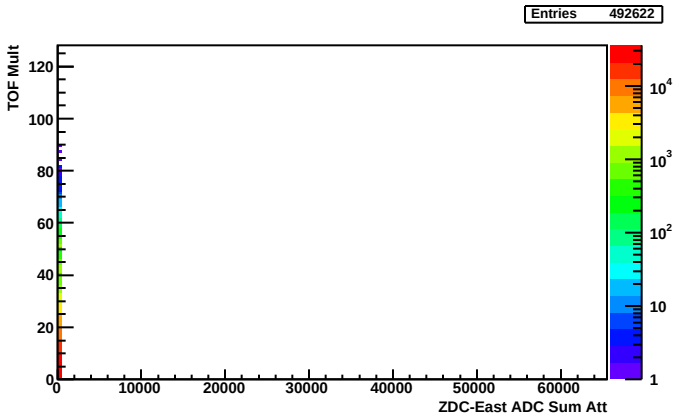

Entries 492622
TOF Mult
BBC-L-West ADC Sum

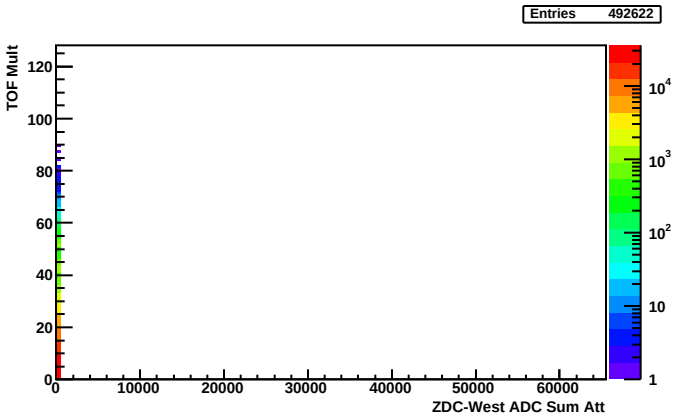

Entries 492622
TOF Mult
BBC-S-East ADC Sum

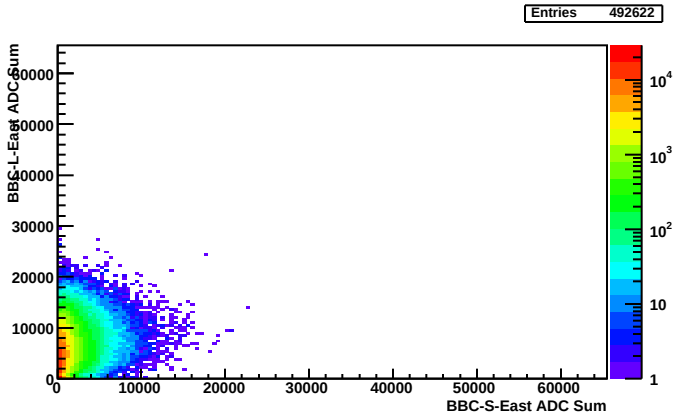

Entries 492622
TOF Mult
BBC-S-West ADC Sum


Entries 492622
TOF Mult
VPD-East ADC Sum


Entries 492622
TOF Mult
VPD-West ADC Sum


Entries 492622
TOF Mult
ZDC-East ADC Sum Att


Entries 492622
TOF Mult
ZDC-West ADC Sum Att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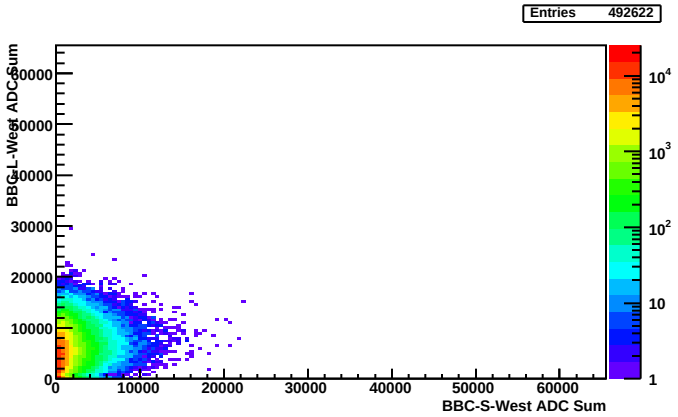
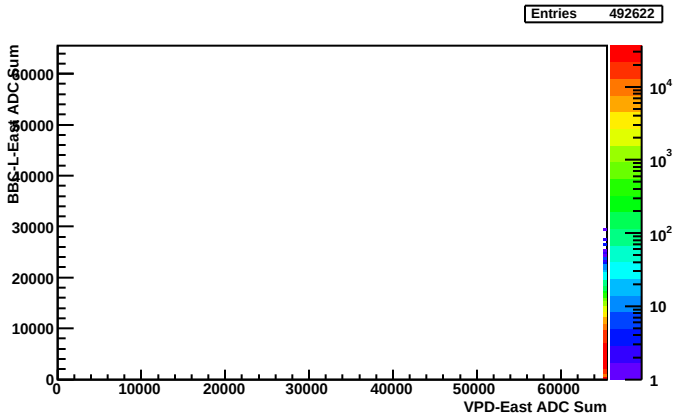
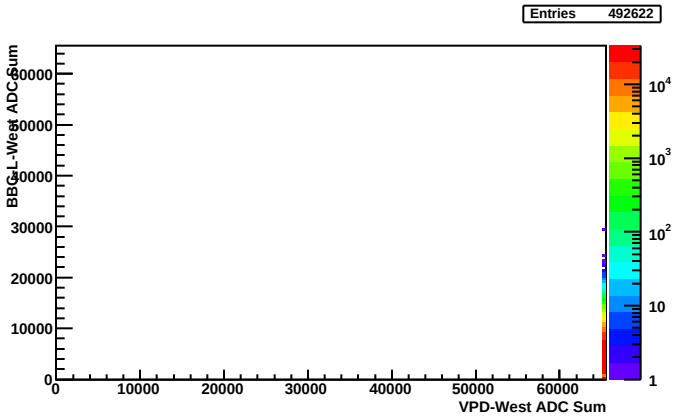
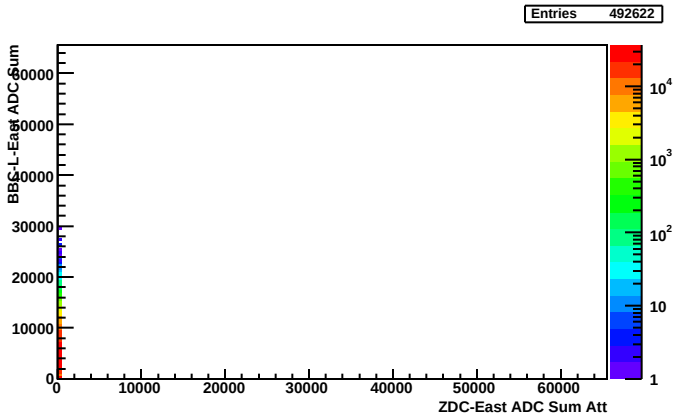
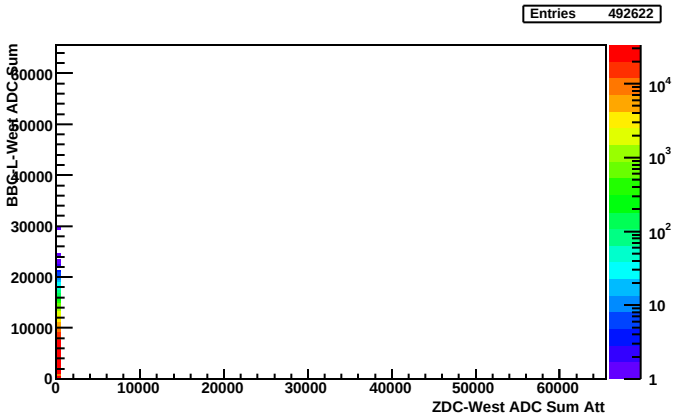
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 492622
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 492622
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 492622
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 492622
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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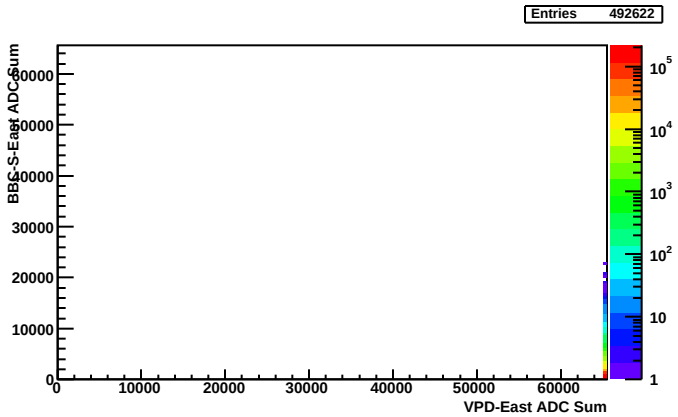
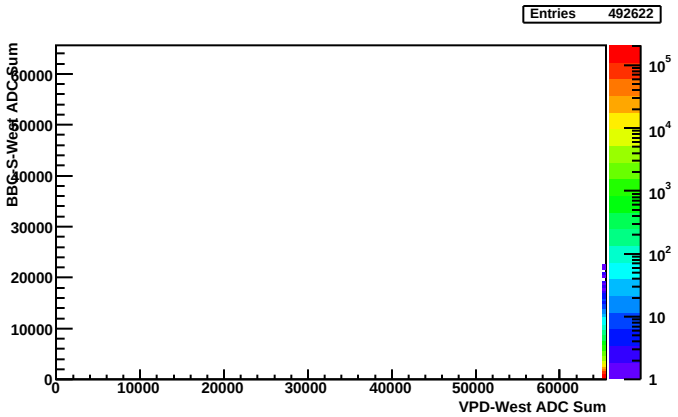
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 492622
BBC-S-East ADC Sum
VPD-East ADC Sum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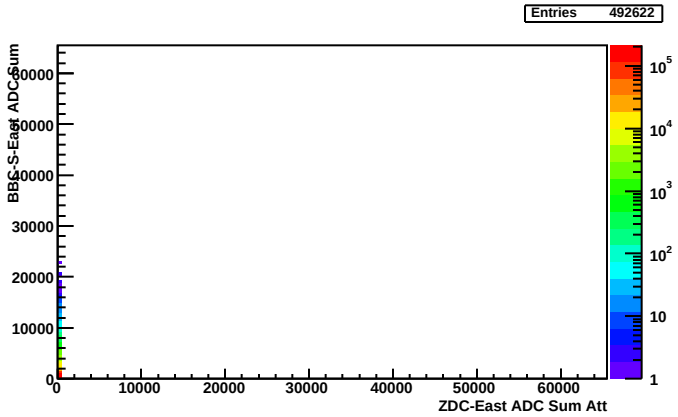
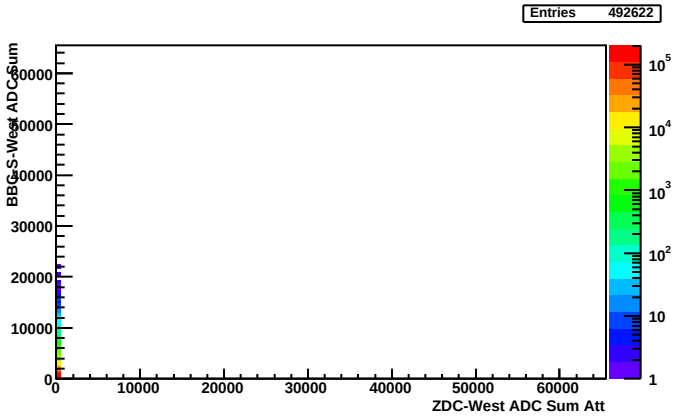
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 492622
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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BBC-S-West ADC Sum
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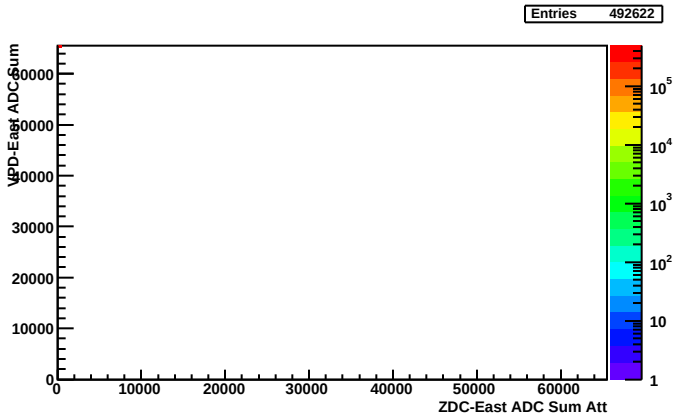
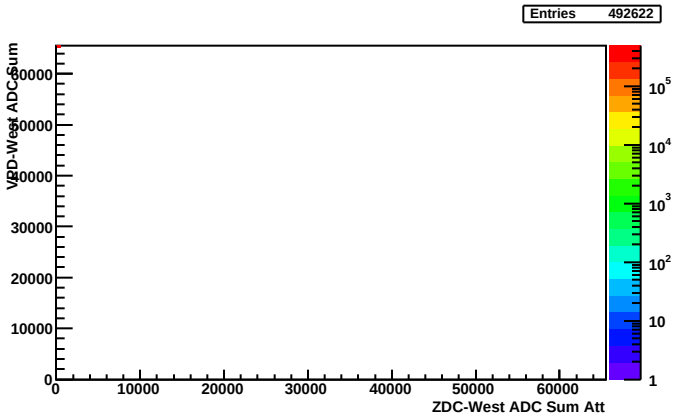

Entries 492622
VPD-East ADC Sum
ZDC-East ADC Sum Att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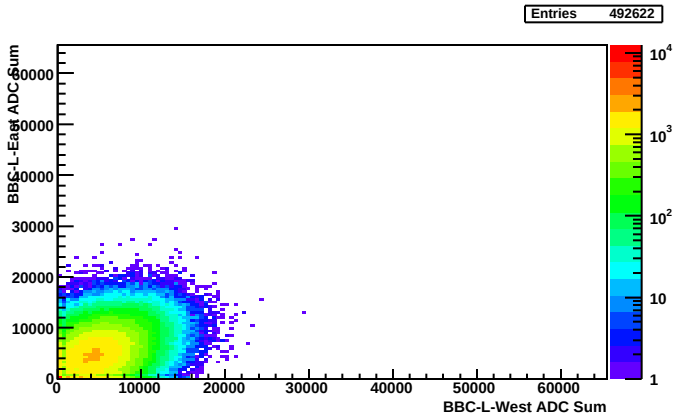
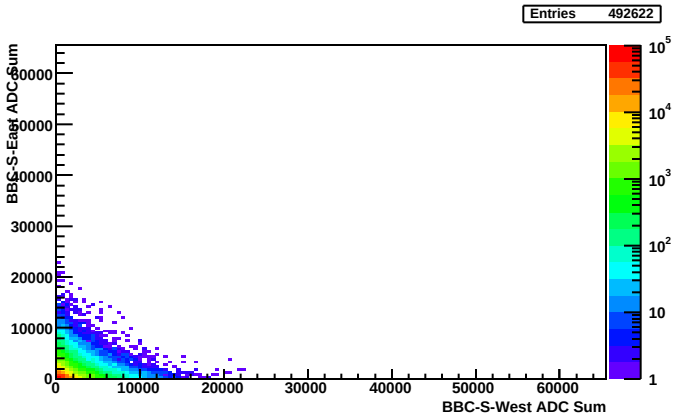
ZDC-West ADC Sum Att


Entries 492622
BBC-L-East ADC Sum
BBC-L-West ADC Sum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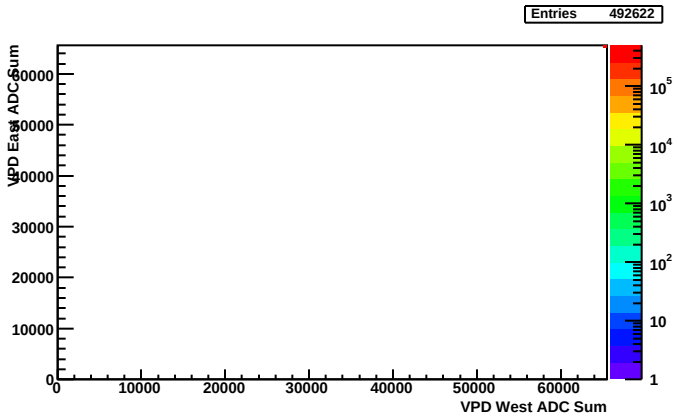
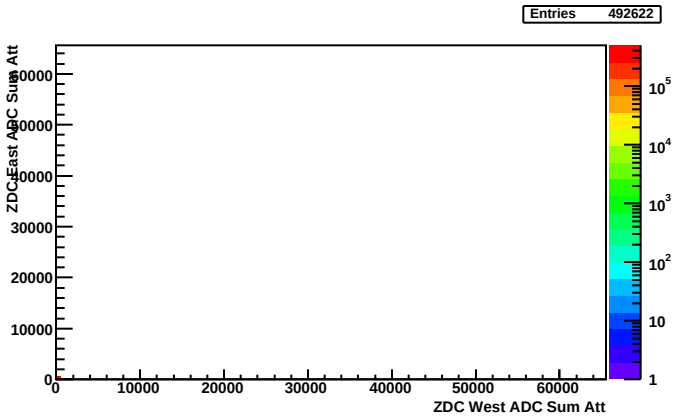
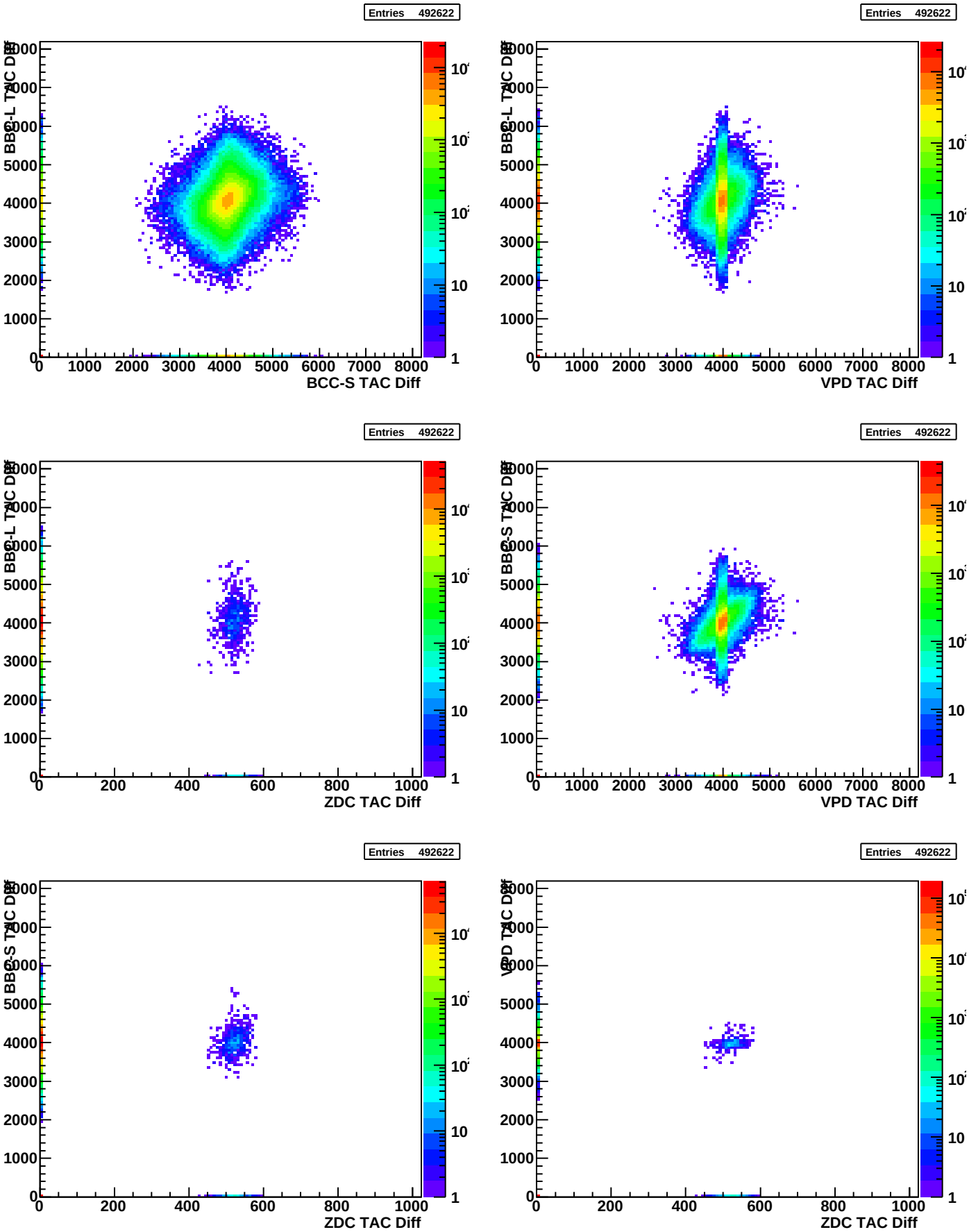
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 492622
VPD East ADC Sum
VPD West ADC Sum


Entries 492622
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 492622
BBC-L TAC Diff
BCC-S TAC Diff
Entries 492622
BBC-L TAC Diff
VPD TAC Diff
Entries 492622
BBC-L TAC Diff
ZDC TAC Diff
Entries 492622
BBC-S TAC Diff
VPD TAC Diff
Entries 492622
BBC-S TAC Diff
ZDC TAC Diff
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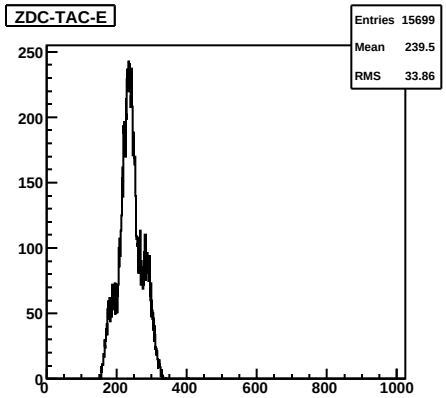
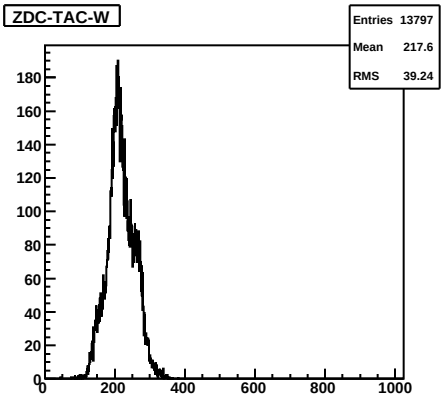
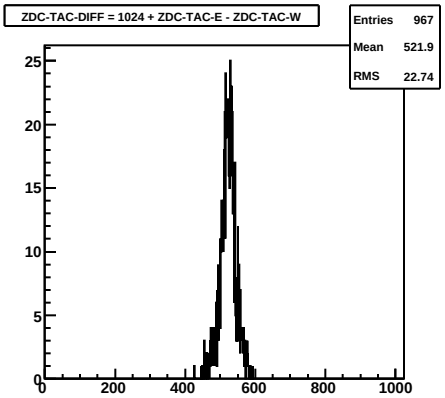
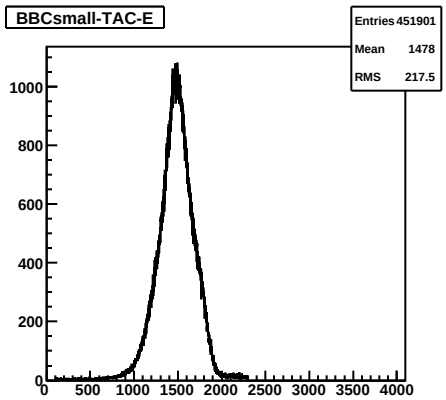
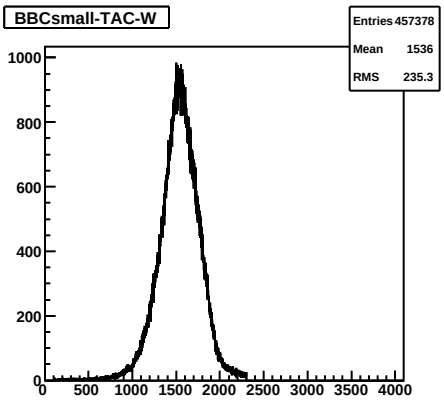
VPD TAC Diff
ZDC TAC Diff

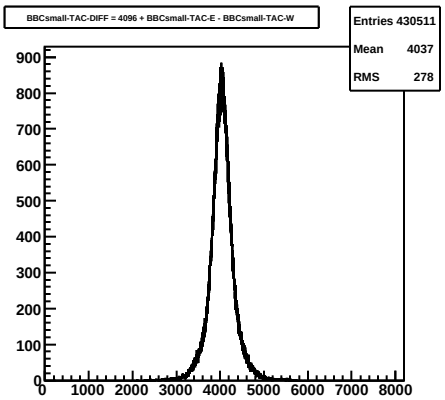

ZDC-TAC-E
Entries 15699
Mean 239.5
RMS 33.86

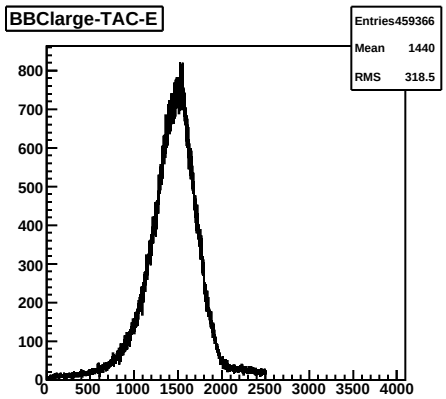

ZDC-TAC-W
Entries 13797
Mean 217.6
RMS 39.24

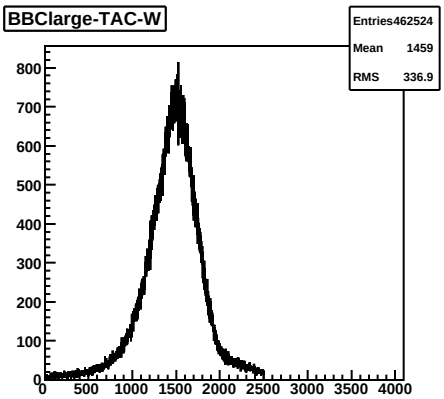

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 967
Mean 521.9
RMS 22.74

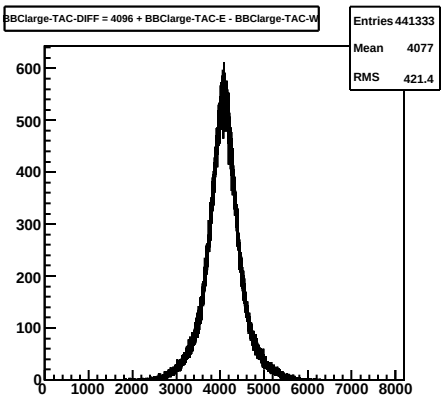

BBCsmall-TAC-E
Entries 451901
Mean 1478
RMS 217.5

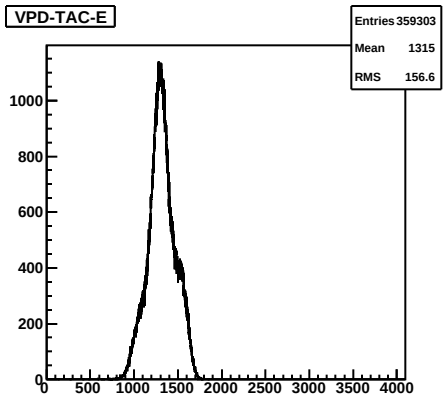

BBCsmall-TAC-W
Entries 457378
Mean 1536
RMS 235.3

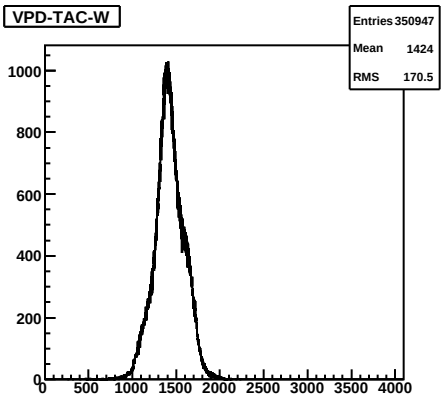

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 430511
Mean 4037
RMS 278

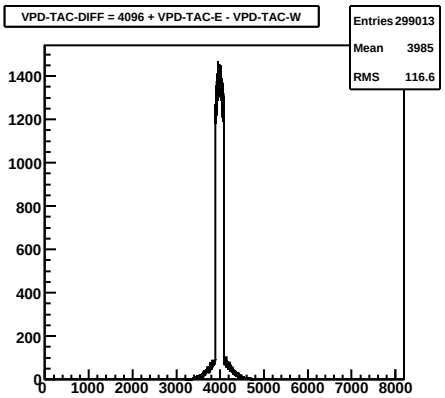

BBClarge-TAC-E
Entries 459366
Mean 1440
RMS 318.5

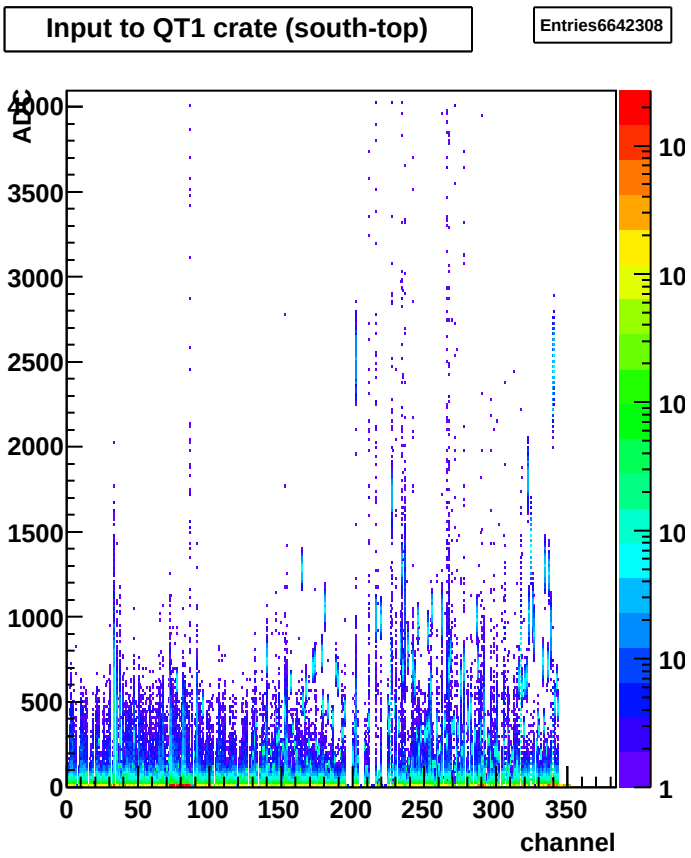

BBClarge-TAC-W
Entries 462524
Mean 1459
RMS 336.9

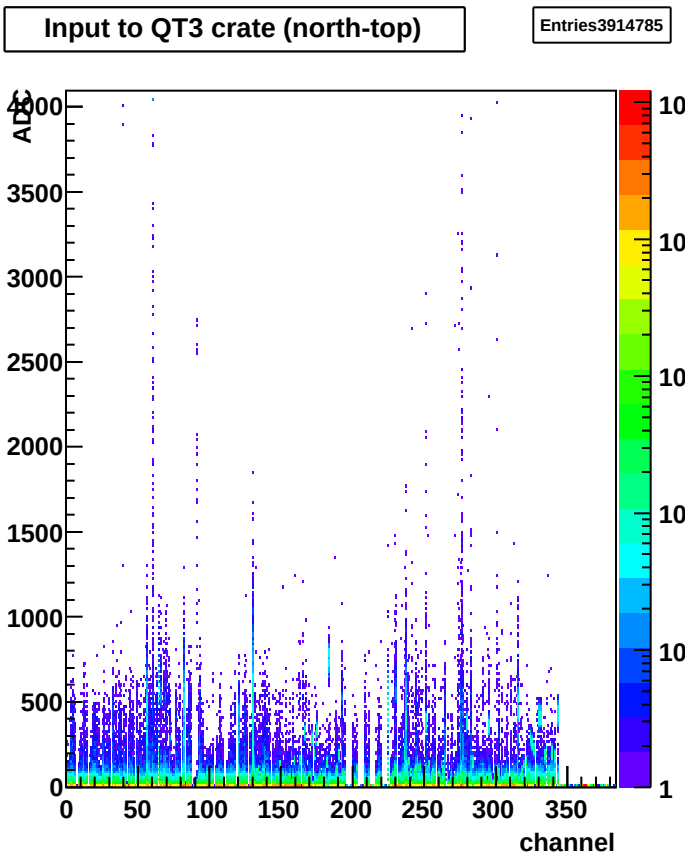

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 441333
Mean 4077
RMS 421.4

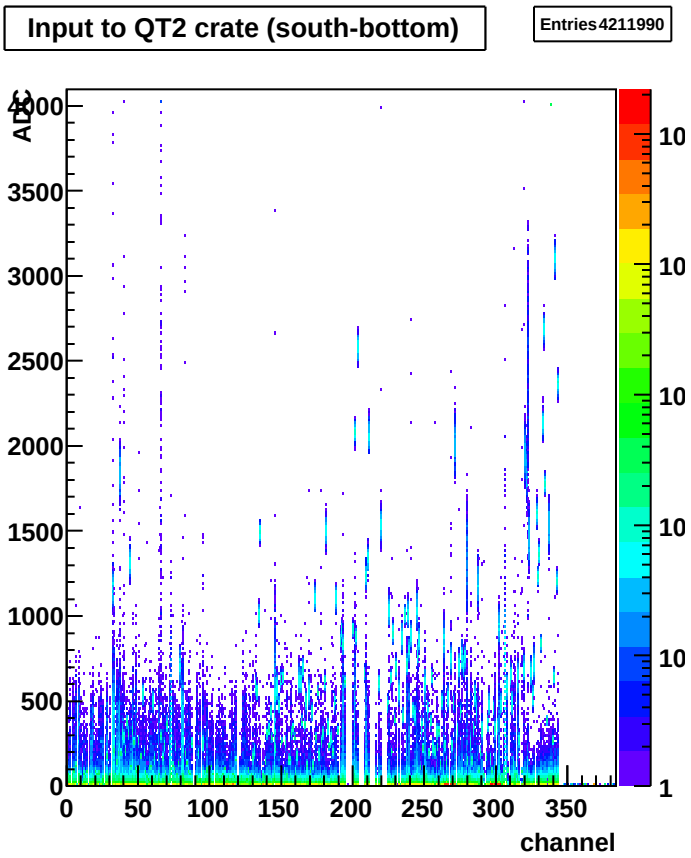

VPD-TAC-E
Entries 359303
Mean 1315
RMS 156.6

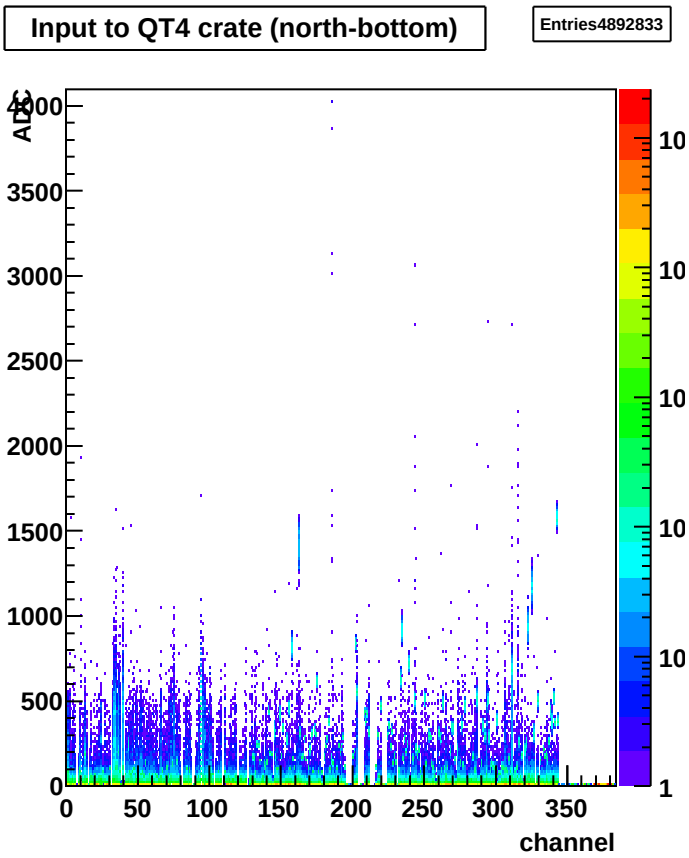

VPD-TAC-W
Entries 350947
Mean 1424
RMS 170.5

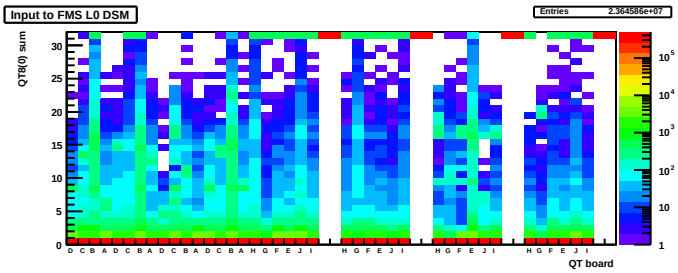

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 299013
Mean 3985
RMS 116.6

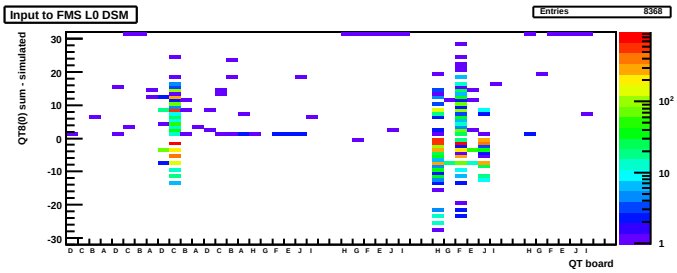

Input to QT1 crate (south-top)
Entries6642308
ADC
channel

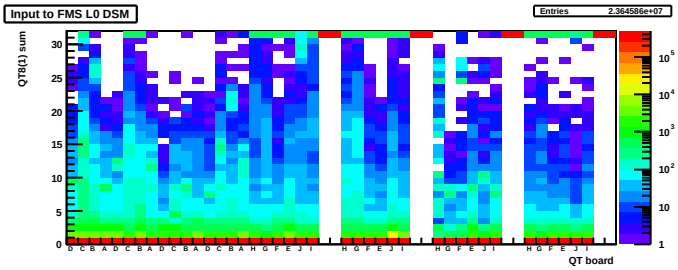

Input to QT3 crate (north-top)
Entries3914785
ADC
channel

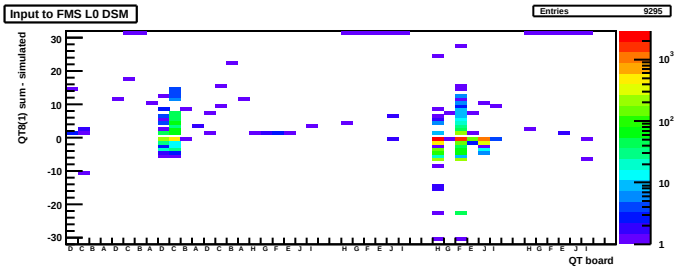

Input to QT2 crate (south-bottom)
Entries4211990
ADC
channel

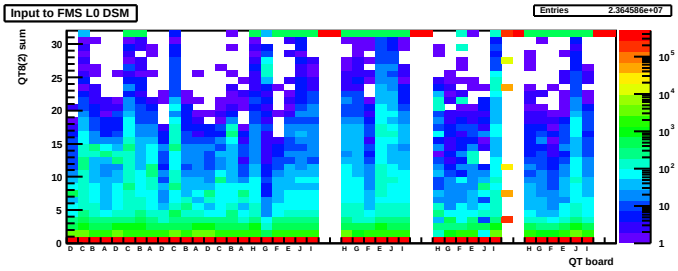

Input to QT4 crate (north-bottom)
Entries4892833
ADC
channel

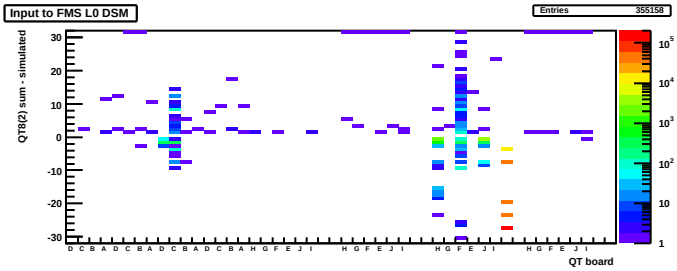

Input to FMS L0 DSM
Entries 2.364586e+07
QT8(0) sum
QT board

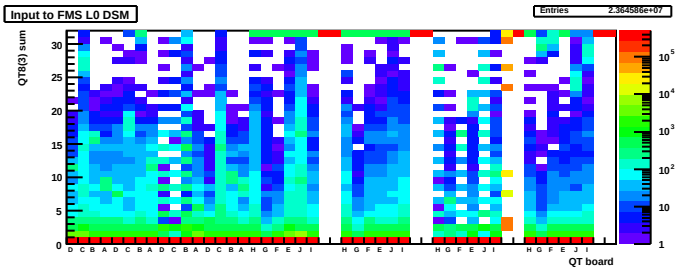

Input to FMS L0 DSM
Entries 8368
QT8(0) sum - simulated
QT board

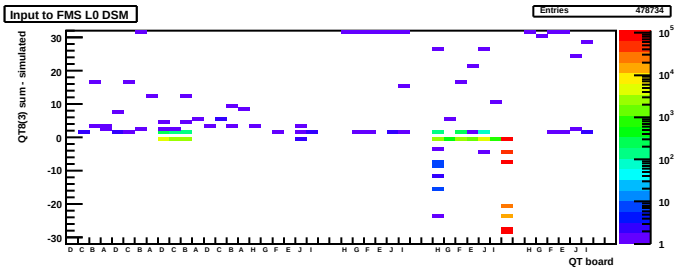

Input to FMS L0 DSM
Entries 2.364586e+07
QT8(1) sum
QT board

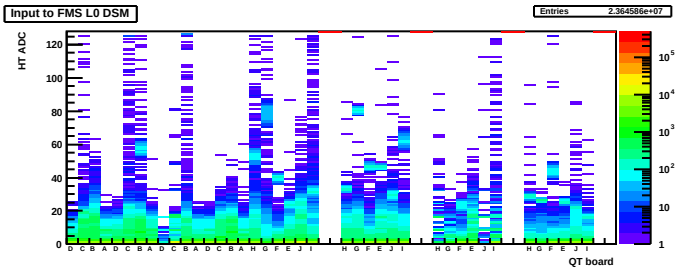

Input to FMS L0 DSM
Entries 9295
QT8(1) sum - simulated
QT board

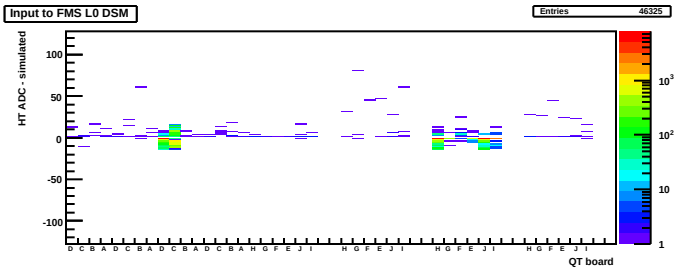

Input to FMS L0 DSM
Entries 2.364586e+07
QT8(2) sum
QT board

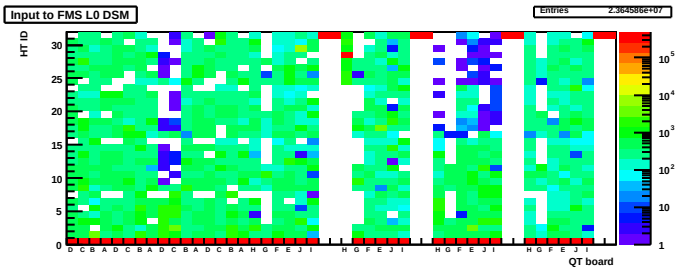

Input to FMS L0 DSM
Entries 355158
QT8(2) sum - simulated
QT board

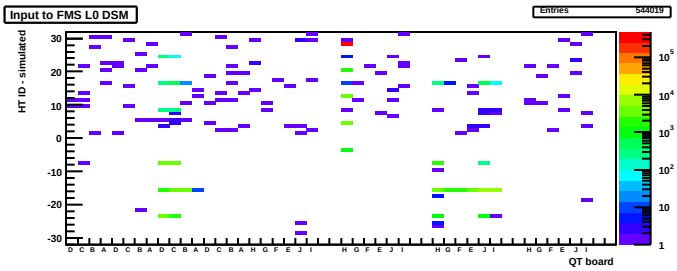

Input to FMS L0 DSM
Entries 2.364586e+07
QT8(3) sum
QT board

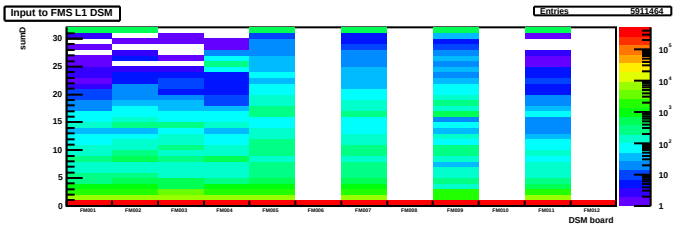

Input to FMS L0 DSM
Entries 478734
QT8(3) sum - simulated
QT board

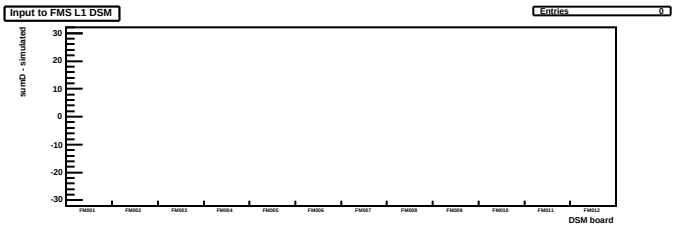

Input to FMS L0 DSM
Entries 2.364586e+07
HT ADC
QT board

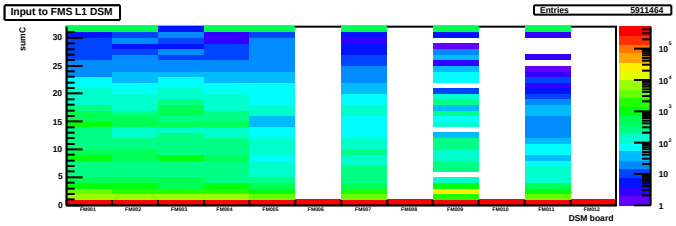

Input to FMS L0 DSM
Entries 46325
HT ADC - simulated
QT board

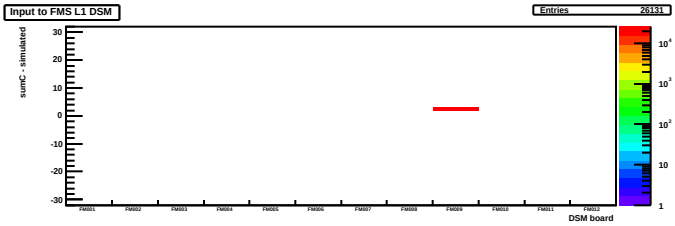

Input to FMS L0 DSM
Entries 2.364586e+07
HT ID
QT board

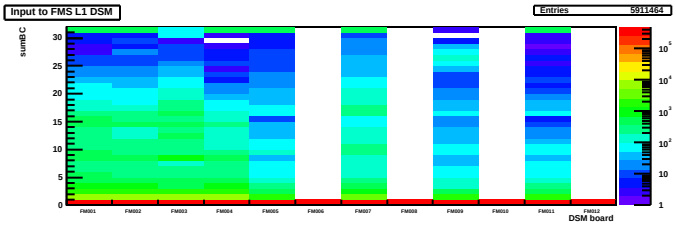

Input to FMS L0 DSM
Entries 544019
HT ID - simulated
QT board

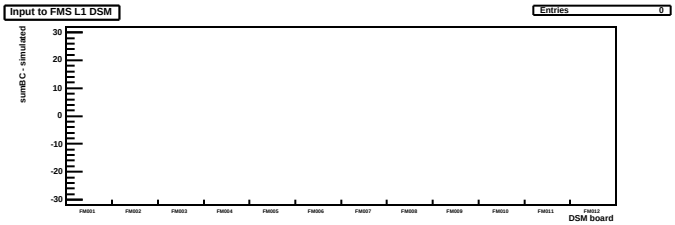

Input to FMS L1 DSM
Entries 5911464
sumD
DSM board

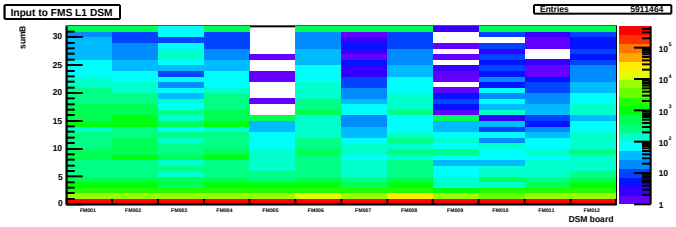

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

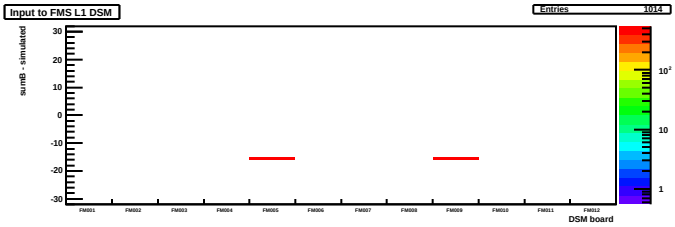

Input to FMS L1 DSM
Entries 5911464
sumC
DSM board

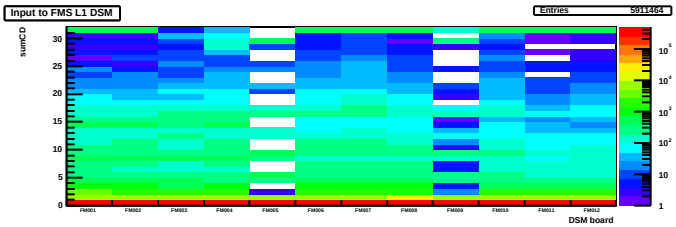

Input to FMS L1 DSM
Entries 26131
sumC - simulated
DSM board

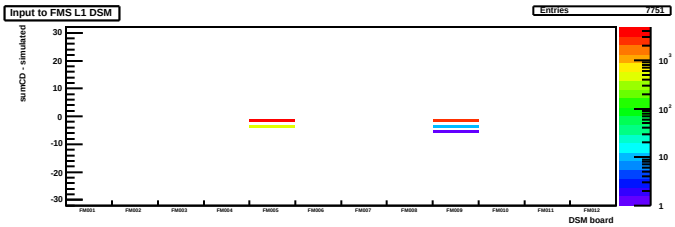

Input to FMS L1 DSM
Entries 5911464
sumBC
DSM board

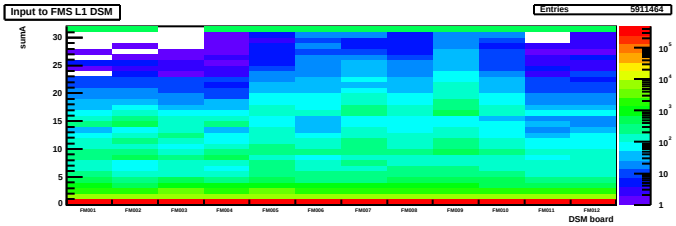

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

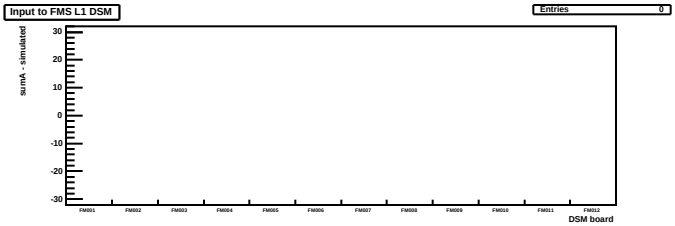

Input to FMS L1 DSM
Entries 5911464
sumB
DSM board

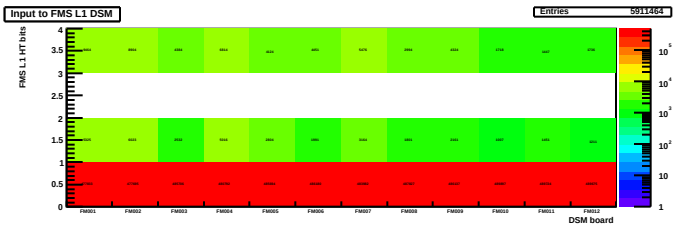

Input to FMS L1 DSM
Entries 1014
sumB - simulated
DSM board

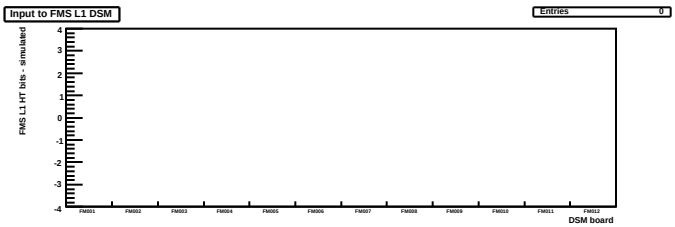

Input to FMS L1 DSM
Entries 5911464
sumCD
DSM board

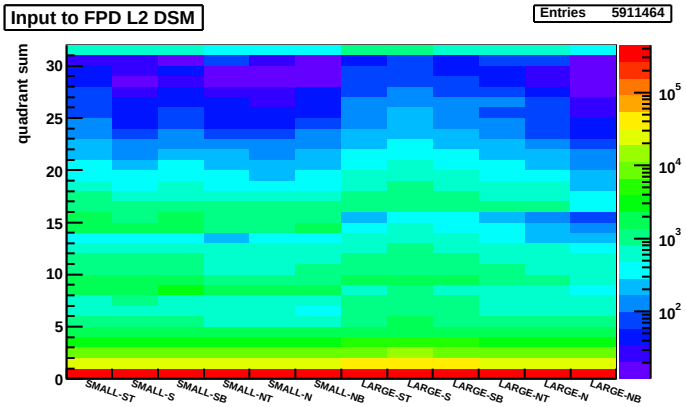

Input to FMS L1 DSM
Entries 7751
sumCD - simulated
DSM board

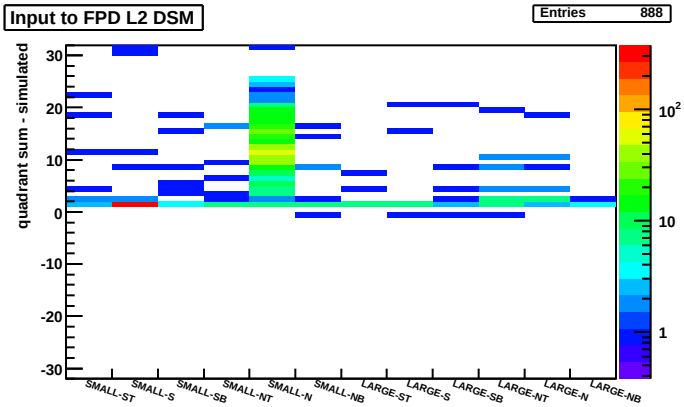

Input to FMS L1 DSM
Entries 5911464
sumA
DSM board

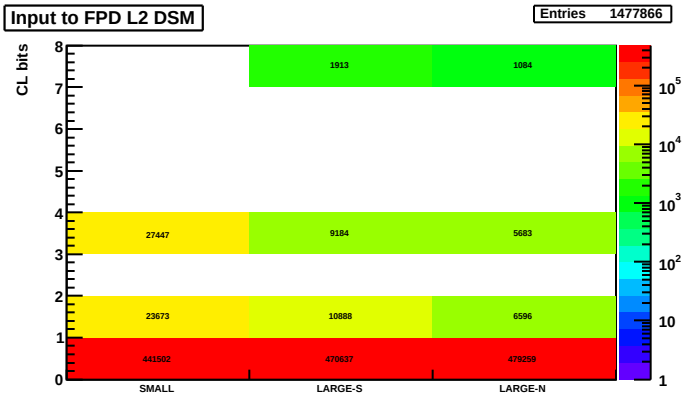

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

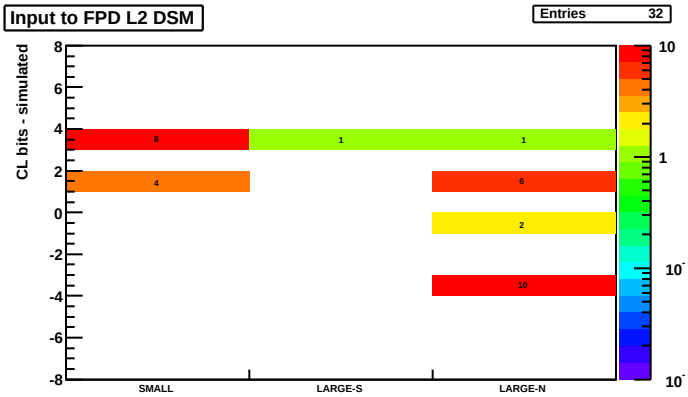

Input to FMS L1 DSM
Entries 5911464
FMS L1 HT bits
DSM board

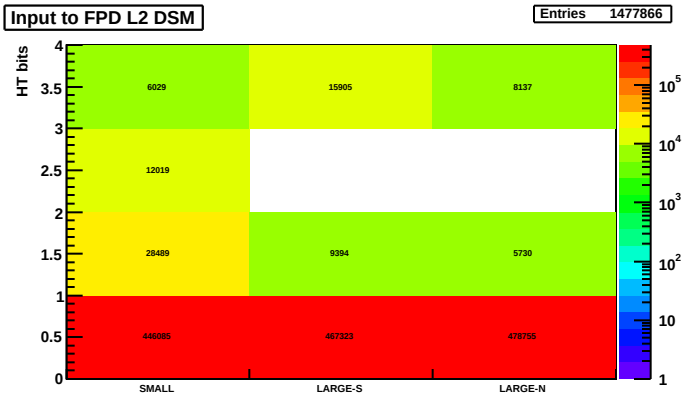

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

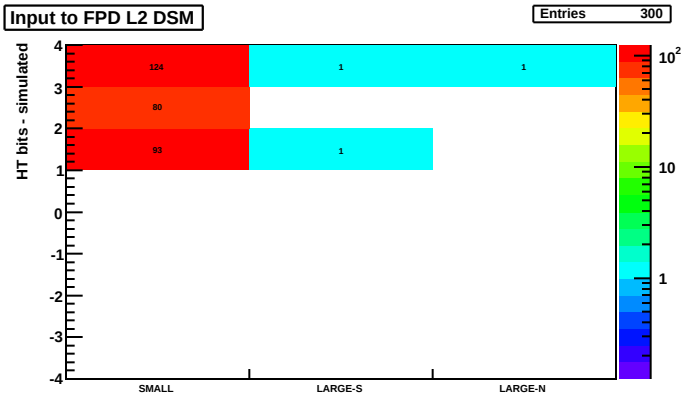

Input to FPD L2 DSM
Entries 5911464
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

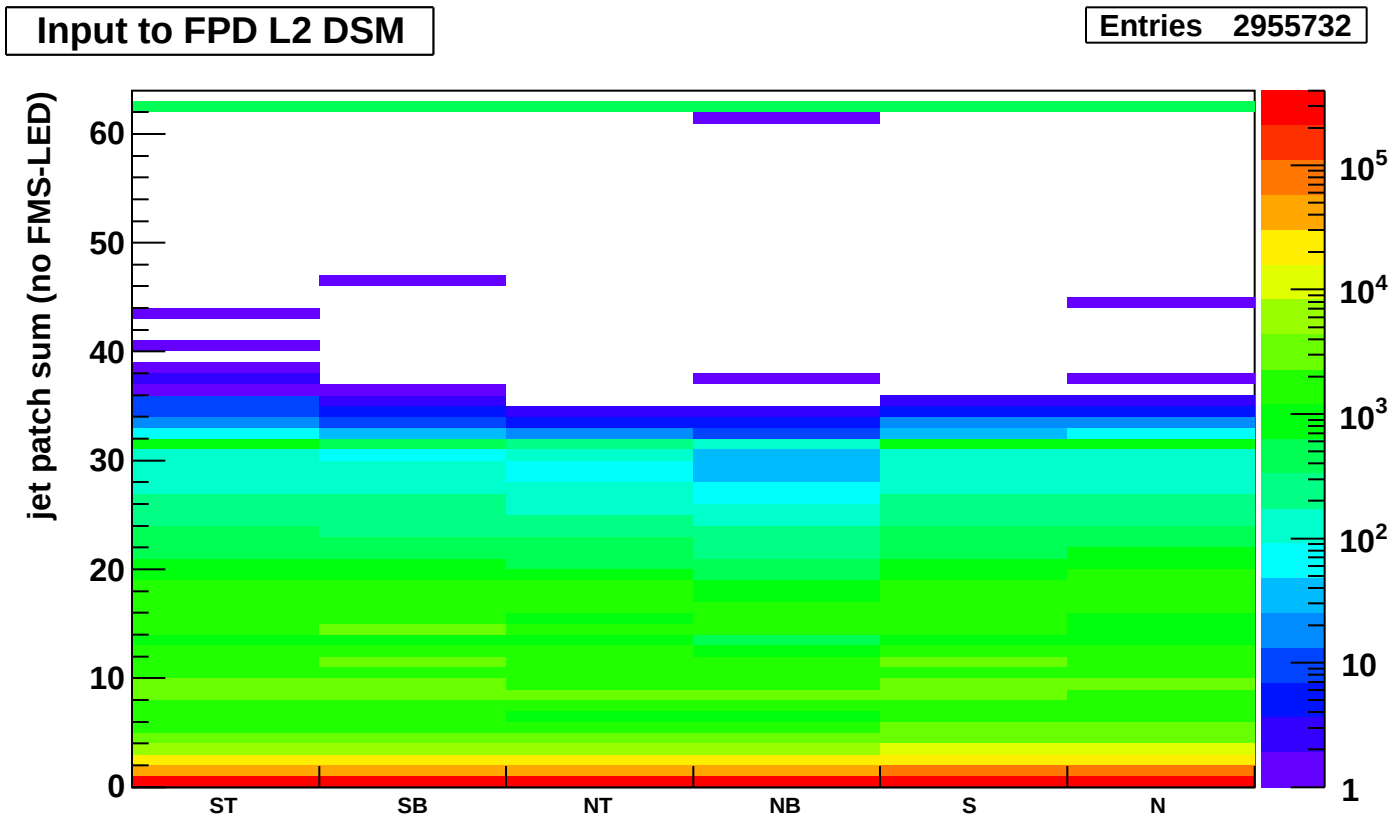

Input to FPD L2 DSM
Entries 888
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

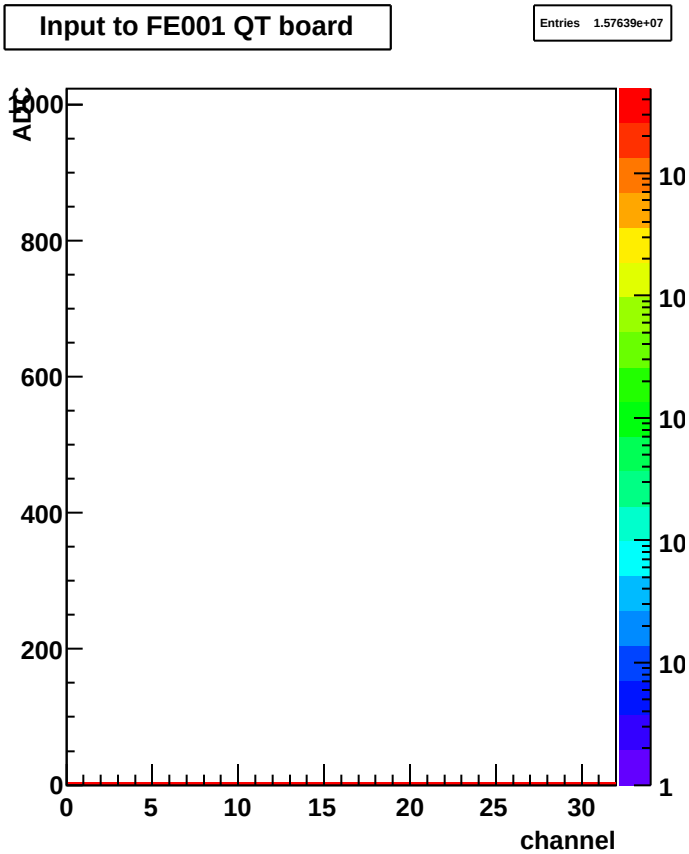

Input to FPD L2 DSM
Entries 1477866
CL bits
1913
1084
27447
9184
5683
23673
10888
6596
441502
470637
479259
SMALL
LARGE-S
LARGE-N

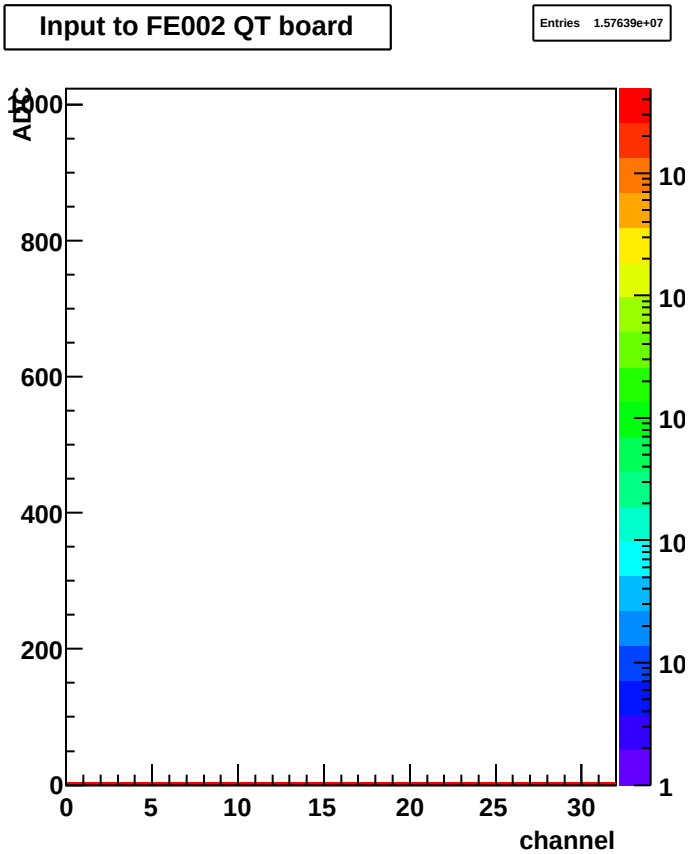

Input to FPD L2 DSM
Entries 32
CL bits - simulated
8
1
1
4
6
2
10
SMALL
LARGE-S
LARGE-N

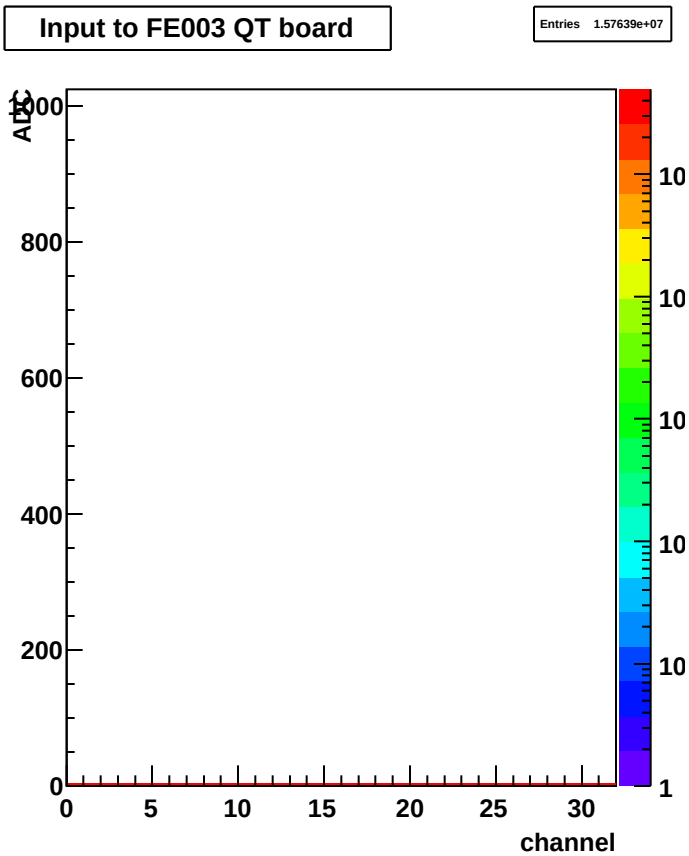

Input to FPD L2 DSM
Entries 1477866
HT bits
6029
15905
8137
12019
28489
9394
5730
446085
467323
478755
SMALL
LARGE-S
LARGE-N

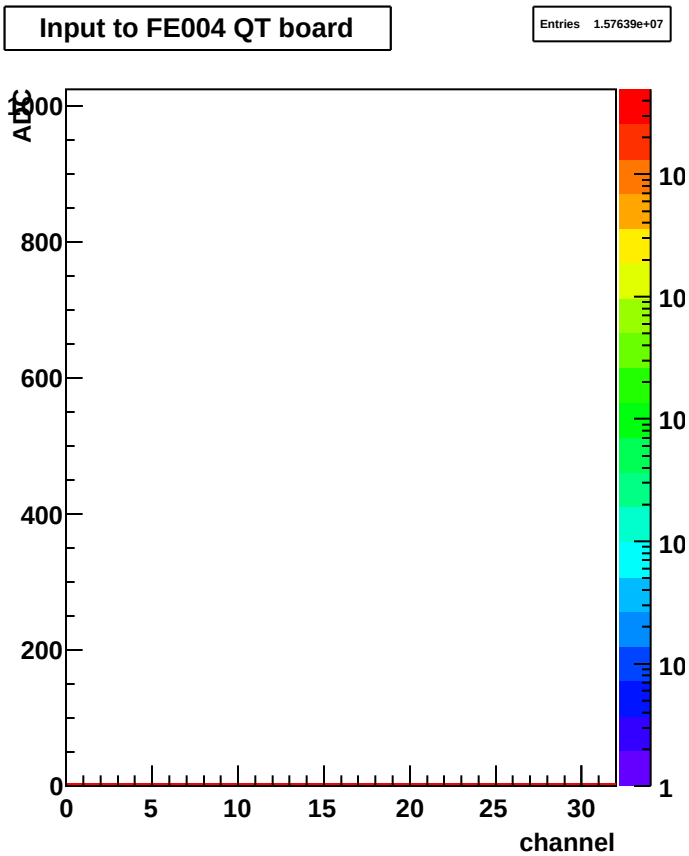

Input to FPD L2 DSM
Entries 300
HT bits - simulated
124
1
1
80
93
1
SMALL
LARGE-S
LARGE-N

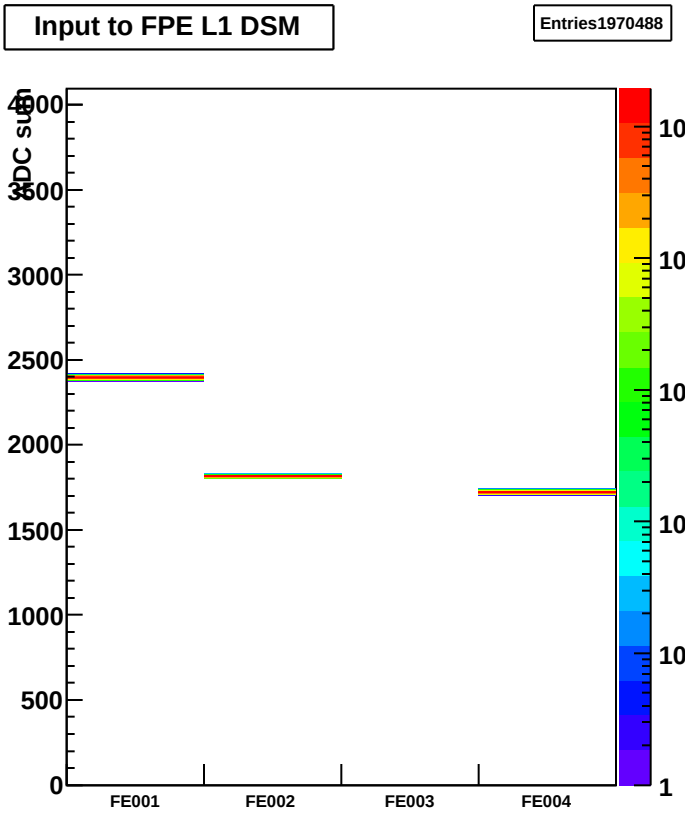

Input to FPD L2 DSM
Entries 2955732
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

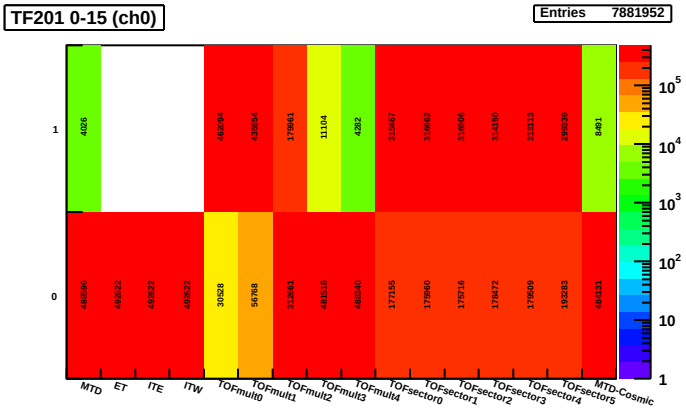

Input to FE001 QT board
Entries 1.57639e+07
ADC
channel

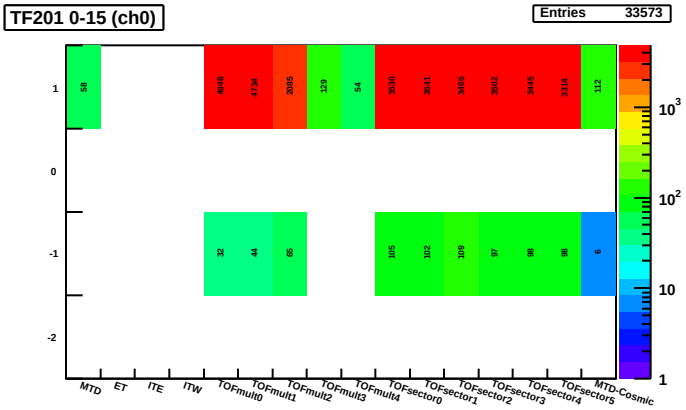

Input to FE002 QT board
Entries 1.57639e+07
ADC
channel

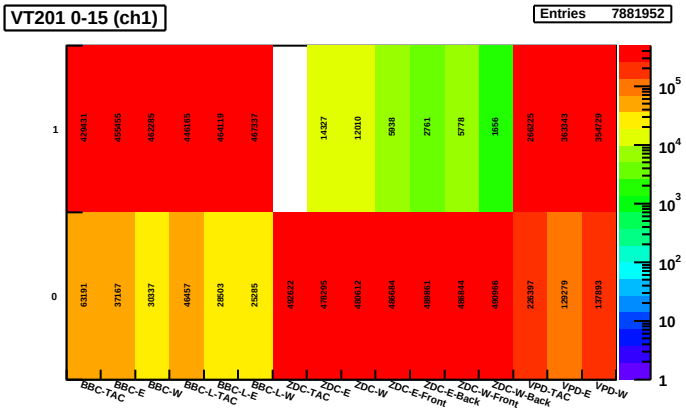

Input to FE003 QT board
Entries 1.57639e+07
ADC
channel

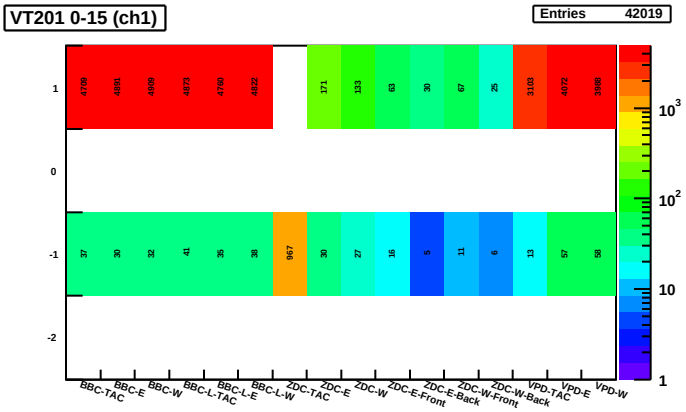

Input to FE004 QT board
Entries 1.57639e+07
ADC
channel

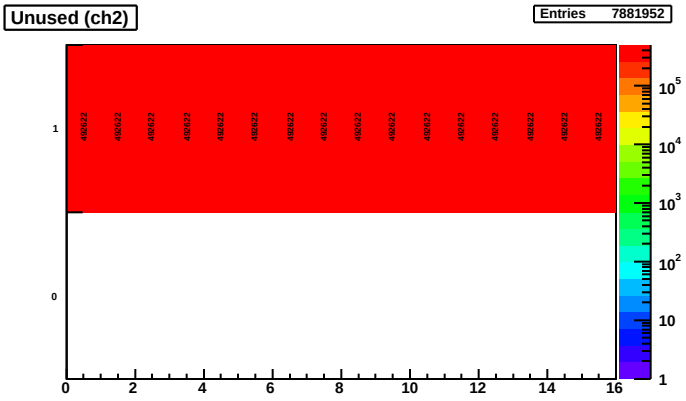

Input to FPE L1 DSM
Entries1970488
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

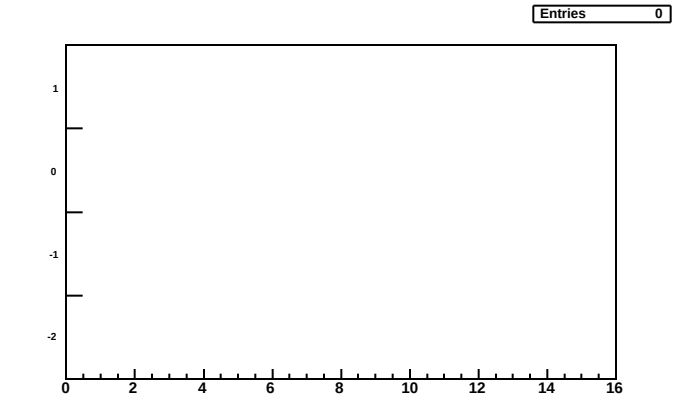

TF201 0-15 (ch0)
Entries 7881952
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic

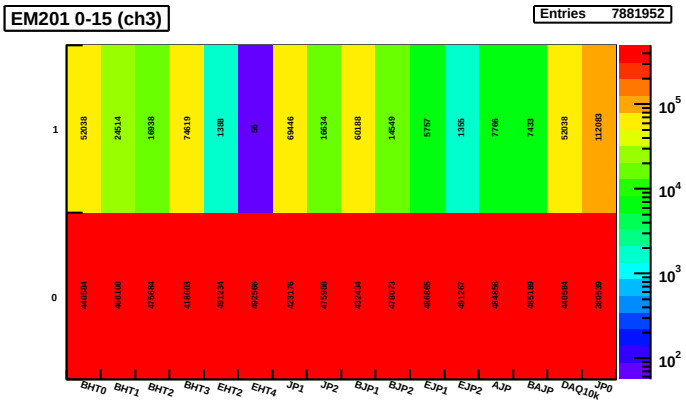

TF201 0-15 (ch0)
Entries 33573
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic


VT201 0-15 (ch1)
Entries 7881952
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-E-Front
ZDC-E-Back
ZDC-W-Front
ZDC-W-Back
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 42019
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-E-Front
ZDC-E-Back
ZDC-W-Front
ZDC-W-Back
VPD-TAC
VPD-E
VPD-W


Unused (ch2)
Entries 7881952


Entries 0


EM201 0-15 (ch3)
Entries 7881952
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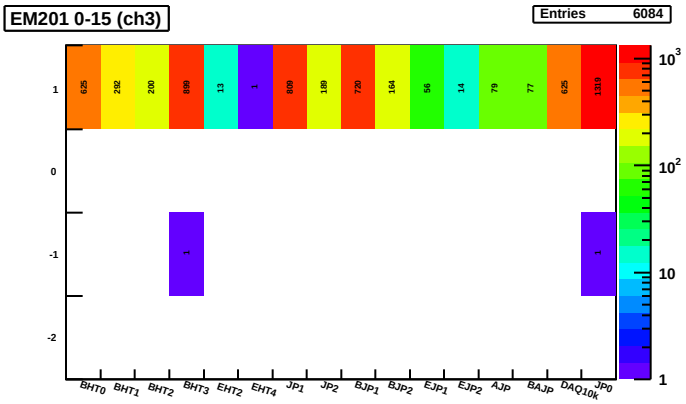
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
DAQ10k
JP0


EM201 0-15 (ch3)
Entries 6084
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BHT1
BHT2
BHT3
EHT2
EHT4
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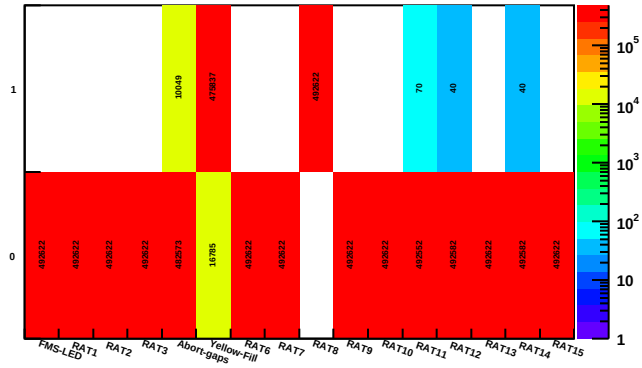
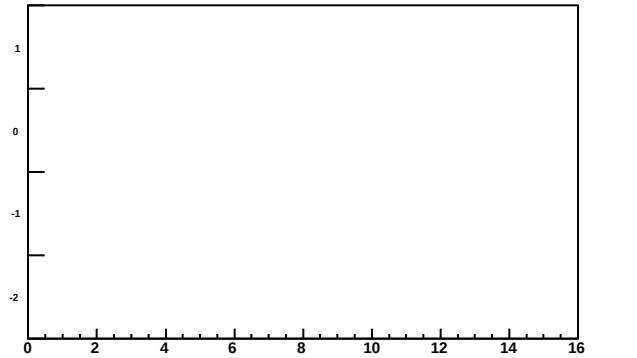
AJP
BAJP
DAQ10k
JP0

RAT board (ch4)





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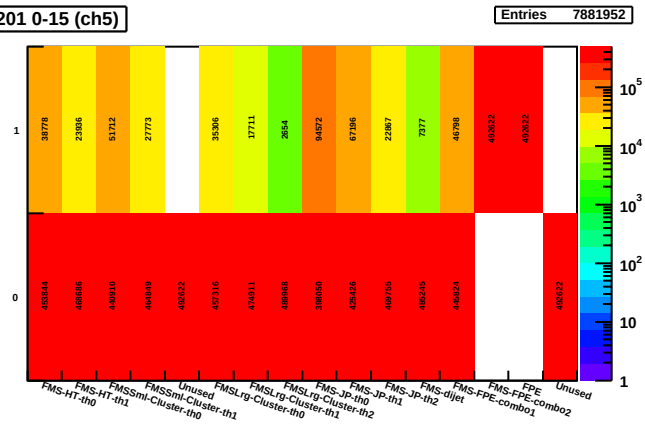
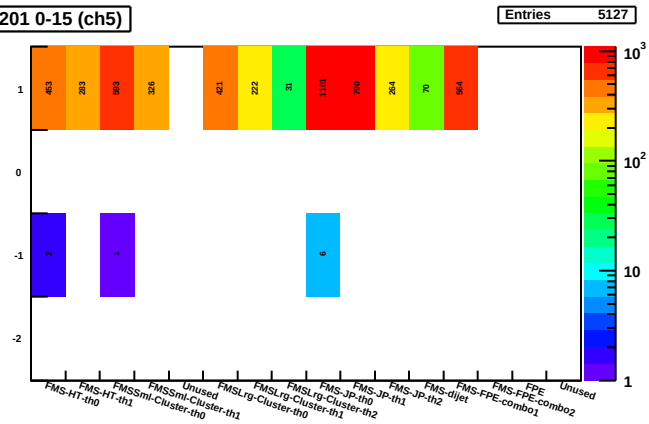
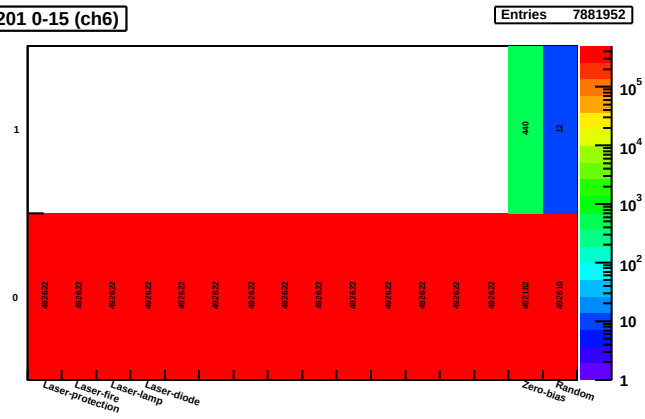
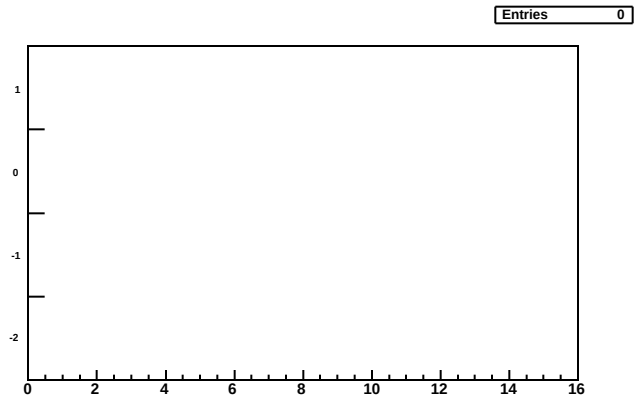
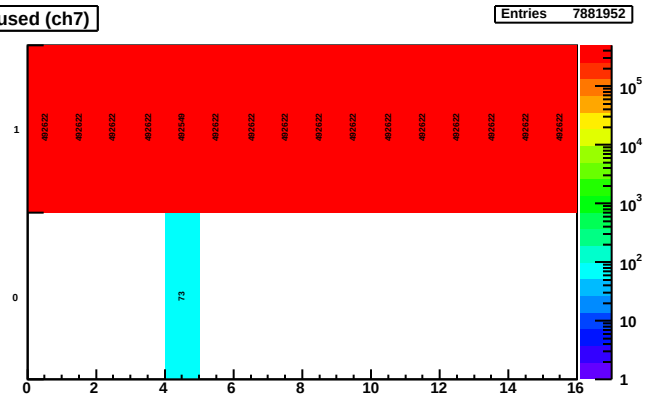
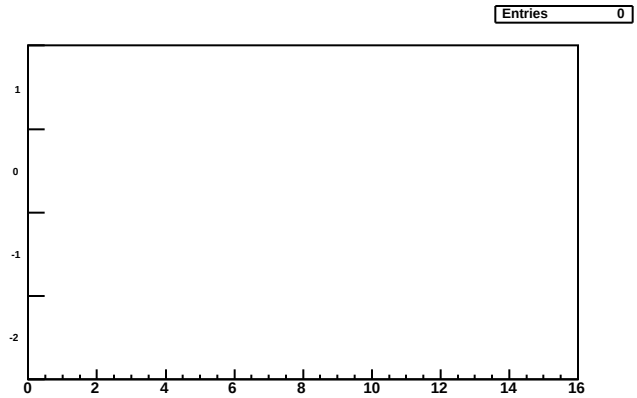


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)