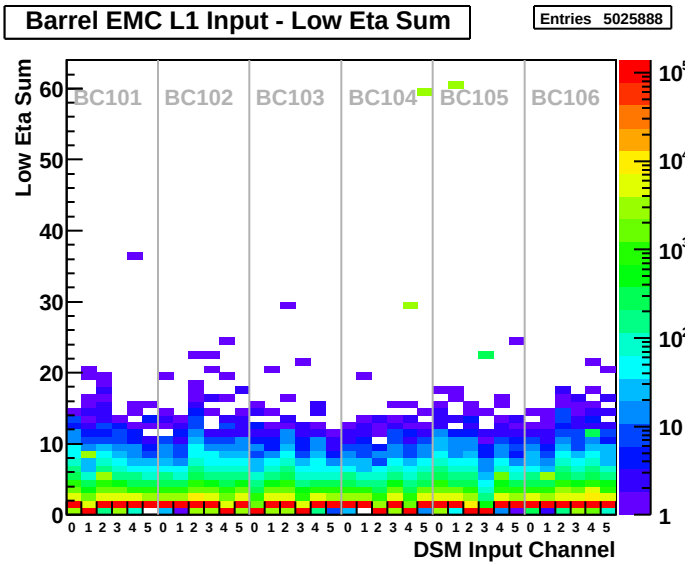
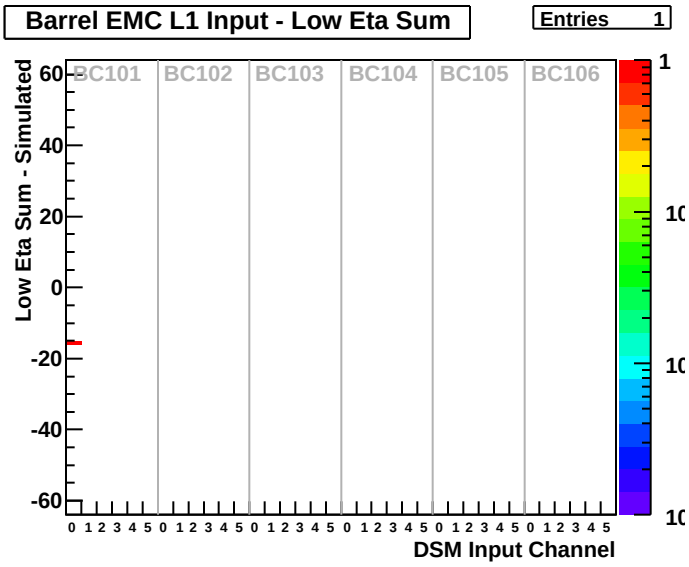
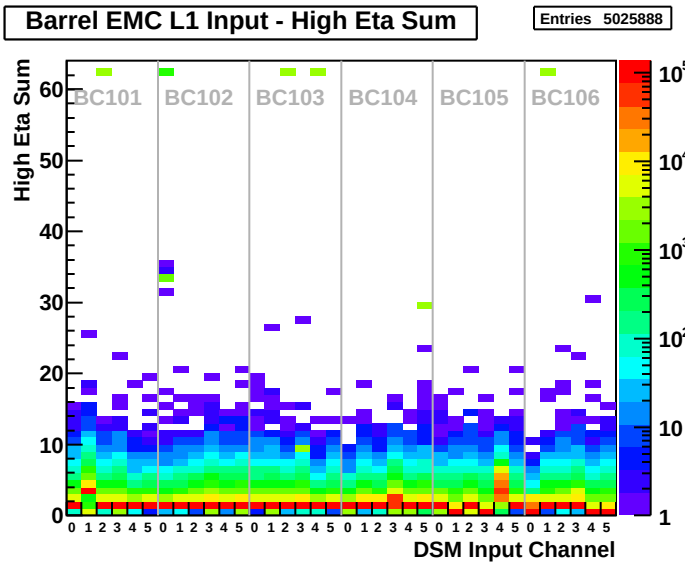
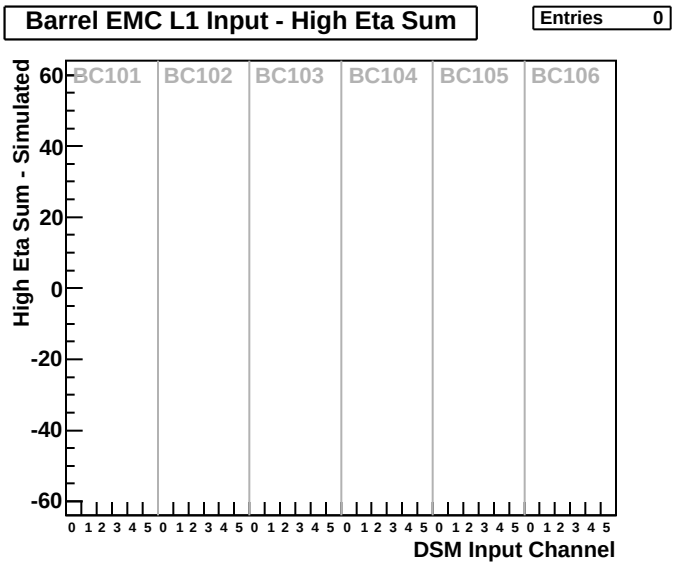
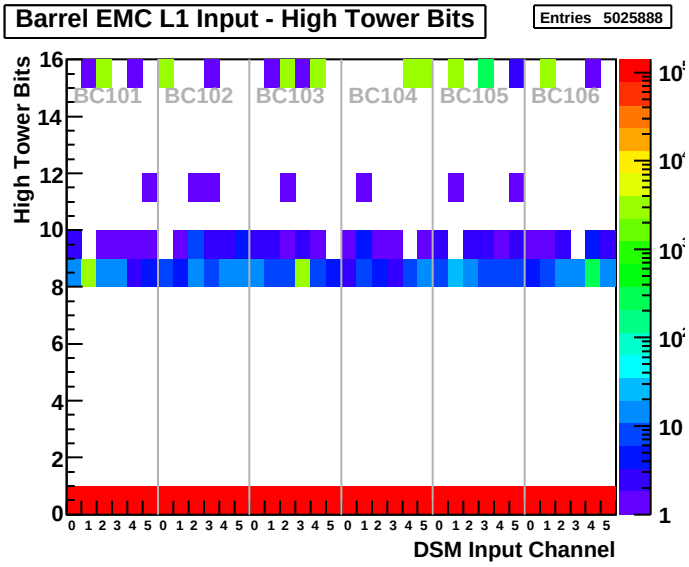
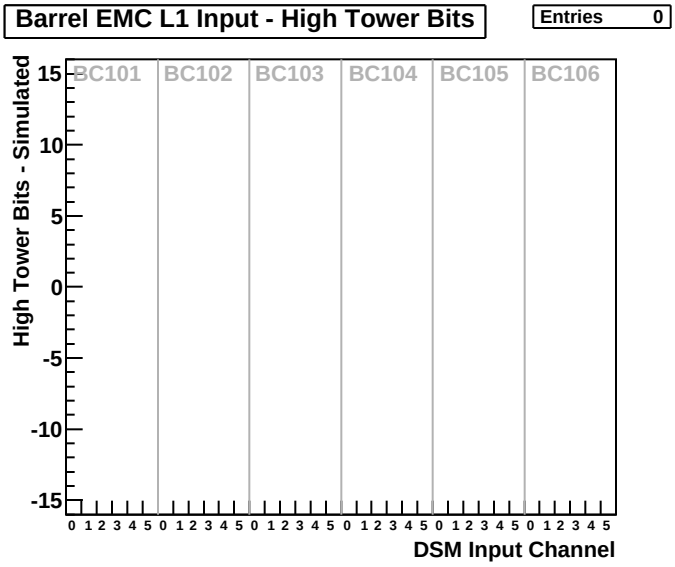

Barrel EMC L1 Input - Low Eta Sum
Entries 5025888
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

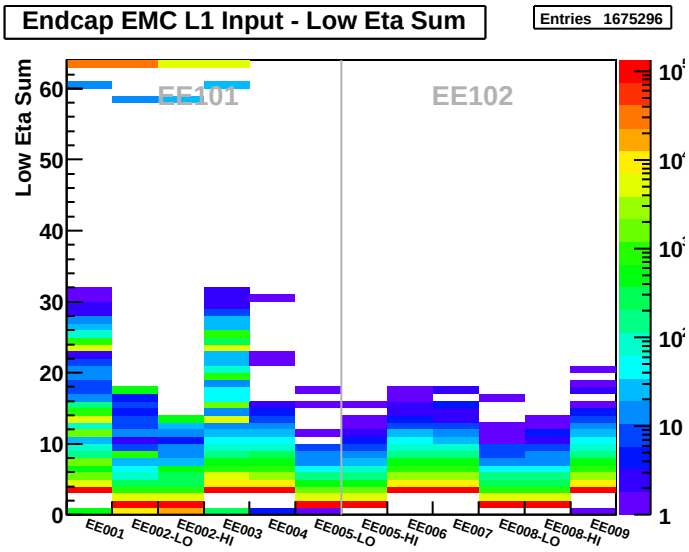

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

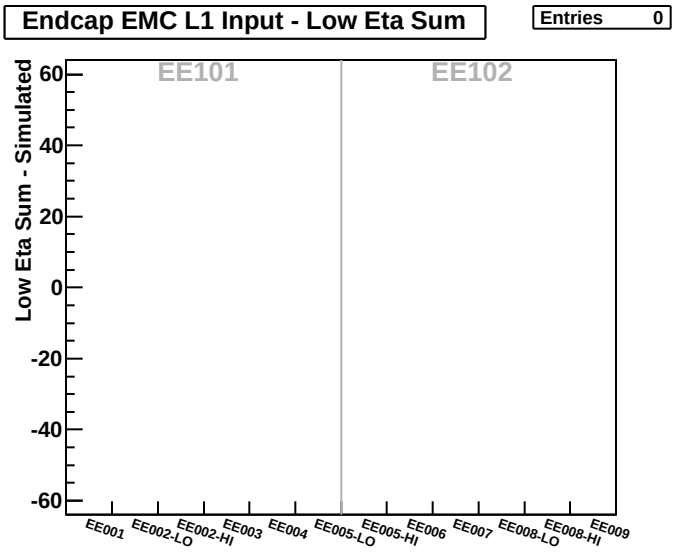

Barrel EMC L1 Input - High Eta Sum
Entries 5025888
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

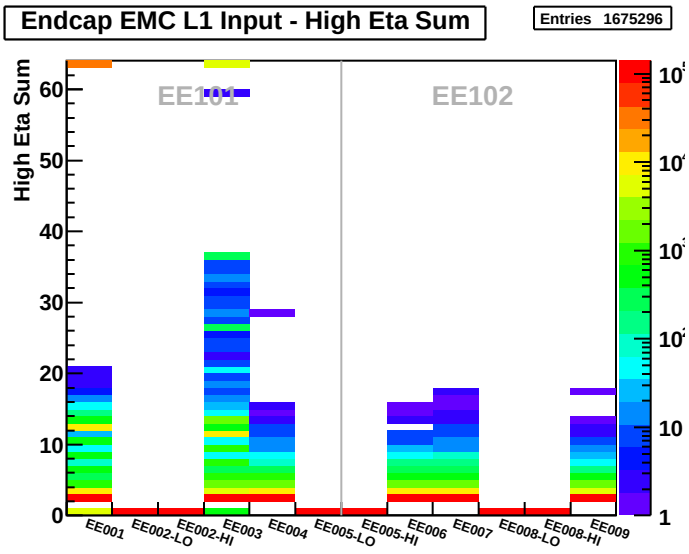

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

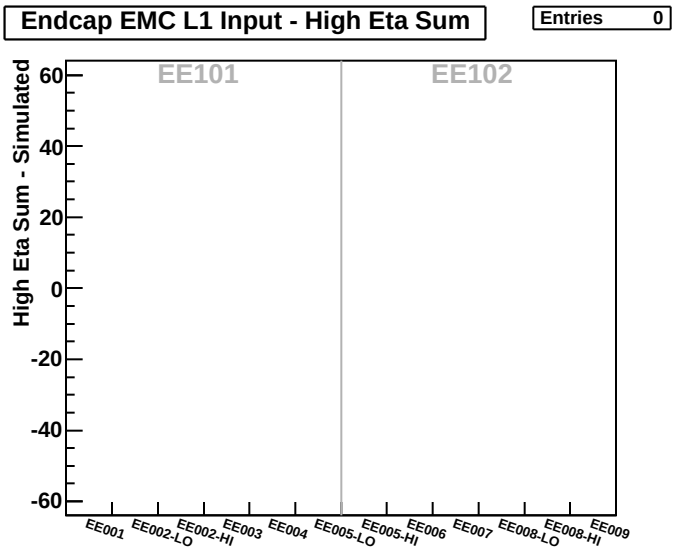

Barrel EMC L1 Input - High Tower Bits
Entries 5025888
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

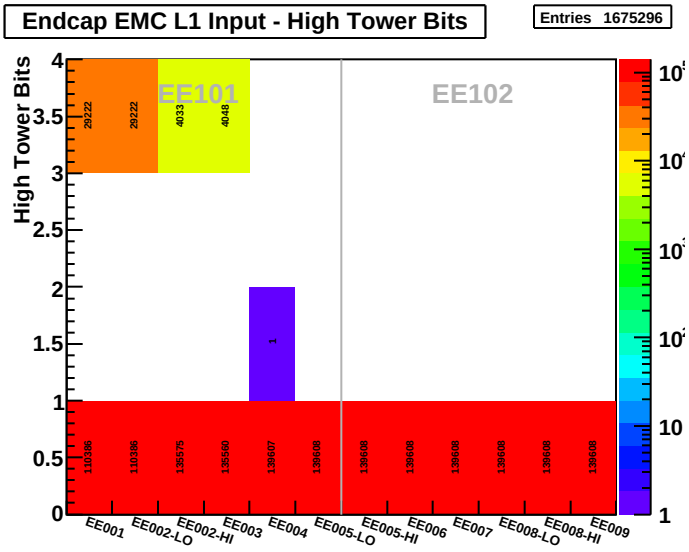

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

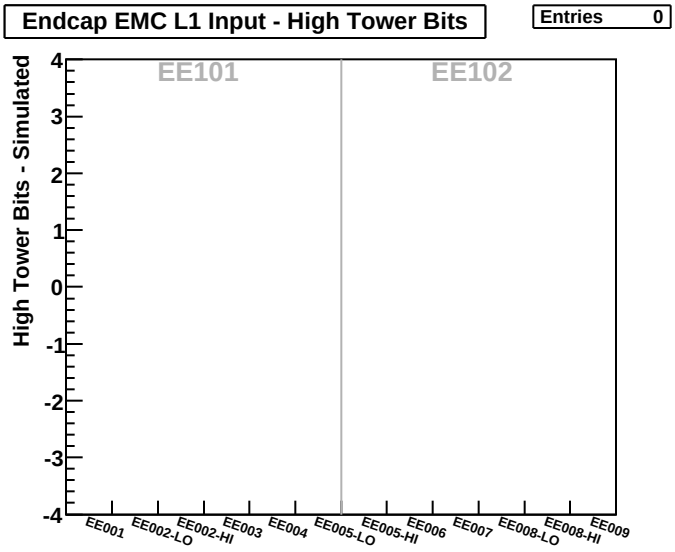

Endcap EMC L1 Input - Low Eta Sum
Entries 1675296
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

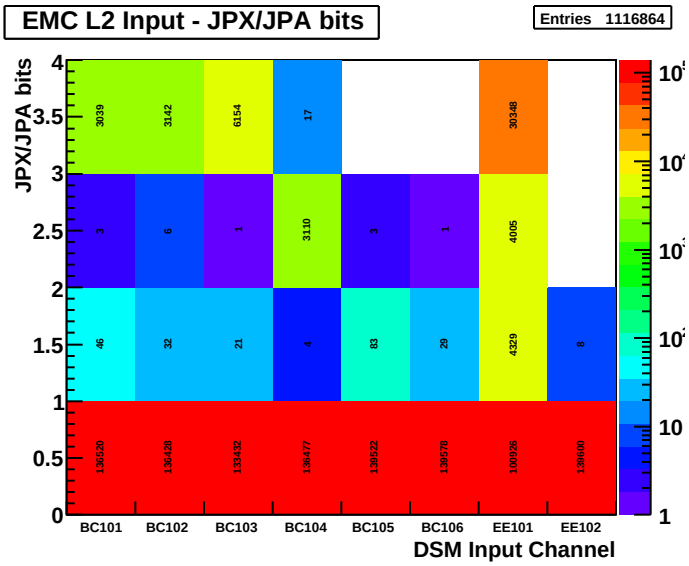

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

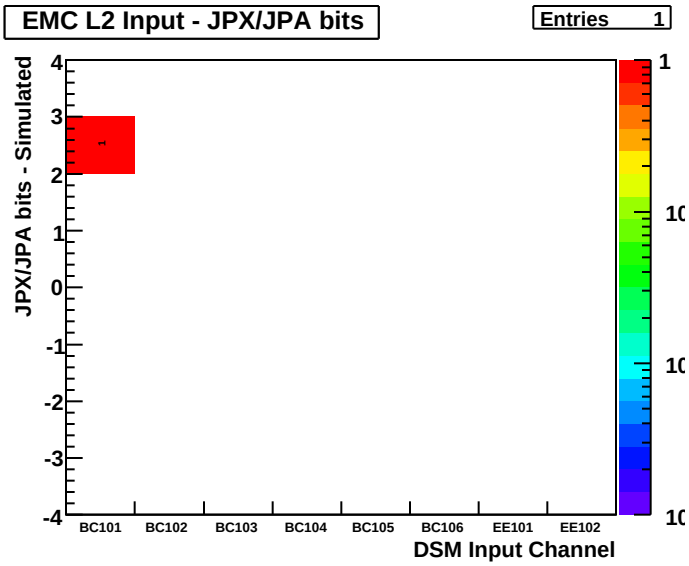

Endcap EMC L1 Input - High Eta Sum
Entries 1675296
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

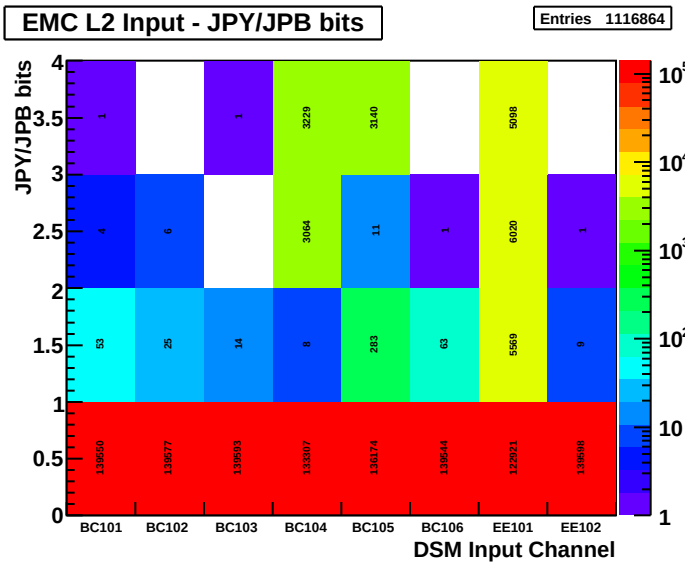

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

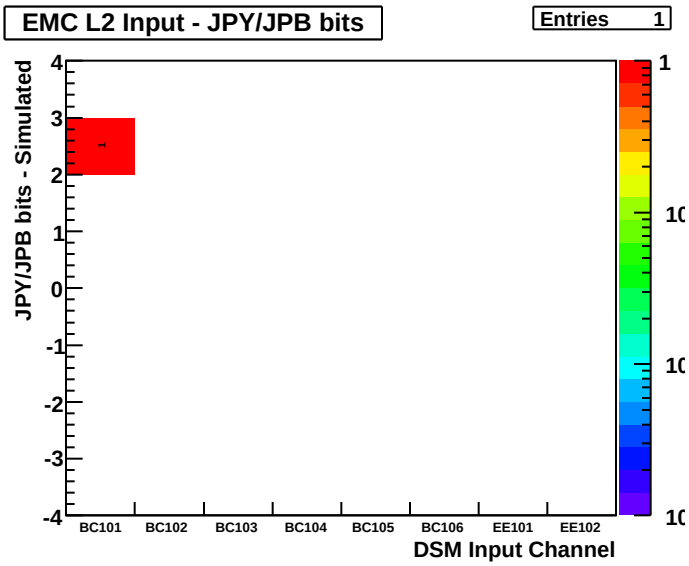

Endcap EMC L1 Input - High Tower Bits
Entries 1675296
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

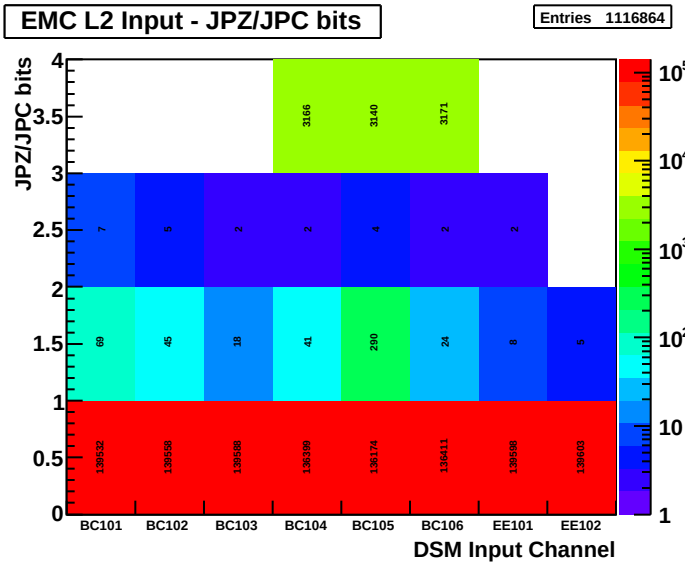

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

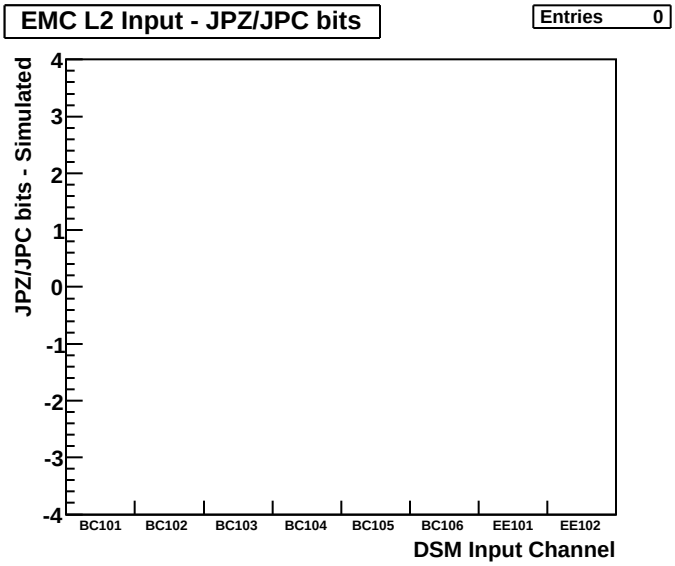

EMC L2 Input - JPX/JPA bits
Entries 1116864
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

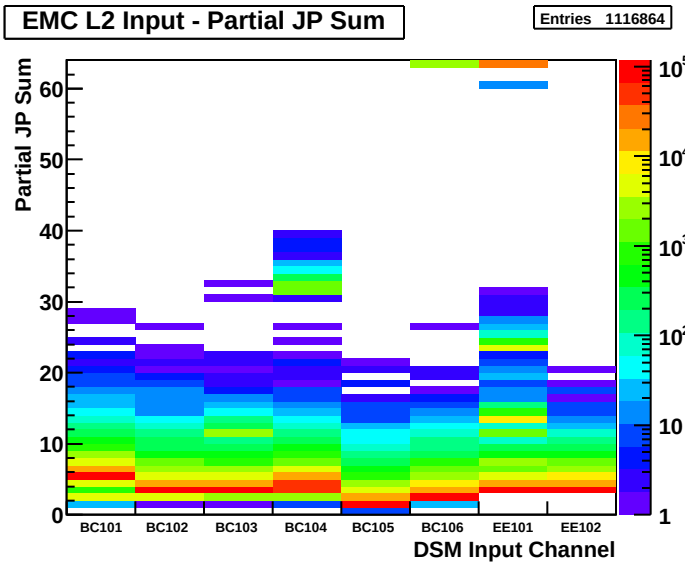

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

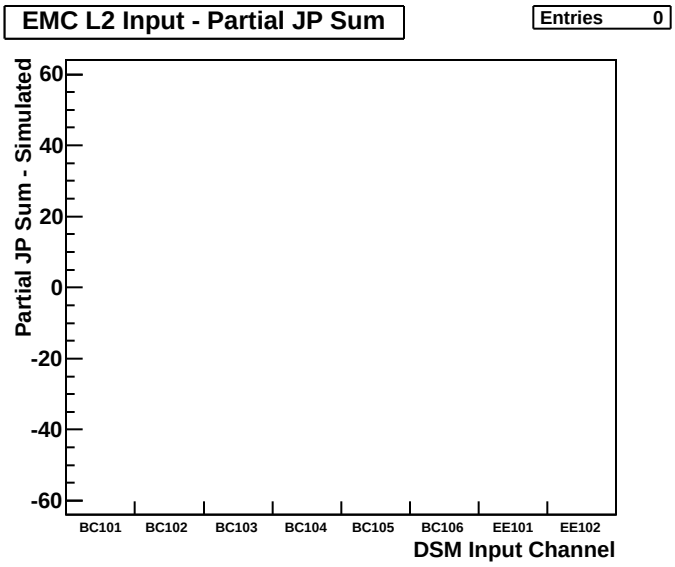

EMC L2 Input - JPY/JPB bits
Entries 1116864
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

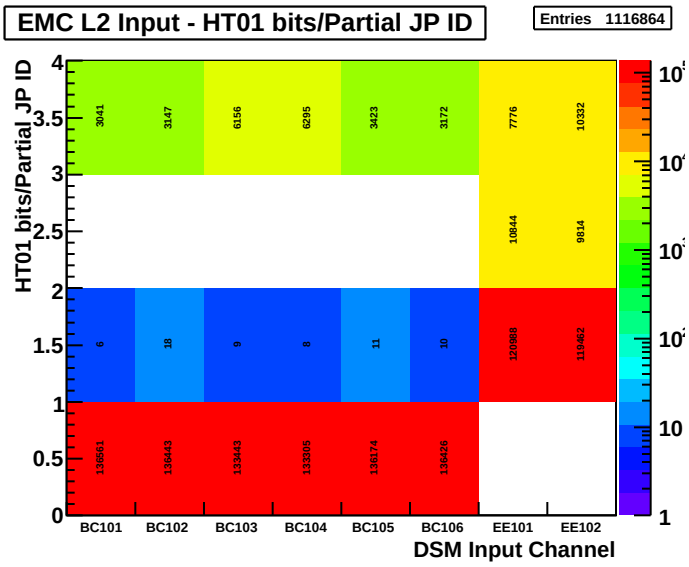

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

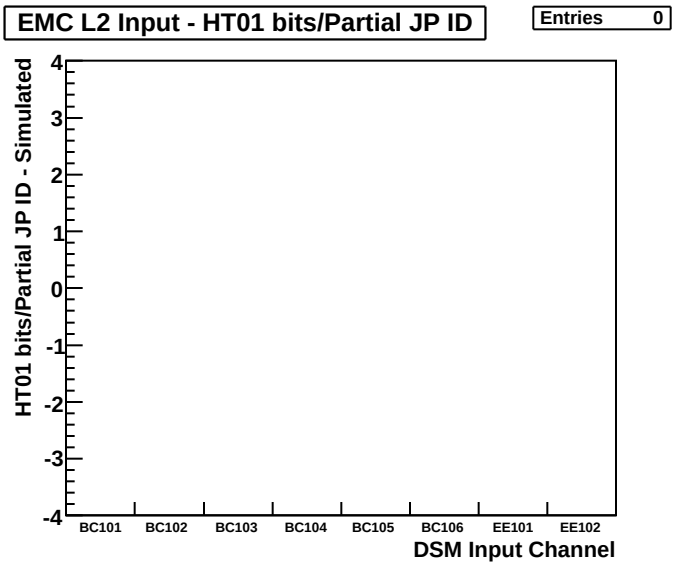

EMC L2 Input - JPZ/JPC bits
Entries 1116864
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

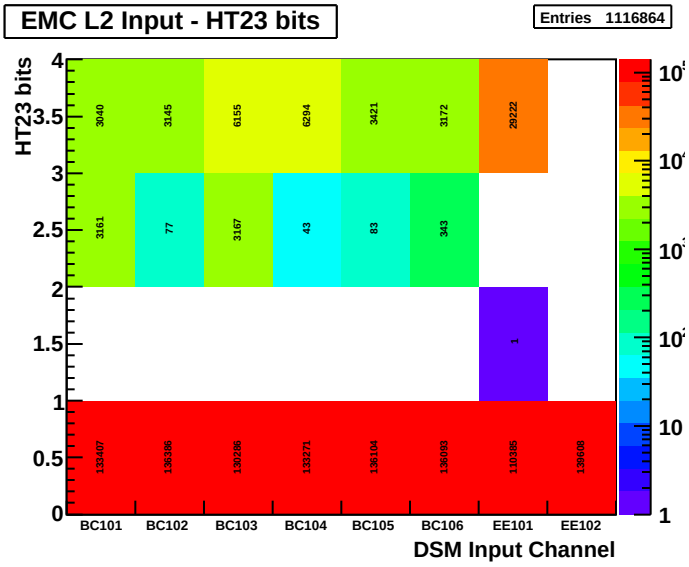

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

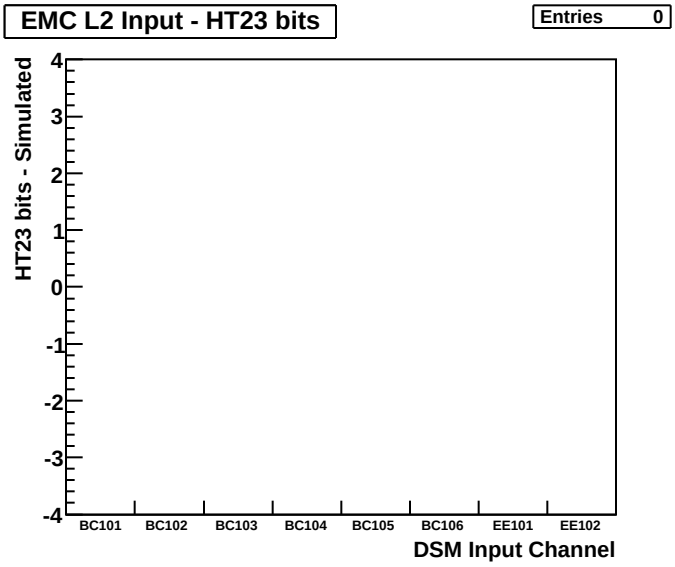

EMC L2 Input - Partial JP Sum
Entries 1116864
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

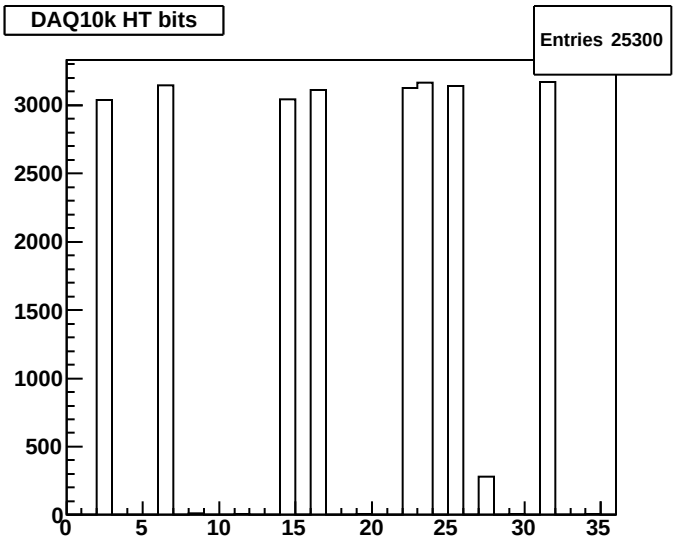

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

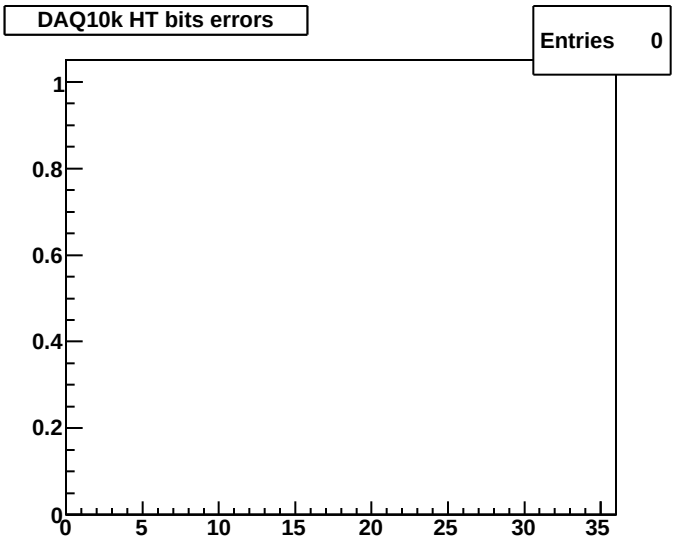

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1116864
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

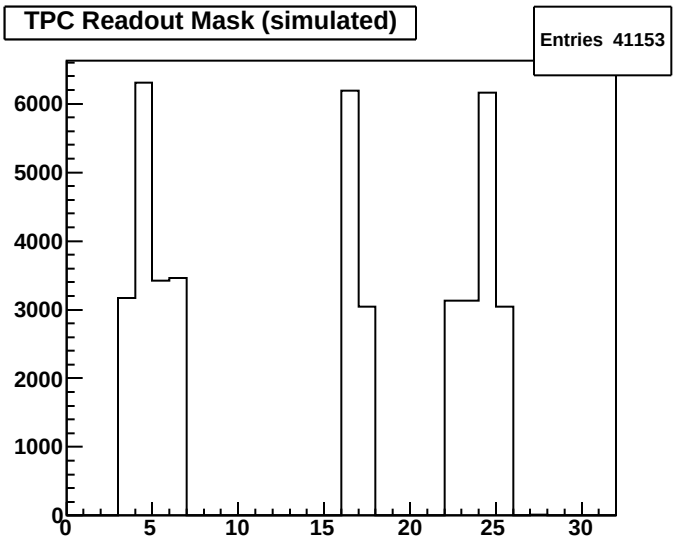

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

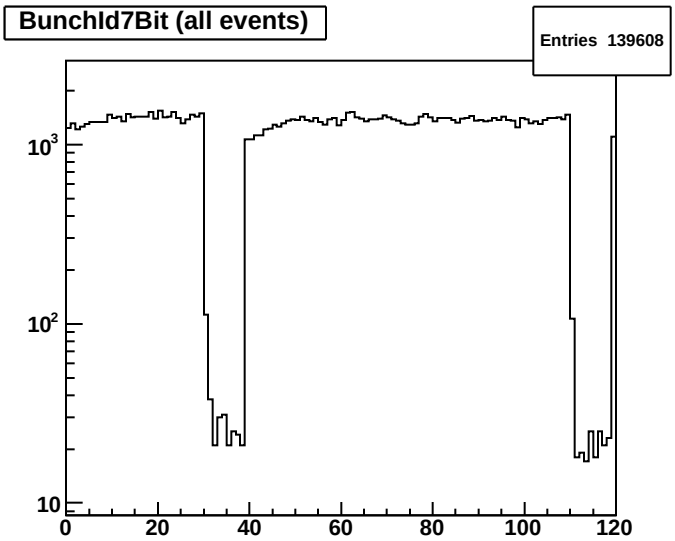

EMC L2 Input - HT23 bits
Entries 1116864
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

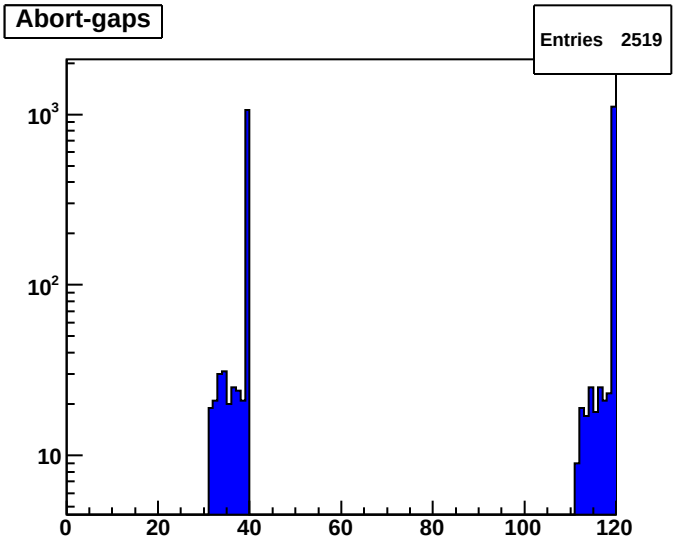

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

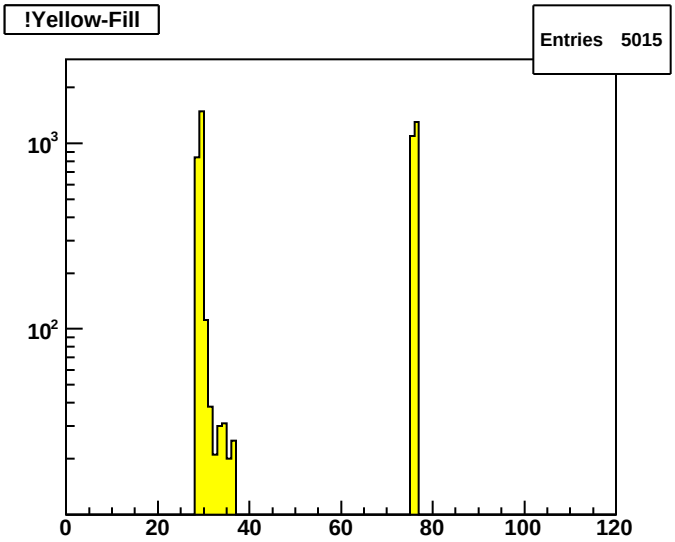

DAQ10k HT bits
Entries 25300

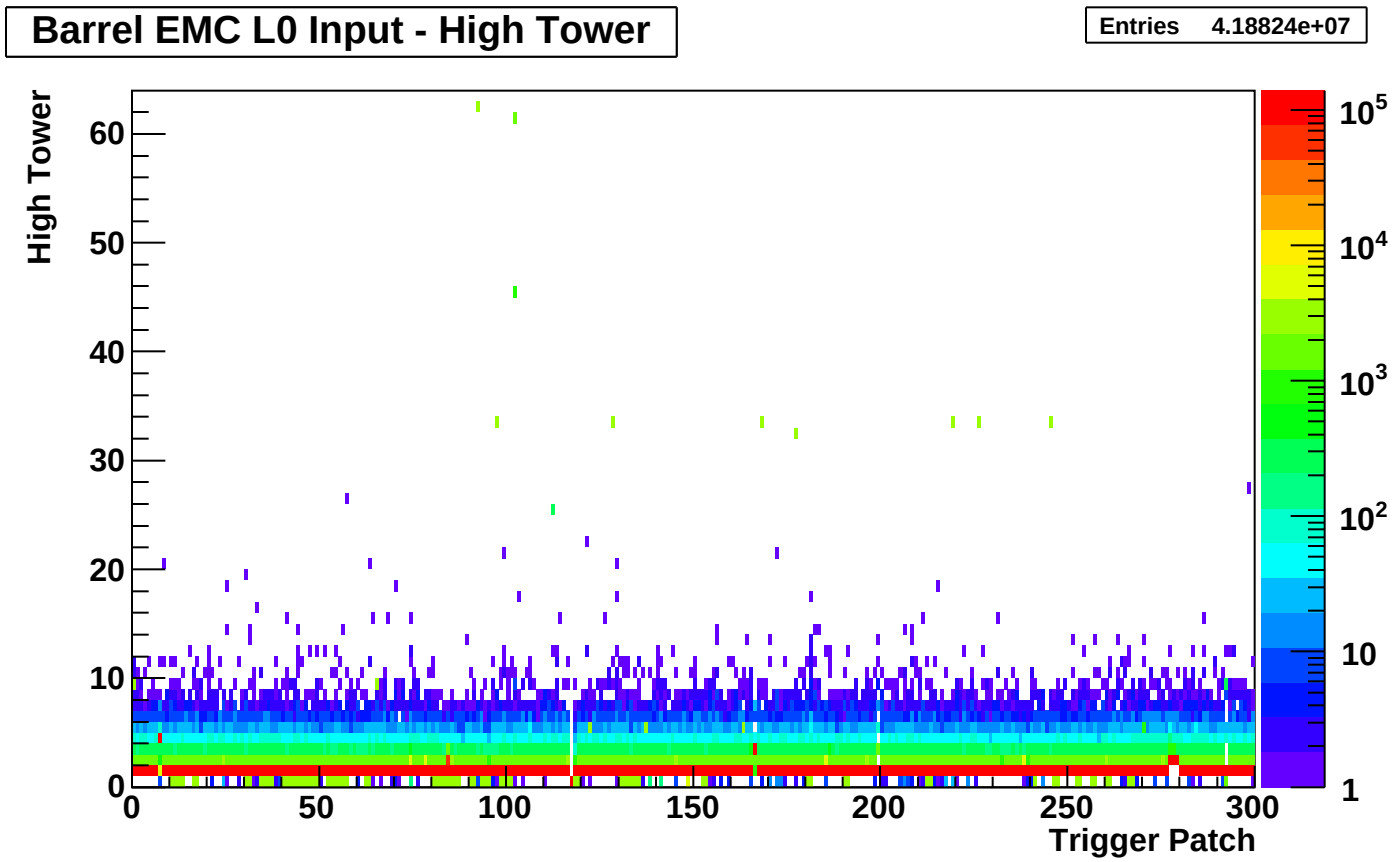

DAQ10k HT bits errors
Entries 0

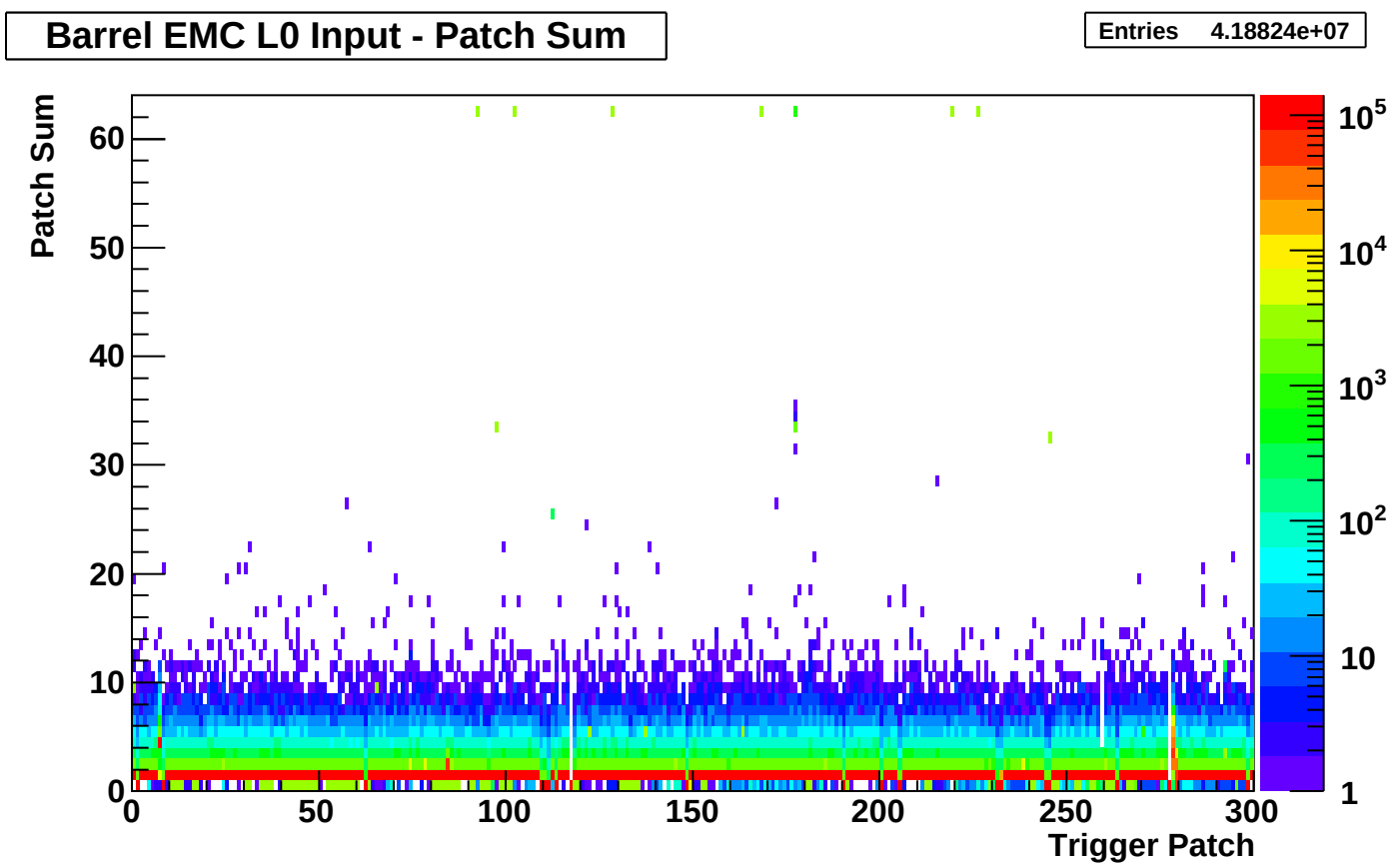

TPC Readout Mask (simulated)
Entries 41153

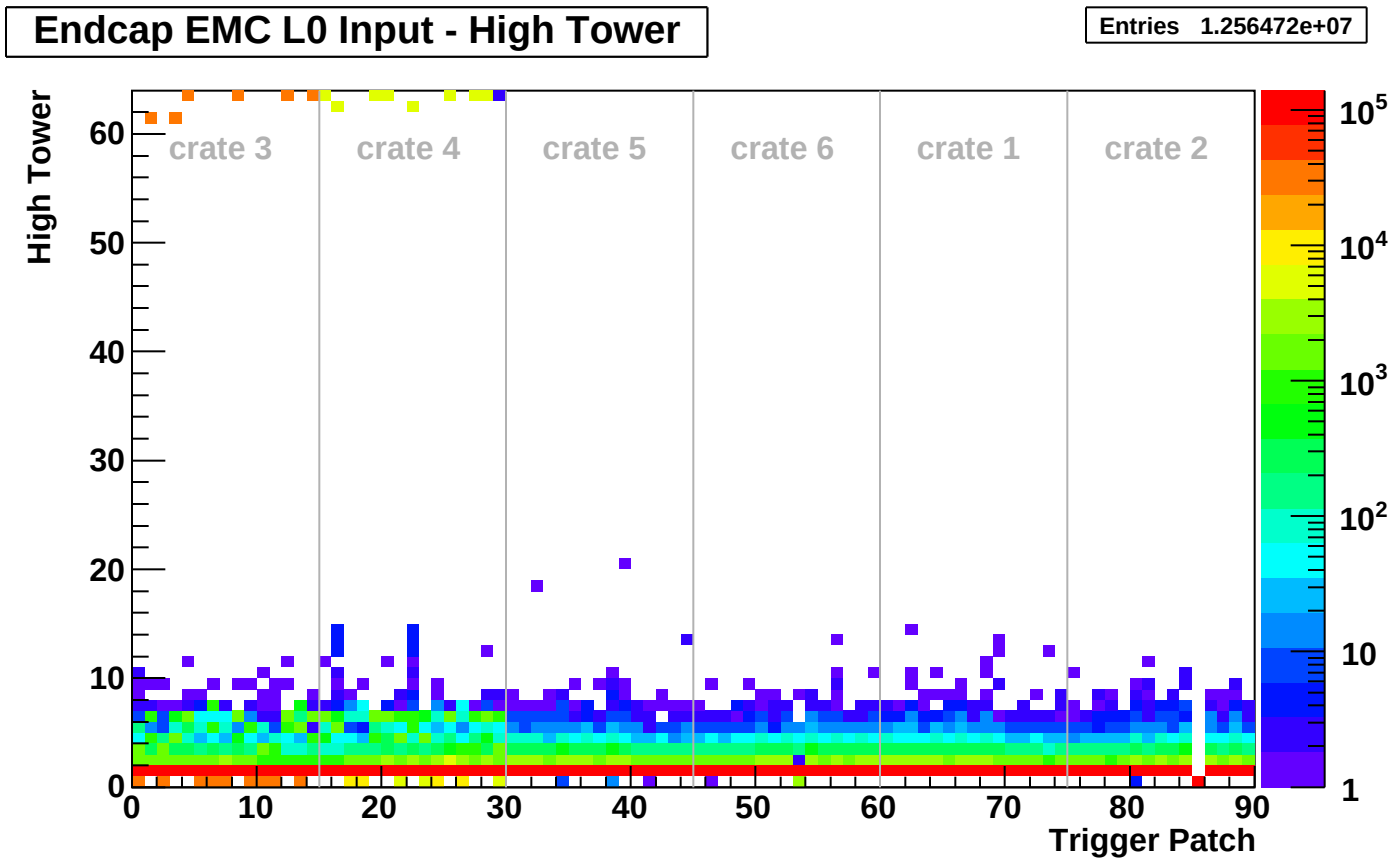

BunchId7Bit (all events)
Entries 139608

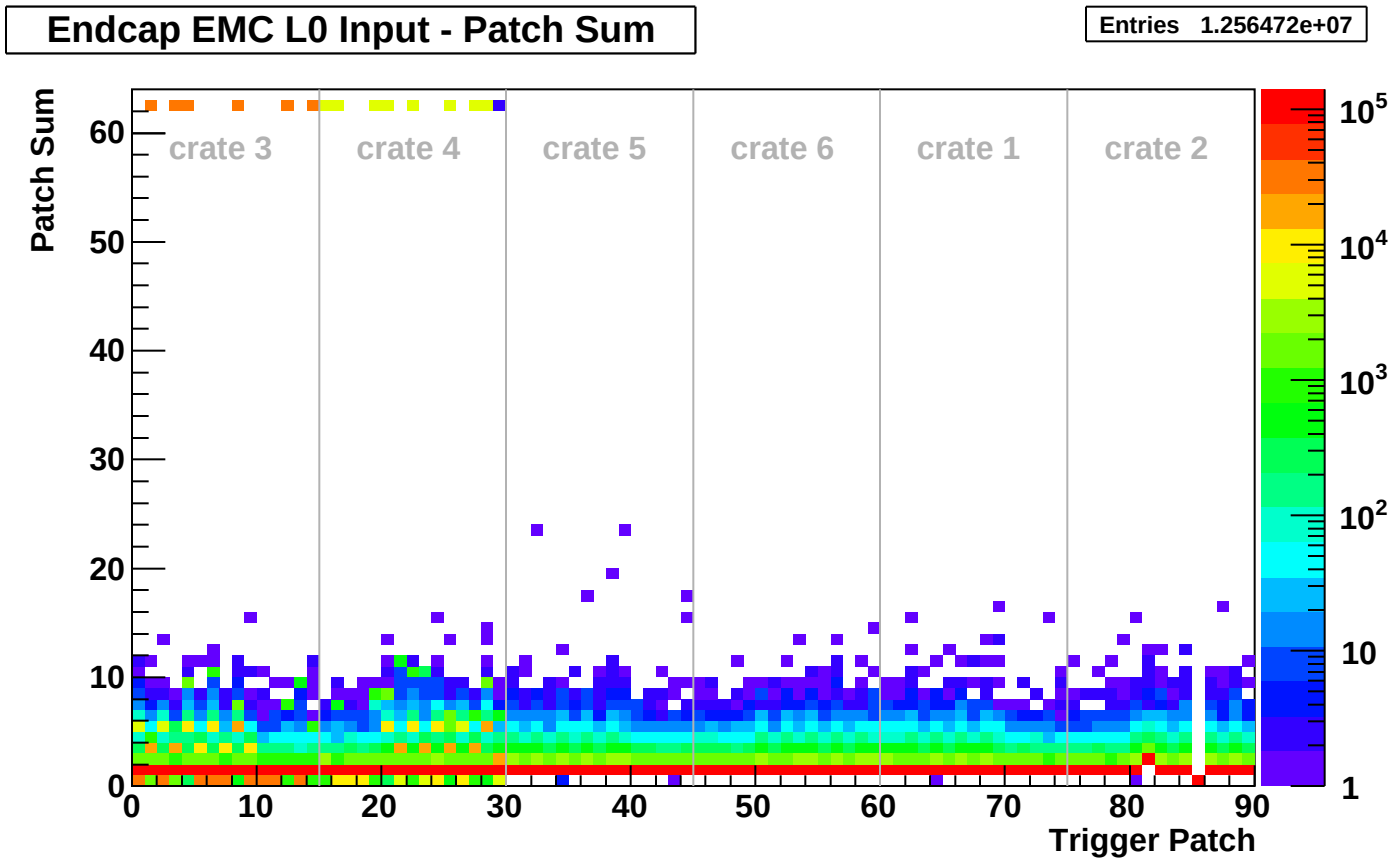

Abort-gaps
Entries 2519

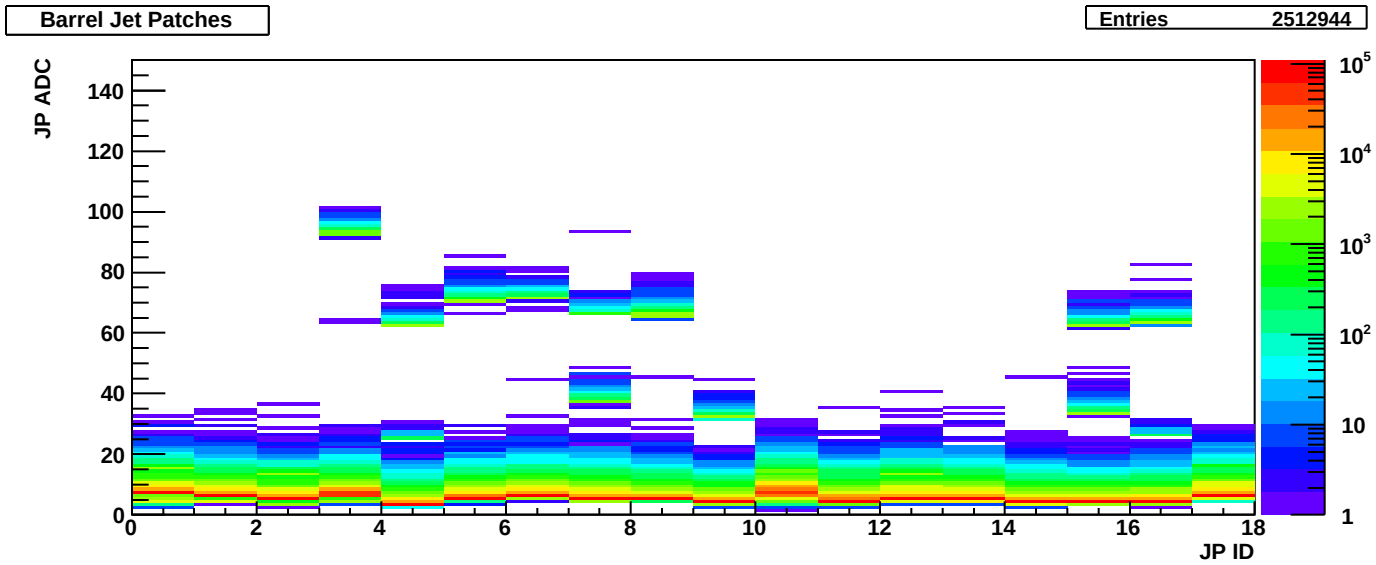

!Yellow-Fill
Entries 5015

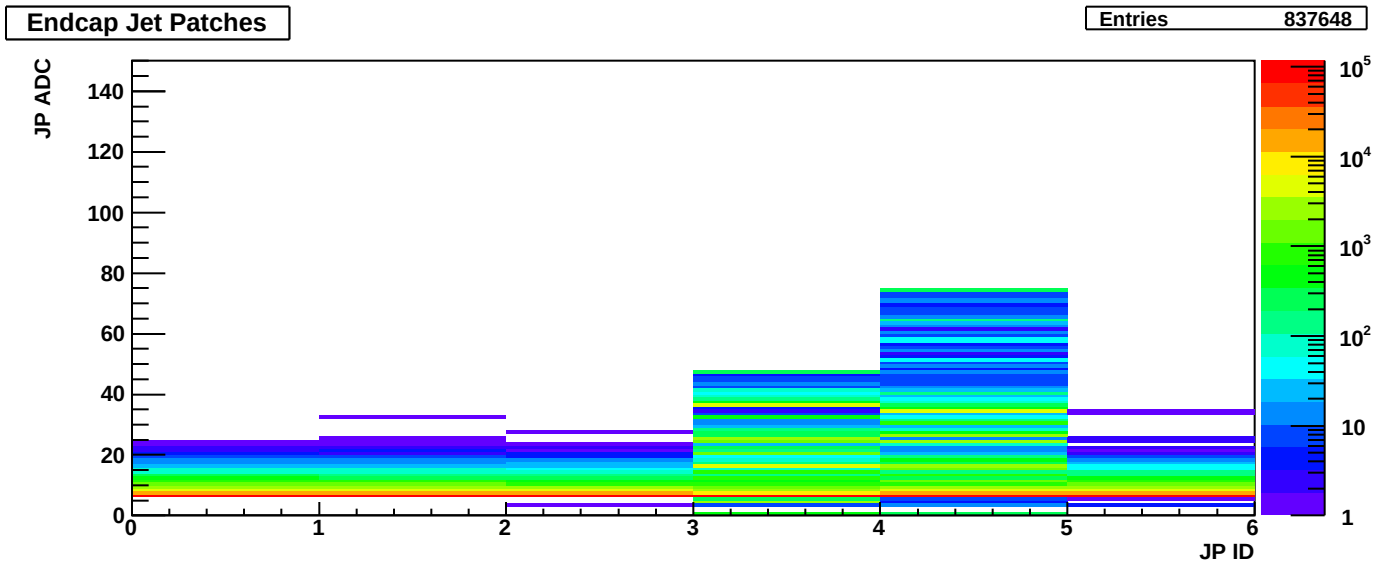

Barrel EMC L0 Input - High Tower
Entries 4.18824e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

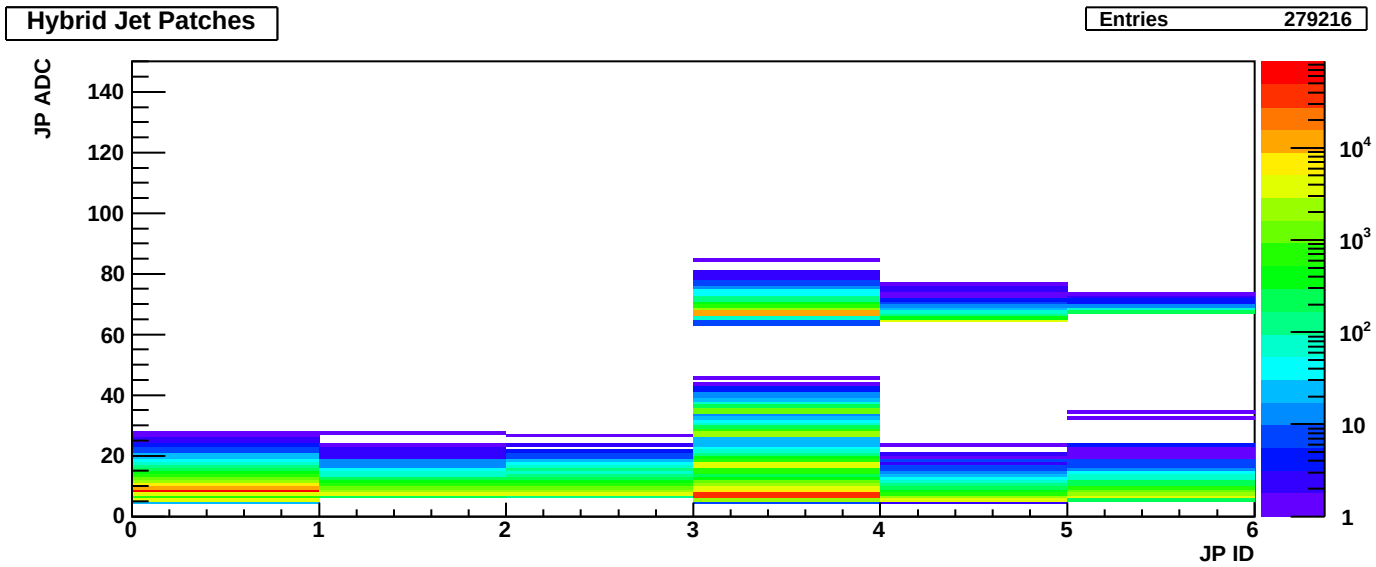

Barrel EMC L0 Input - Patch Sum
Entries 4.18824e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

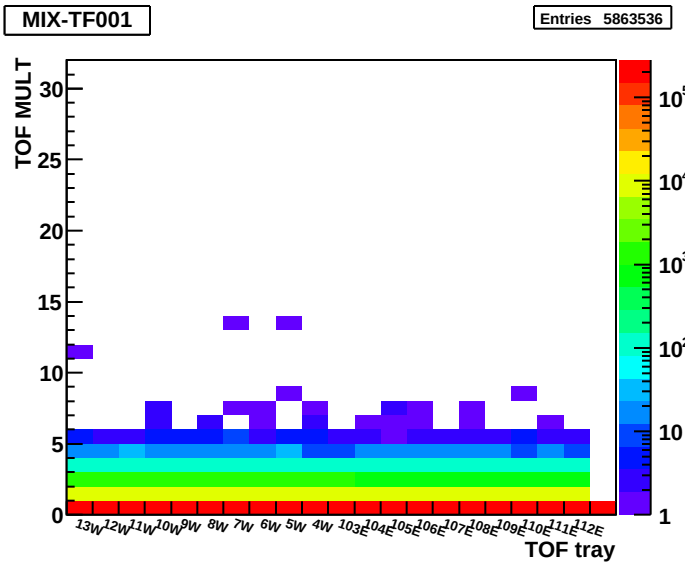

Endcap EMC L0 Input - High Tower
Entries 1.256472e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

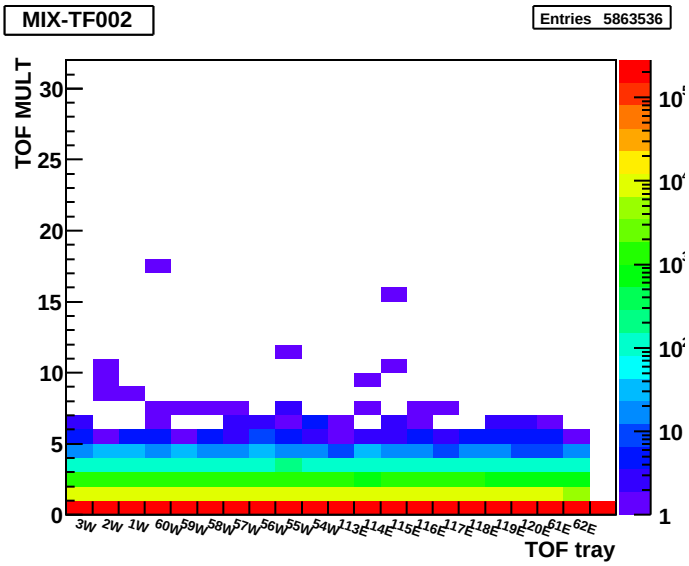

Endcap EMC L0 Input - Patch Sum
Entries 1.256472e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

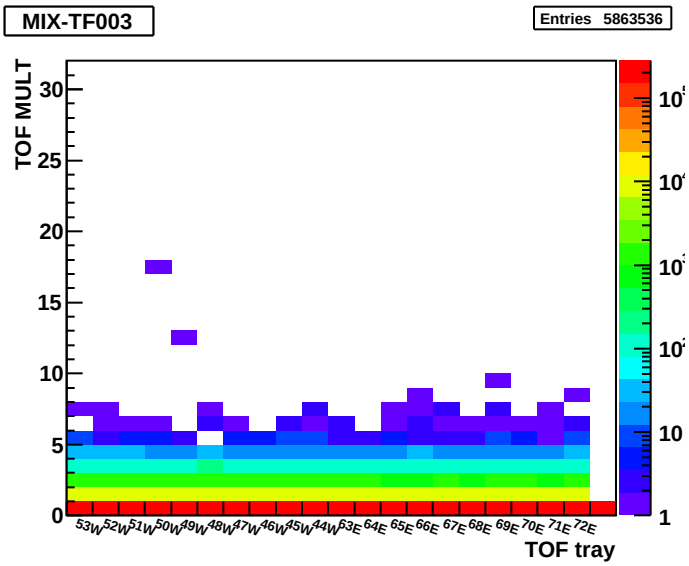

Barrel Jet Patches
Entries 2512944
JP ADC
JP ID

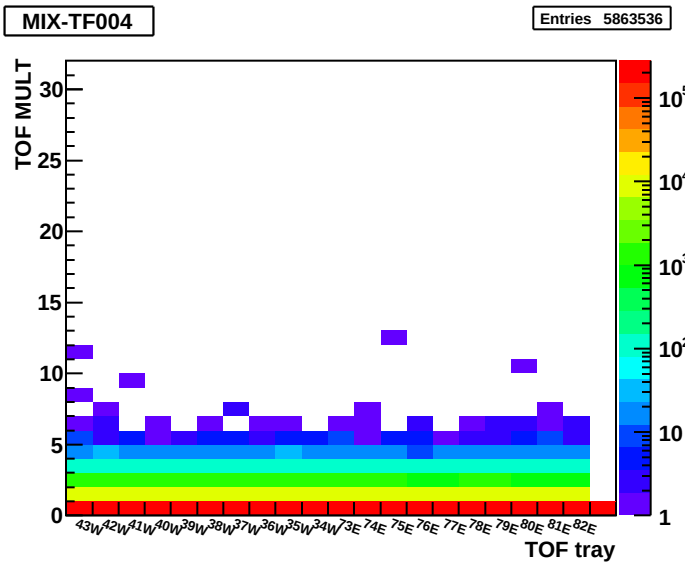

Endcap Jet Patches
Entries 837648
JP ADC
JP ID

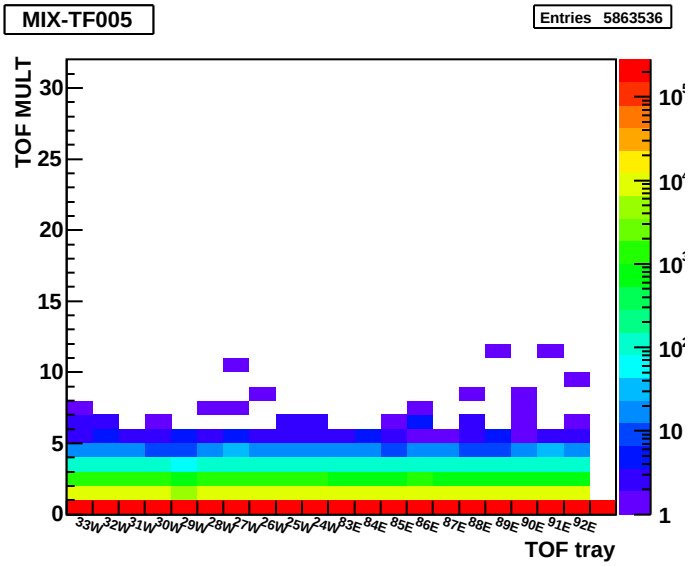

Hybrid Jet Patches
Entries 279216
JP ADC
JP ID

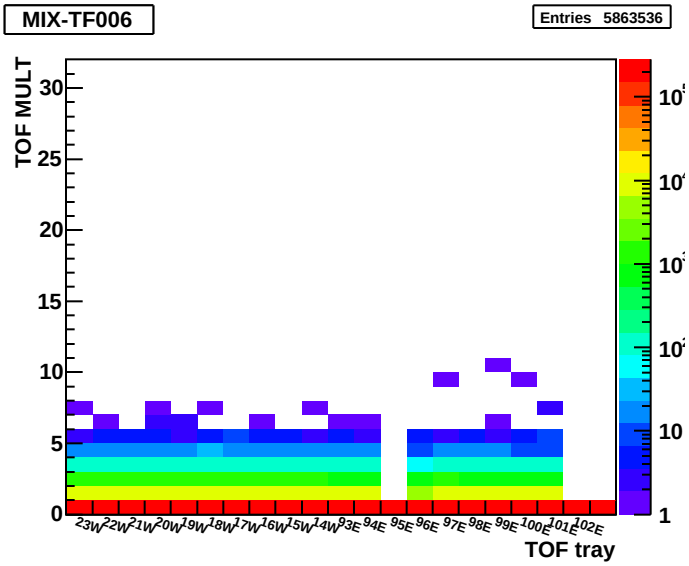

MIX-TF001
Entries 5863536
TOF MULT
TOF tray

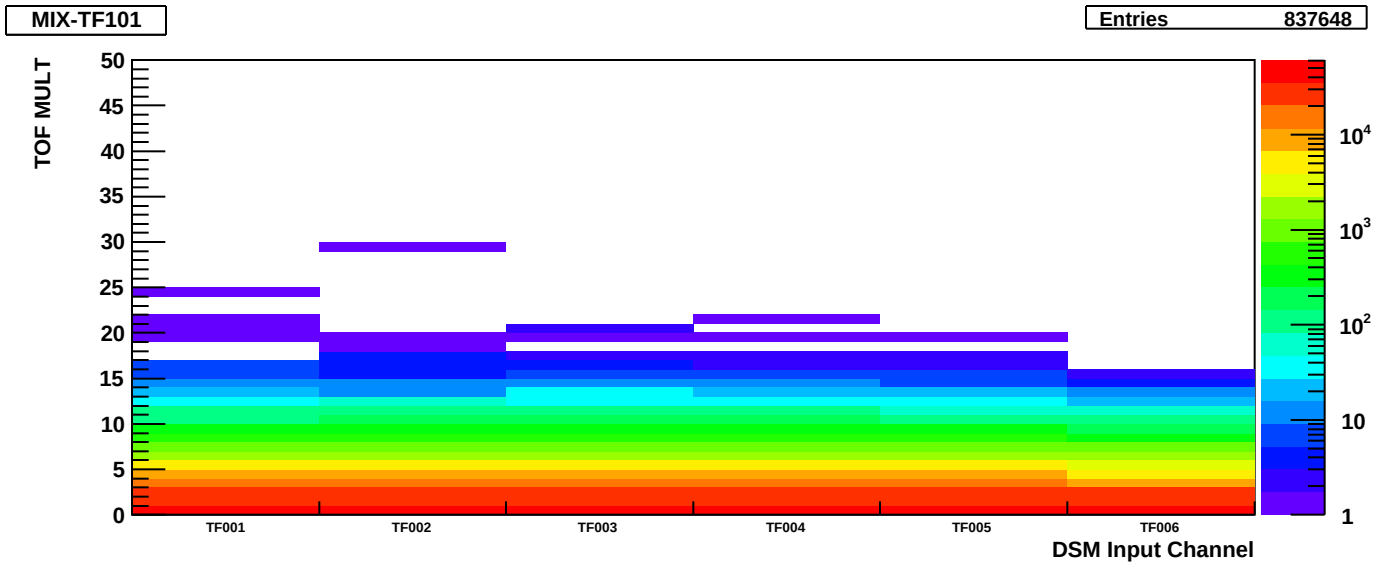

MIX-TF002
Entries 5863536
TOF MULT
TOF tray

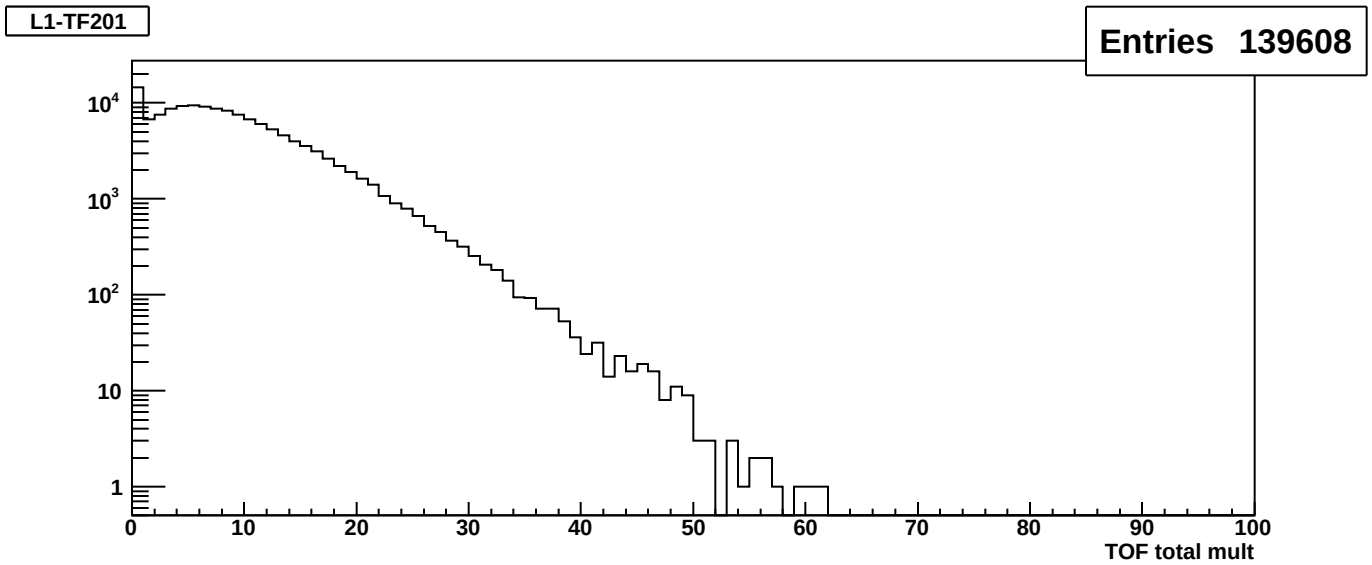

MIX-TF003
Entries 5863536
TOF MULT
TOF tray

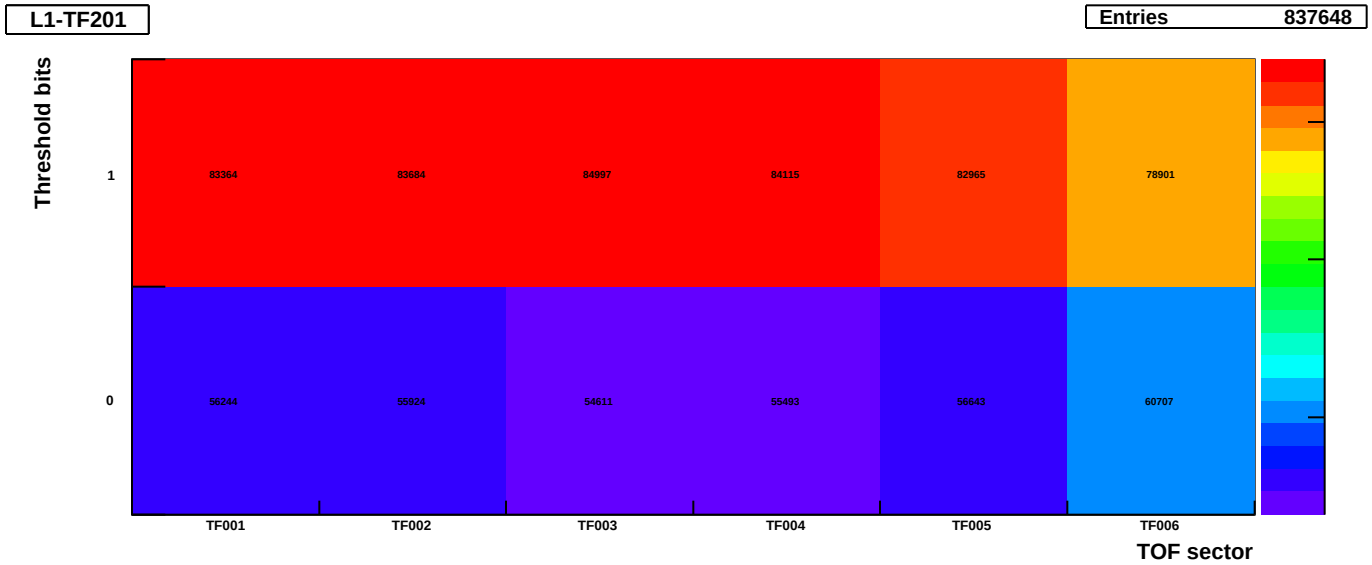

MIX-TF004
Entries 5863536
TOF MULT
TOF tray

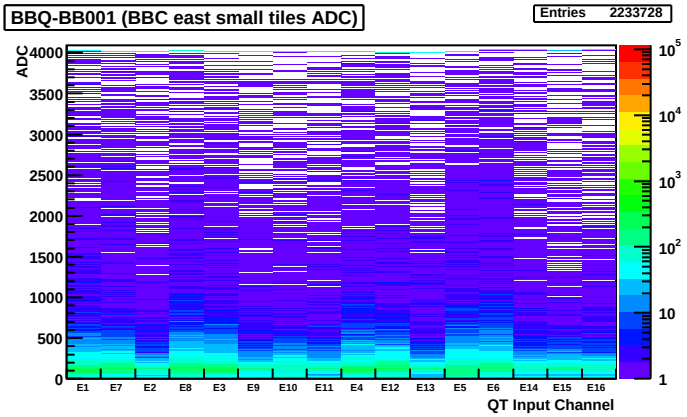

MIX-TF005
Entries 5863536
TOF MULT
TOF tray

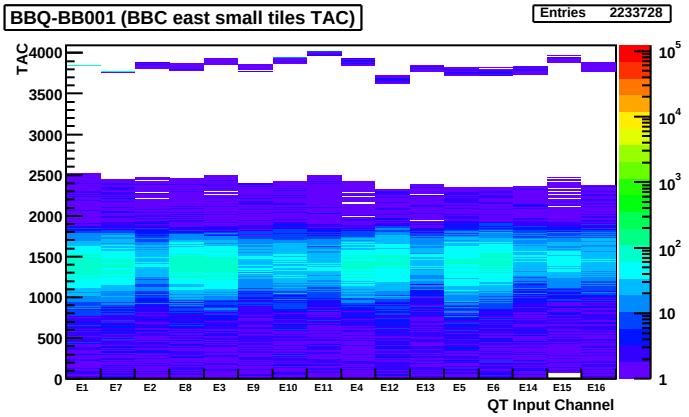

MIX-TF006
Entries 5863536
TOF MULT
TOF tray

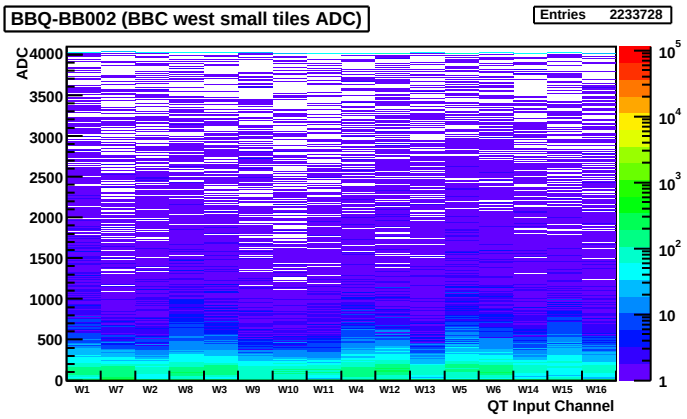

MIX-TF101
Entries 837648
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

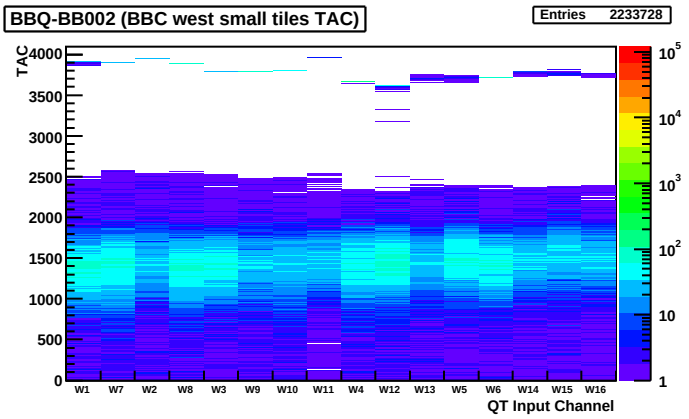

L1-TF201
Entries 139608
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

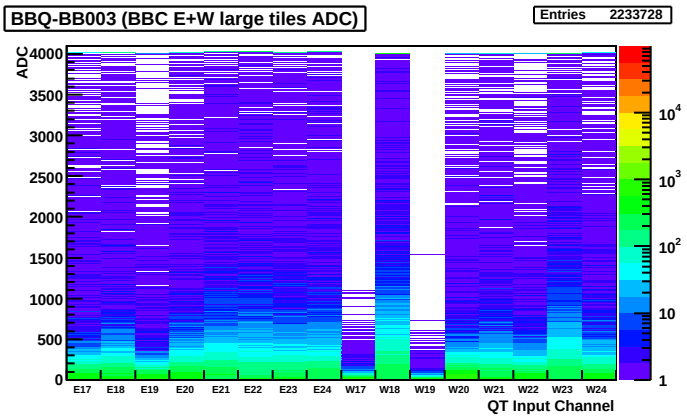

L1-TF201
Entries 837648
Threshold bits
1
0
83364
83684
84997
84115
82965
78901
56244
55924
54611
55493
56643
60707
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

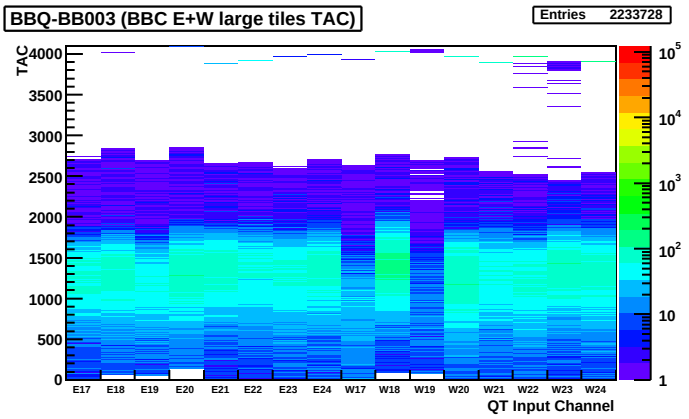

BBQ-BB001 (BBC east small tiles ADC)
Entries 2233728
ADC
QT Input Channel

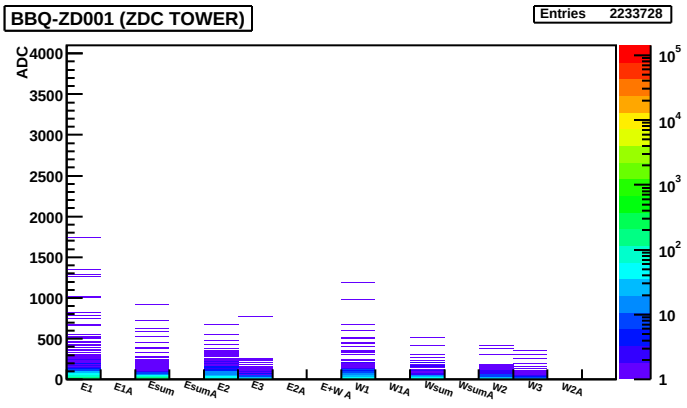

BBQ-BB001 (BBC east small tiles TAC)
Entries 2233728
TAC
QT Input Channel

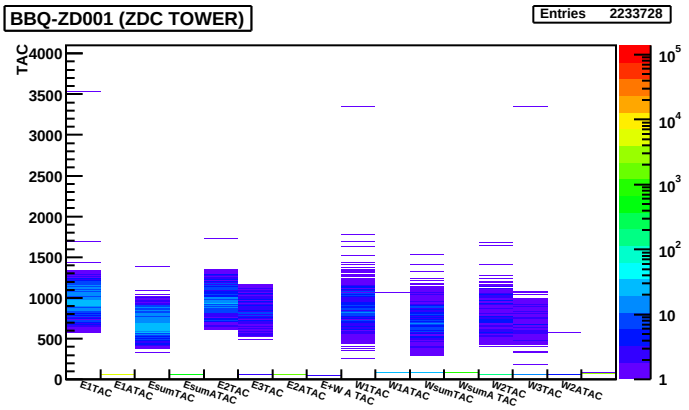

BBQ-BB002 (BBC west small tiles ADC)
Entries 2233728
ADC
QT Input Channel

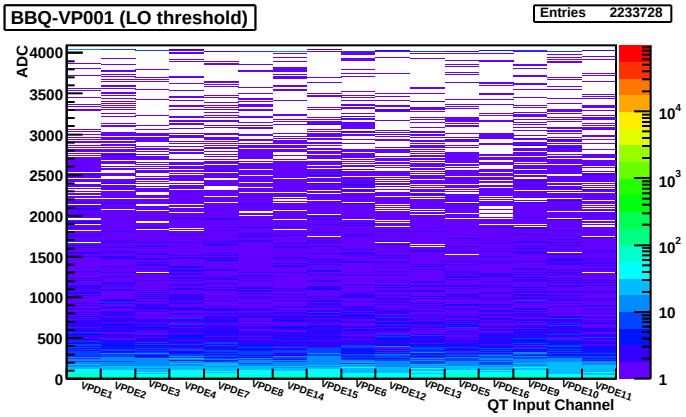

BBQ-BB002 (BBC west small tiles TAC)
Entries 2233728
TAC
QT Input Channel

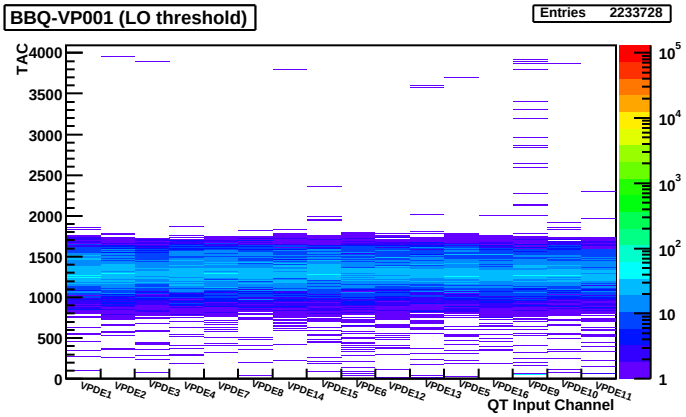

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2233728
ADC
QT Input Channel

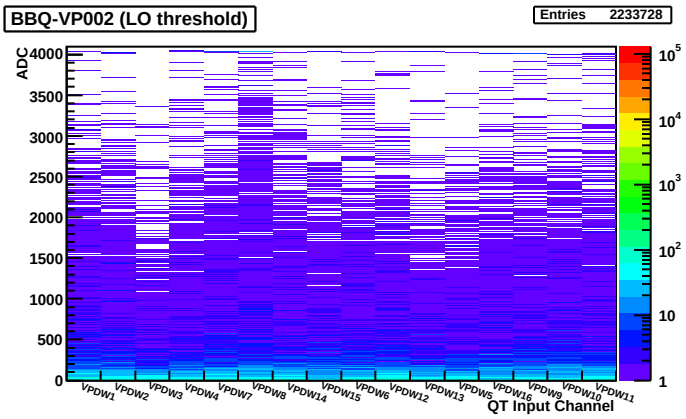

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2233728
TAC
QT Input Channel

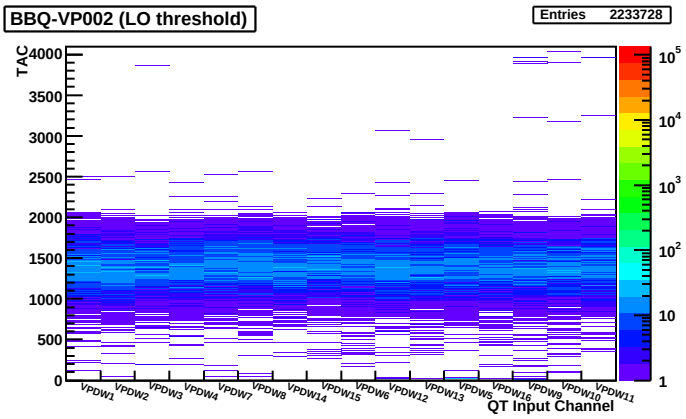

BBQ-ZD001 (ZDC TOWER)
Entries 2233728
ADC

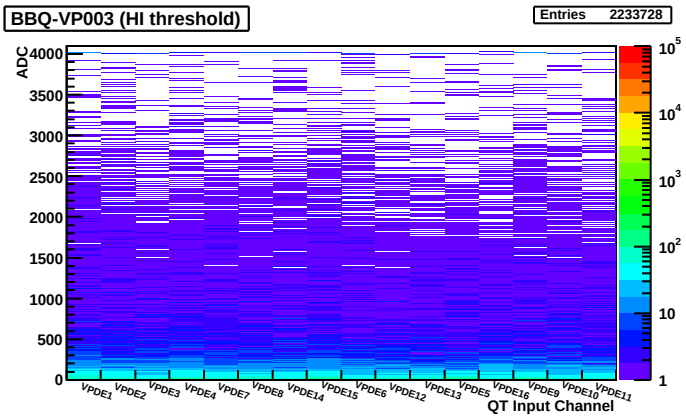

BBQ-ZD001 (ZDC TOWER)
Entries 2233728
TAC

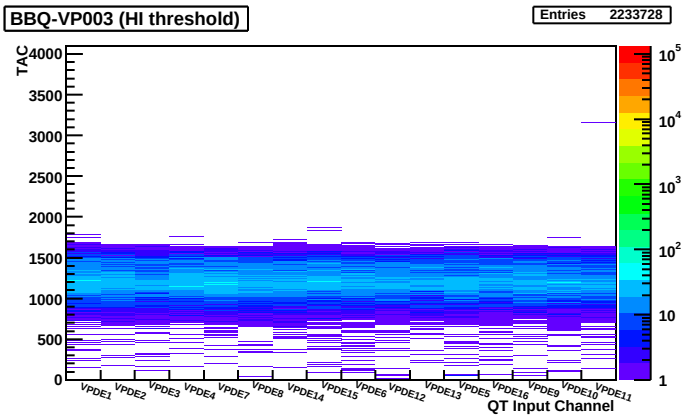

BBQ-VP001 (LO threshold)
Entries 2233728
ADC
QT Input Channel

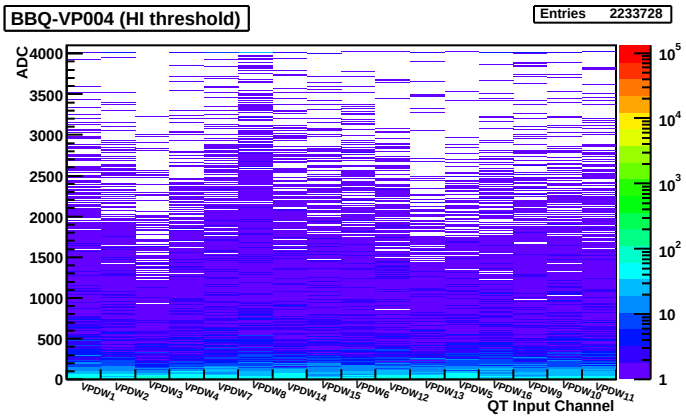

BBQ-VP001 (LO threshold)
Entries 2233728
TAC
QT Input Channel

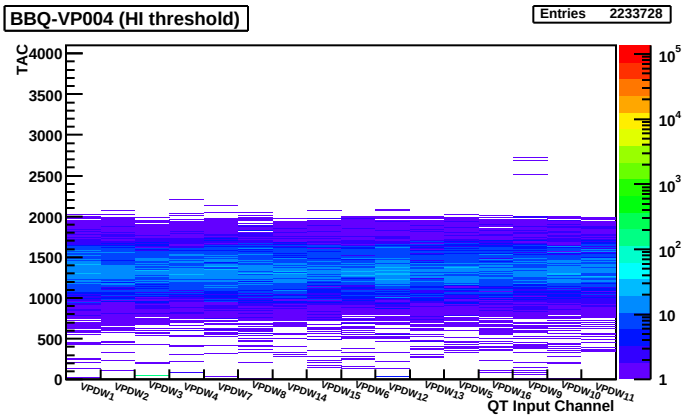

BBQ-VP002 (LO threshold)
Entries 2233728
ADC
QT Input Channel

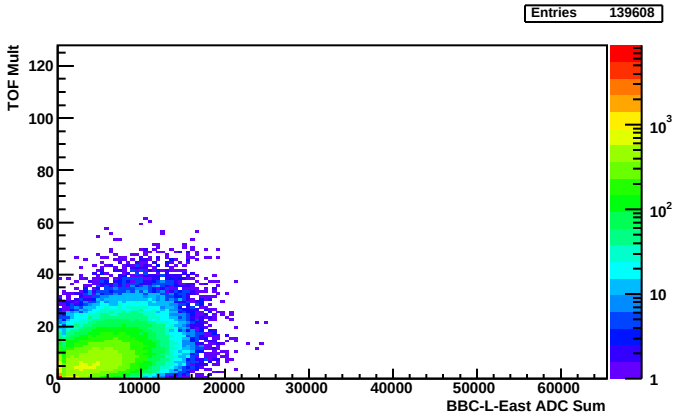

BBQ-VP002 (LO threshold)
Entries 2233728
TAC
QT Input Channel

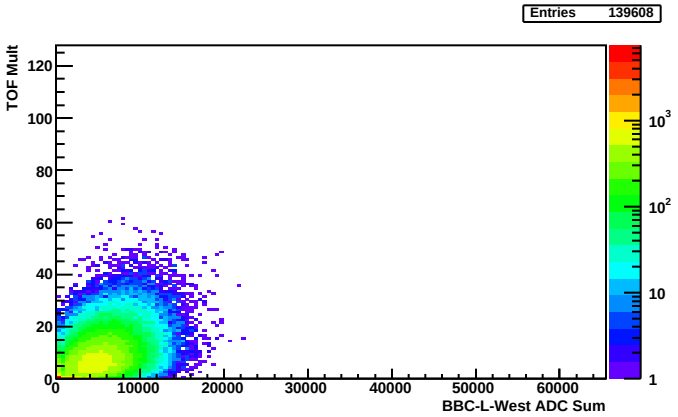

BBQ-VP003 (HI threshold)
Entries 2233728
ADC
QT Input Channel

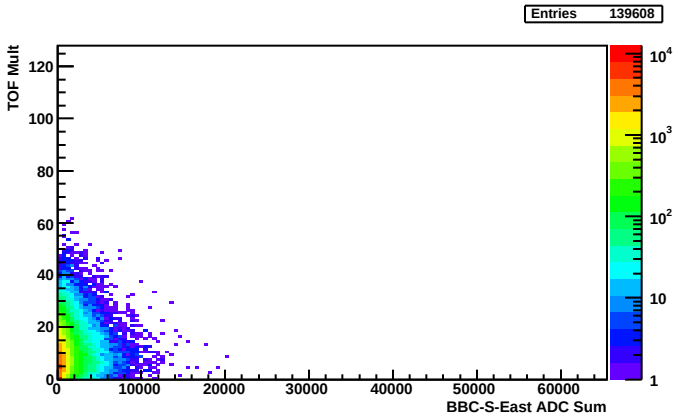

BBQ-VP003 (HI threshold)
Entries 2233728
TAC
QT Input Channel

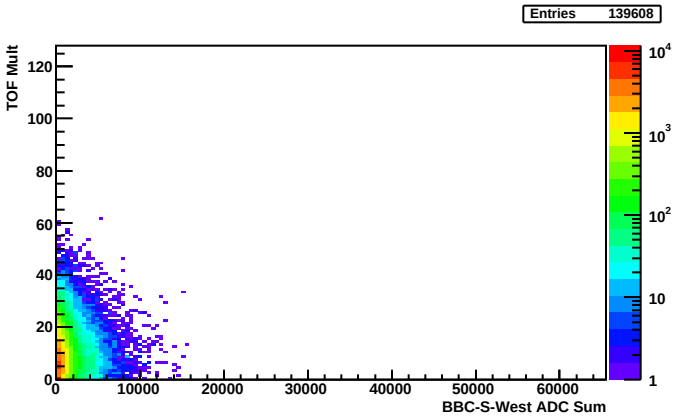

BBQ-VP004 (HI threshold)
Entries 2233728
ADC
QT Input Channel

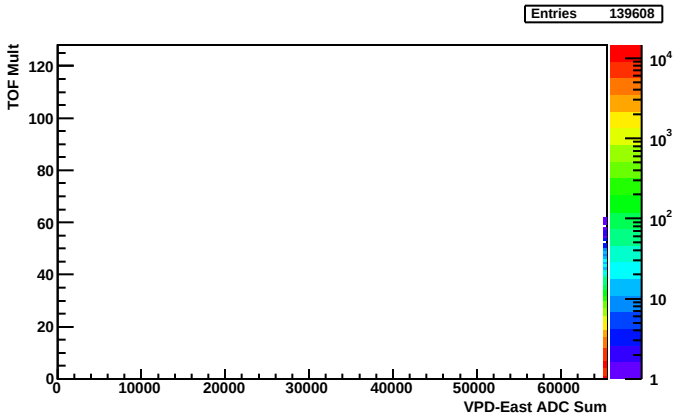

BBQ-VP004 (HI threshold)
Entries 2233728
TAC
QT Input Channel

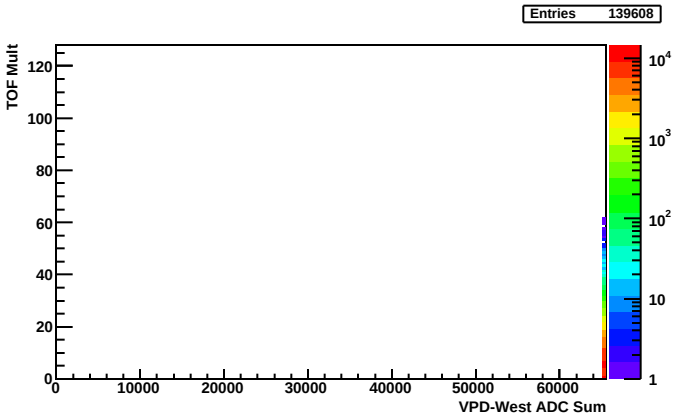

Entries 139608
TOF Mult
BBC-L-East ADC Sum

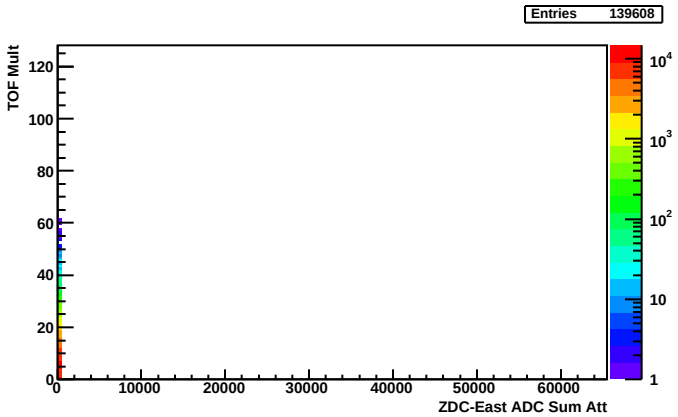

Entries 139608
TOF Mult
BBC-L-West ADC Sum

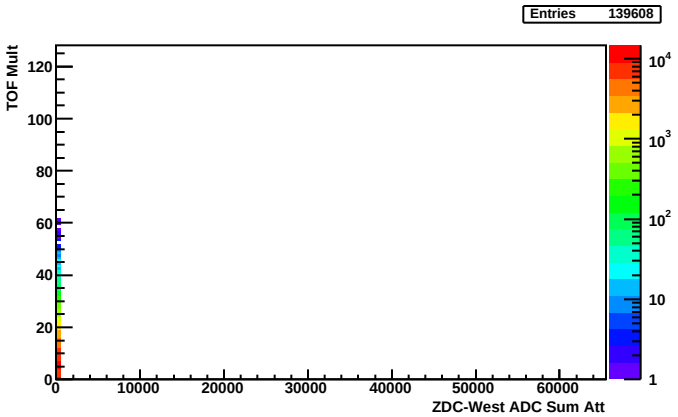

Entries 139608
TOF Mult
BBC-S-East ADC Sum

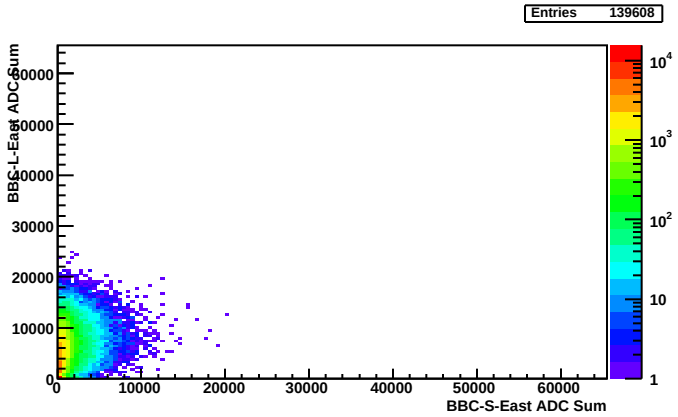

Entries 139608
TOF Mult
BBC-S-West ADC Sum

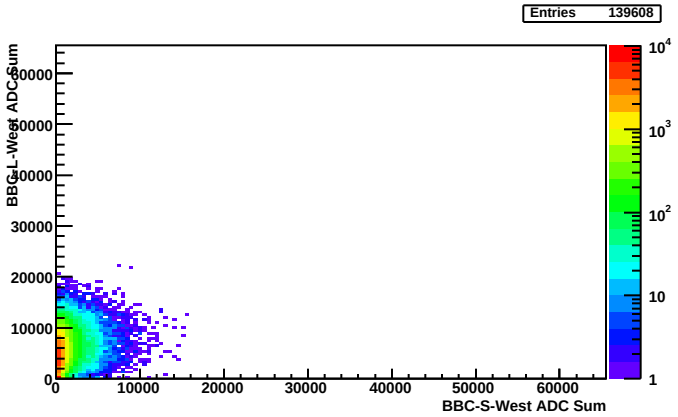

Entries 139608
TOF Mult
VPD-East ADC Sum

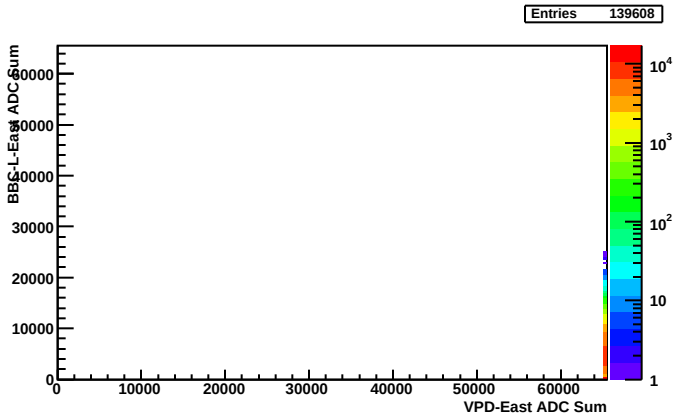

Entries 139608
TOF Mult
VPD-West ADC Sum

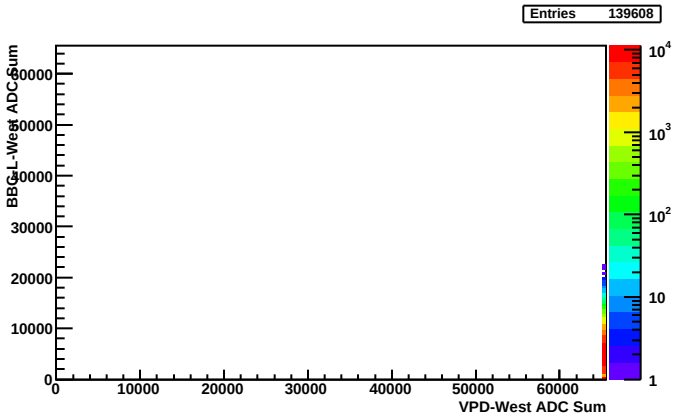

Entries 139608
TOF Mult
ZDC-East ADC Sum Att

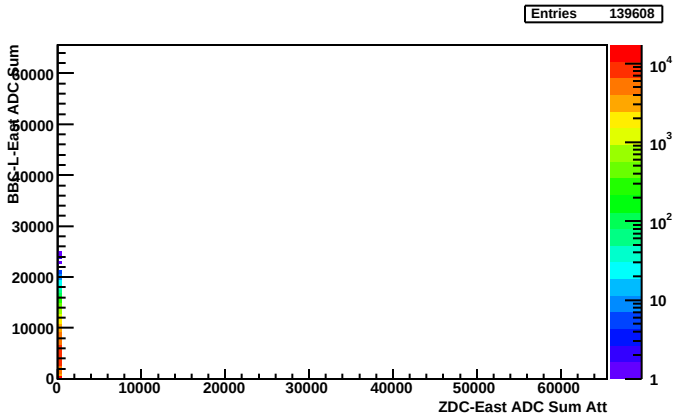

Entries 139608
TOF Mult
ZDC-West ADC Sum Att

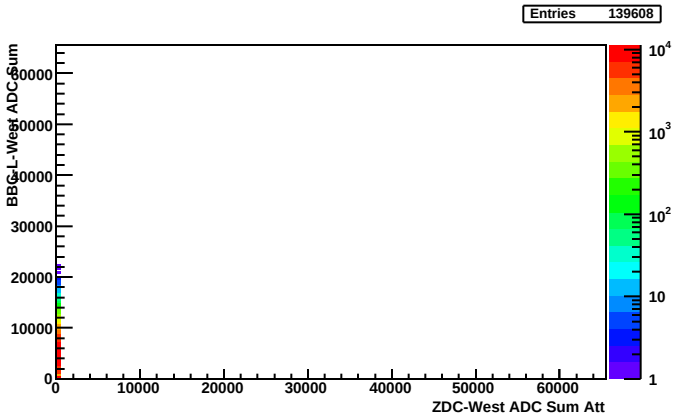

Entries 139608
BBC-L-East ADC Sum
BBC-S-East ADC Sum

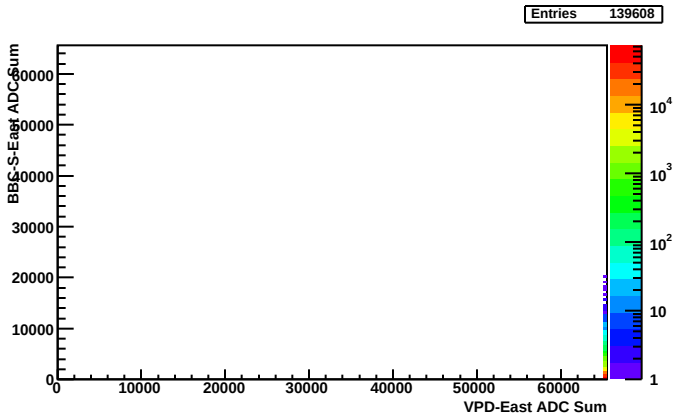

Entries 139608
BBC-L-West ADC Sum
BBC-S-West ADC Sum

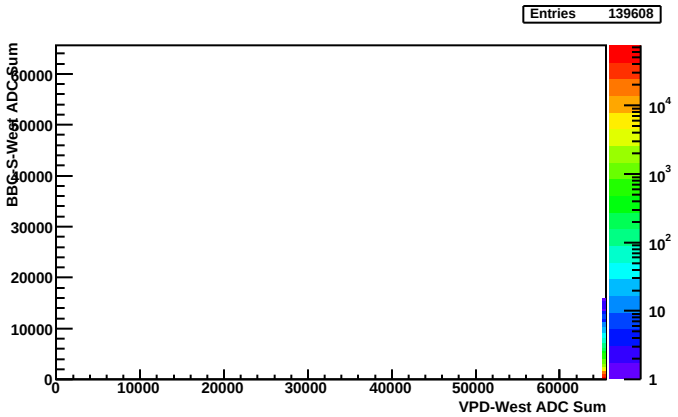

Entries 139608
BBC-L-East ADC Sum
VPD-East ADC Sum

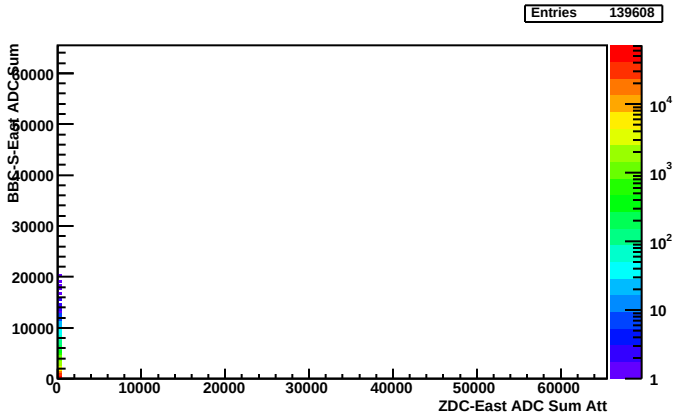

Entries 139608
BBC-L-West ADC Sum
VPD-West ADC Sum

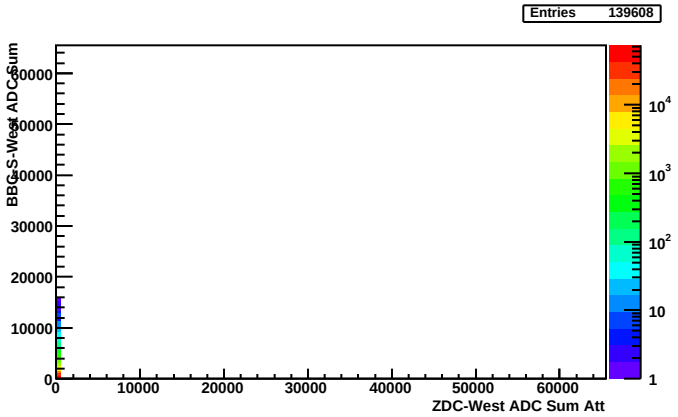

Entries 139608
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

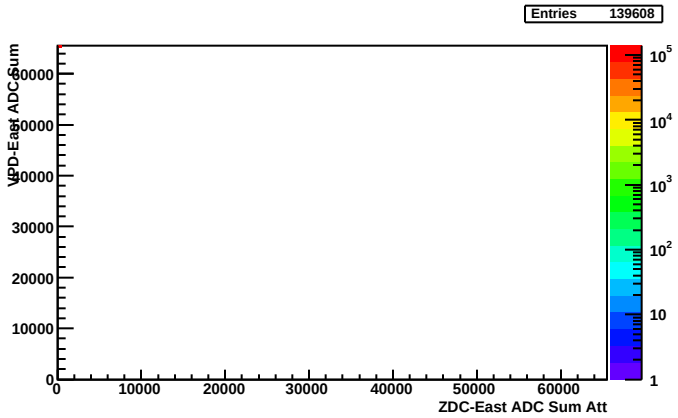

Entries 139608
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

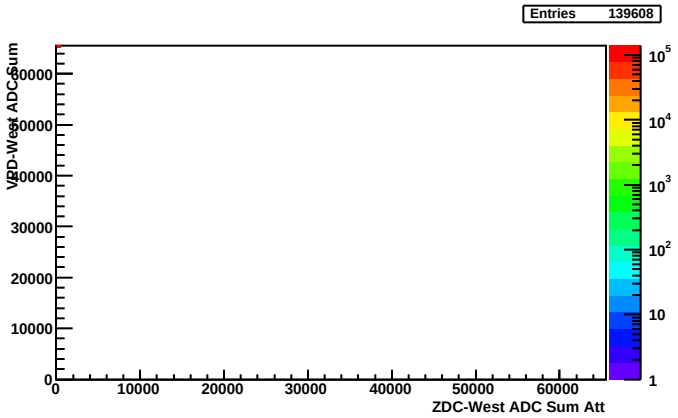

Entries 139608
BBC-S-East ADC Sum
VPD-East ADC Sum

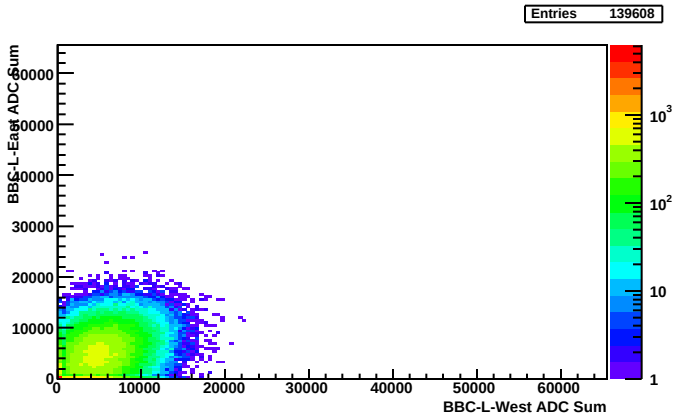

Entries 139608
BBC-S-West ADC Sum
VPD-West ADC Sum

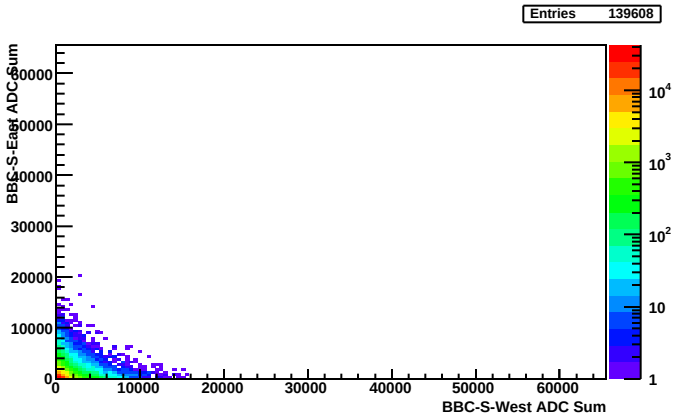

Entries 139608
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

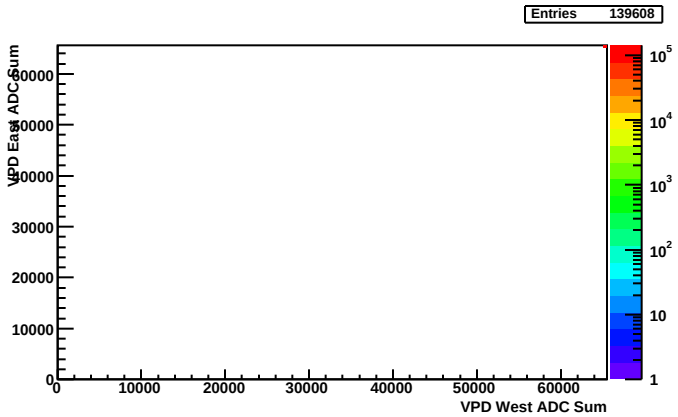

Entries 139608
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

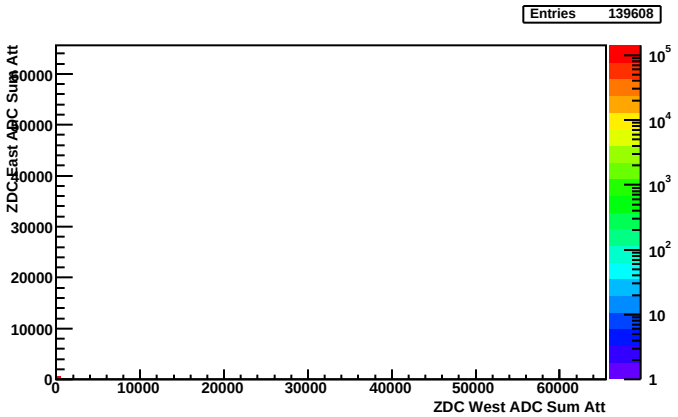

Entries 139608
VPD-East ADC Sum
ZDC-East ADC Sum Att

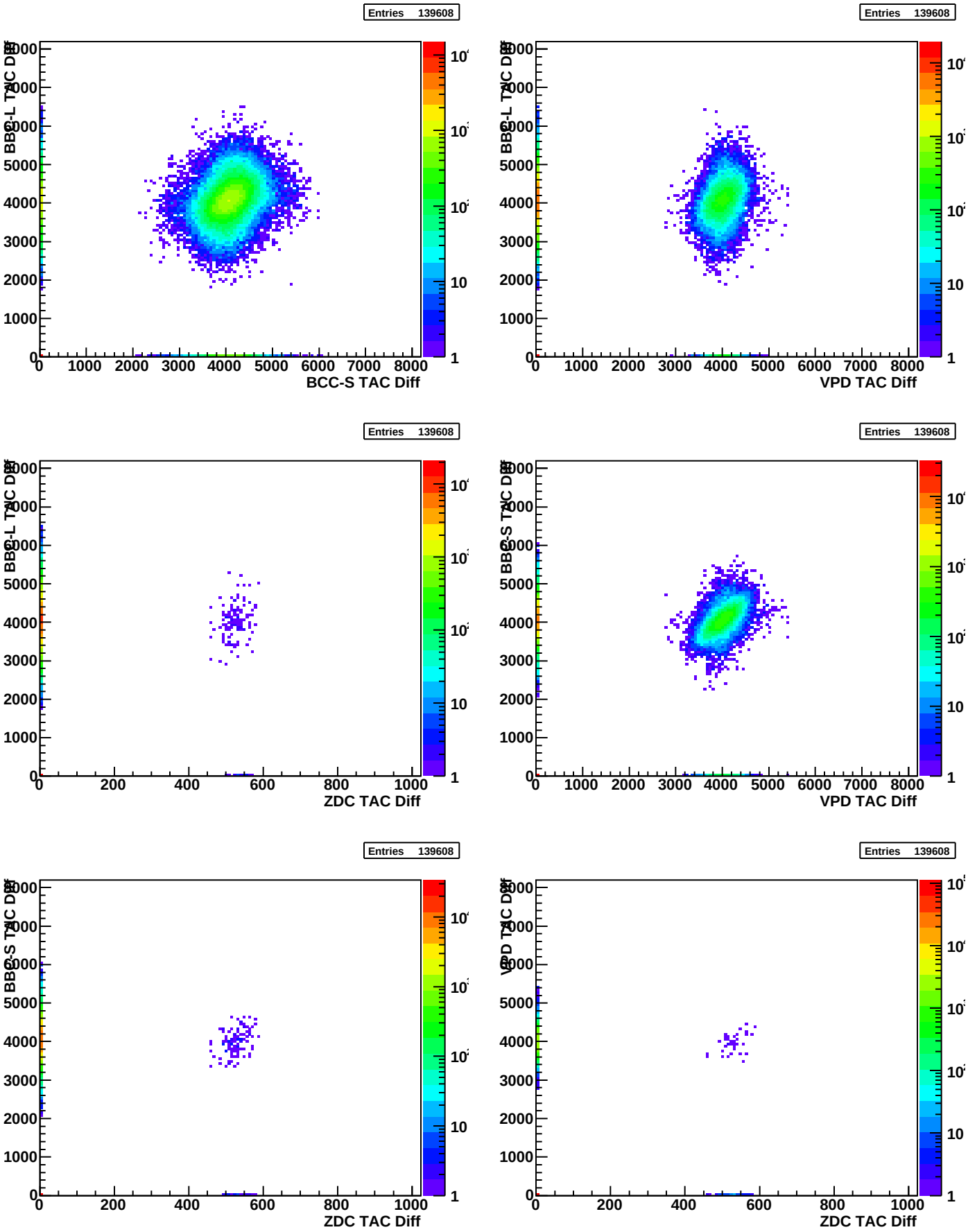

Entries 139608
VPD-West ADC Sum
ZDC-West ADC Sum Att

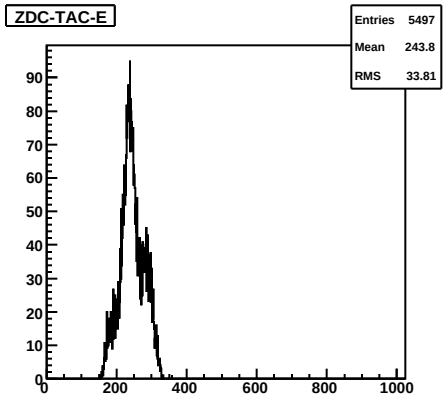

Entries 139608
BBC-L-East ADC Sum
BBC-L-West ADC Sum

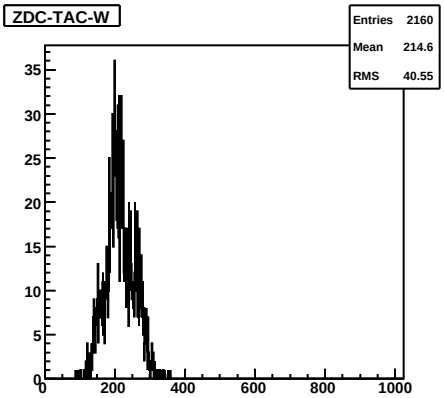

Entries 139608
BBC-S-East ADC Sum
BBC-S-West ADC Sum

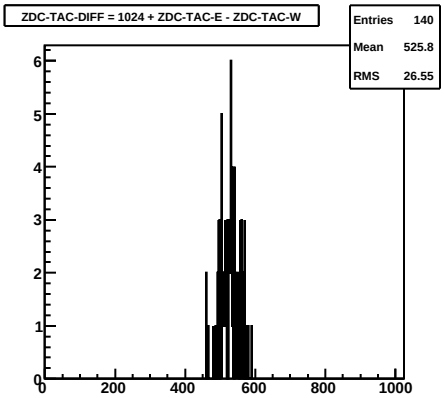

Entries 139608
VPD East ADC Sum
VPD West ADC Sum

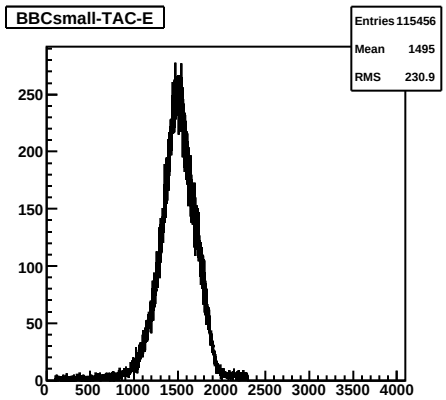

Entries 139608
ZDC East ADC Sum Att
ZDC West ADC Sum Att

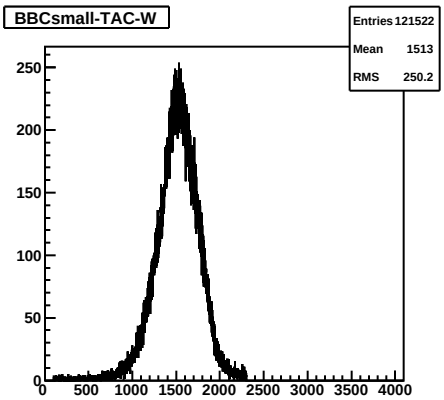

Entries 139608
BBC-L TAC Diff
BCC-S TAC Diff
Entries 139608
BBC-L TAC Diff
VPD TAC Diff
Entries 139608
BBC-L TAC Diff
ZDC TAC Diff
Entries 139608
BBC-S TAC Diff
VPD TAC Diff
Entries 139608
BBC-S TAC Diff
ZDC TAC Diff
Entries 139608
VPD TAC Diff
ZDC TAC Diff

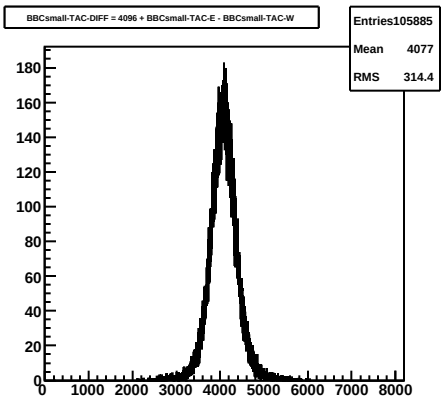

ZDC-TAC-E
Entries 5497
Mean 243.8
RMS 33.81

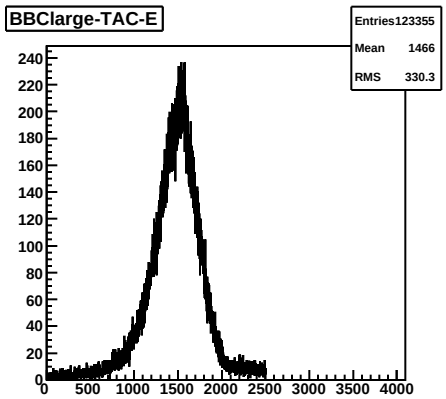

ZDC-TAC-W
Entries 2160
Mean 214.6
RMS 40.55

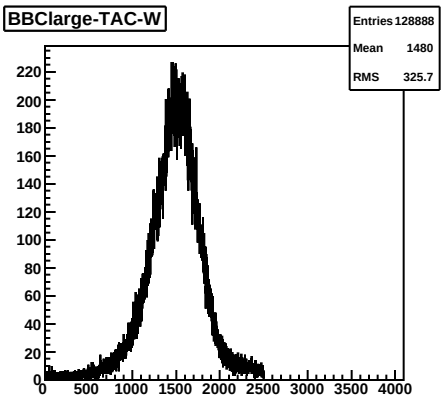

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 140
Mean 525.8
RMS 26.55

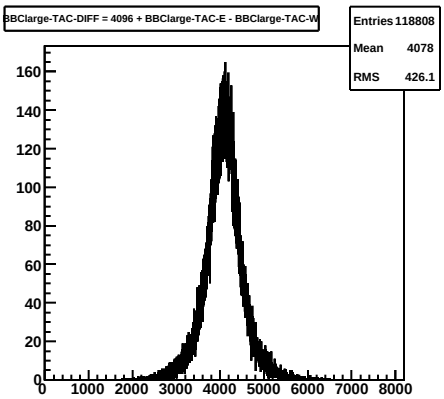

BBCsmall-TAC-E
Entries 115456
Mean 1495
RMS 230.9

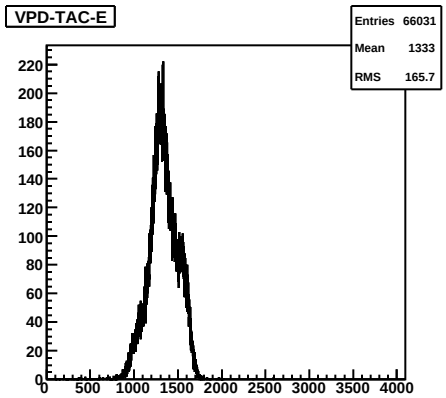

BBCsmall-TAC-W
Entries 121522
Mean 1513
RMS 250.2

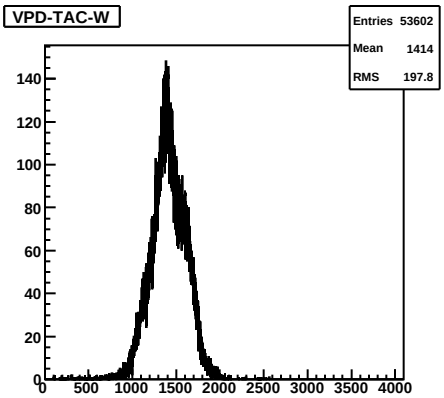

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 105885
Mean 4077
RMS 314.4

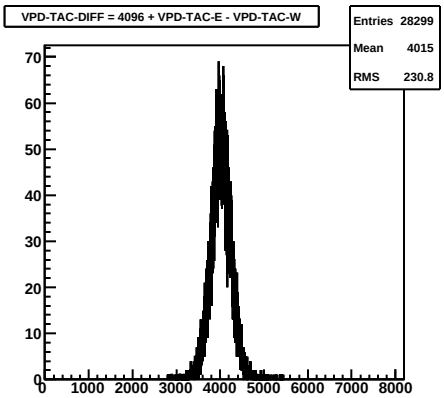

BBClarge-TAC-E
Entries 123355
Mean 1466
RMS 330.3

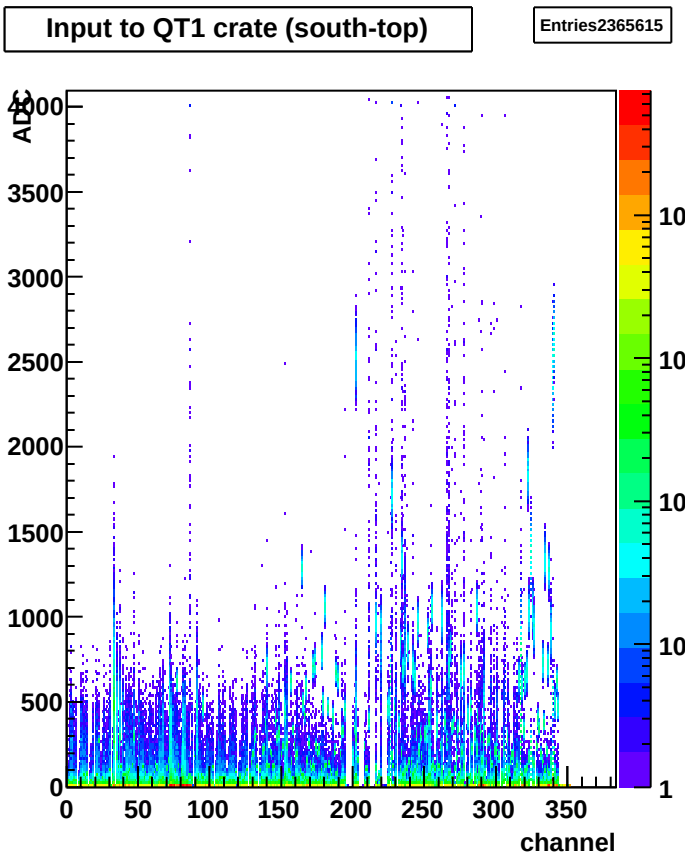

BBClarge-TAC-W
Entries 128888
Mean 1480
RMS 325.7

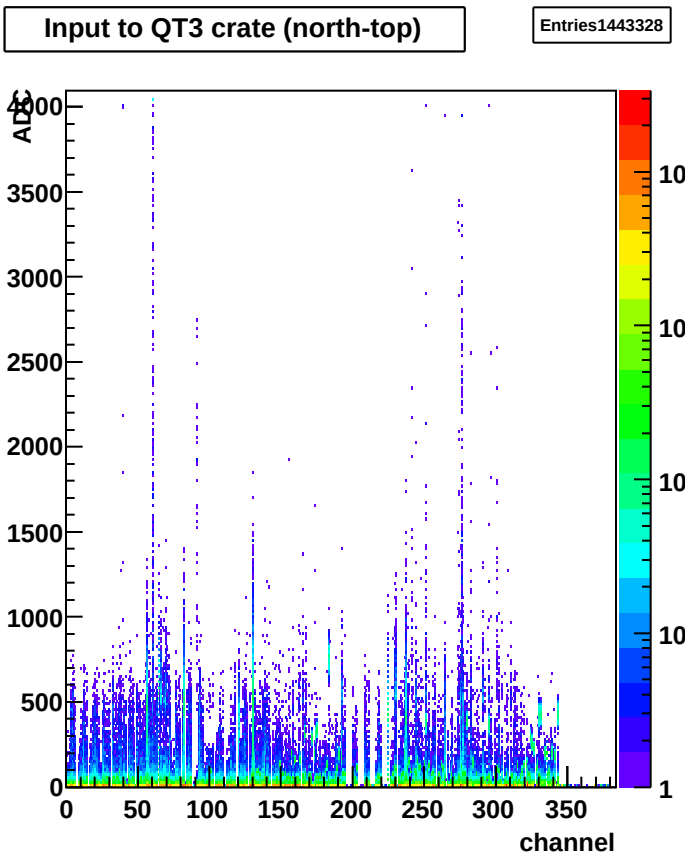

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 118808
Mean 4078
RMS 426.1

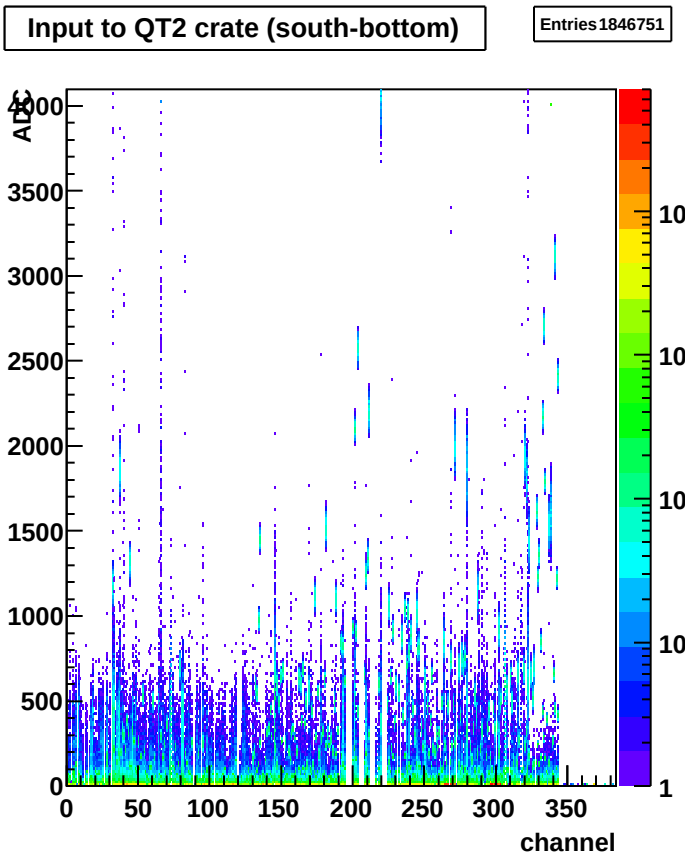

VPD-TAC-E
Entries 66031
Mean 1333
RMS 165.7

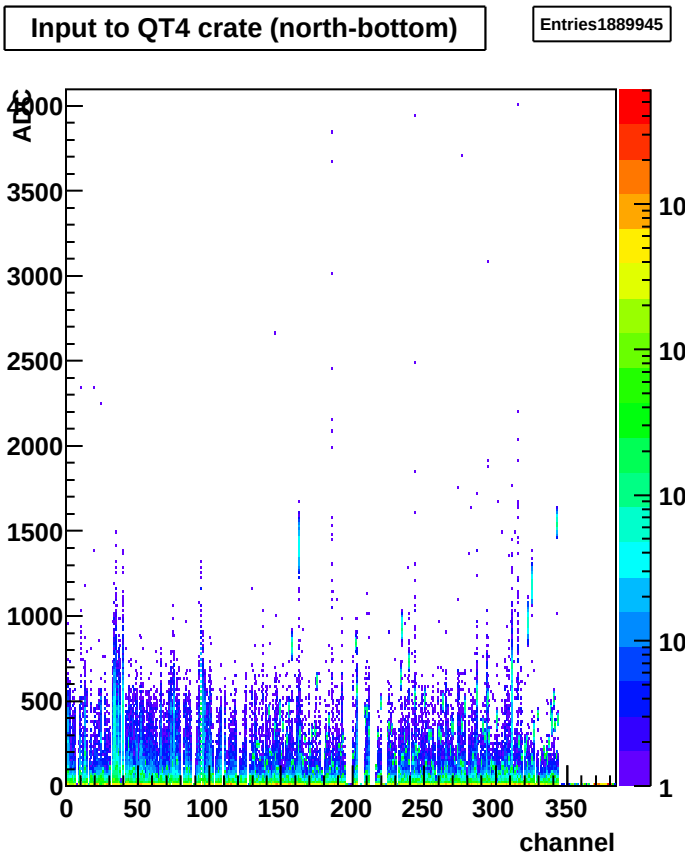

VPD-TAC-W
Entries 53602
Mean 1414
RMS 197.8

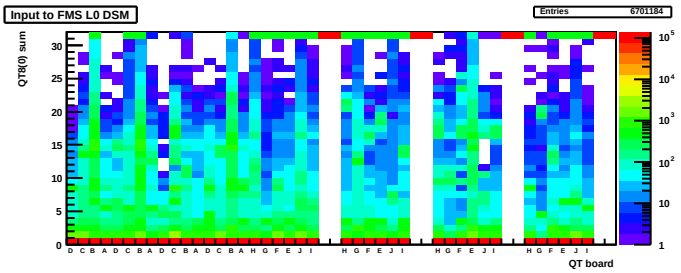

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 28299
Mean 4015
RMS 230.8

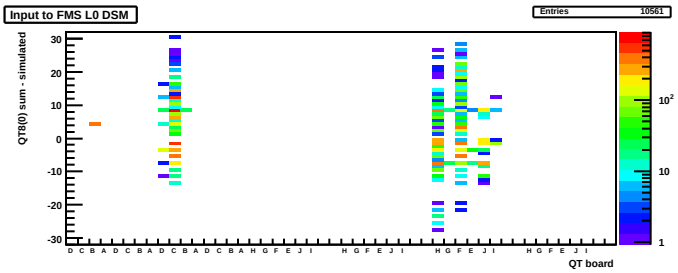

Input to QT1 crate (south-top)
Entries2365615
ADC
channel

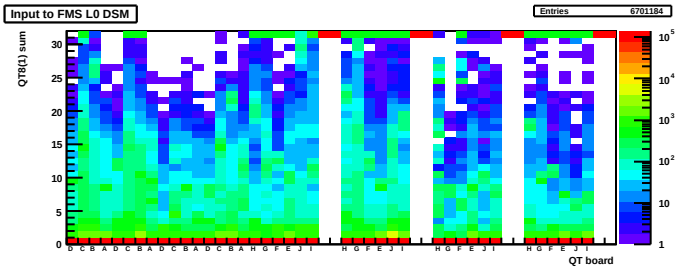

Input to QT3 crate (north-top)
Entries1443328
ADC
channel

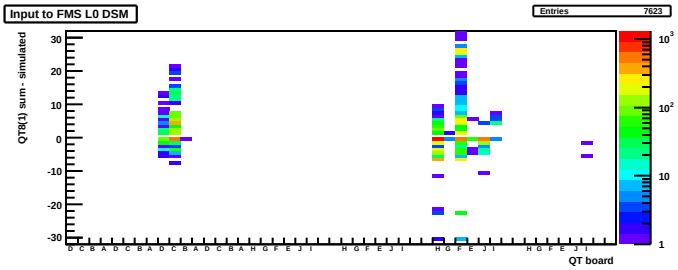

Input to QT2 crate (south-bottom)
Entries1846751
ADC
channel

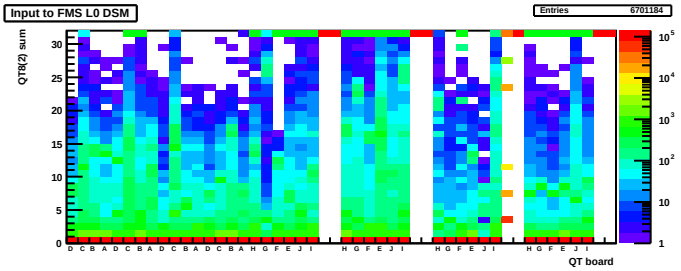

Input to QT4 crate (north-bottom)
Entries1889945
ADC
channel

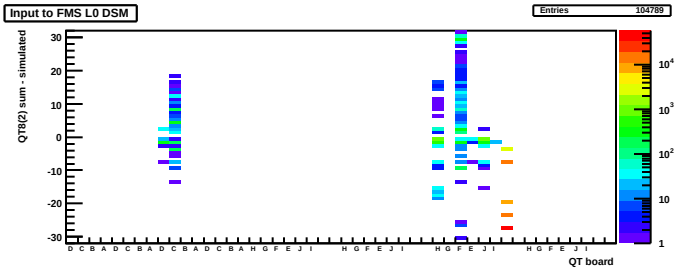

Input to FMS L0 DSM
Entries 6701184
QT8(0) sum
QT board

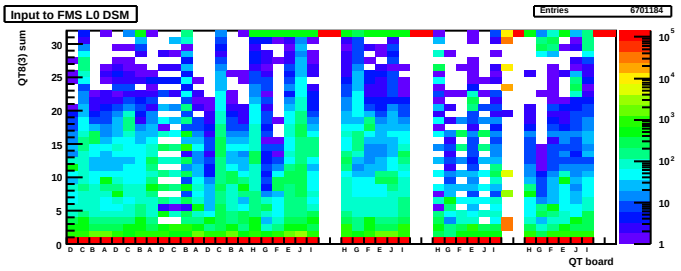

Input to FMS L0 DSM
Entries 10561
QT8(0) sum - simulated
QT board

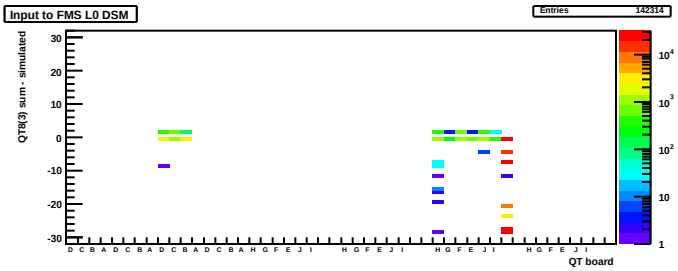

Input to FMS L0 DSM
Entries 6701184
QT8(1) sum
QT board

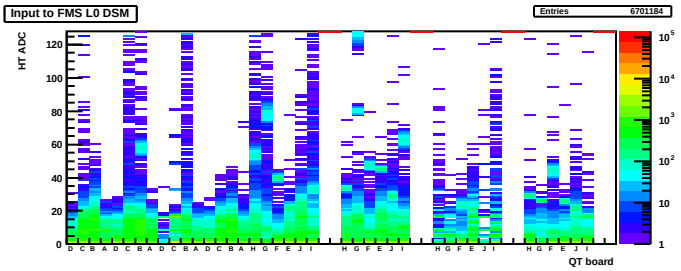

Input to FMS L0 DSM
Entries 7623
QT8(1) sum - simulated
QT board

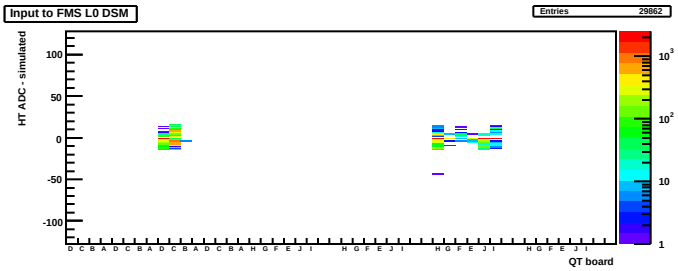

Input to FMS L0 DSM
Entries 6701184
QT8(2) sum
QT board

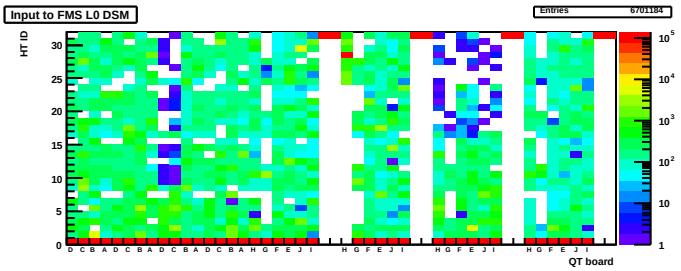

Input to FMS L0 DSM
Entries 104789
QT8(2) sum - simulated
QT board

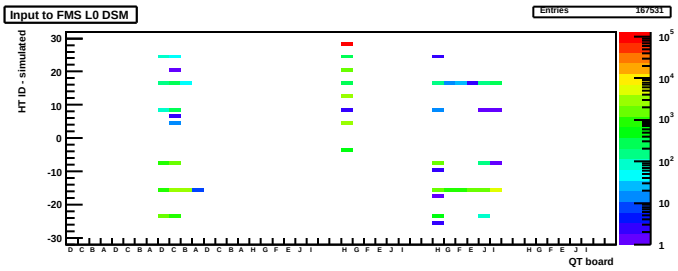

Input to FMS L0 DSM
Entries 6701184
QT8(3) sum
QT board

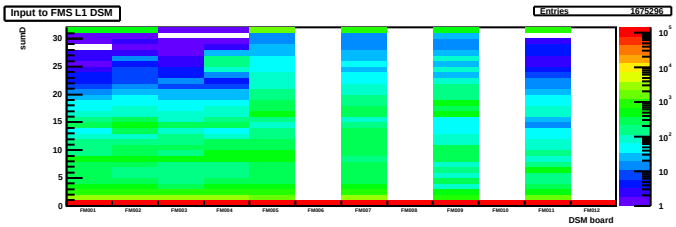

Input to FMS L0 DSM
Entries 142314
QT8(3) sum - simulated
QT board

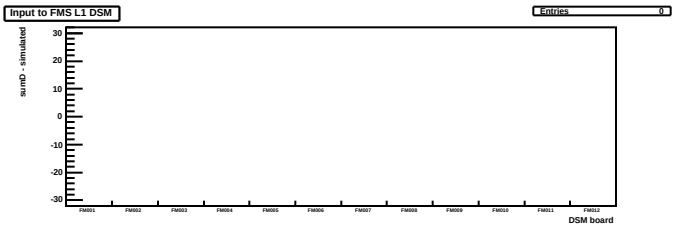

Input to FMS L0 DSM
Entries 6701184
HT ADC
QT board

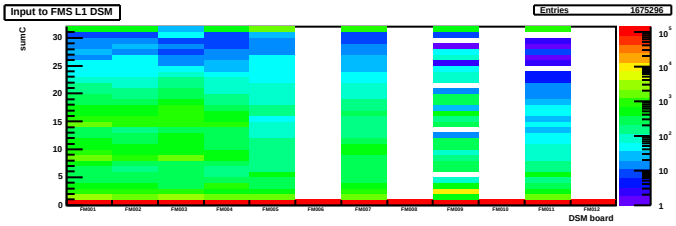

Input to FMS L0 DSM
Entries 29862
HT ADC - simulated
QT board

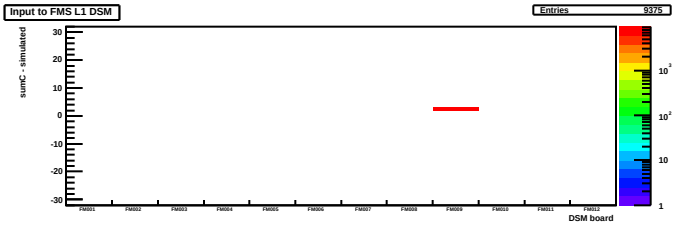

Input to FMS L0 DSM
Entries 6701184
HT ID
QT board

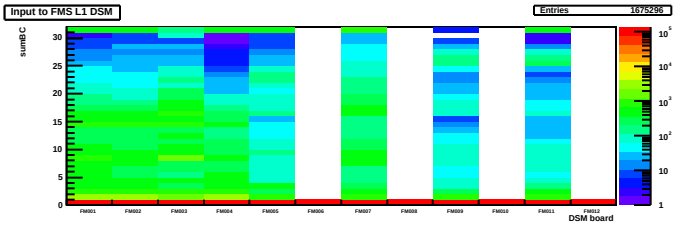

Input to FMS L0 DSM
Entries 167531
HT ID - simulated
QT board

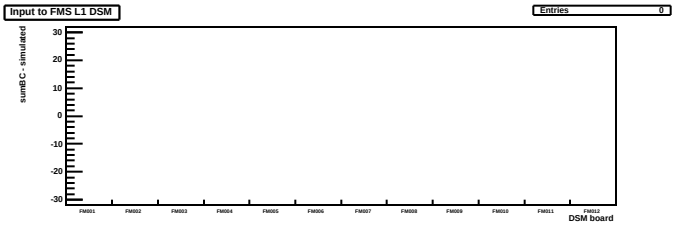

Input to FMS L1 DSM
Entries 1675296
sumD
DSM board

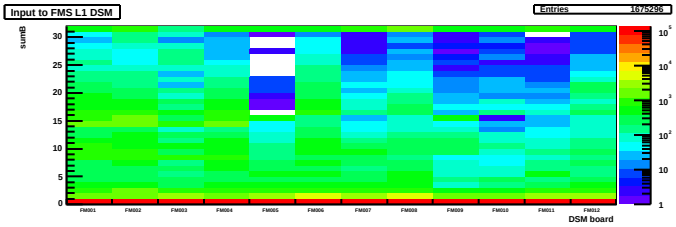

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

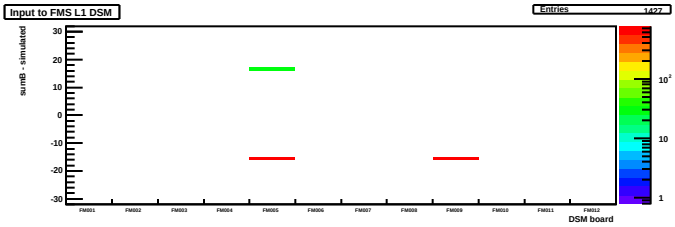

Input to FMS L1 DSM
Entries 1675296
sumC
DSM board

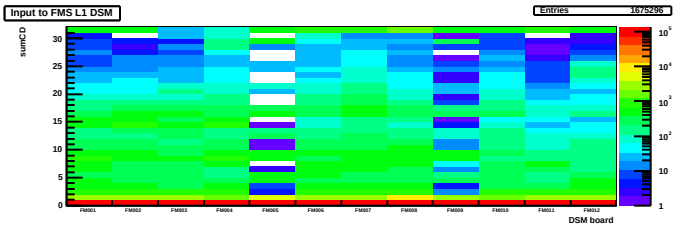

Input to FMS L1 DSM
Entries 9375
sumC - simulated
DSM board

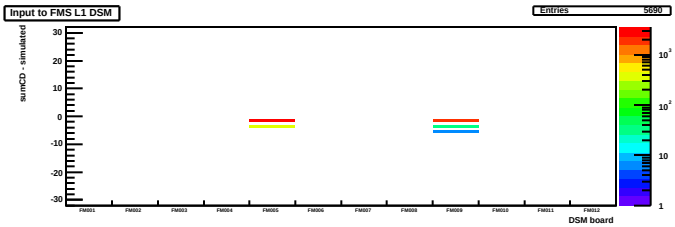

Input to FMS L1 DSM
Entries 1675296
sumBC
DSM board

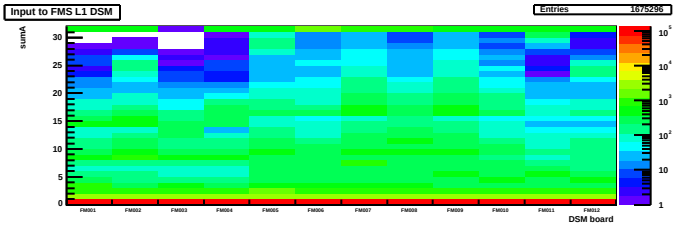

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

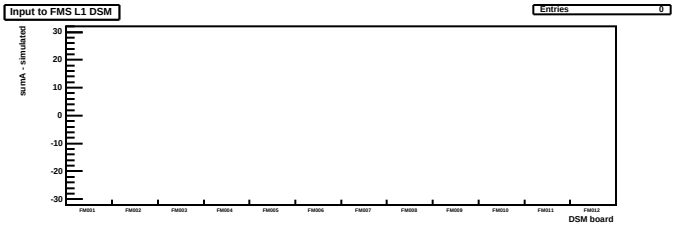

Input to FMS L1 DSM
Entries 1675296
sumB
DSM board

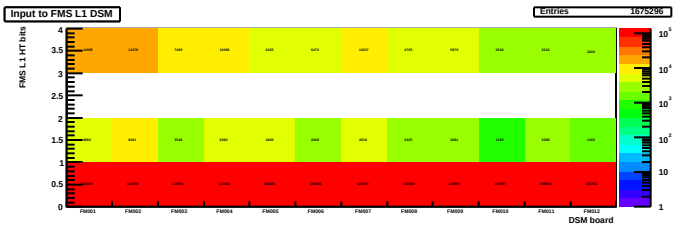

Input to FMS L1 DSM
Entries 1427
sumB - simulated
DSM board

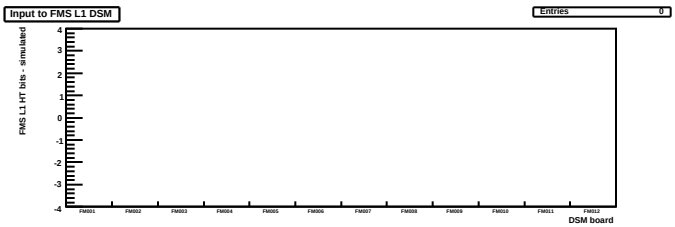

Input to FMS L1 DSM
Entries 1675296
sumCD
DSM board

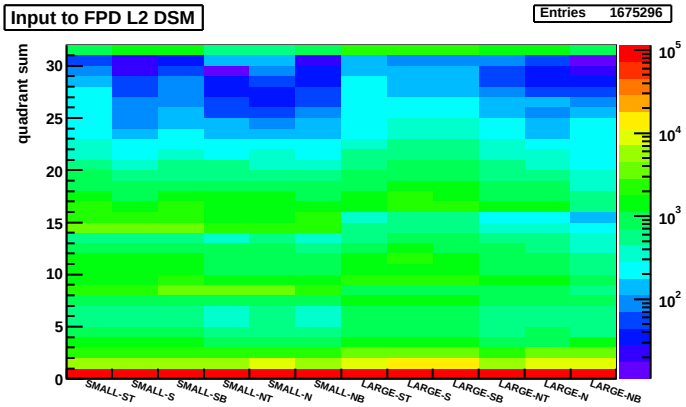

Input to FMS L1 DSM
Entries 5690
sumCD - simulated
DSM board

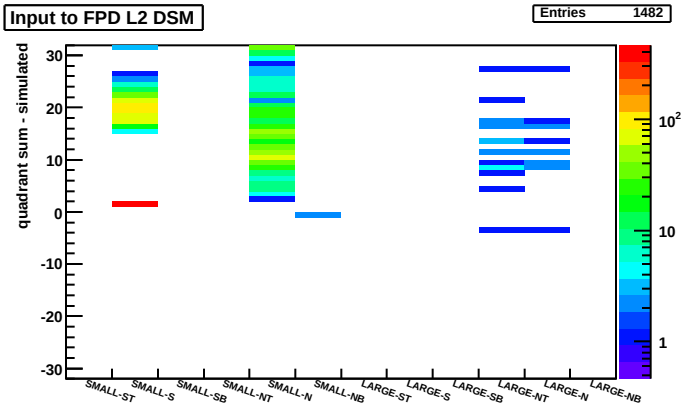

Input to FMS L1 DSM
Entries 1675296
sumA
DSM board

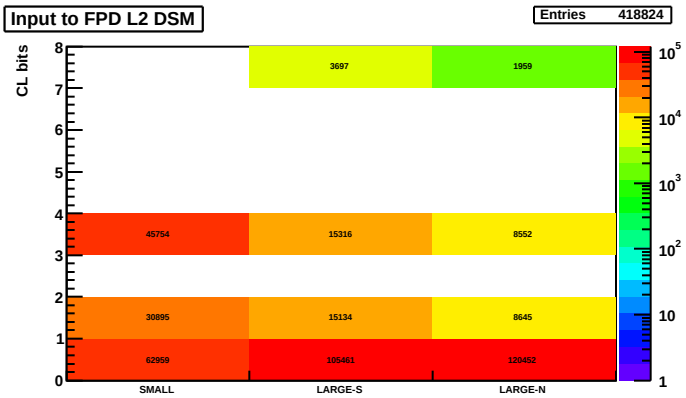

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

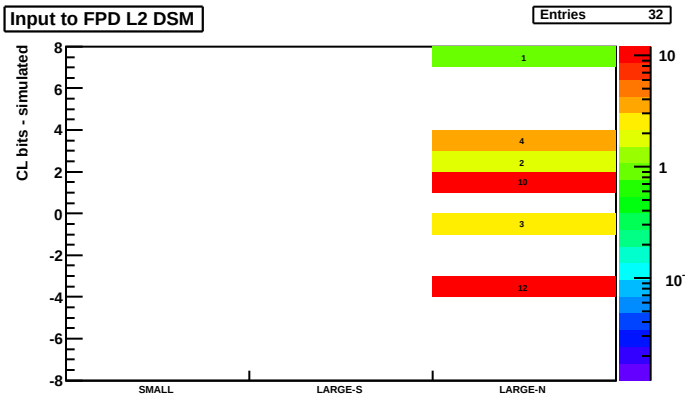

Input to FMS L1 DSM
Entries 1675296
FMS L1 HT bits
DSM board

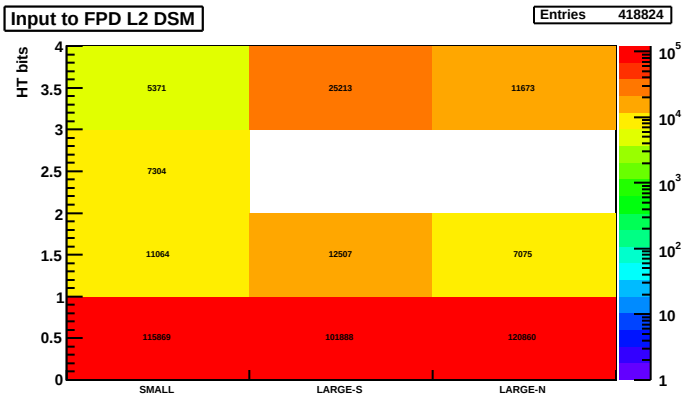

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

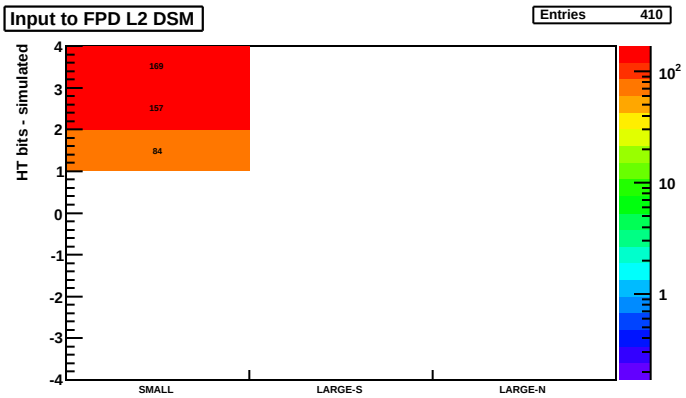

Input to FPD L2 DSM
Entries 1675296
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

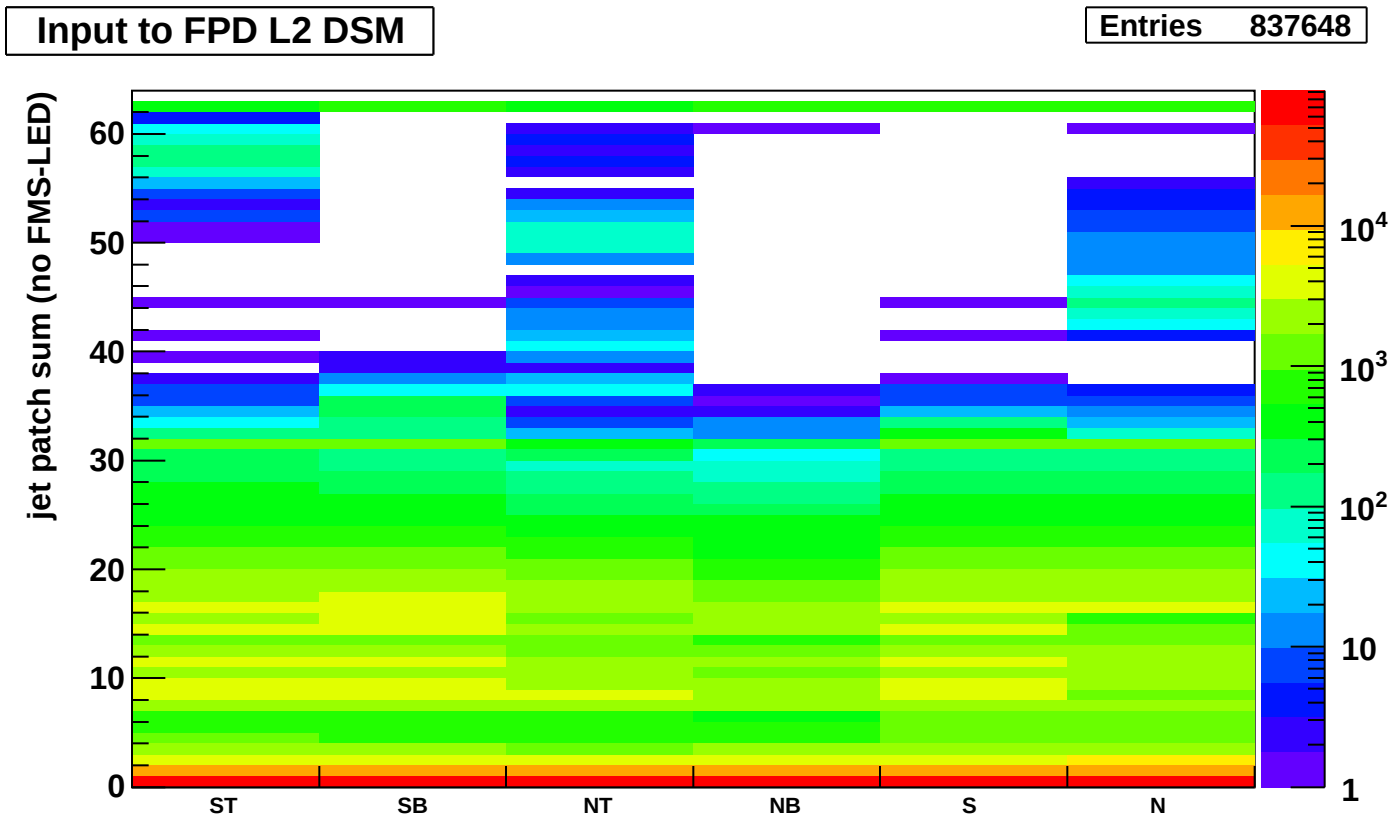

Input to FPD L2 DSM
Entries 1482
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

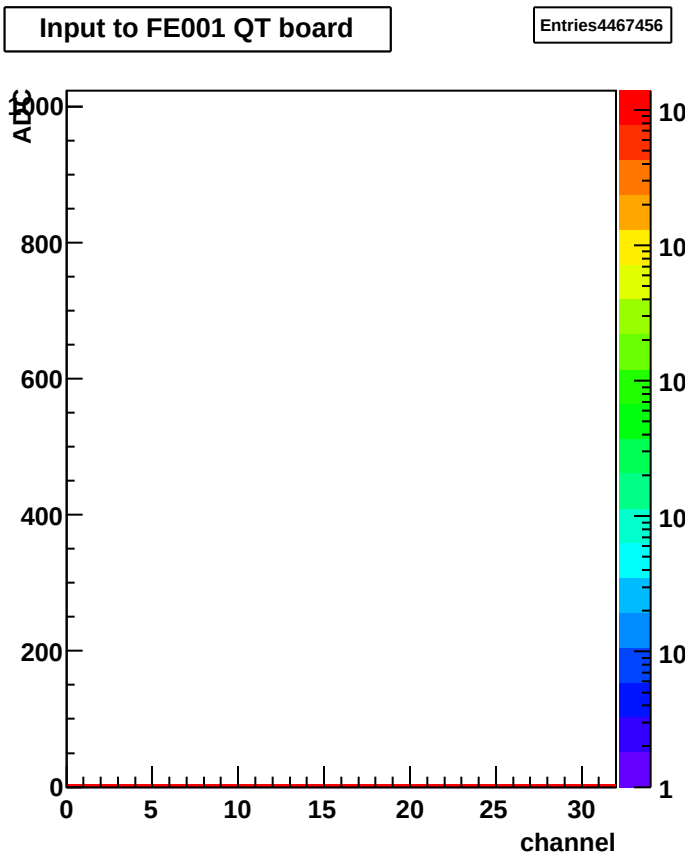

Input to FPD L2 DSM
Entries 418824
CL bits
3697
1959
45754
15316
8552
30895
15134
8645
62959
105461
120452
SMALL
LARGE-S
LARGE-N

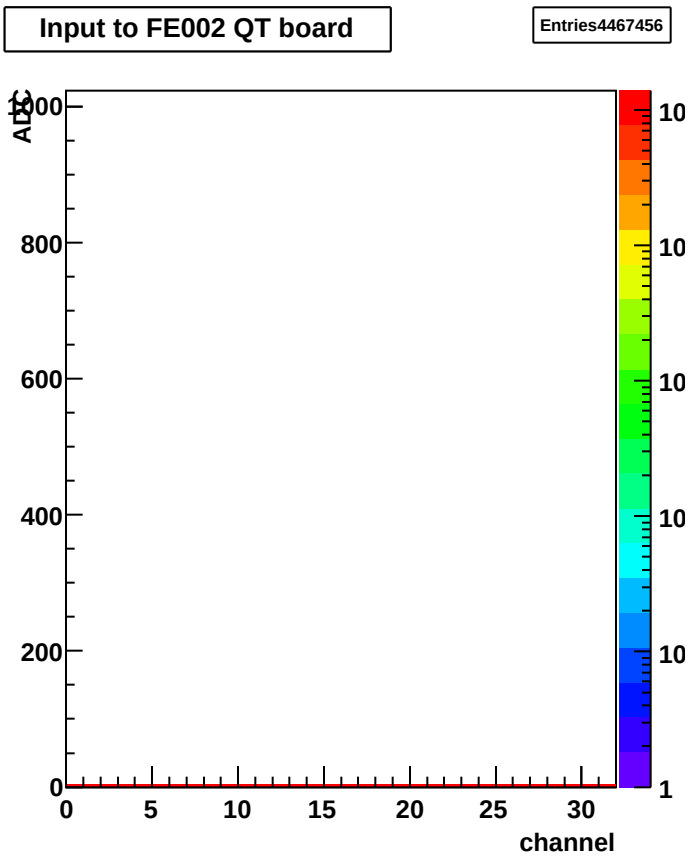

Input to FPD L2 DSM
Entries 32
CL bits - simulated
1
4
2
10
3
12
SMALL
LARGE-S
LARGE-N

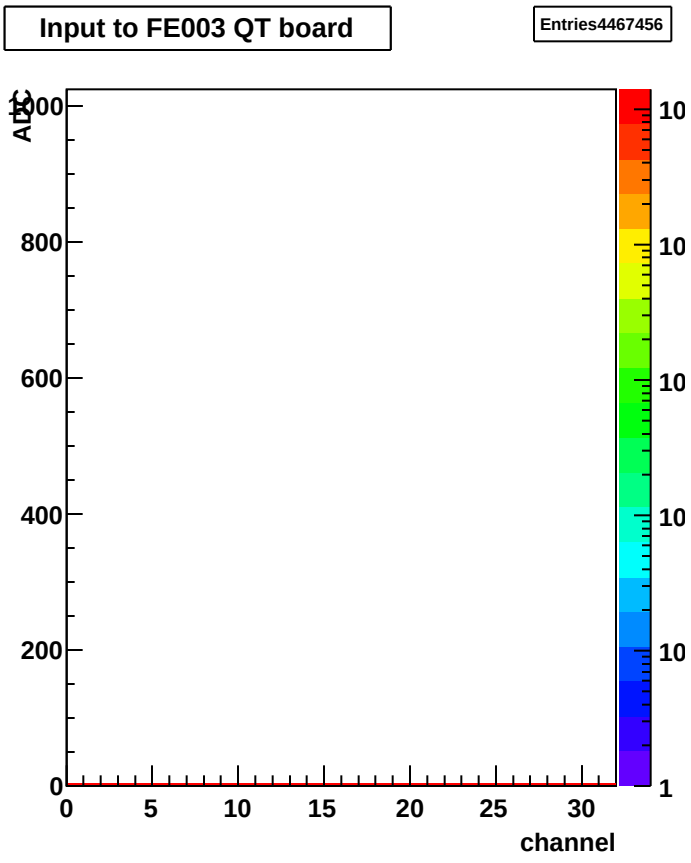

Input to FPD L2 DSM
Entries 418824
HT bits
5371
25213
11673
7304
11064
12507
7075
115869
101888
120860
SMALL
LARGE-S
LARGE-N

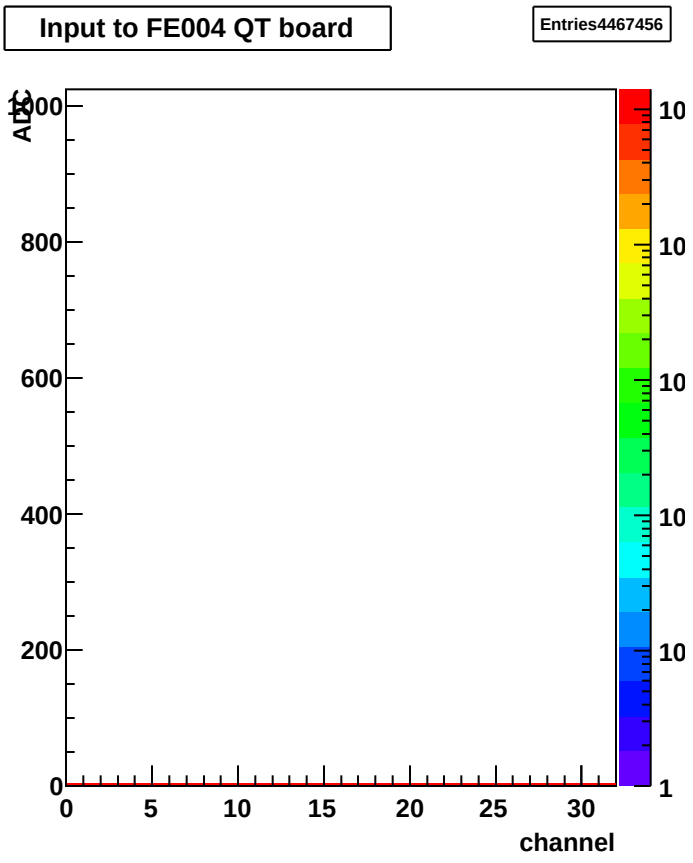

Input to FPD L2 DSM
Entries 410
HT bits - simulated
169
157
84
SMALL
LARGE-S
LARGE-N

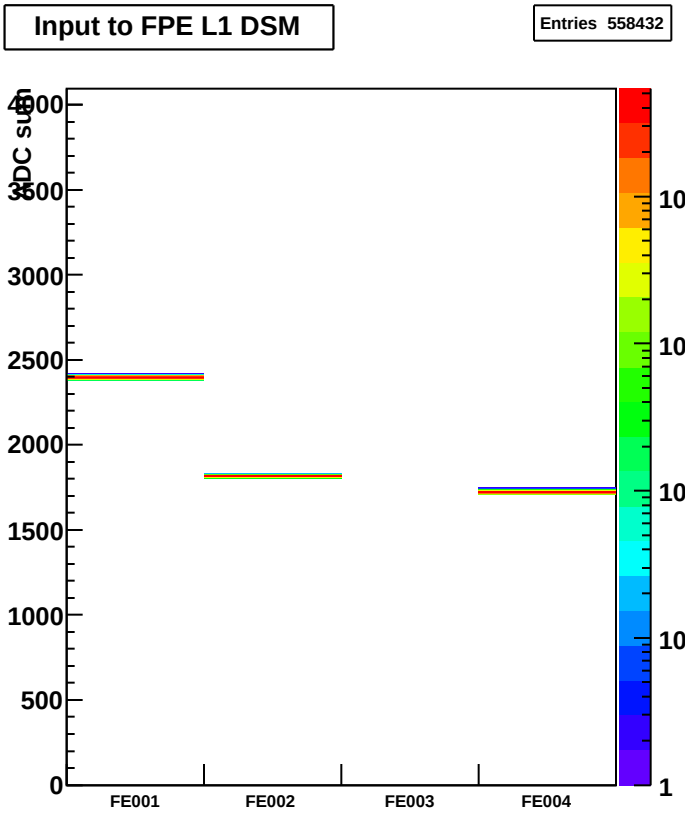

Input to FPD L2 DSM
Entries 837648
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

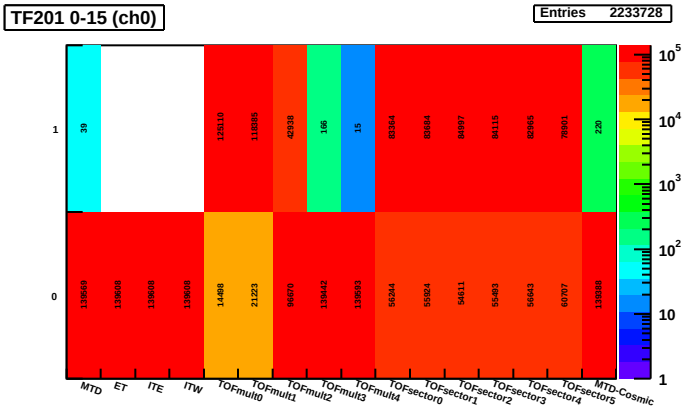

Input to FE001 QT board
Entries4467456
ADC
channel

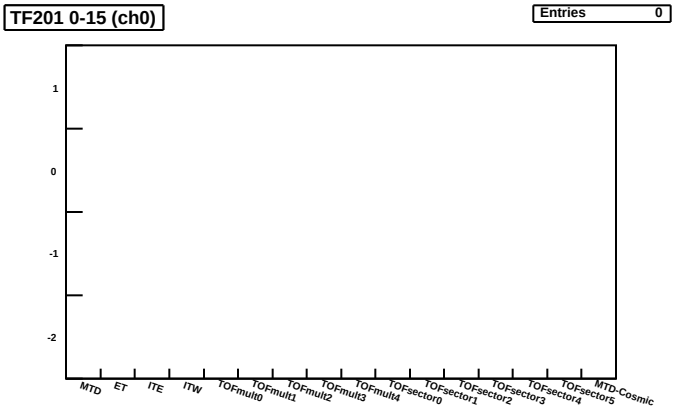

Input to FE002 QT board
Entries4467456
ADC
channel

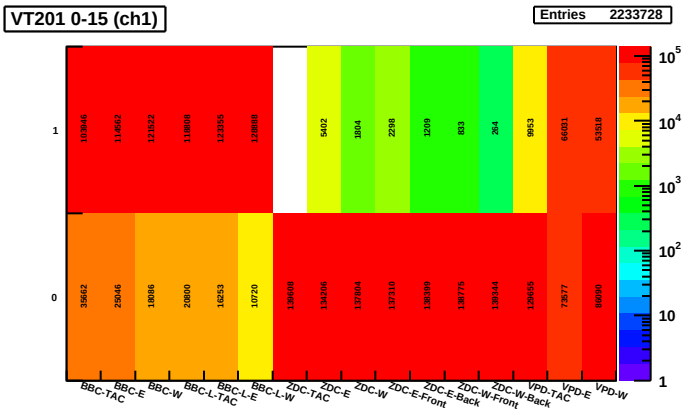

Input to FE003 QT board
Entries4467456
ADC
channel

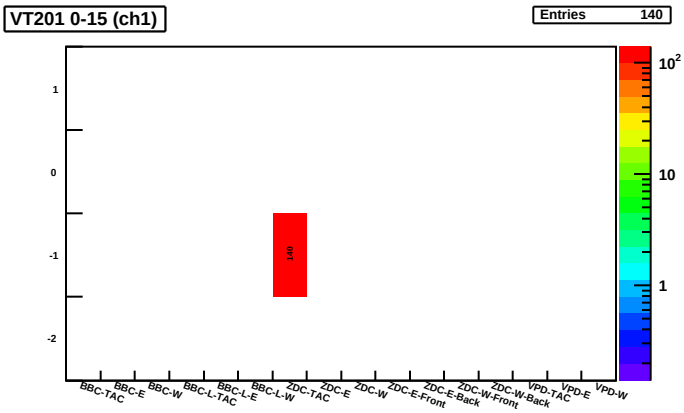

Input to FE004 QT board
Entries4467456
ADC
channel

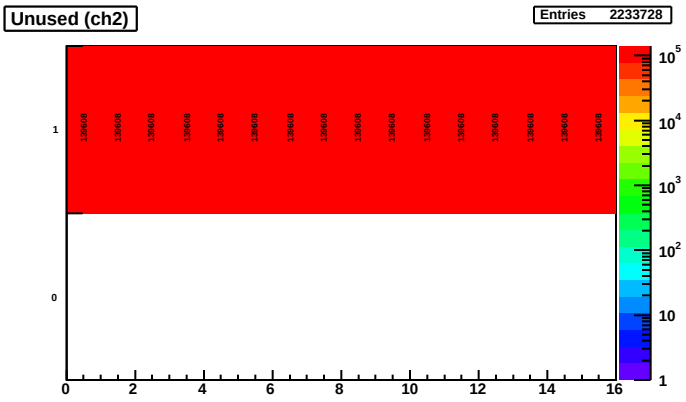

Input to FPE L1 DSM
Entries 558432
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

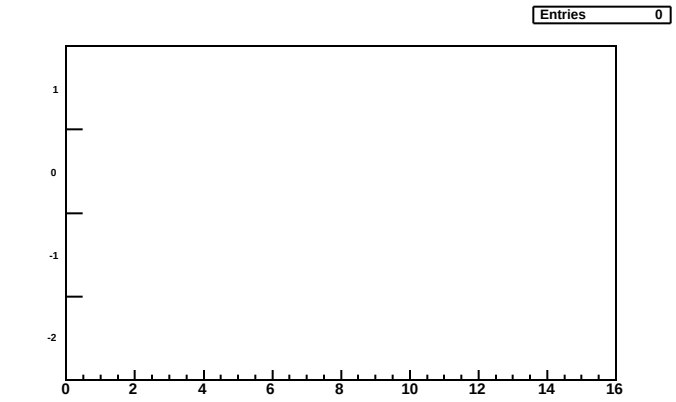

TF201 0-15 (ch0)
Entries 2233728
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

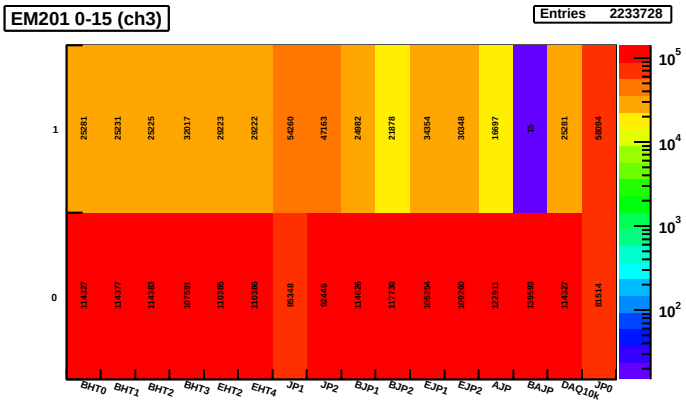

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

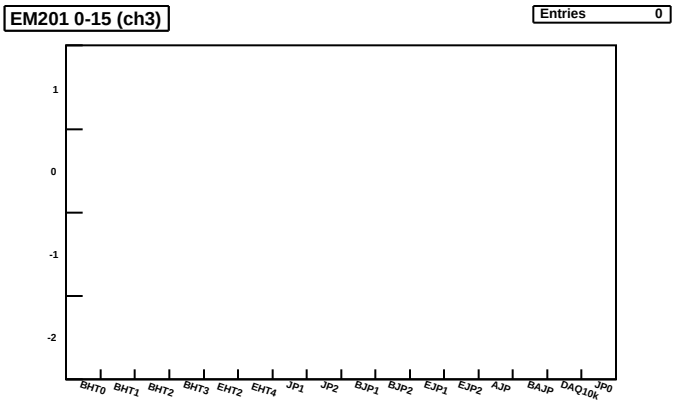

VT201 0-15 (ch1)
Entries 2233728
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 140
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

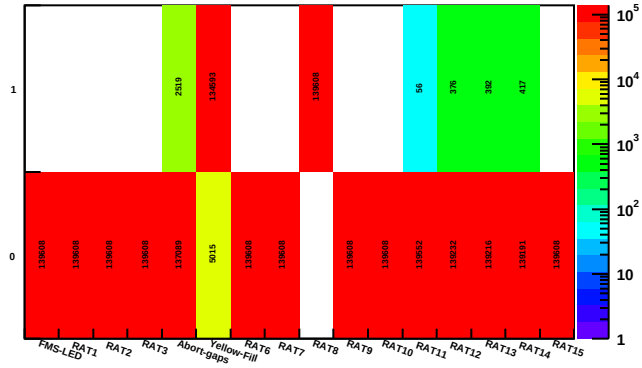

Unused (ch2)
Entries 2233728

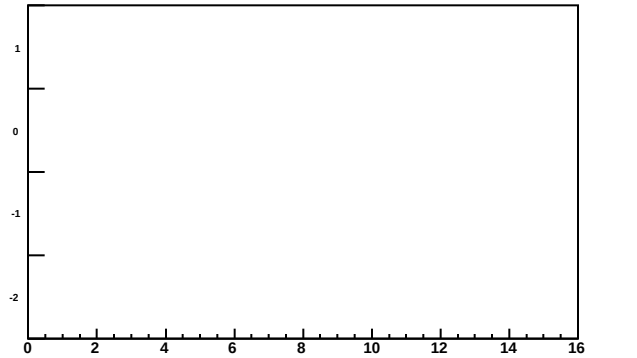

Entries 0


EM201 0-15 (ch3)
Entries 2233728
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

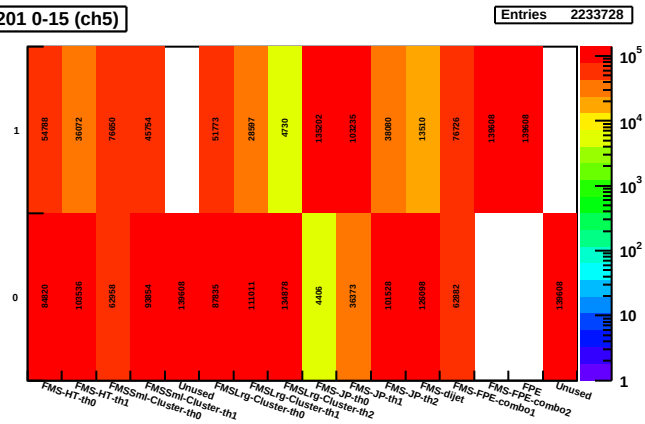

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

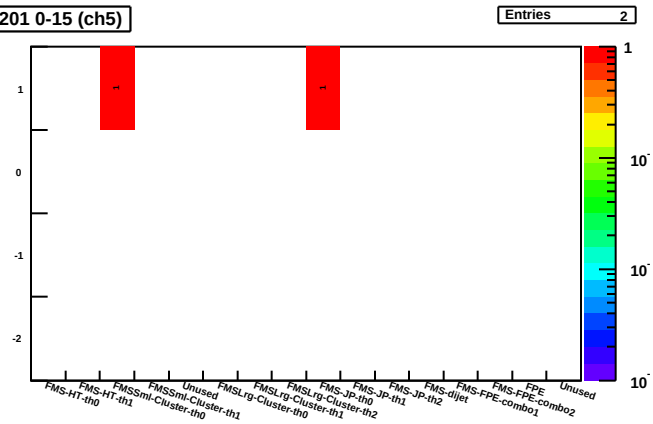




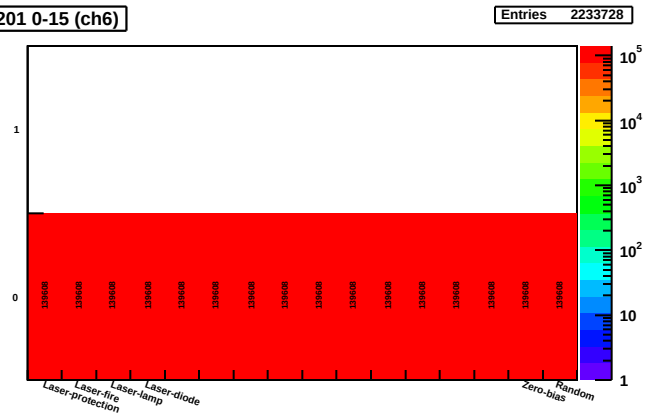
FP201 0-15 (ch5)

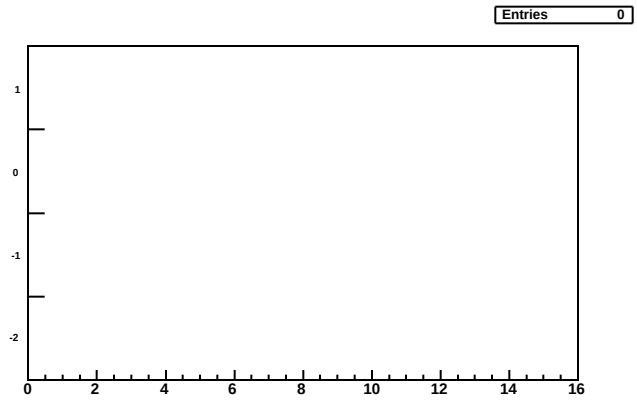
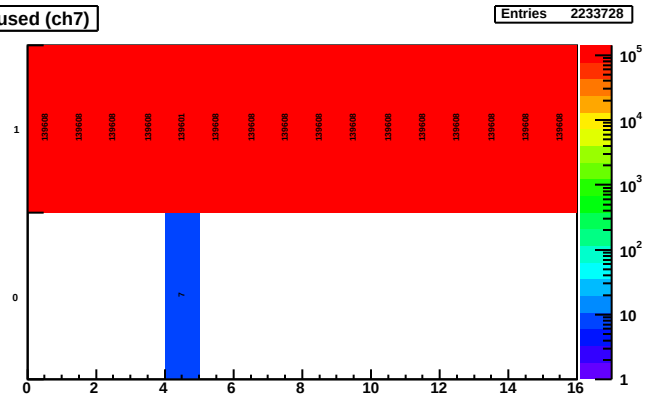
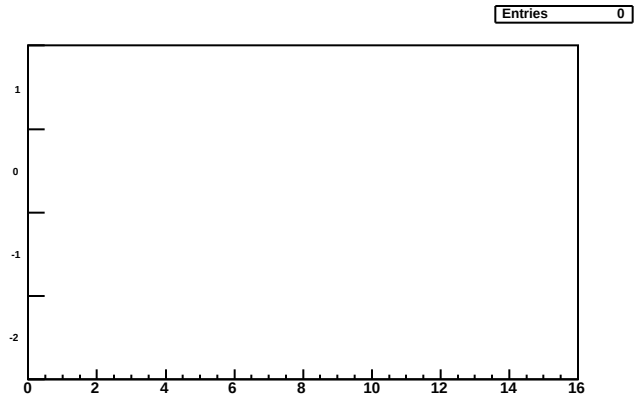


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)