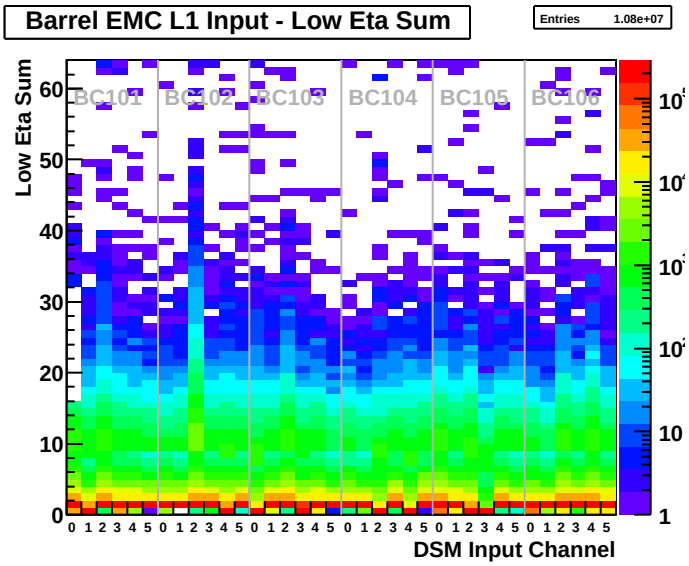
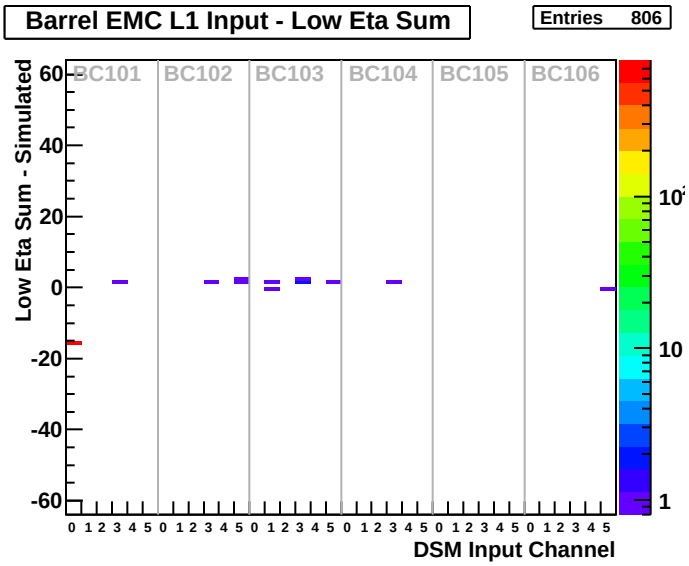
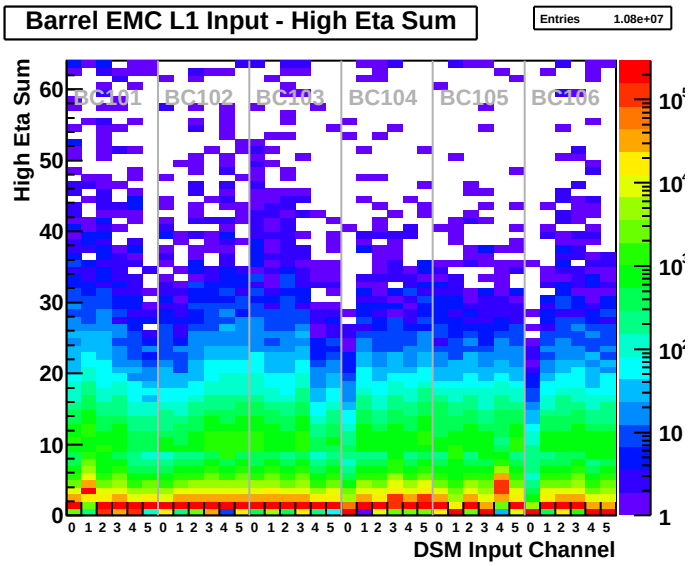
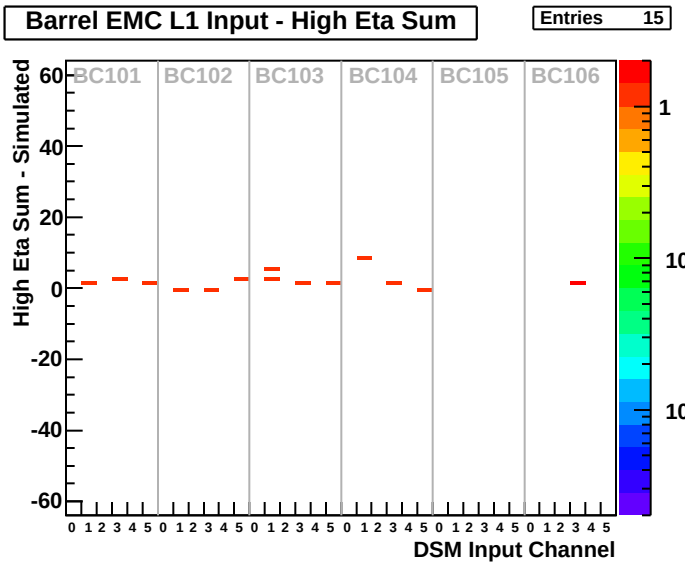
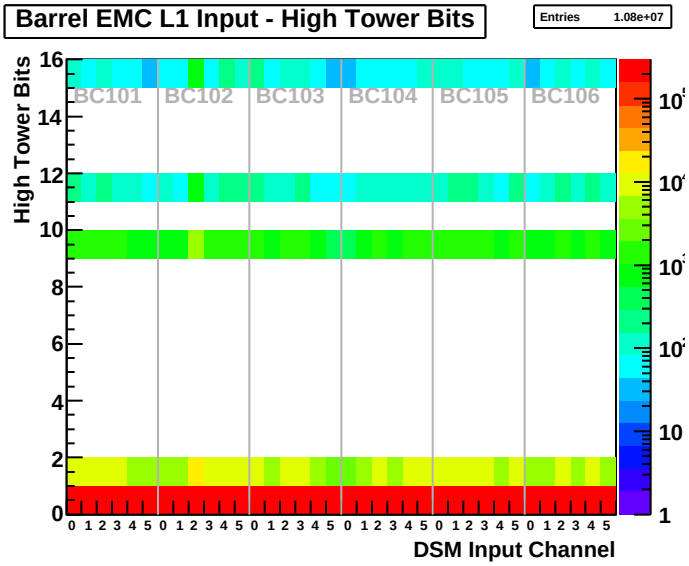
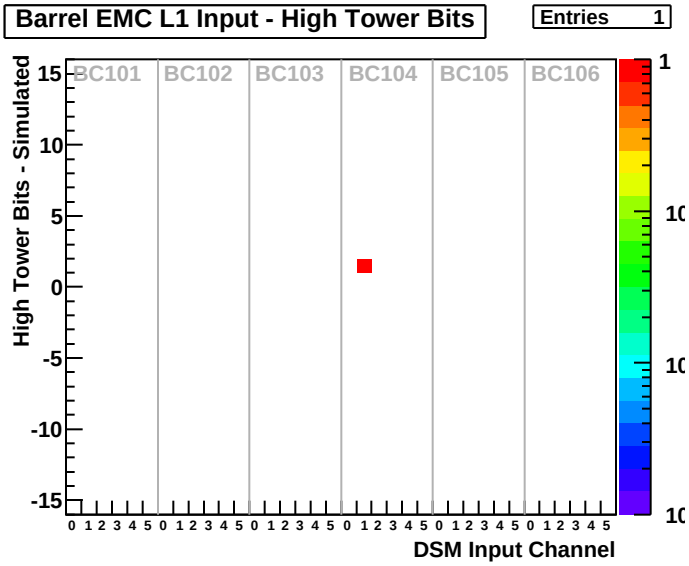

Barrel EMC L1 Input - Low Eta Sum
Entries 1.08e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

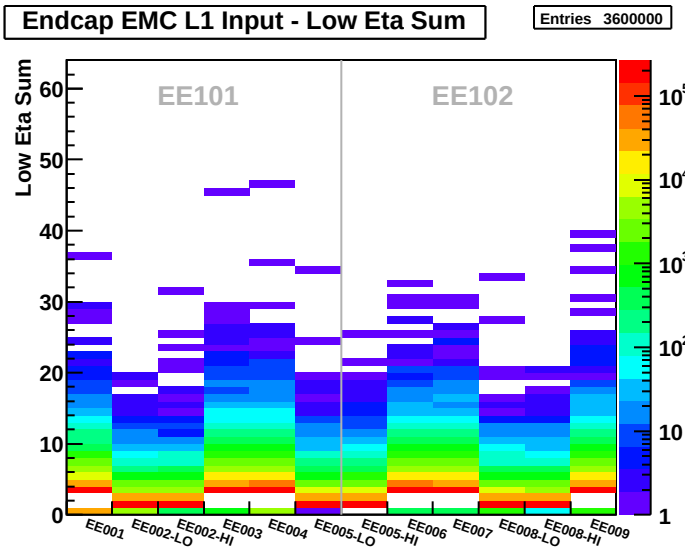

Barrel EMC L1 Input - Low Eta Sum
Entries 806
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

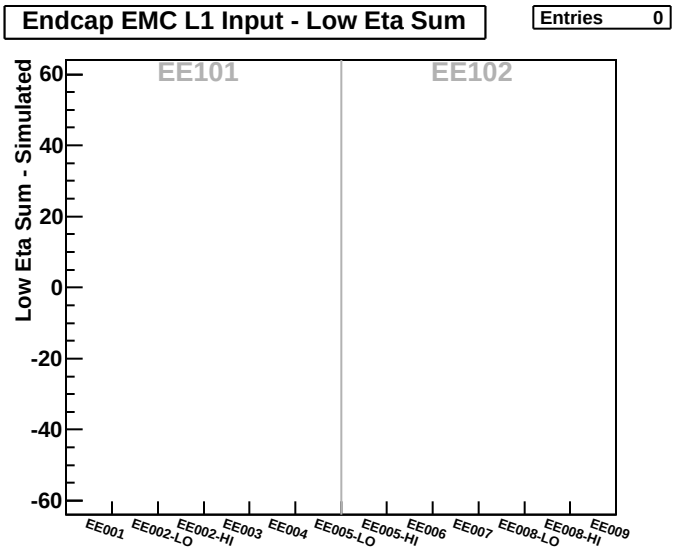

Barrel EMC L1 Input - High Eta Sum
Entries 1.08e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

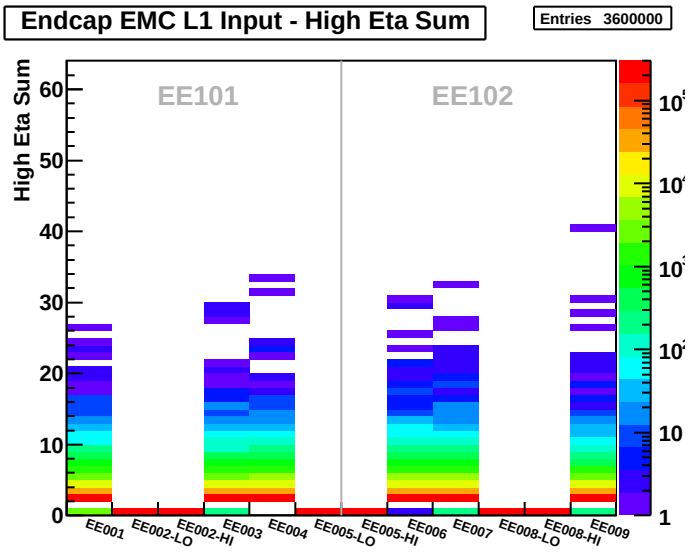

Barrel EMC L1 Input - High Eta Sum
Entries 15
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

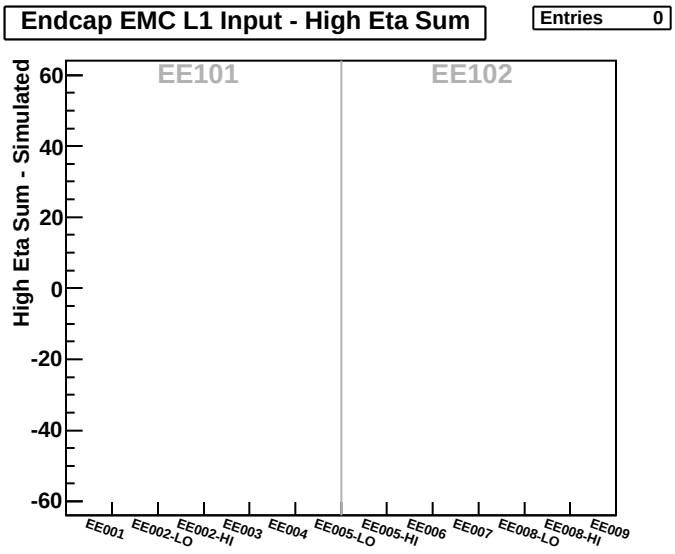

Barrel EMC L1 Input - High Tower Bits
Entries 1.08e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

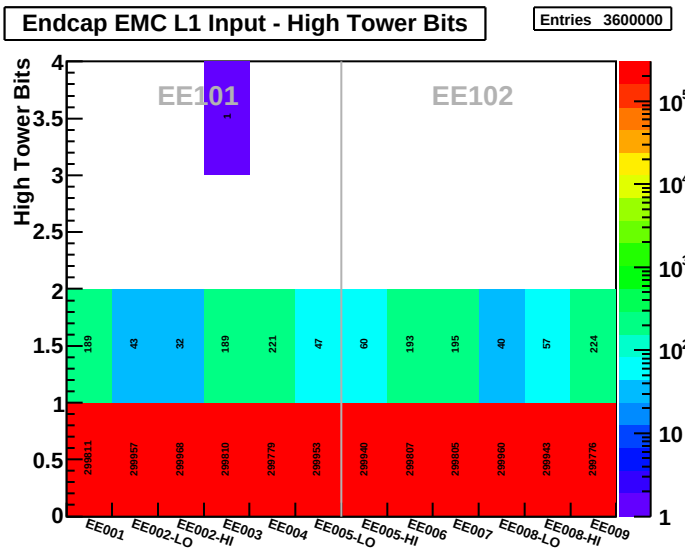

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

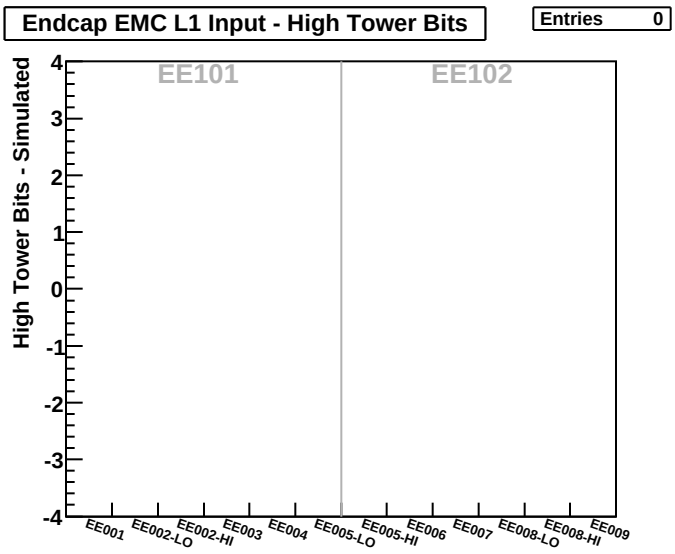

Endcap EMC L1 Input - Low Eta Sum
Entries 3600000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

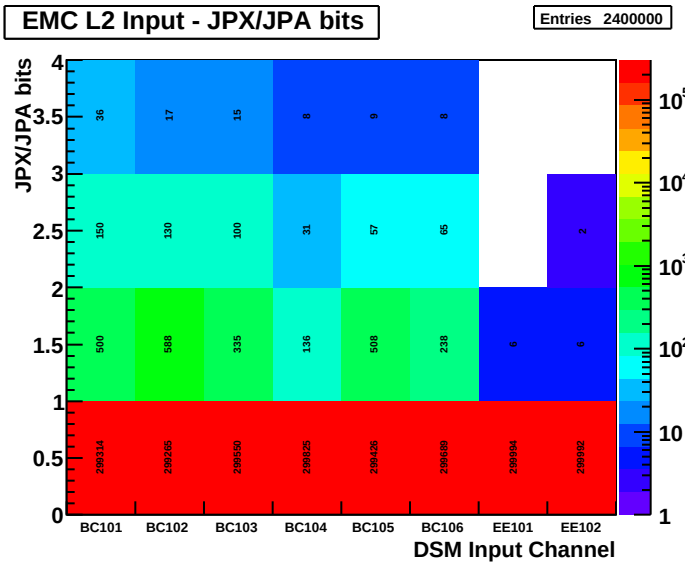

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

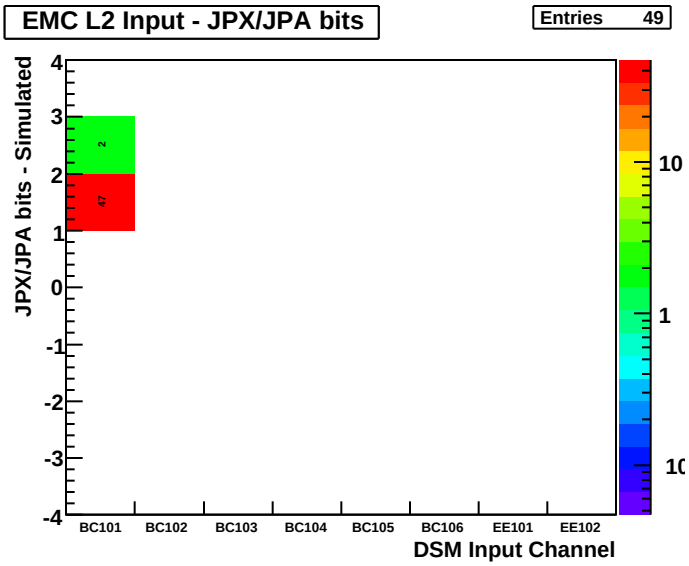

Endcap EMC L1 Input - High Eta Sum
Entries 3600000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

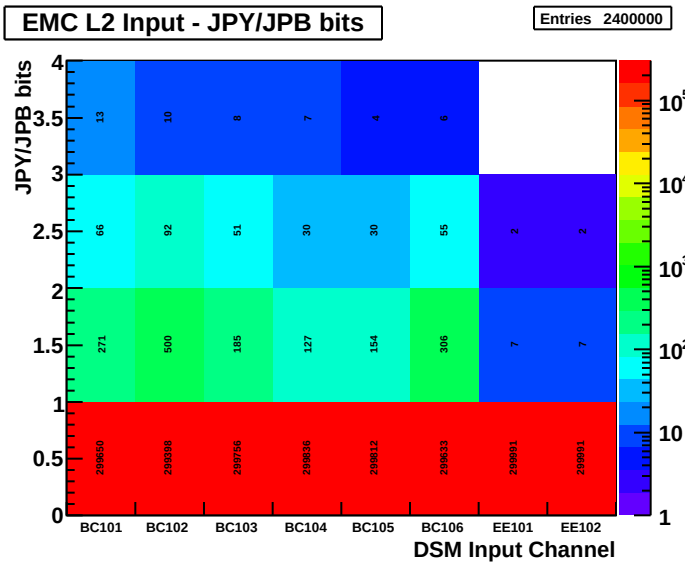

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

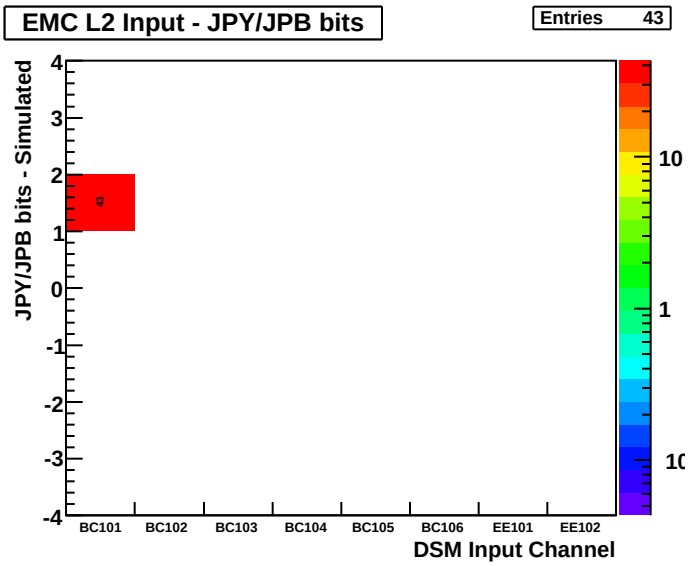

Endcap EMC L1 Input - High Tower Bits
Entries 3600000
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

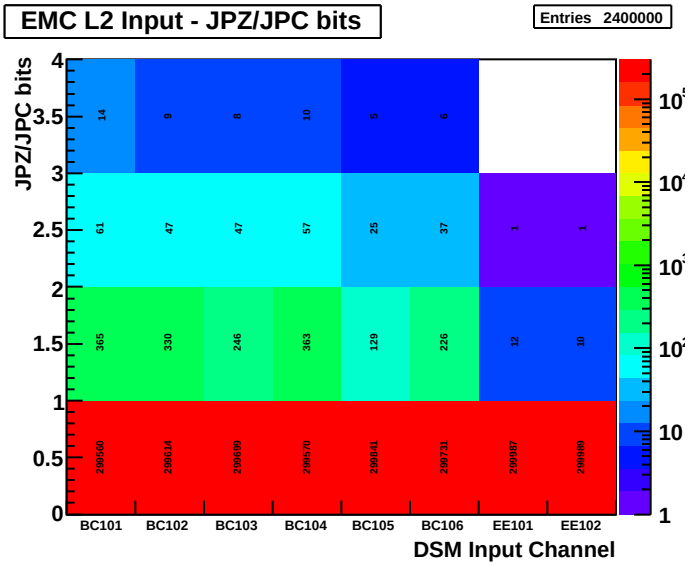

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

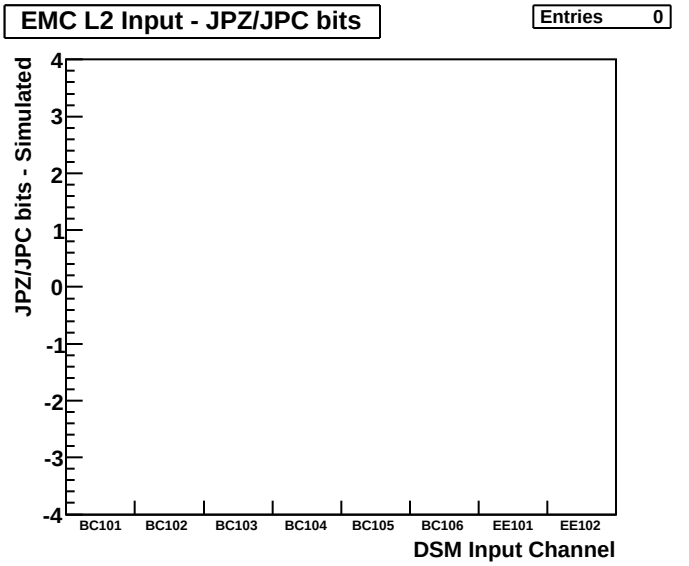

EMC L2 Input - JPX/JPA bits
Entries 2400000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36 17 15 8 9 8
150 130 100 31 57 65 2
500 588 335 136 508 238 6 6
299314 299265 299550 299825 299426 299689 299994 299992

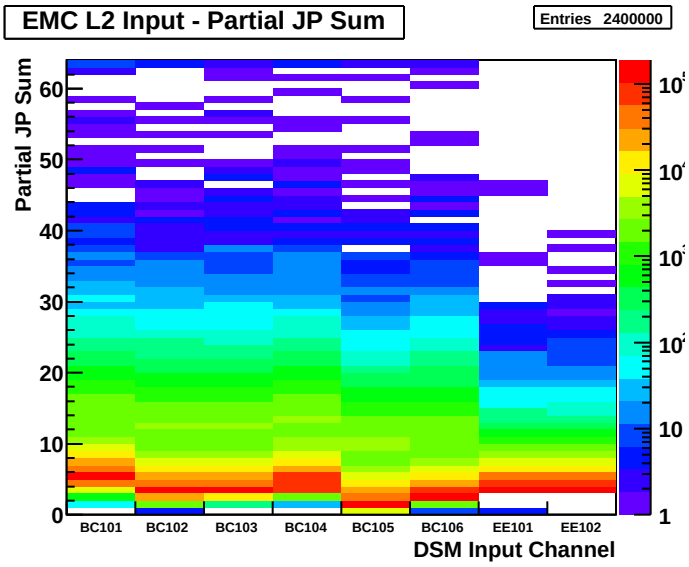

EMC L2 Input - JPX/JPA bits
Entries 49
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2
47

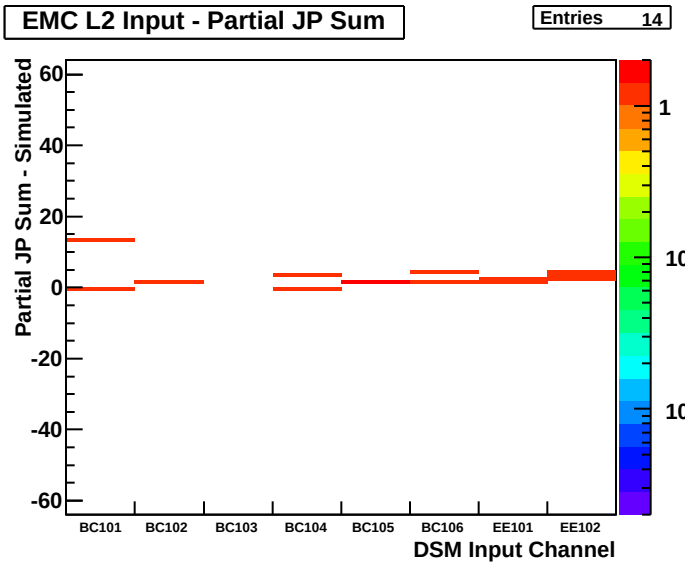

EMC L2 Input - JPY/JPB bits
Entries 2400000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
13 10 8 7 4 6
66 92 51 30 30 55 2 2
271 500 185 127 154 306 7 7
299650 299398 299756 299836 299812 299633 299991 299991

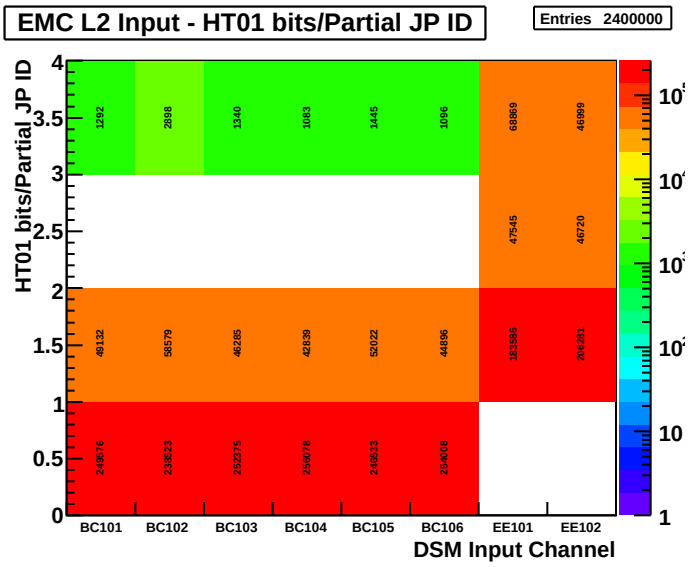

EMC L2 Input - JPY/JPB bits
Entries 43
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
43

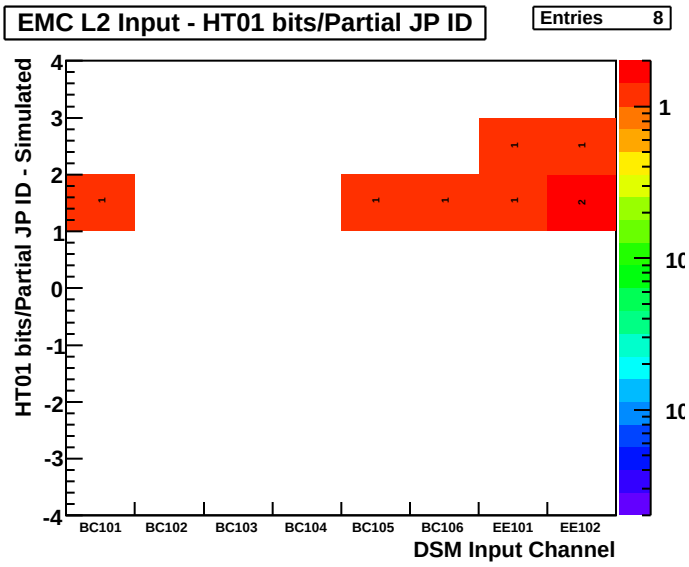

EMC L2 Input - JPZ/JPC bits
Entries 2400000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
14 9 8 10 5 6
61 47 47 57 25 37 1 1
365 330 246 363 129 226 12 10
299560 299514 299599 299570 299841 299731 299987 299989

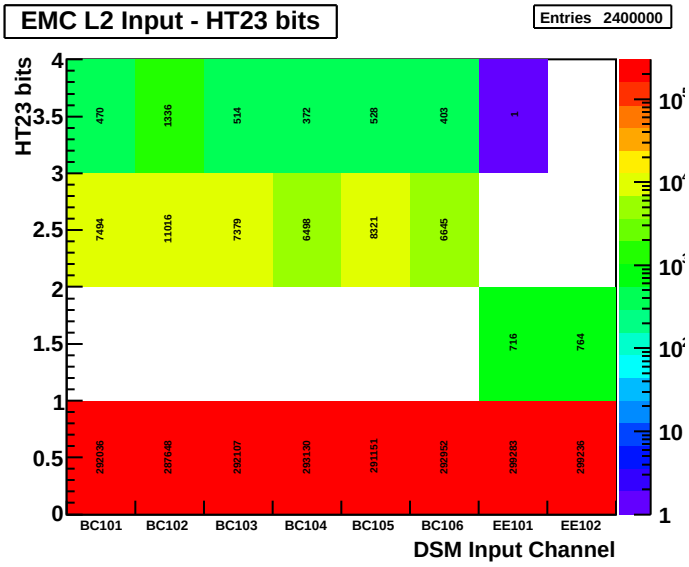

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

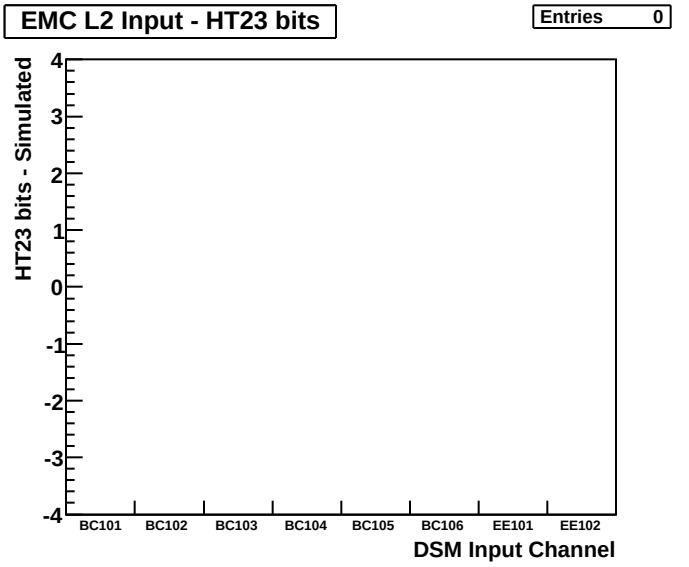

EMC L2 Input - Partial JP Sum
Entries 2400000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

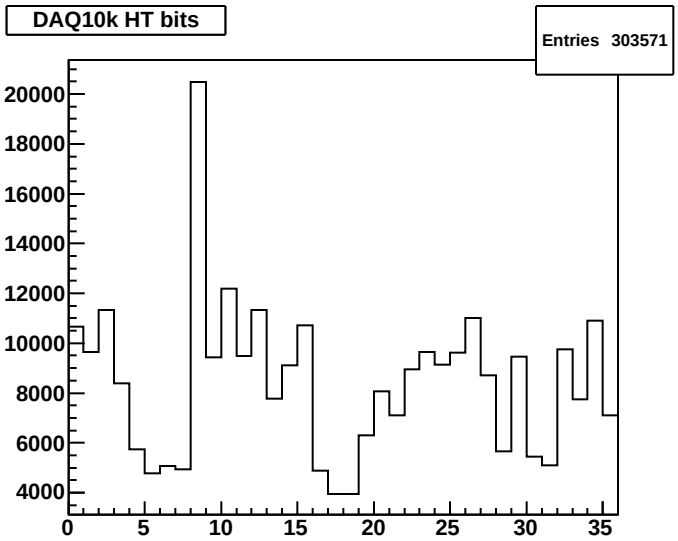

EMC L2 Input - Partial JP Sum
Entries 14
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

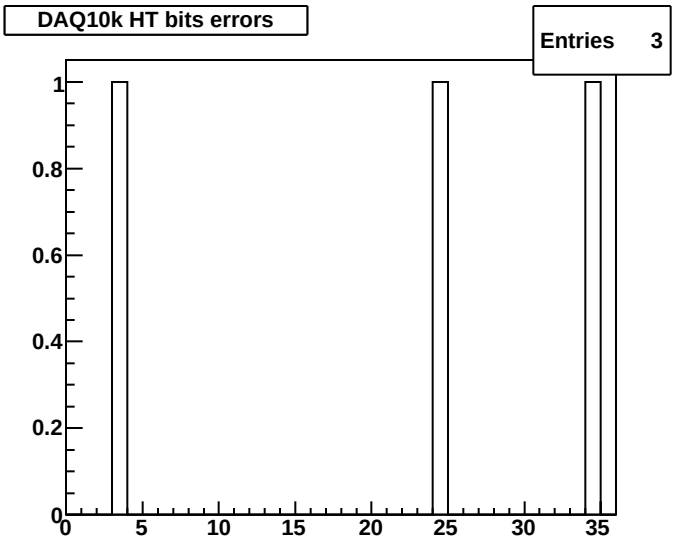

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2400000
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

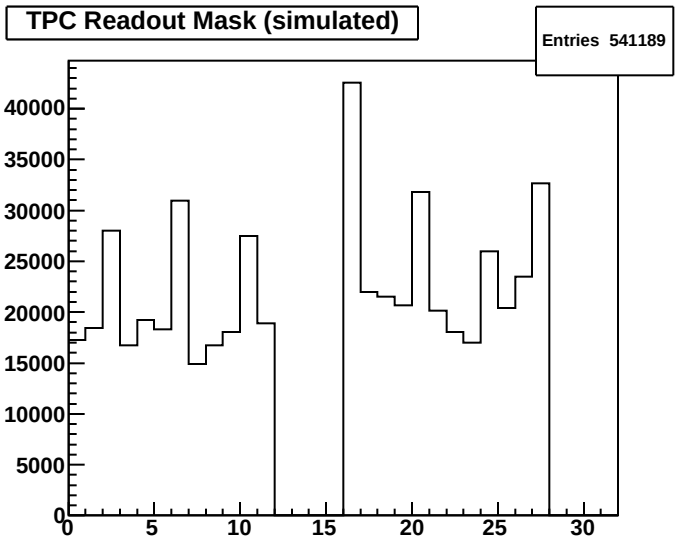

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

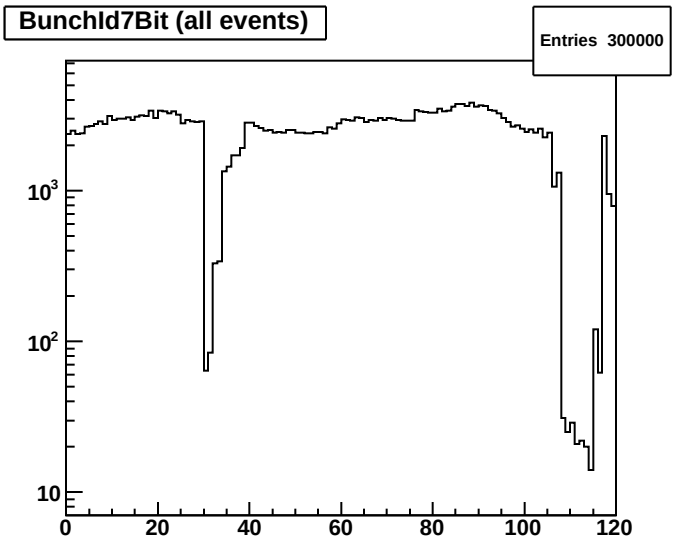

EMC L2 Input - HT23 bits
Entries 2400000
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

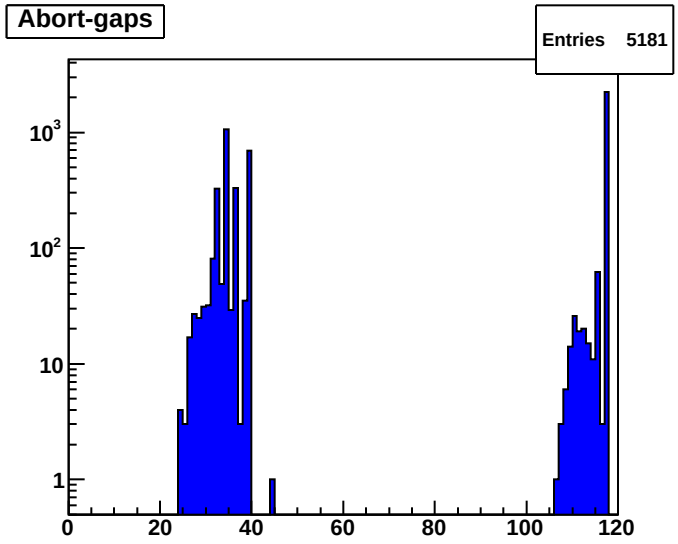

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

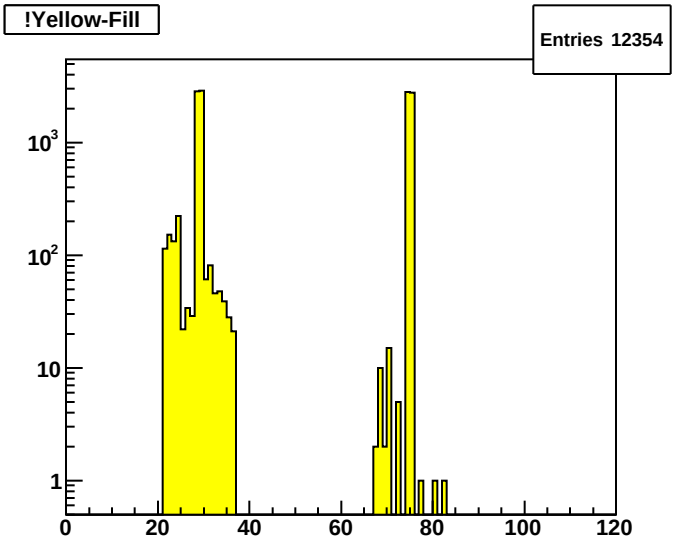

DAQ10k HT bits
Entries 303571

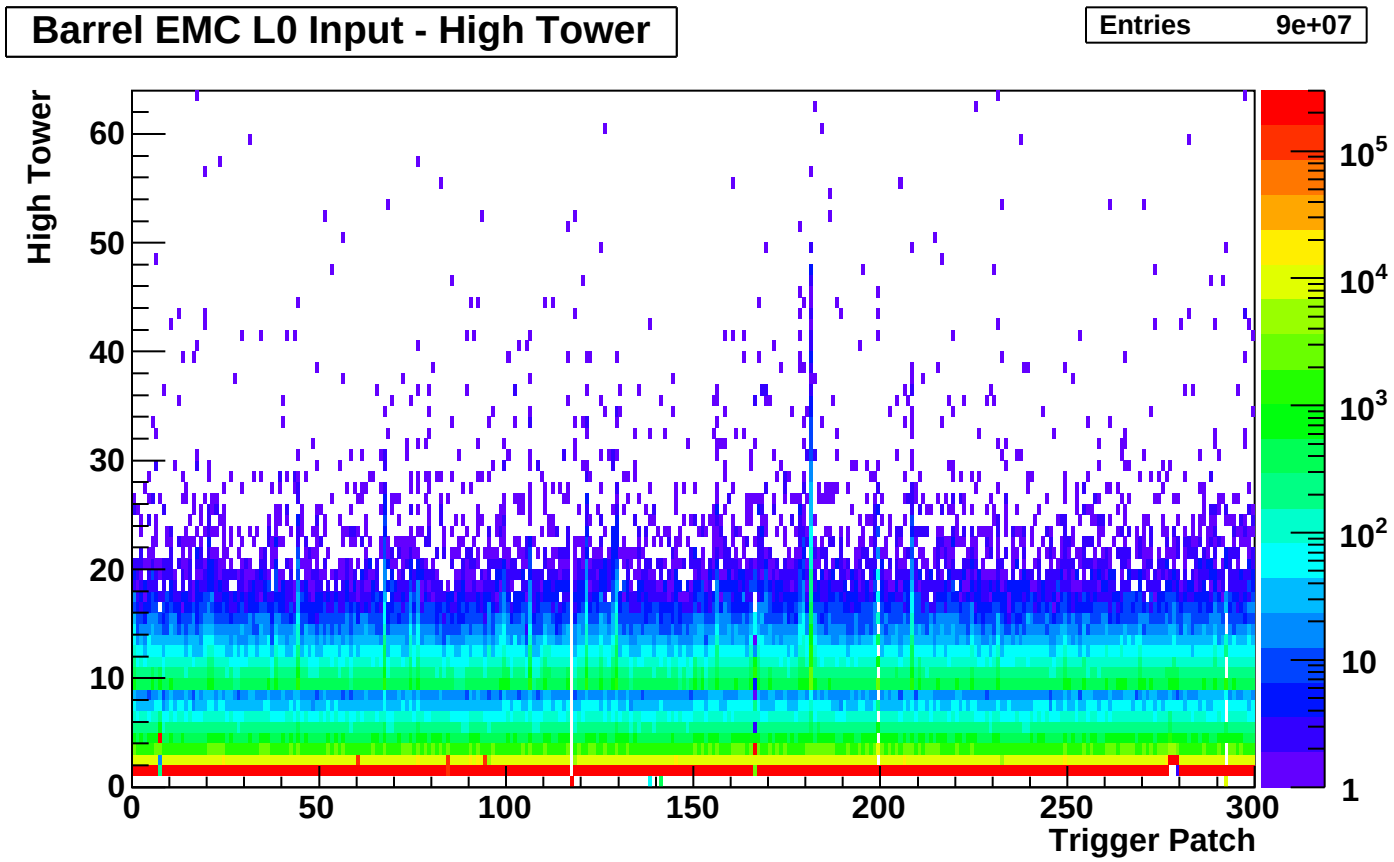

DAQ10k HT bits errors
Entries 3

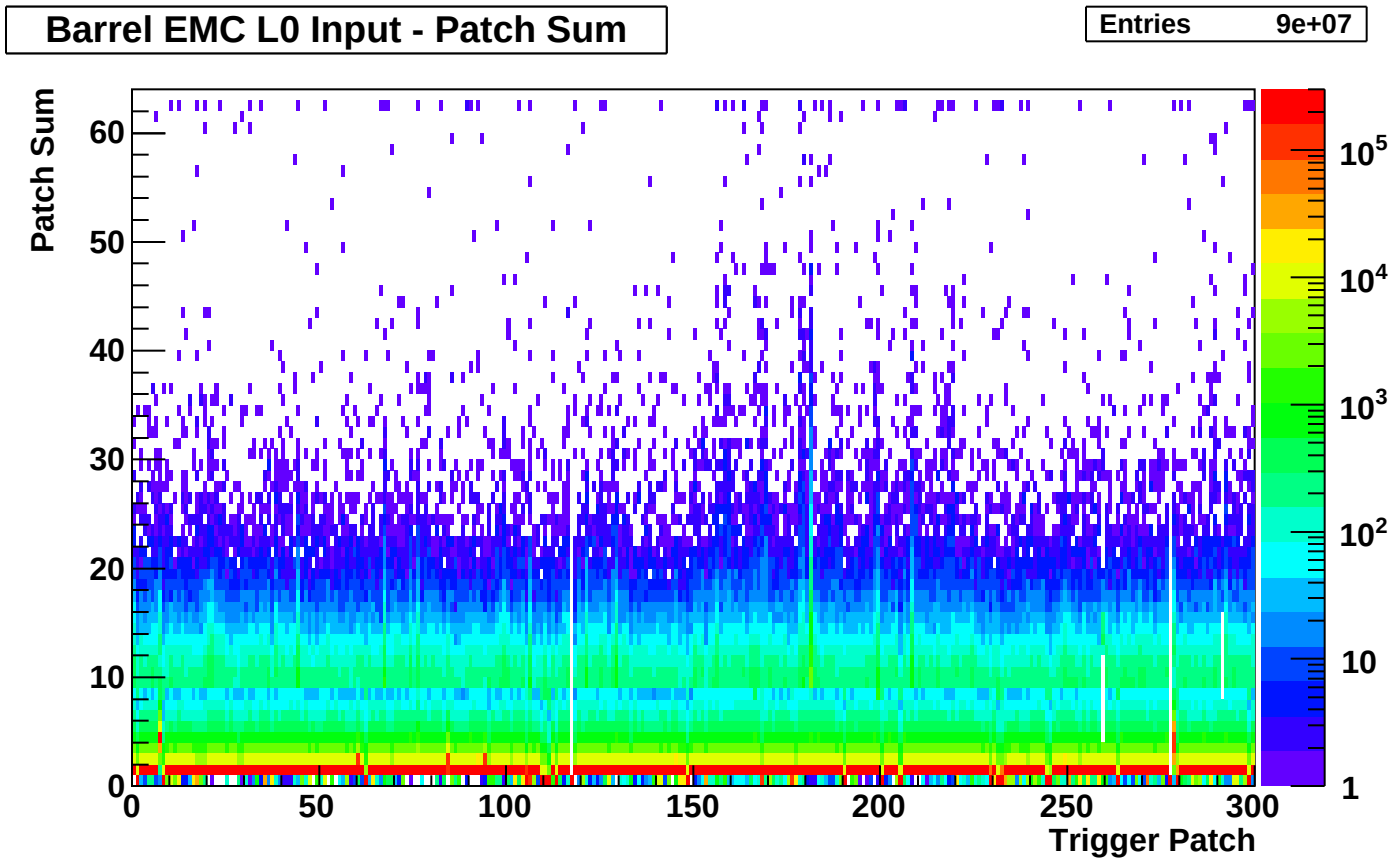

TPC Readout Mask (simulated)
Entries 541189

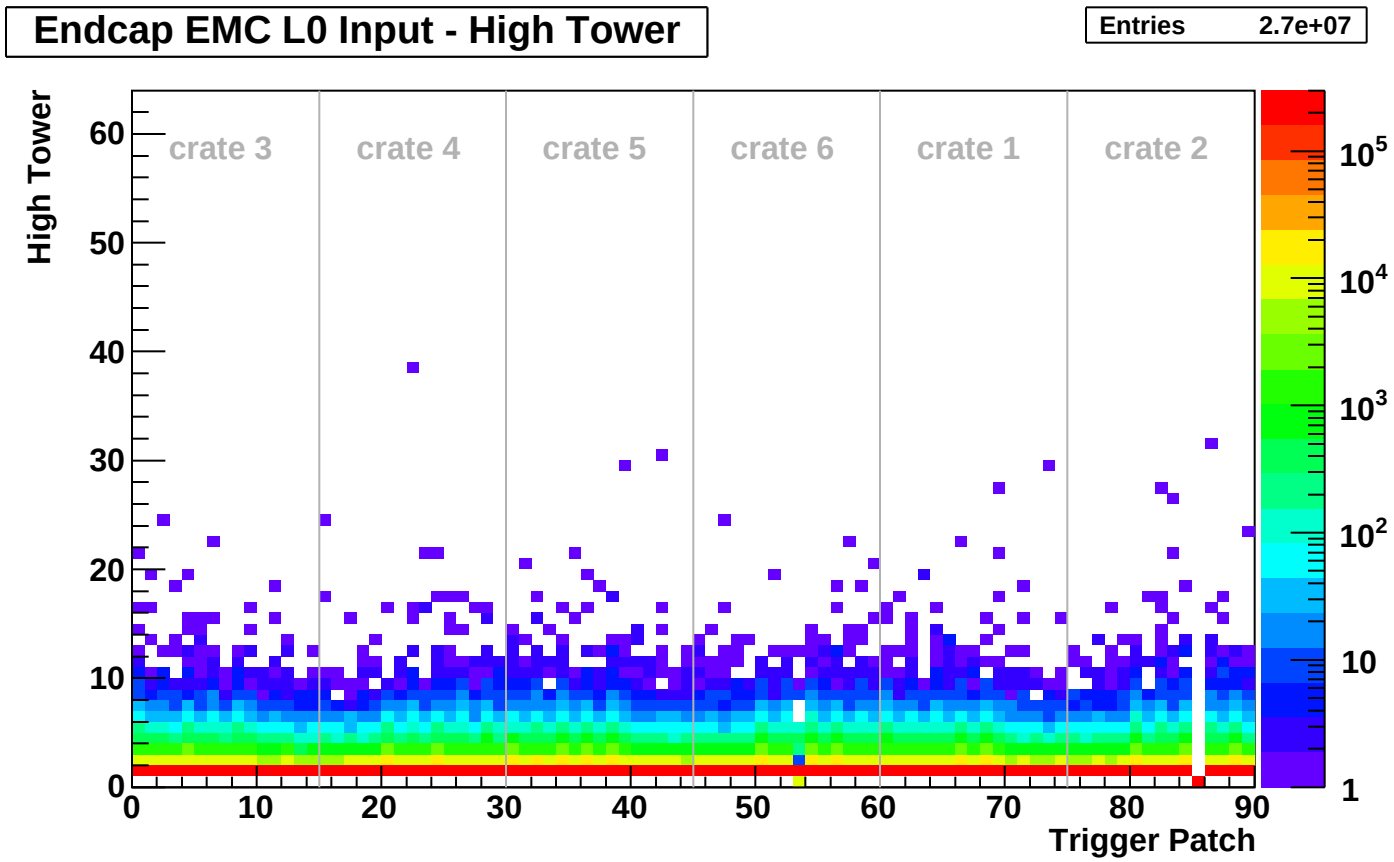

BunchId7Bit (all events)
Entries 300000

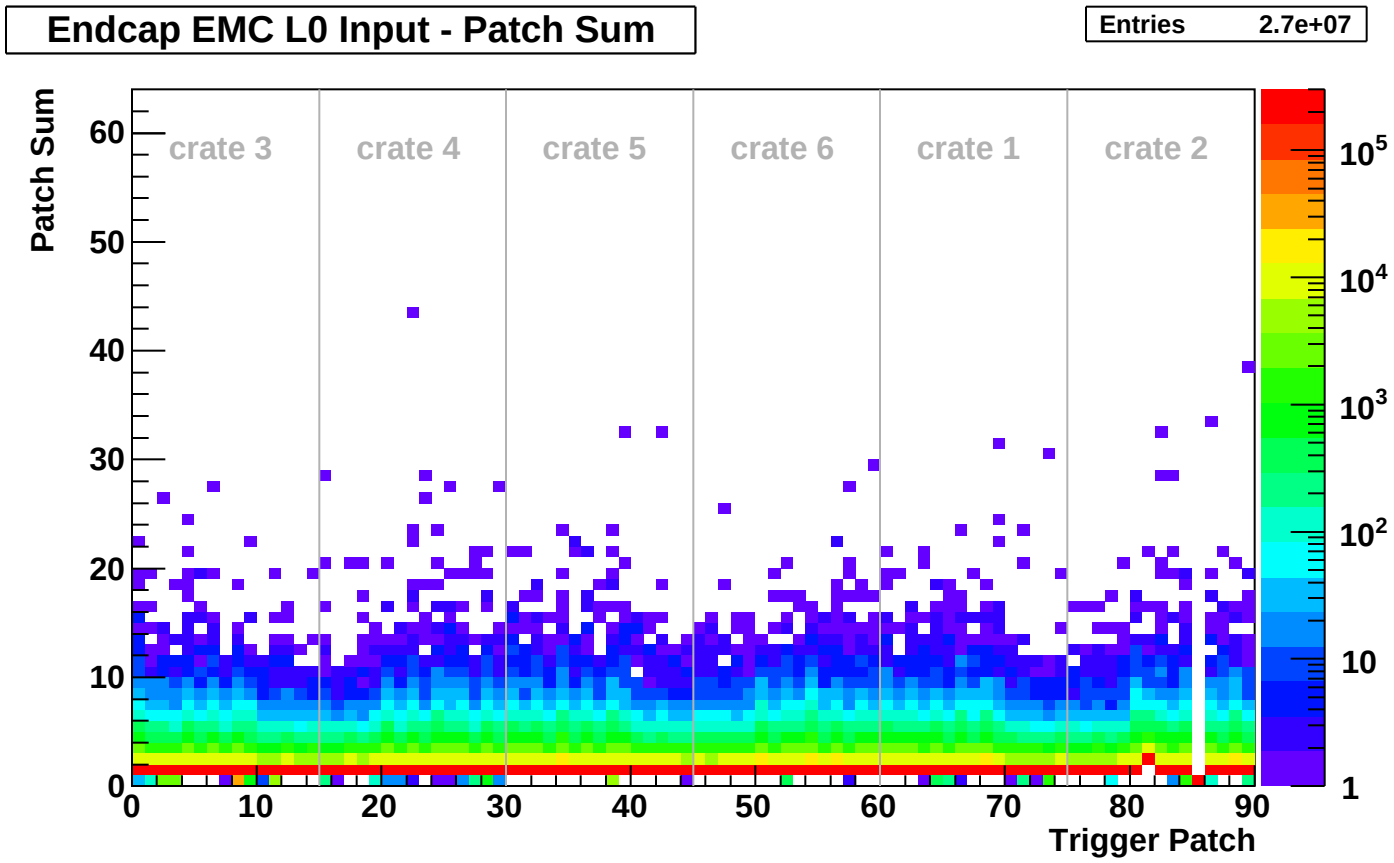

Abort-gaps
Entries 5181

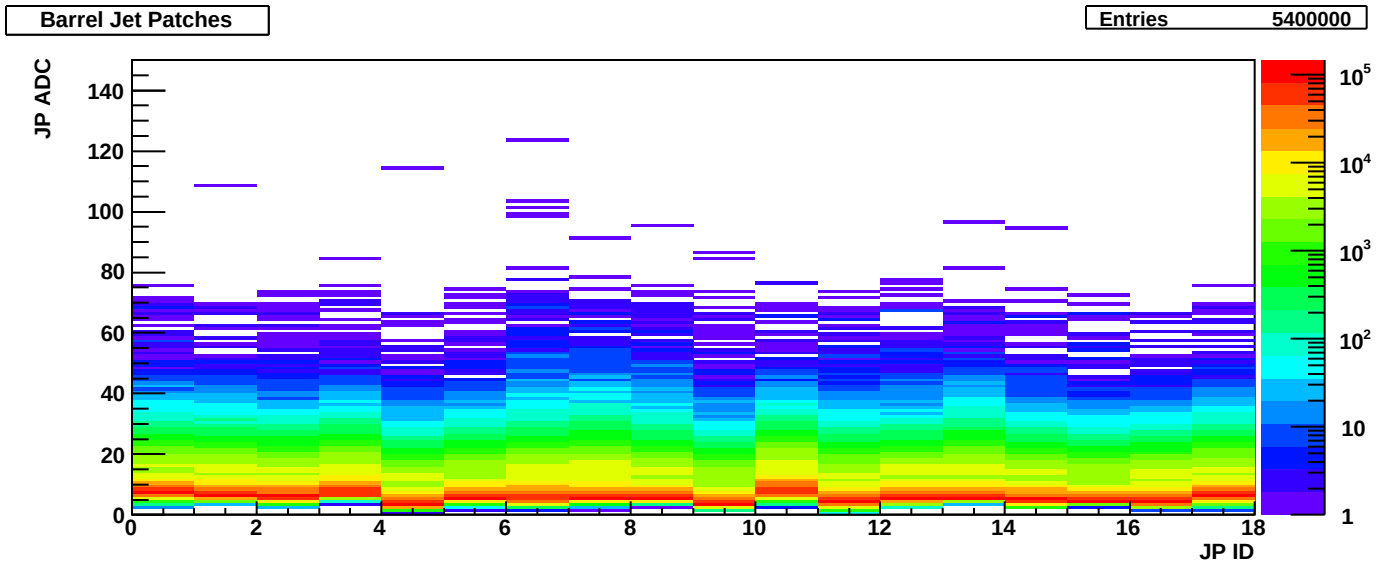

!Yellow-Fill
Entries 12354

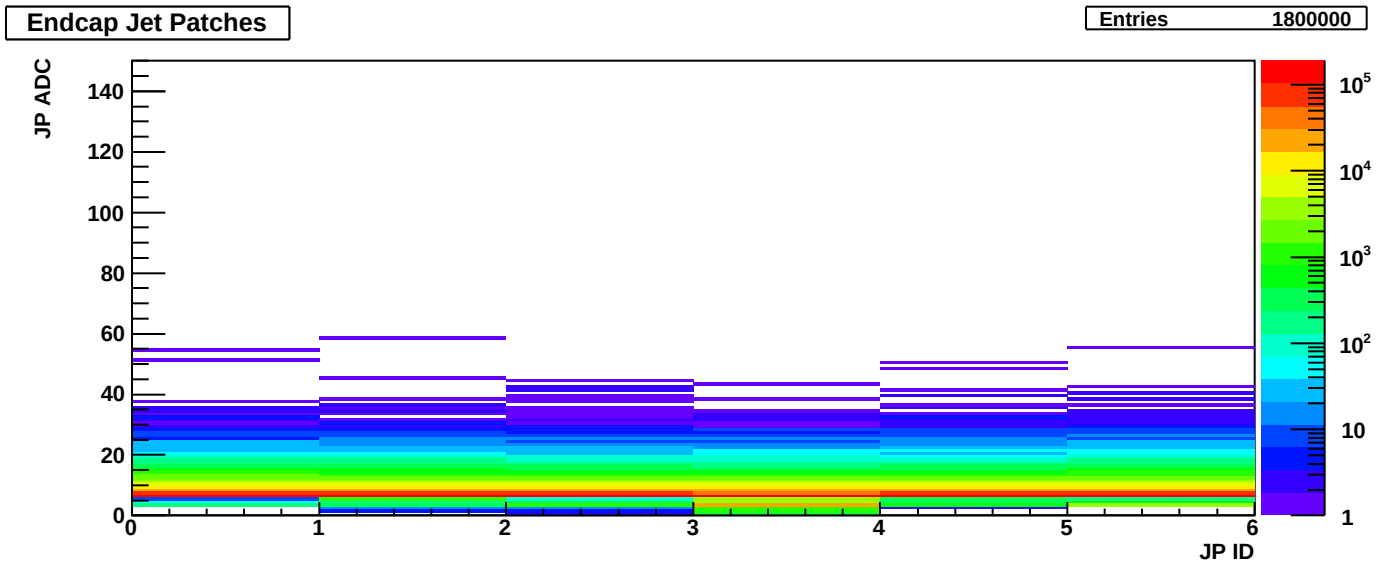

Barrel EMC L0 Input - High Tower
Entries 9e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

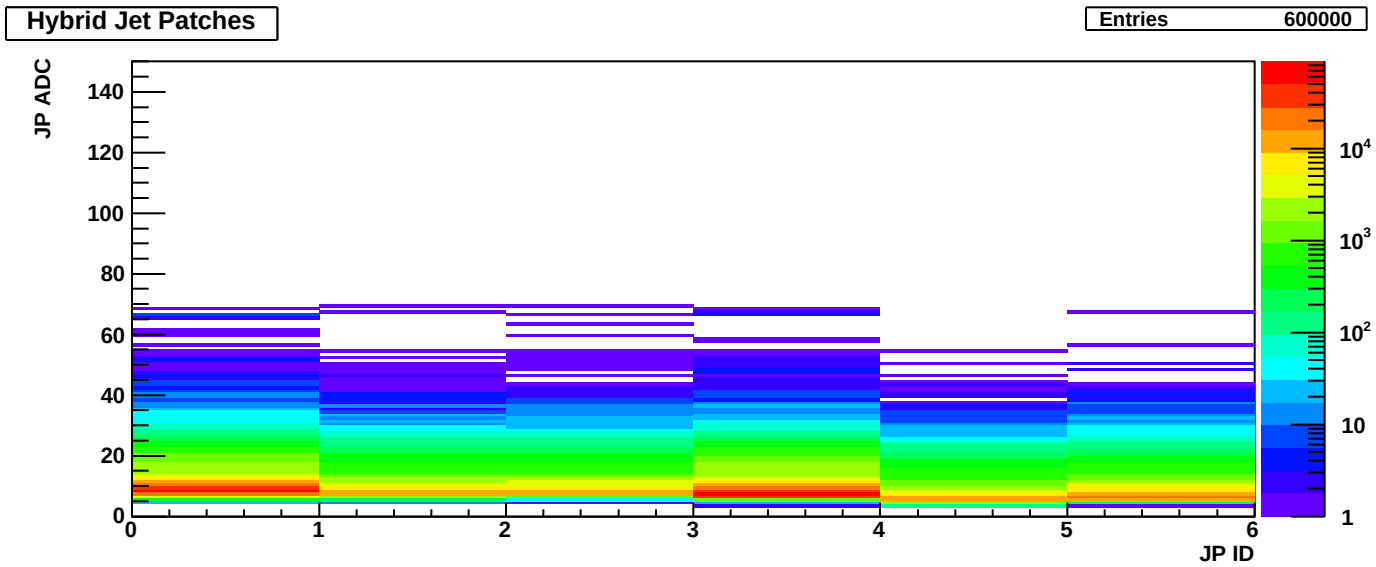

Barrel EMC L0 Input - Patch Sum
Entries 9e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

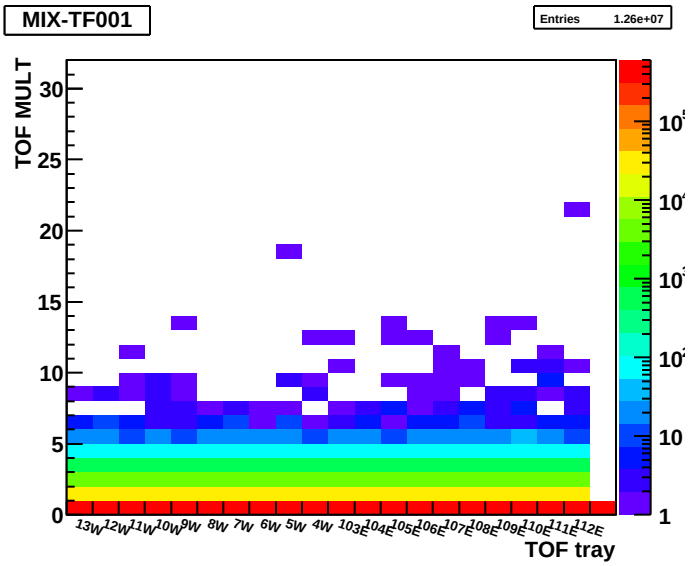

Endcap EMC L0 Input - High Tower
Entries 2.7e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

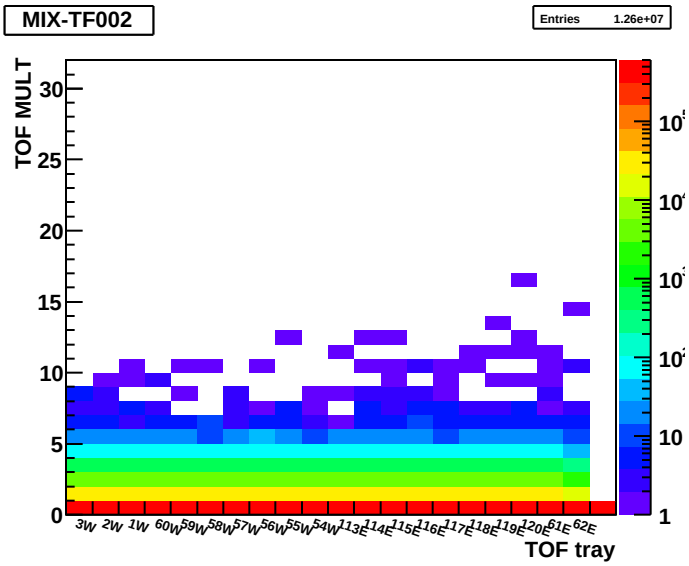

Endcap EMC L0 Input - Patch Sum
Entries 2.7e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

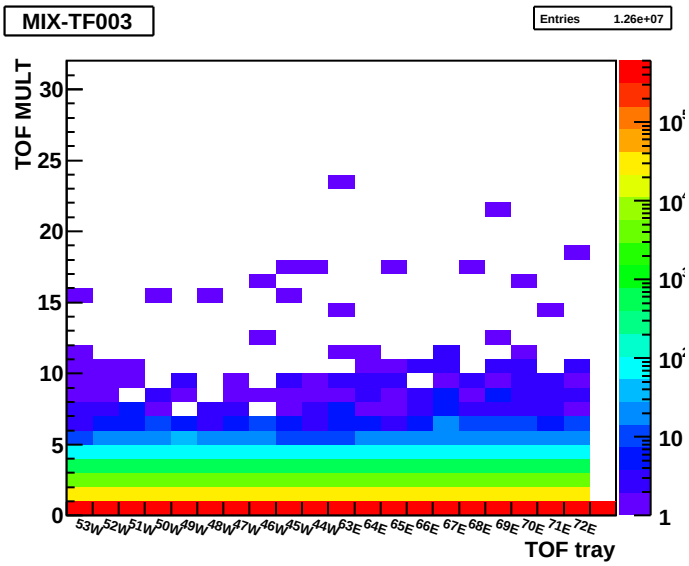

Barrel Jet Patches
Entries 5400000
JP ADC
JP ID

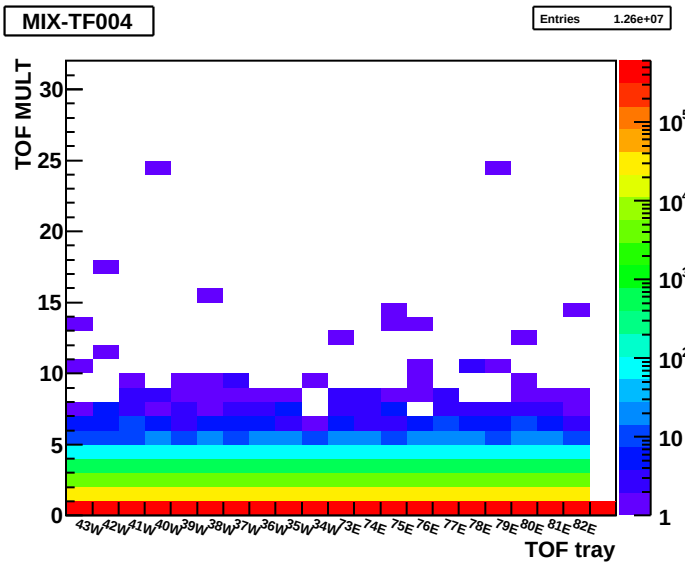

Endcap Jet Patches
Entries 1800000
JP ADC
JP ID

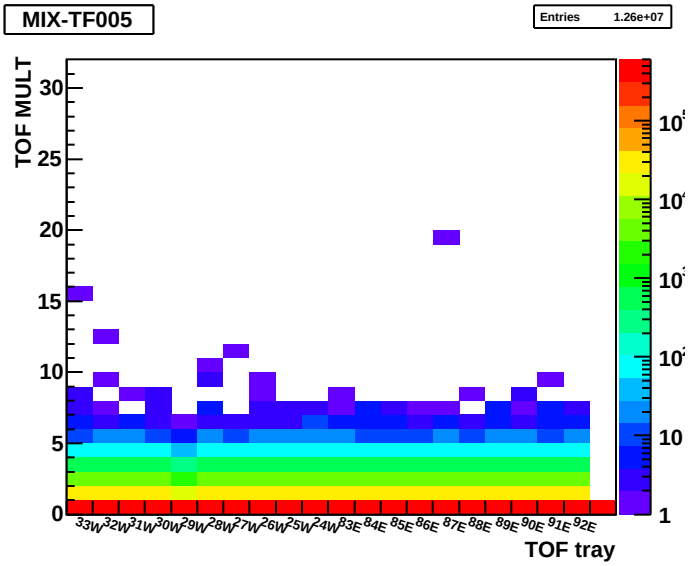

Hybrid Jet Patches
Entries 600000
JP ADC
JP ID

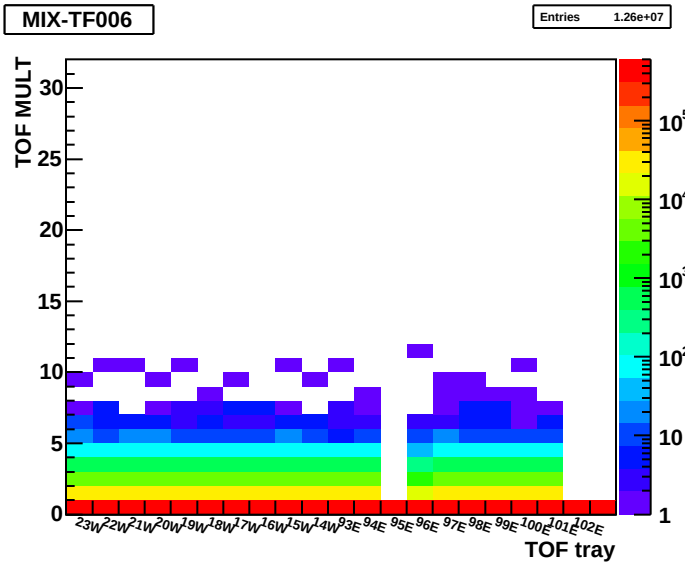

MIX-TF001
Entries 1.26e+07
TOF MULT
TOF tray

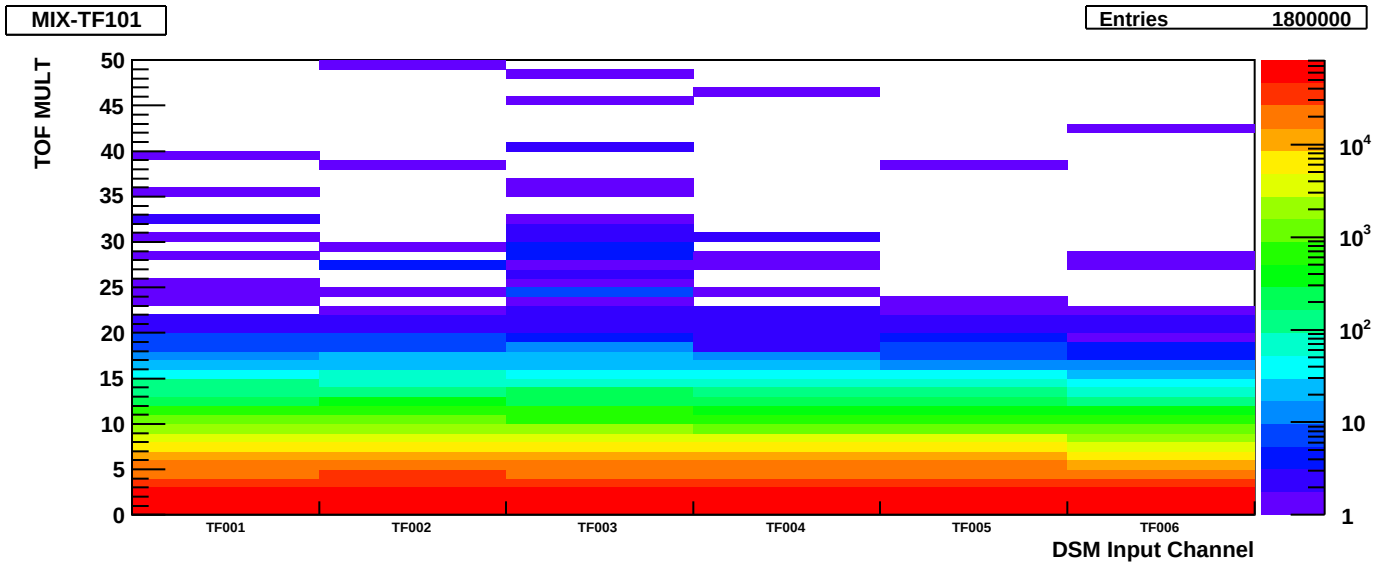

MIX-TF002
Entries 1.26e+07
TOF MULT
TOF tray

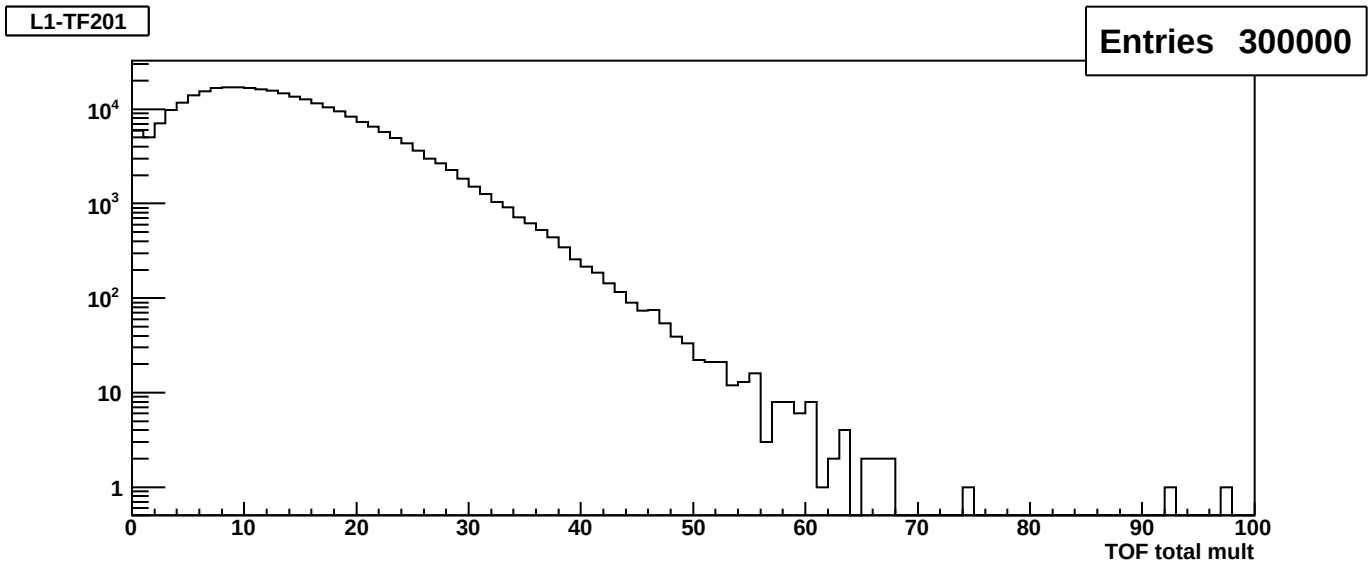

MIX-TF003
Entries 1.26e+07
TOF MULT
TOF tray

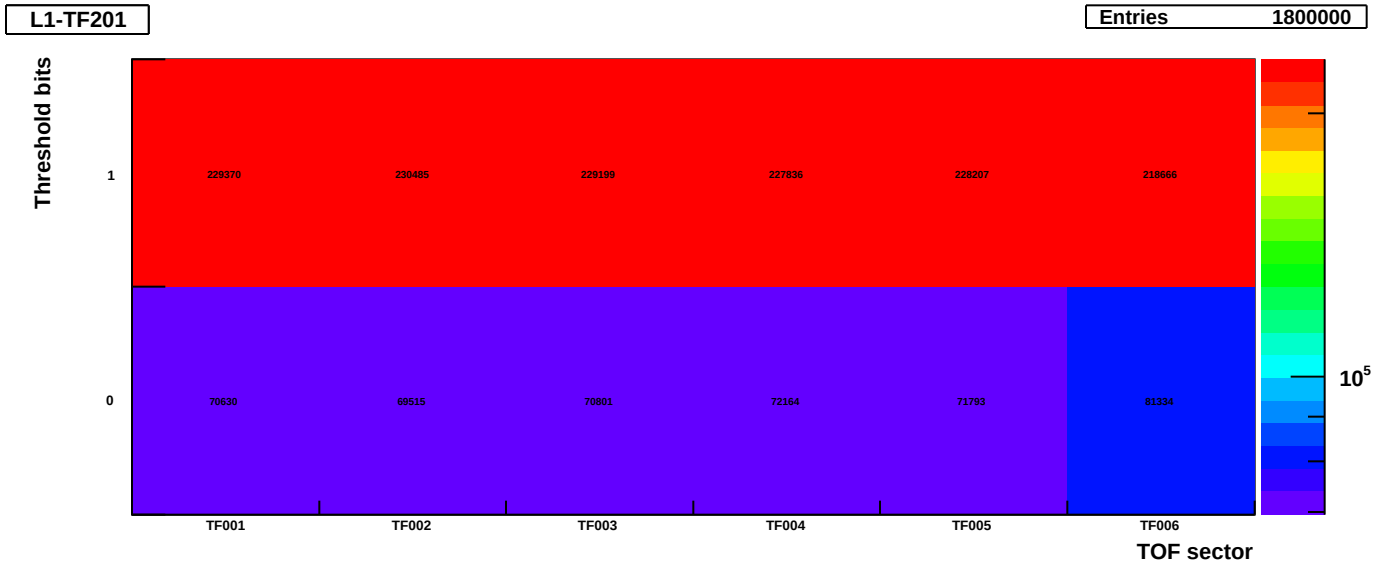

MIX-TF004
Entries 1.26e+07
TOF MULT
TOF tray

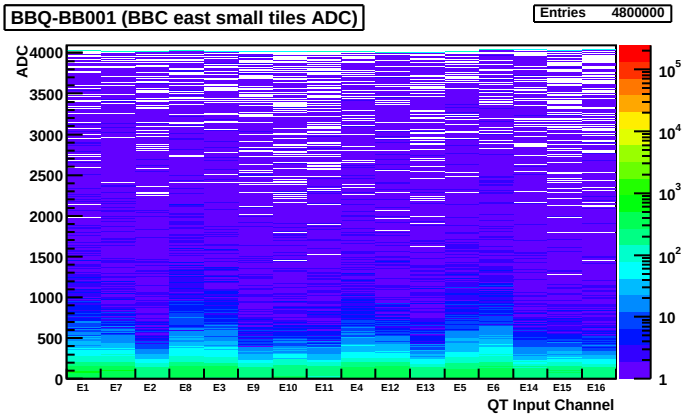

MIX-TF005
Entries 1.26e+07
TOF MULT
TOF tray

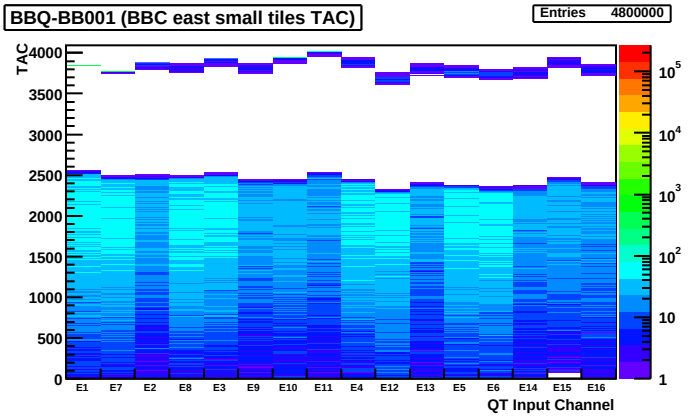

MIX-TF006
Entries 1.26e+07
TOF MULT
TOF tray

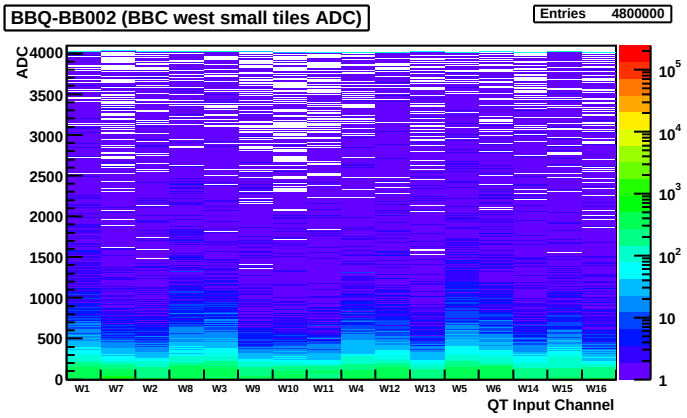

MIX-TF101
Entries 1800000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

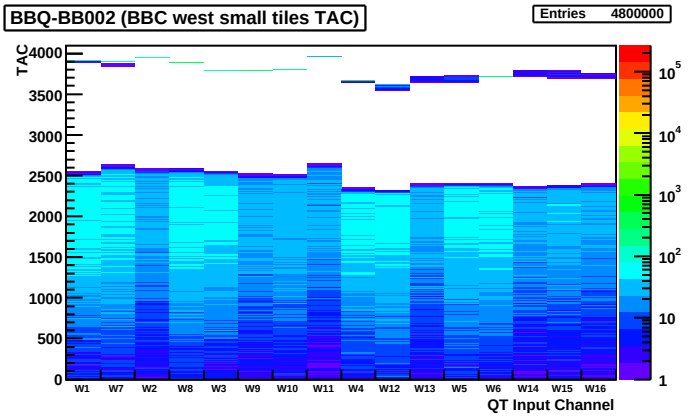

L1-TF201
Entries 300000
TOF total mult

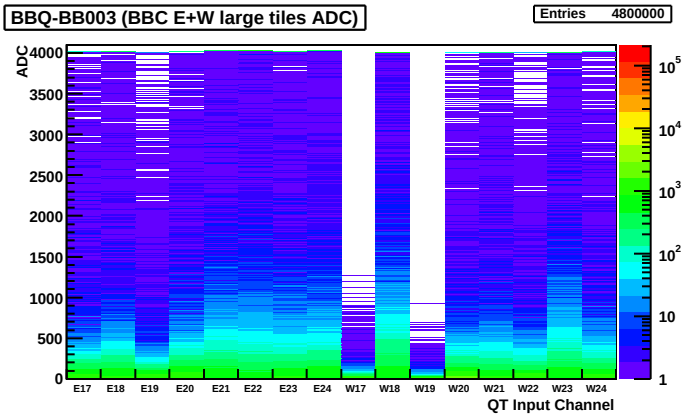

L1-TF201
Entries 1800000
Threshold bits
229370
230485
229199
227836
228207
218666
70630
69515
70801
72164
71793
81334
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

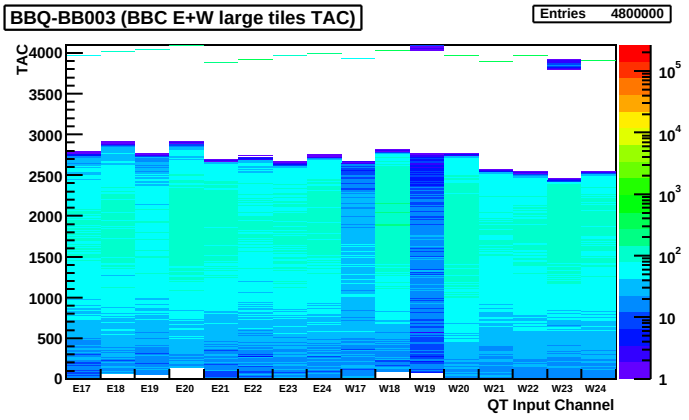

BBQ-BB001 (BBC east small tiles ADC)
Entries 4800000
ADC
QT Input Channel

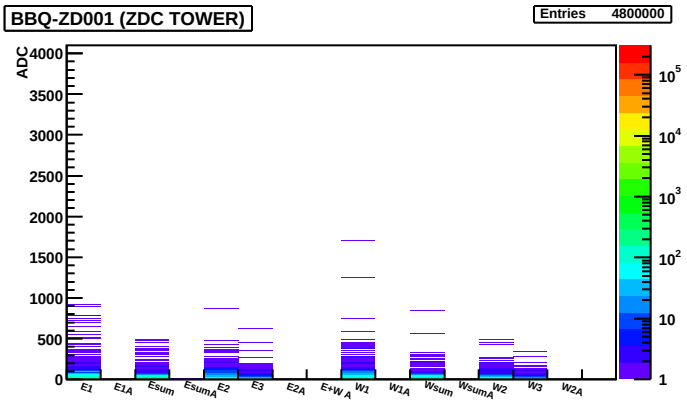

BBQ-BB001 (BBC east small tiles TAC)
Entries 4800000
TAC
QT Input Channel

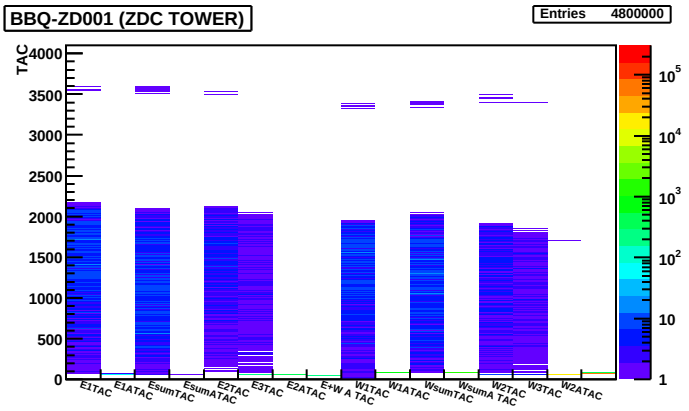

BBQ-BB002 (BBC west small tiles ADC)
Entries 4800000
ADC
QT Input Channel

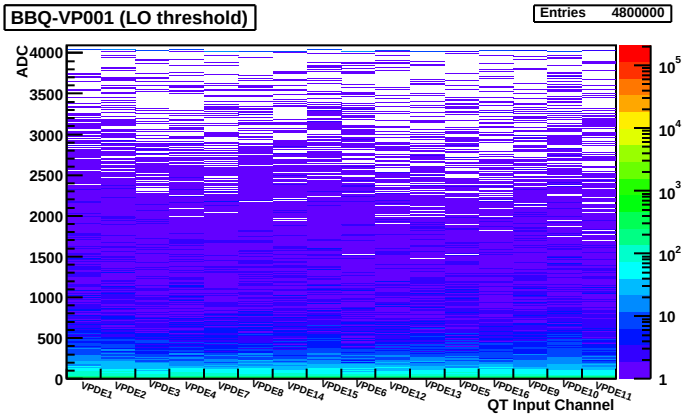

BBQ-BB002 (BBC west small tiles TAC)
Entries 4800000
TAC
QT Input Channel

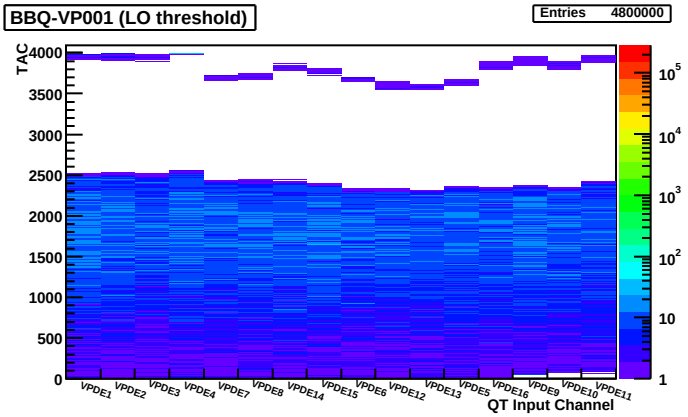

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4800000
ADC
QT Input Channel

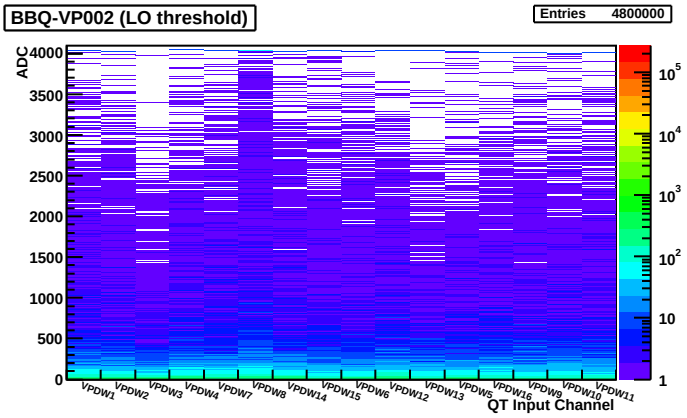

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4800000
TAC
QT Input Channel

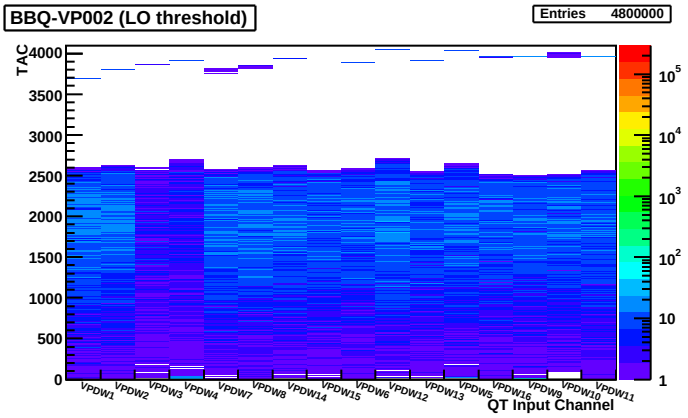

BBQ-ZD001 (ZDC TOWER)
Entries 4800000
ADC

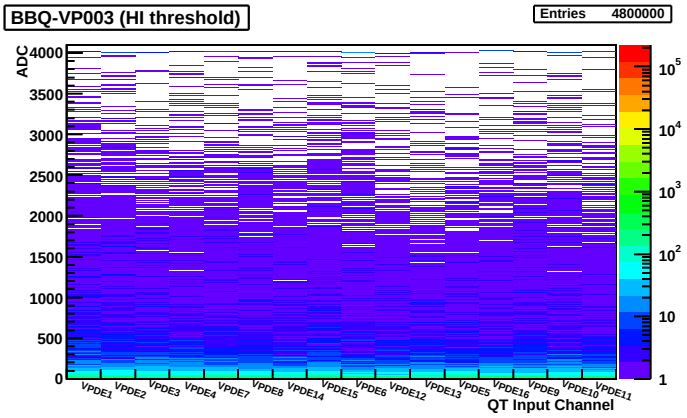

BBQ-ZD001 (ZDC TOWER)
Entries 4800000
TAC

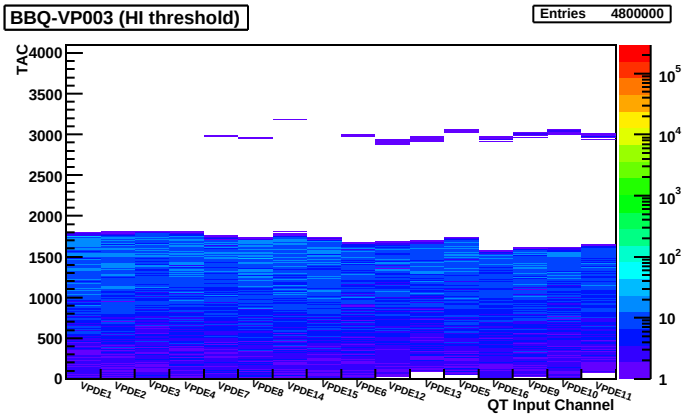

BBQ-VP001 (LO threshold)
Entries 4800000
ADC
QT Input Channel

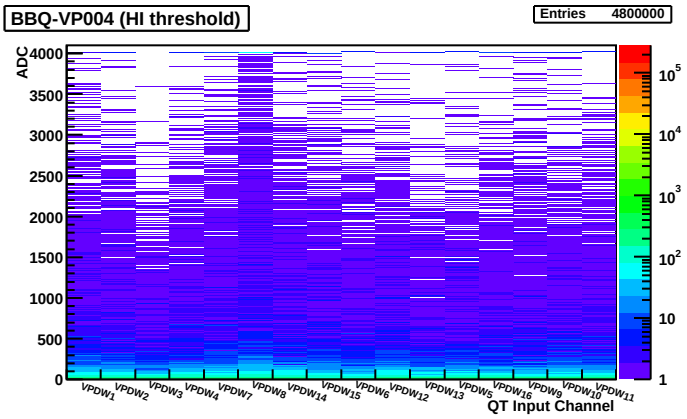

BBQ-VP001 (LO threshold)
Entries 4800000
TAC
QT Input Channel

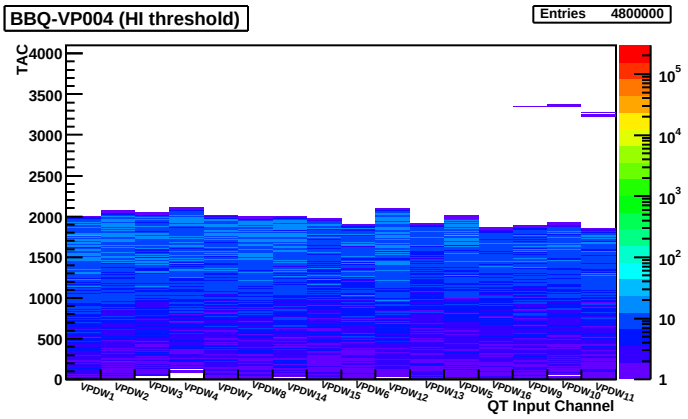

BBQ-VP002 (LO threshold)
Entries 4800000
ADC
QT Input Channel

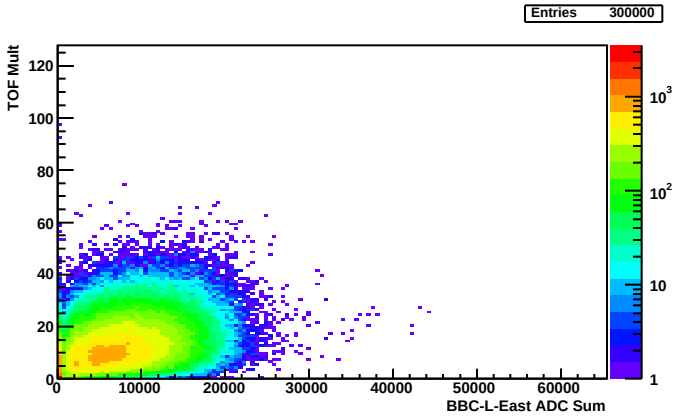

BBQ-VP002 (LO threshold)
Entries 4800000
TAC
QT Input Channel

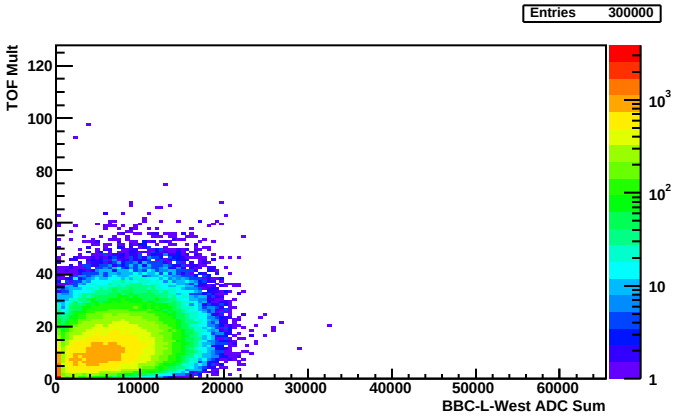

BBQ-VP003 (HI threshold)
Entries 4800000
ADC
QT Input Channel

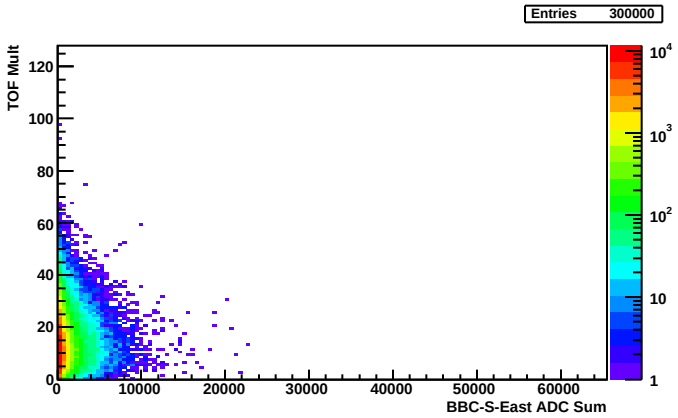

BBQ-VP003 (HI threshold)
Entries 4800000
TAC
QT Input Channel

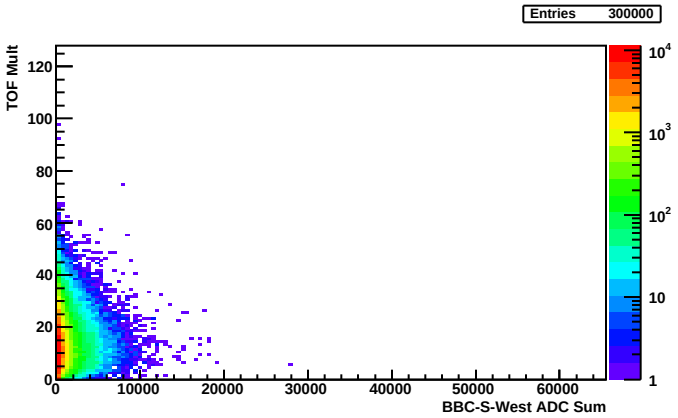

BBQ-VP004 (HI threshold)
Entries 4800000
ADC
QT Input Channel

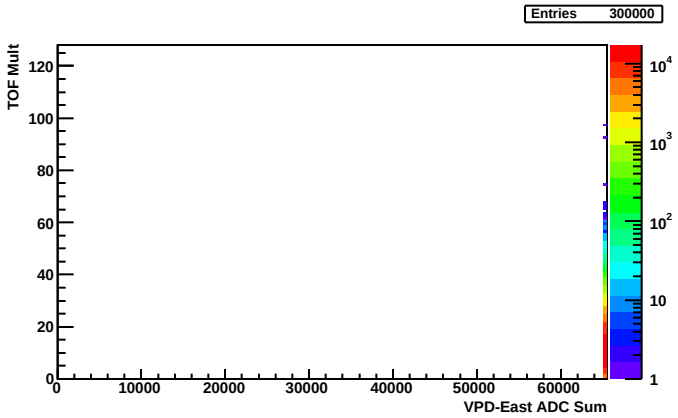

BBQ-VP004 (HI threshold)
Entries 4800000
TAC
QT Input Channel

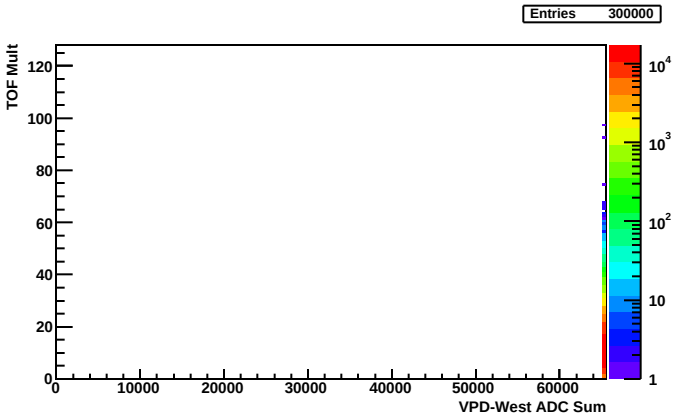

Entries 300000
TOF Mult
BBC-L-East ADC Sum

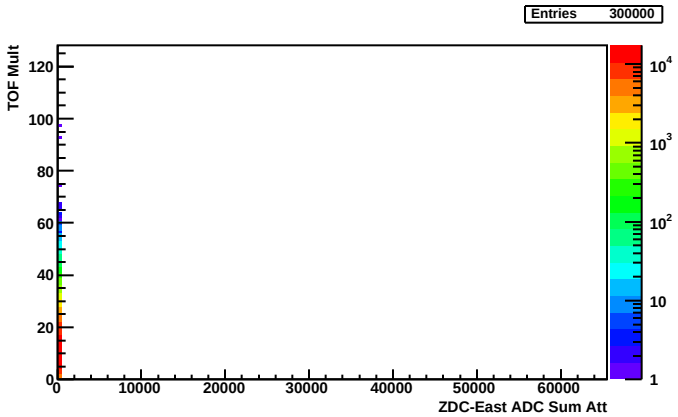

Entries 300000
TOF Mult
BBC-L-West ADC Sum

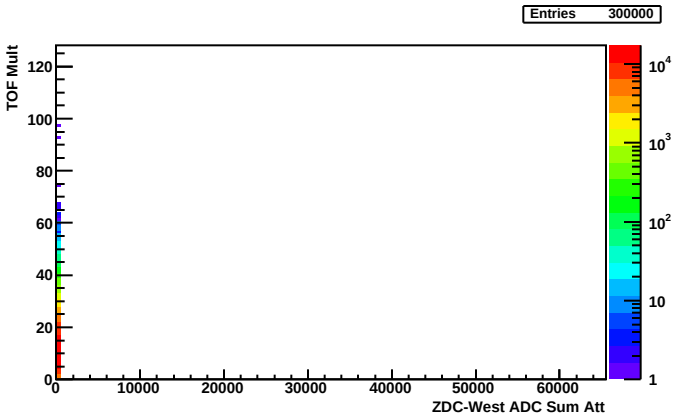

Entries 300000
TOF Mult
BBC-S-East ADC Sum

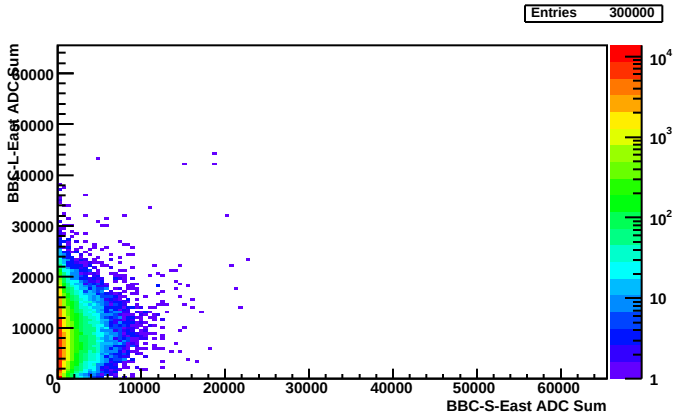

Entries 300000
TOF Mult
BBC-S-West ADC Sum

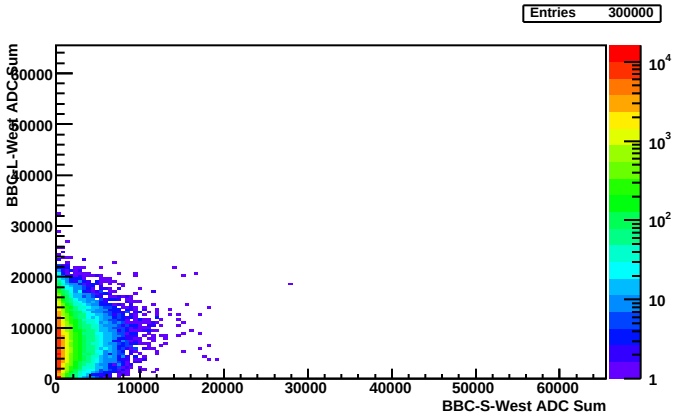

Entries 300000
TOF Mult
VPD-East ADC Sum

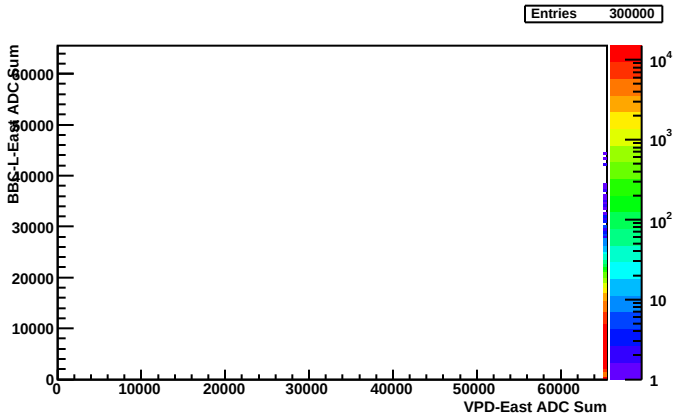

Entries 300000
TOF Mult
VPD-West ADC Sum

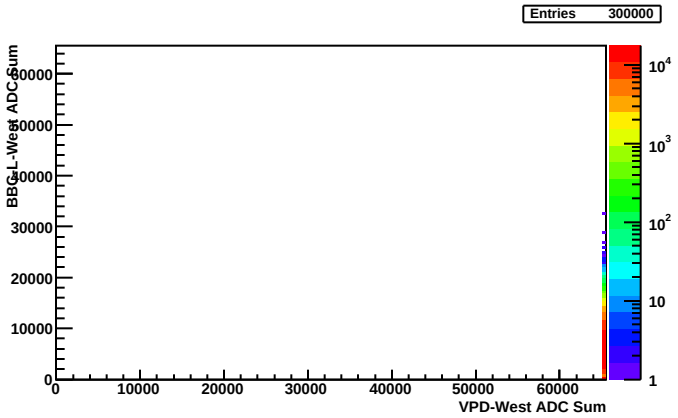

Entries 300000
TOF Mult
ZDC-East ADC Sum Att

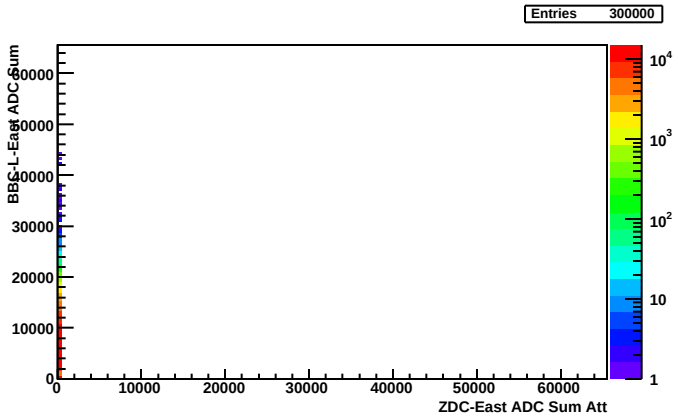

Entries 300000
TOF Mult
ZDC-West ADC Sum Att

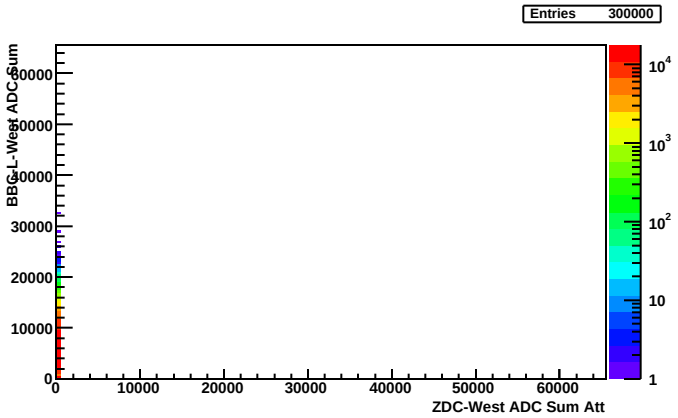

Entries 300000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

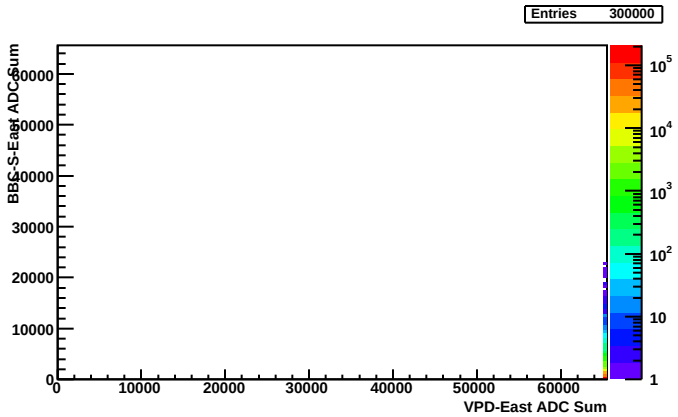

Entries 300000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

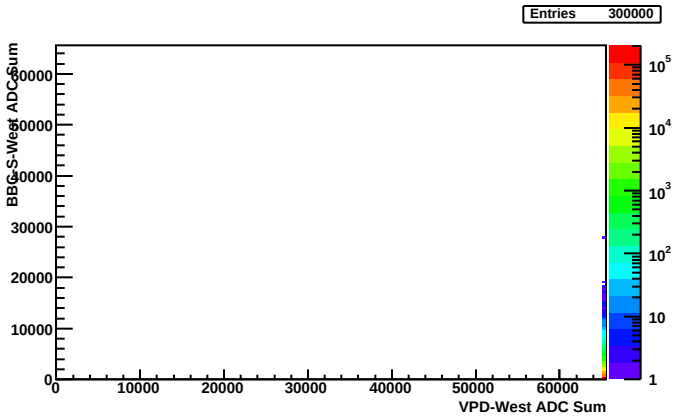

Entries 300000
BBC-L-East ADC Sum
VPD-East ADC Sum

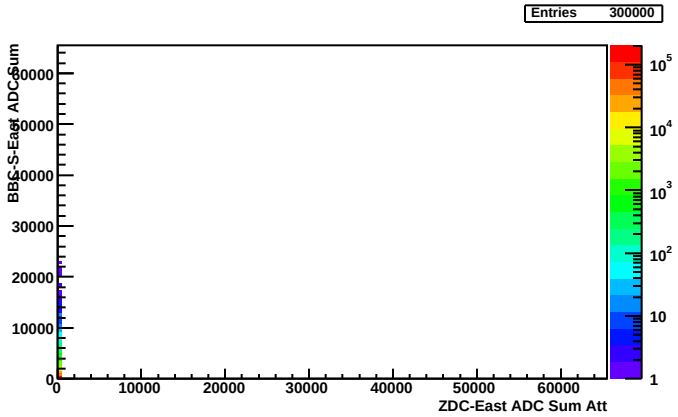

Entries 300000
BBC-L-West ADC Sum
VPD-West ADC Sum

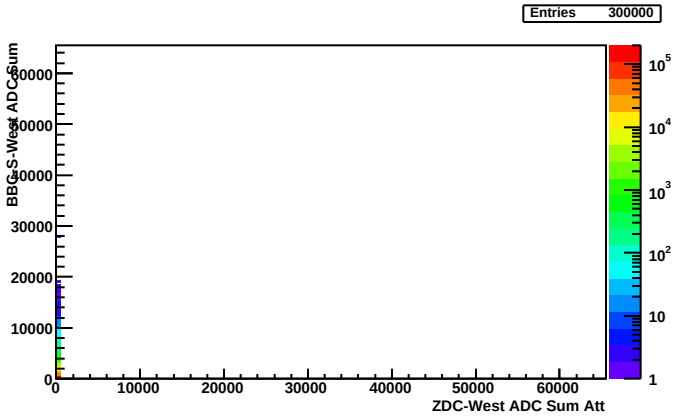

Entries 300000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

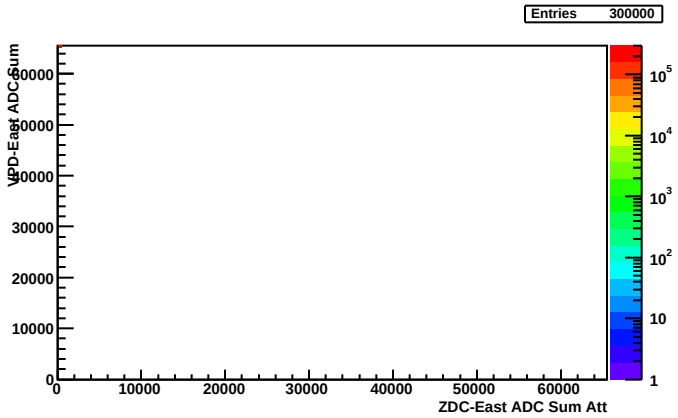

Entries 300000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

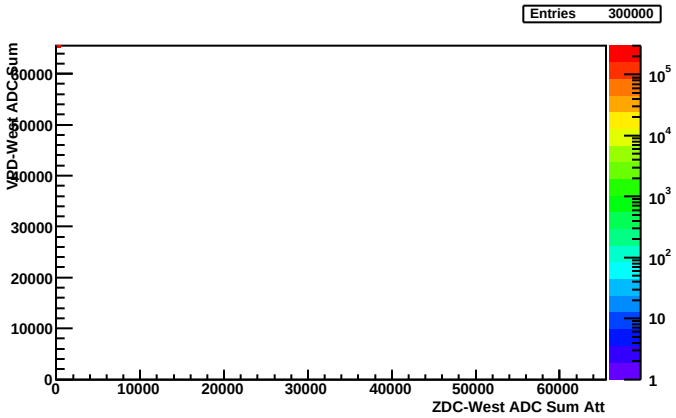

Entries 300000
BBC-S-East ADC Sum
VPD-East ADC Sum

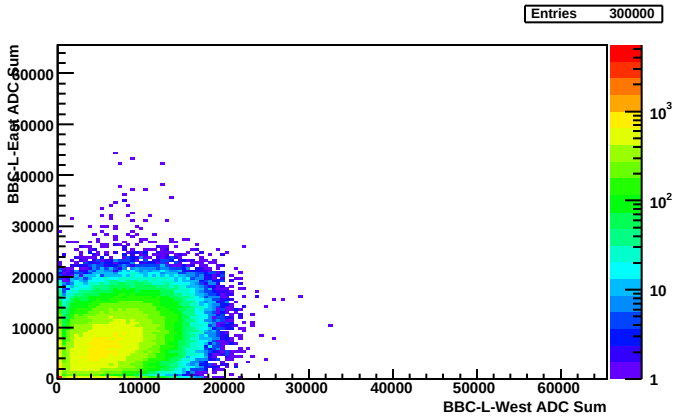

Entries 300000
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 300000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 300000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 300000
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 300000
VPD-West ADC Sum
ZDC-West ADC Sum Att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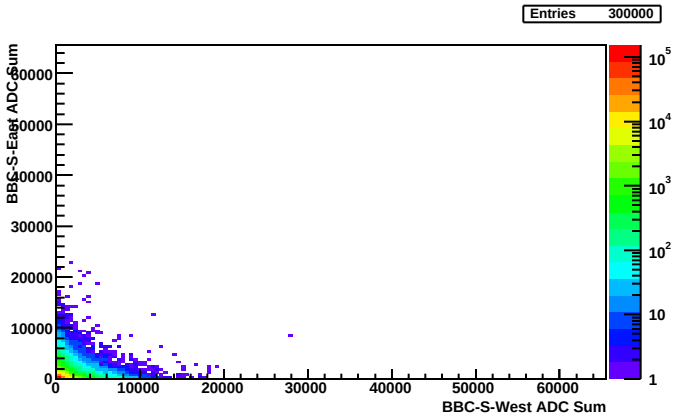
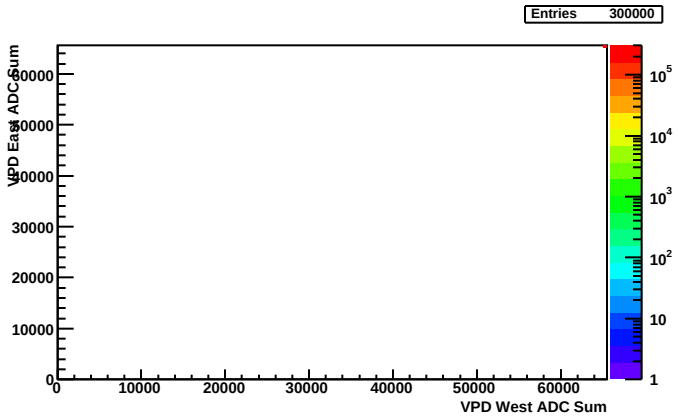
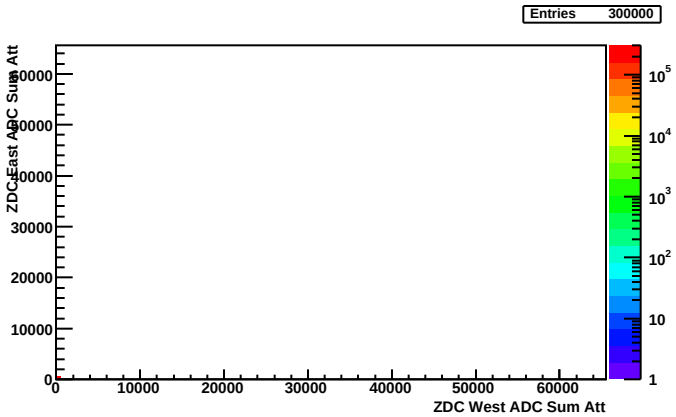
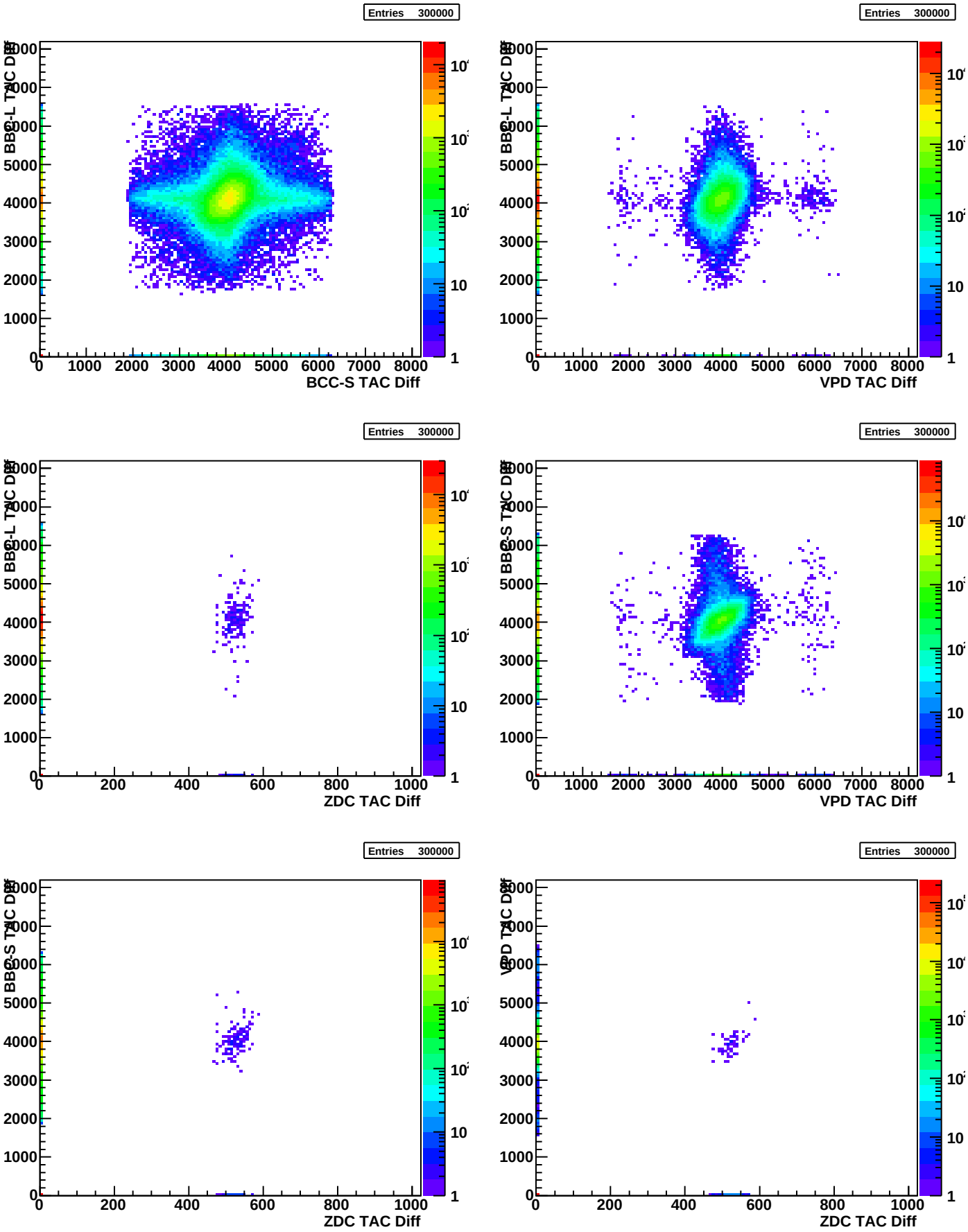
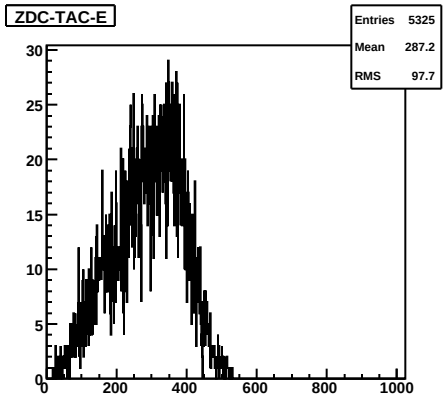
BBC-L-East ADC Sum
BBC-L-West ADC Sum

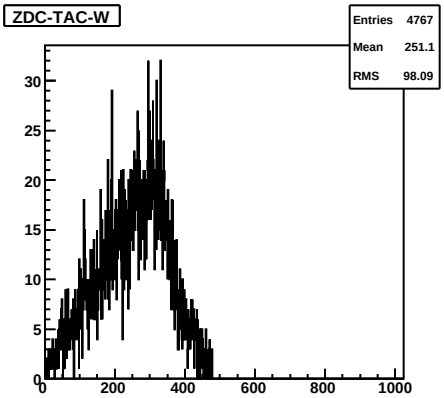

Entries 300000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

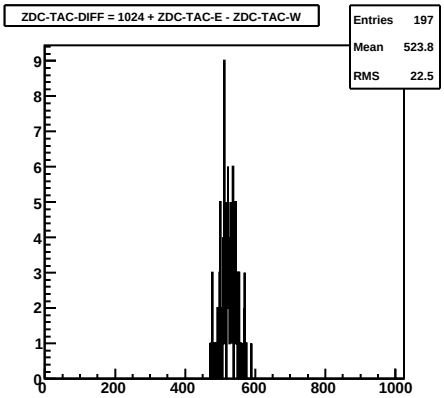

Entries 300000
VPD East ADC Sum
VPD West ADC Sum

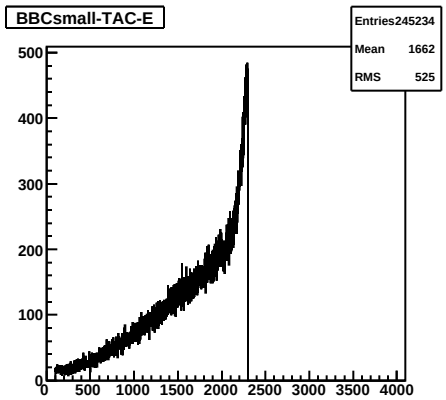

Entries 300000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

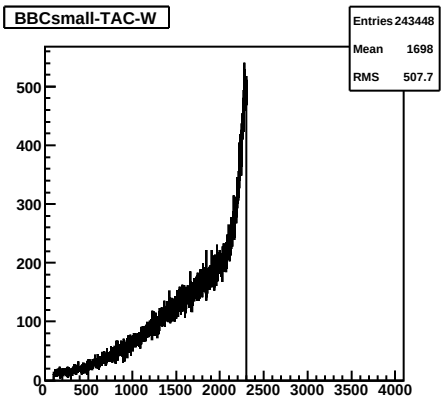

Entries 300000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 300000
BBC-L TAC Diff
VPD TAC Diff
Entries 300000
BBC-L TAC Diff
ZDC TAC Diff
Entries 300000
BBC-S TAC Diff
VPD TAC Diff
Entries 300000
BBC-S TAC Diff
ZDC TAC Diff
Entries 300000
VPD TAC Diff
ZDC TAC Diff

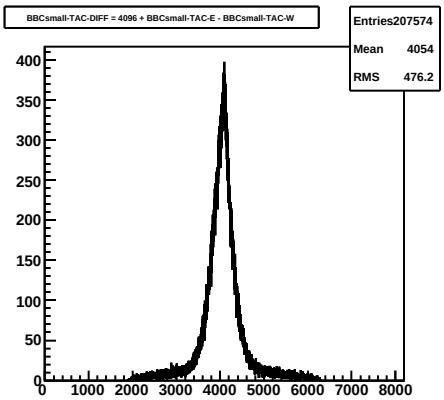

ZDC-TAC-E
Entries 5325
Mean 287.2
RMS 97.7

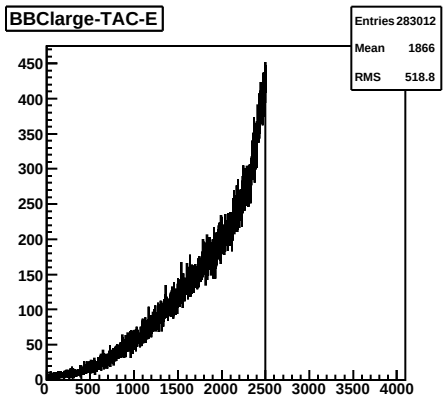

ZDC-TAC-W
Entries 4767
Mean 251.1
RMS 98.09

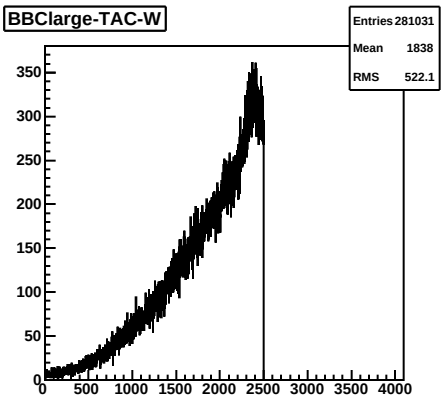

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 197
Mean 523.8
RMS 22.5

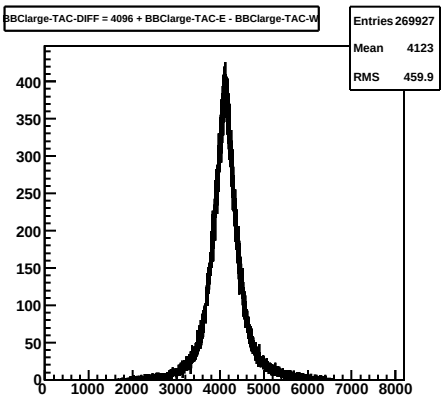

BBCsmall-TAC-E
Entries 245234
Mean 1662
RMS 525

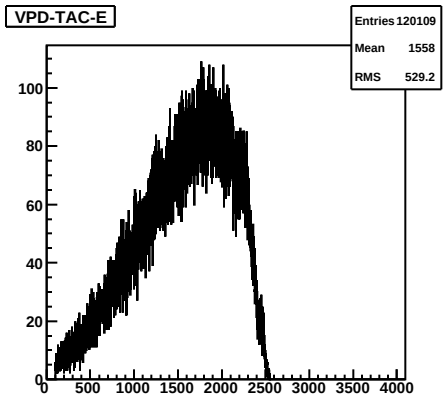

BBCsmall-TAC-W
Entries 243448
Mean 1698
RMS 507.7

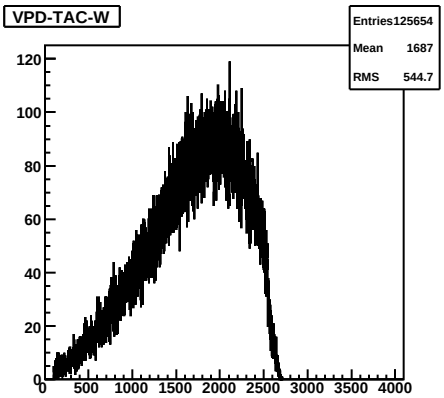

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 207574
Mean 4054
RMS 476.2

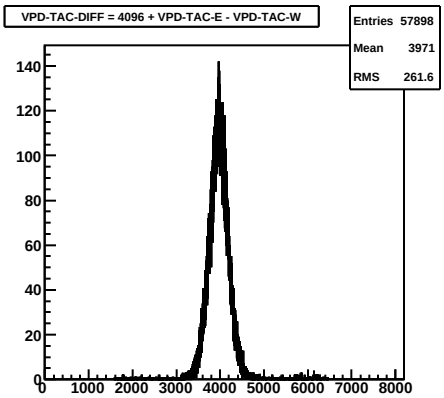

BBClarge-TAC-E
Entries 283012
Mean 1866
RMS 518.8

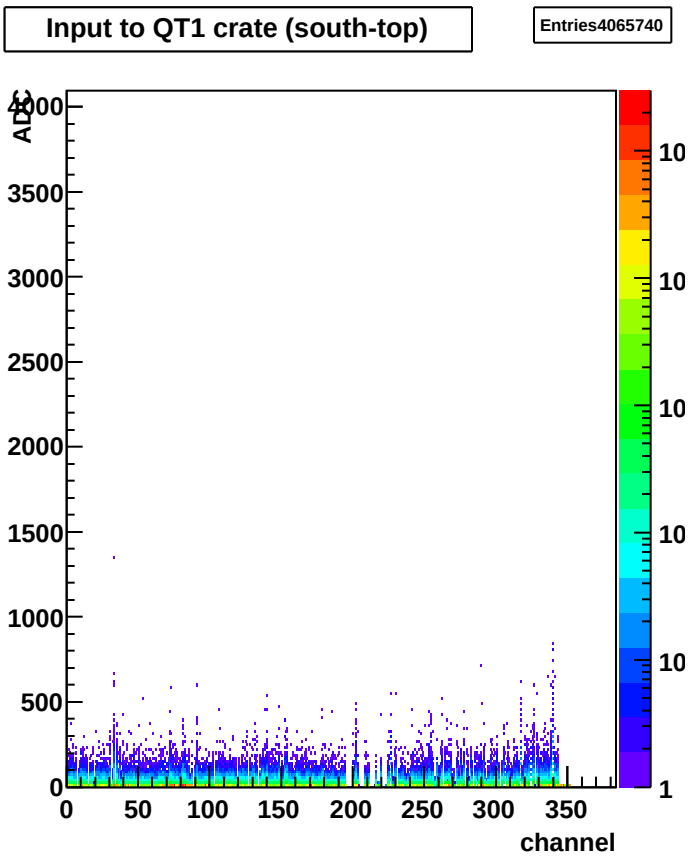

BBClarge-TAC-W
Entries 281031
Mean 1838
RMS 522.1

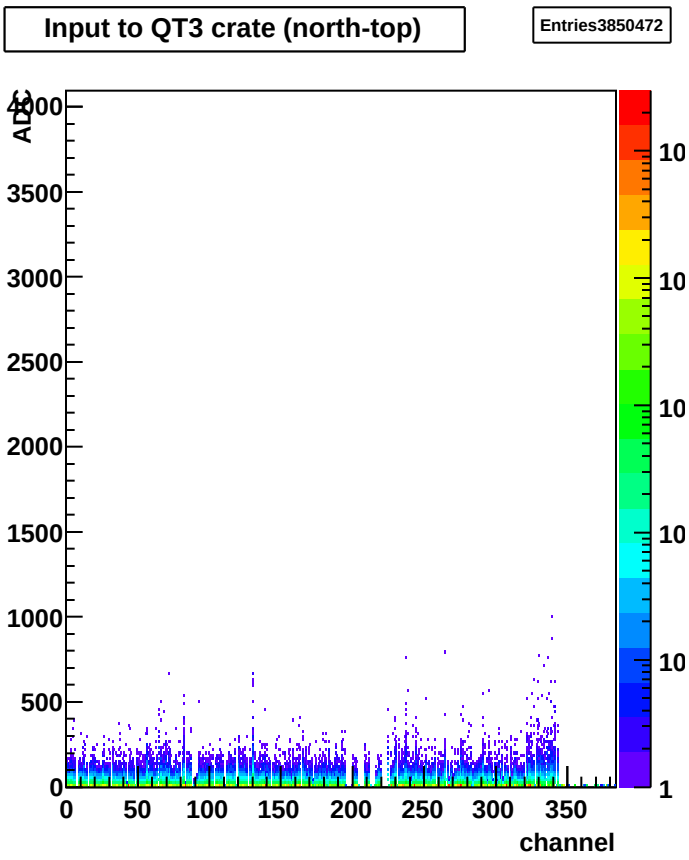

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 269927
Mean 4123
RMS 459.9

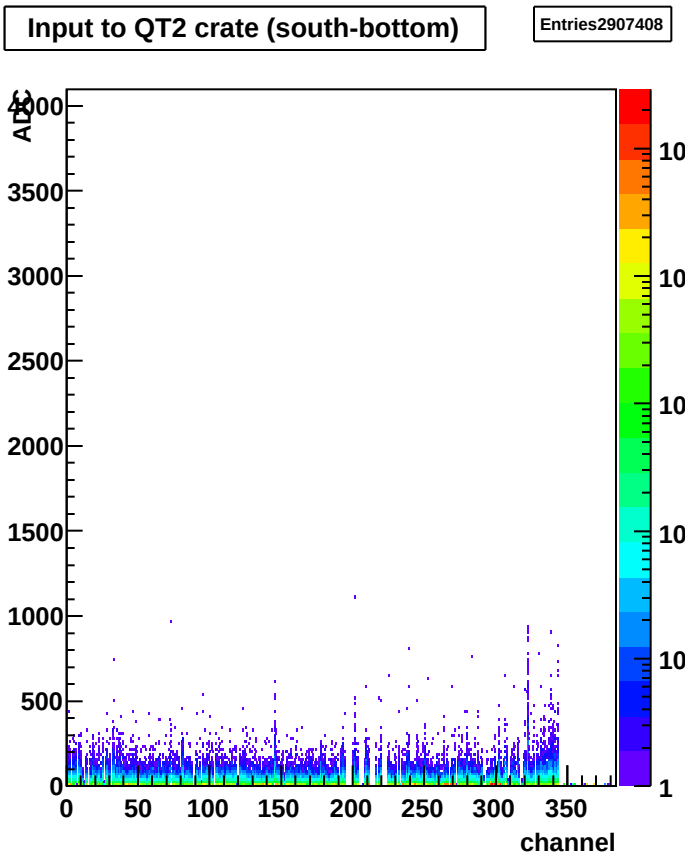

VPD-TAC-E
Entries 120109
Mean 1558
RMS 529.2

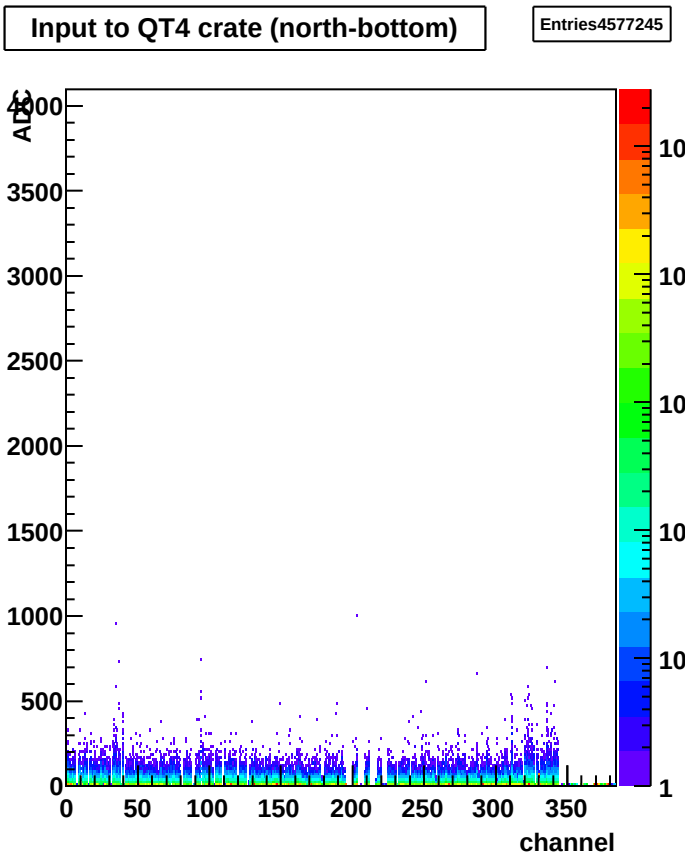

VPD-TAC-W
Entries 125654
Mean 1687
RMS 544.7

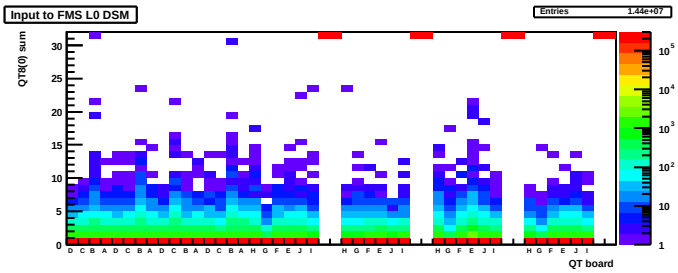

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 57898
Mean 3971
RMS 261.6

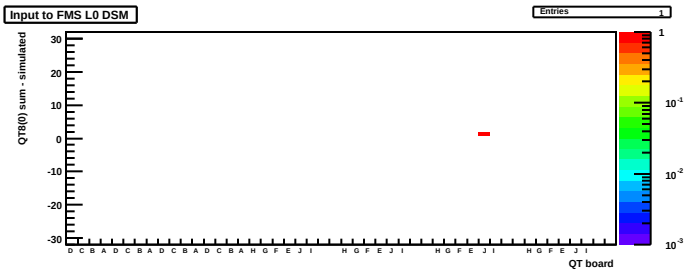

Input to QT1 crate (south-top)
Entries4065740
ADC
channel

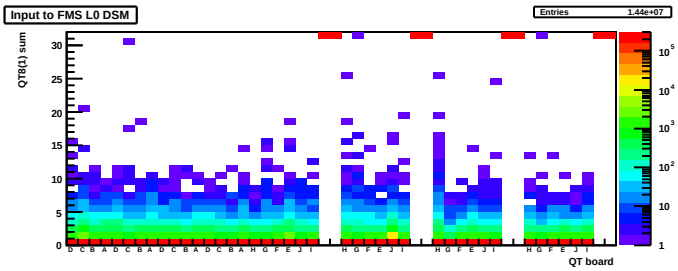

Input to QT3 crate (north-top)
Entries3850472
ADC
channel

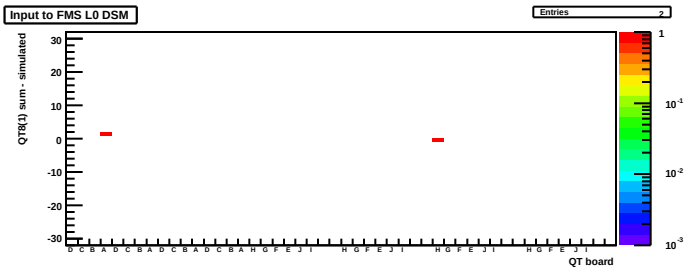

Input to QT2 crate (south-bottom)
Entries2907408
ADC
channel

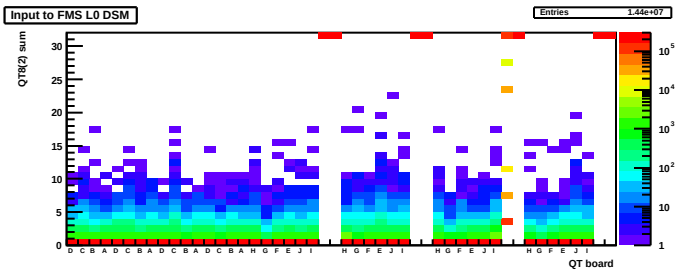

Input to QT4 crate (north-bottom)
Entries4577245
ADC
channel

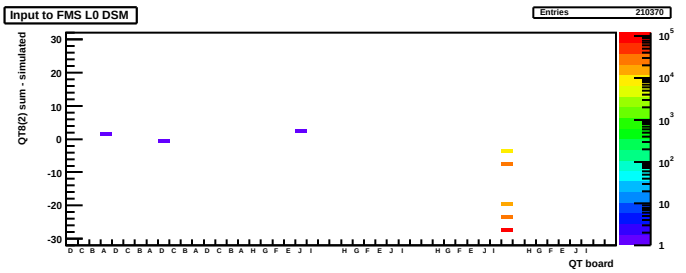

Input to FMS L0 DSM
Entries 1.44e+07
QT8(0) sum
QT board

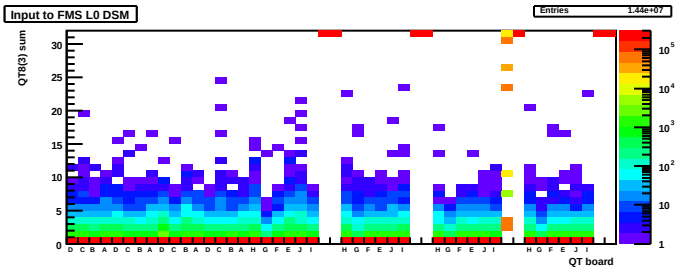

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

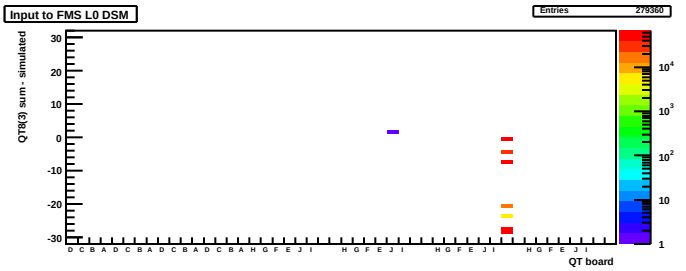

Input to FMS L0 DSM
Entries 1.44e+07
QT8(1) sum
QT board

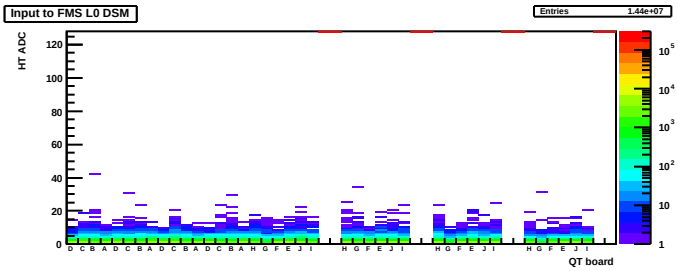

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

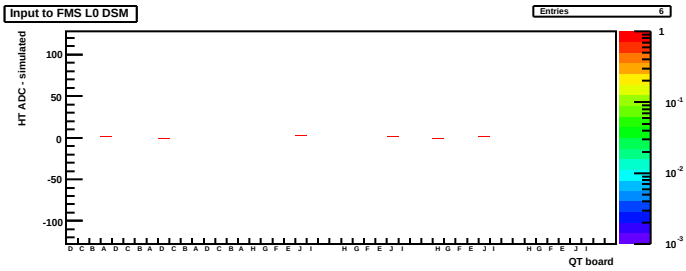

Input to FMS L0 DSM
Entries 1.44e+07
QT8(2) sum
QT board

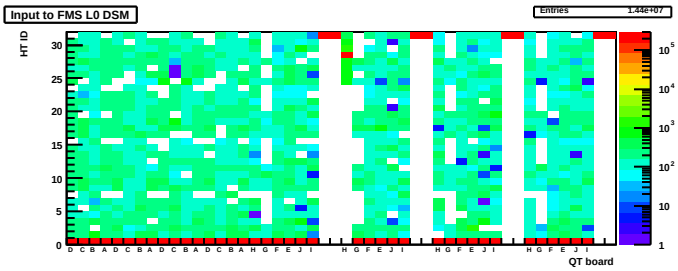

Input to FMS L0 DSM
Entries 210370
QT8(2) sum - simulated
QT board

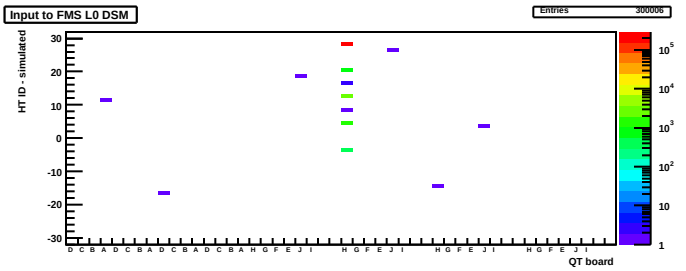

Input to FMS L0 DSM
Entries 1.44e+07
QT8(3) sum
QT board

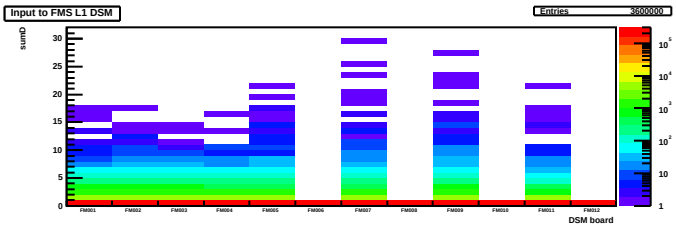

Input to FMS L0 DSM
Entries 279360
QT8(3) sum - simulated
QT board

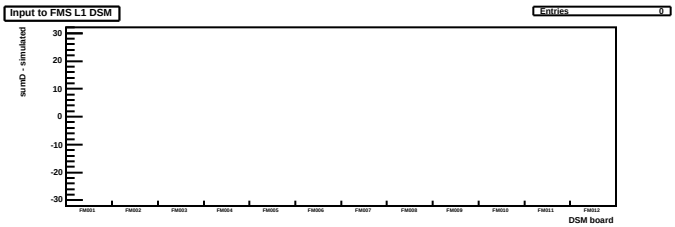

Input to FMS L0 DSM
Entries 1.44e+07
HT ADC
QT board

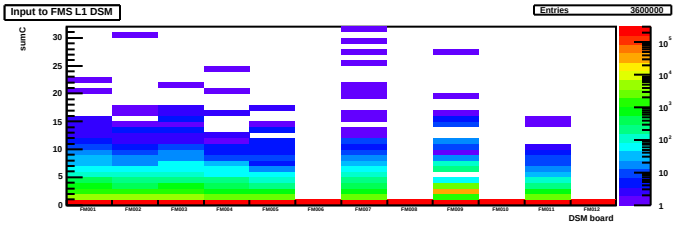

Input to FMS L0 DSM
Entries 6
HT ADC - simulated
QT board

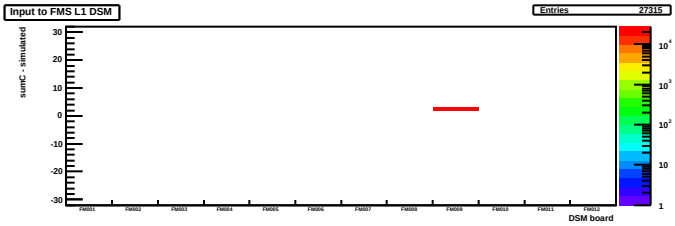

Input to FMS L0 DSM
Entries 1.44e+07
HT ID
QT board

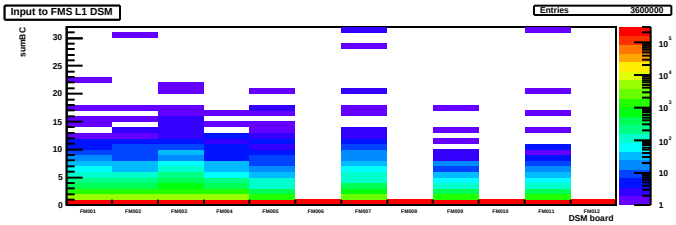

Input to FMS L0 DSM
Entries 300006
HT ID - simulated
QT board

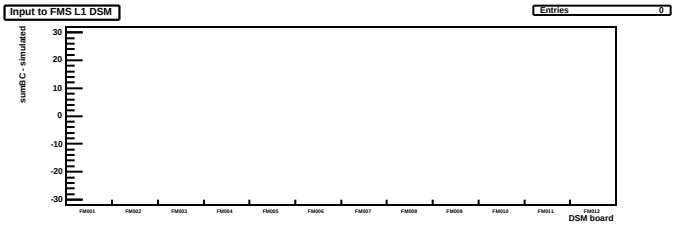

Input to FMS L1 DSM
Entries 3600000
sumD
DSM board

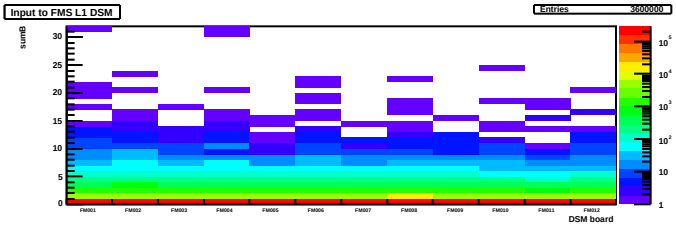

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

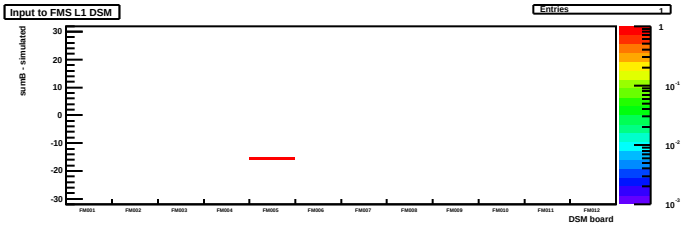

Input to FMS L1 DSM
Entries 3600000
sumC
DSM board

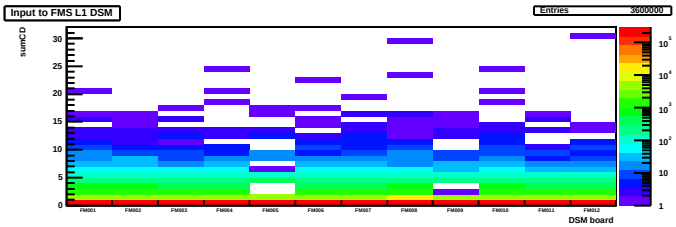

Input to FMS L1 DSM
Entries 27315
sumC - simulated
DSM board

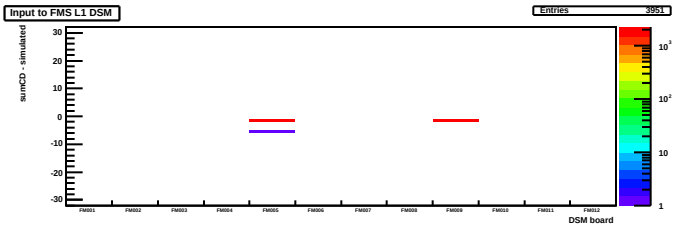

Input to FMS L1 DSM
Entries 3600000
sumBC
DSM board

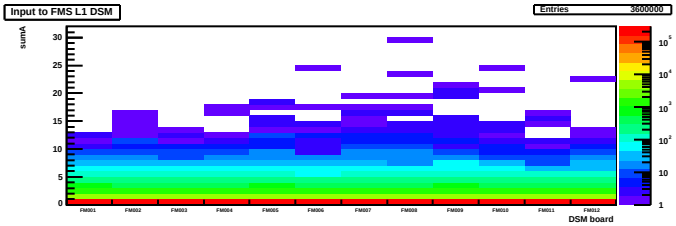

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

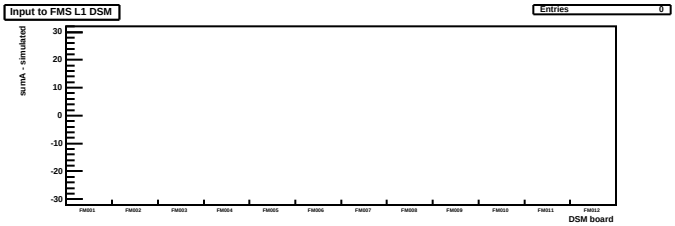

Input to FMS L1 DSM
Entries 3600000
sumB
DSM board

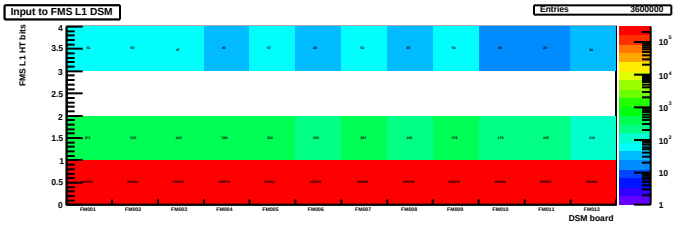

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

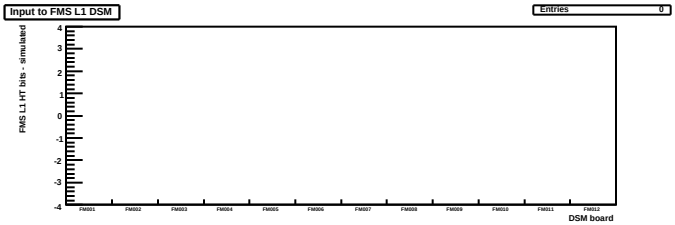

Input to FMS L1 DSM
Entries 3600000
sumCD
DSM board

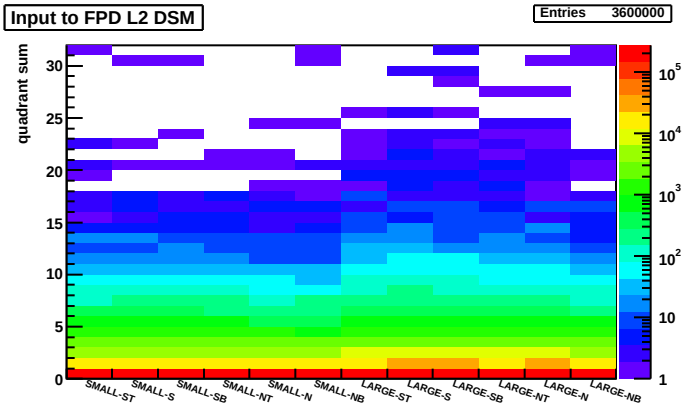

Input to FMS L1 DSM
Entries 3951
sumCD - simulated
DSM board

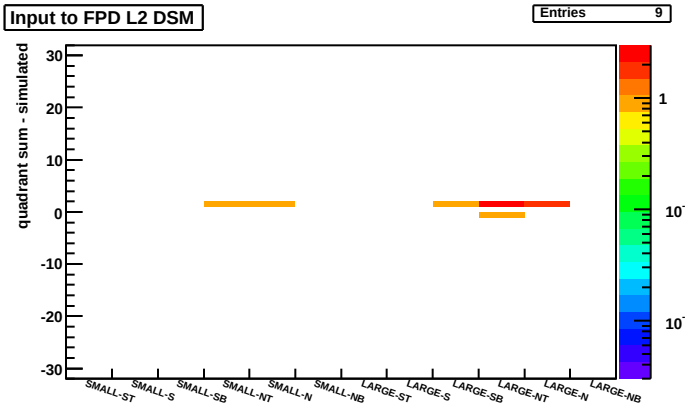

Input to FMS L1 DSM
Entries 3600000
sumA
DSM board

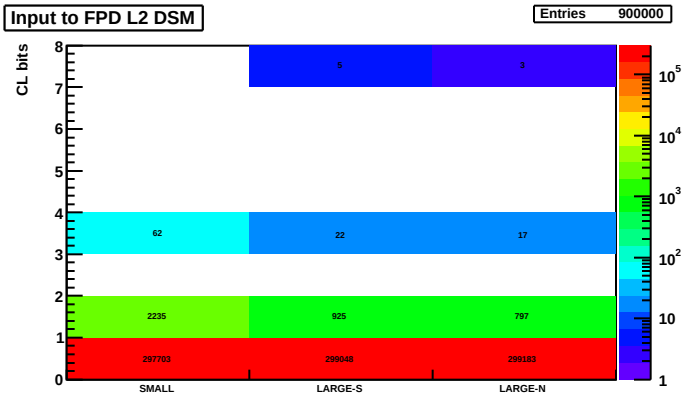

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

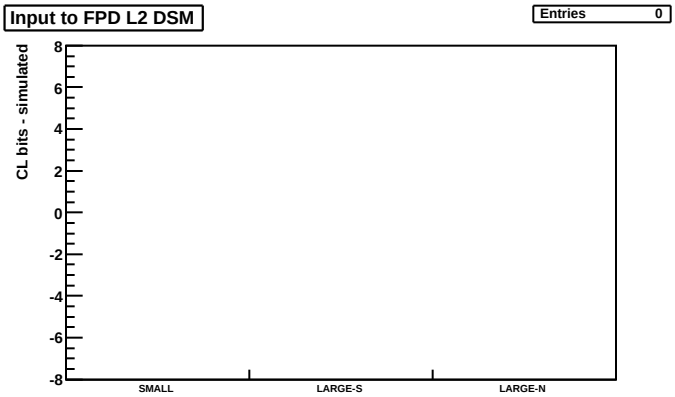

Input to FMS L1 DSM
Entries 3600000
FMS L1 HT bits
DSM board

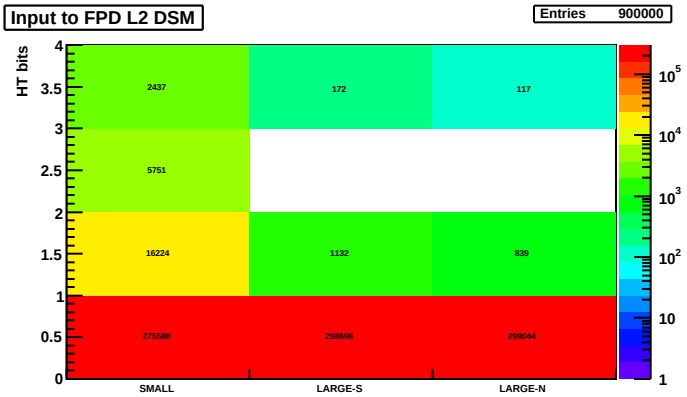

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

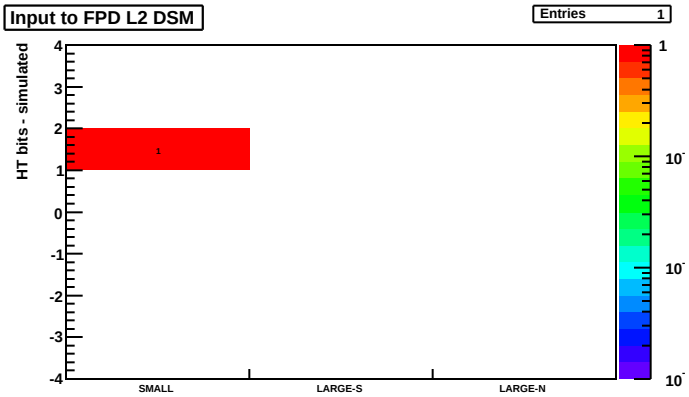

Input to FPD L2 DSM
Entries 3600000
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

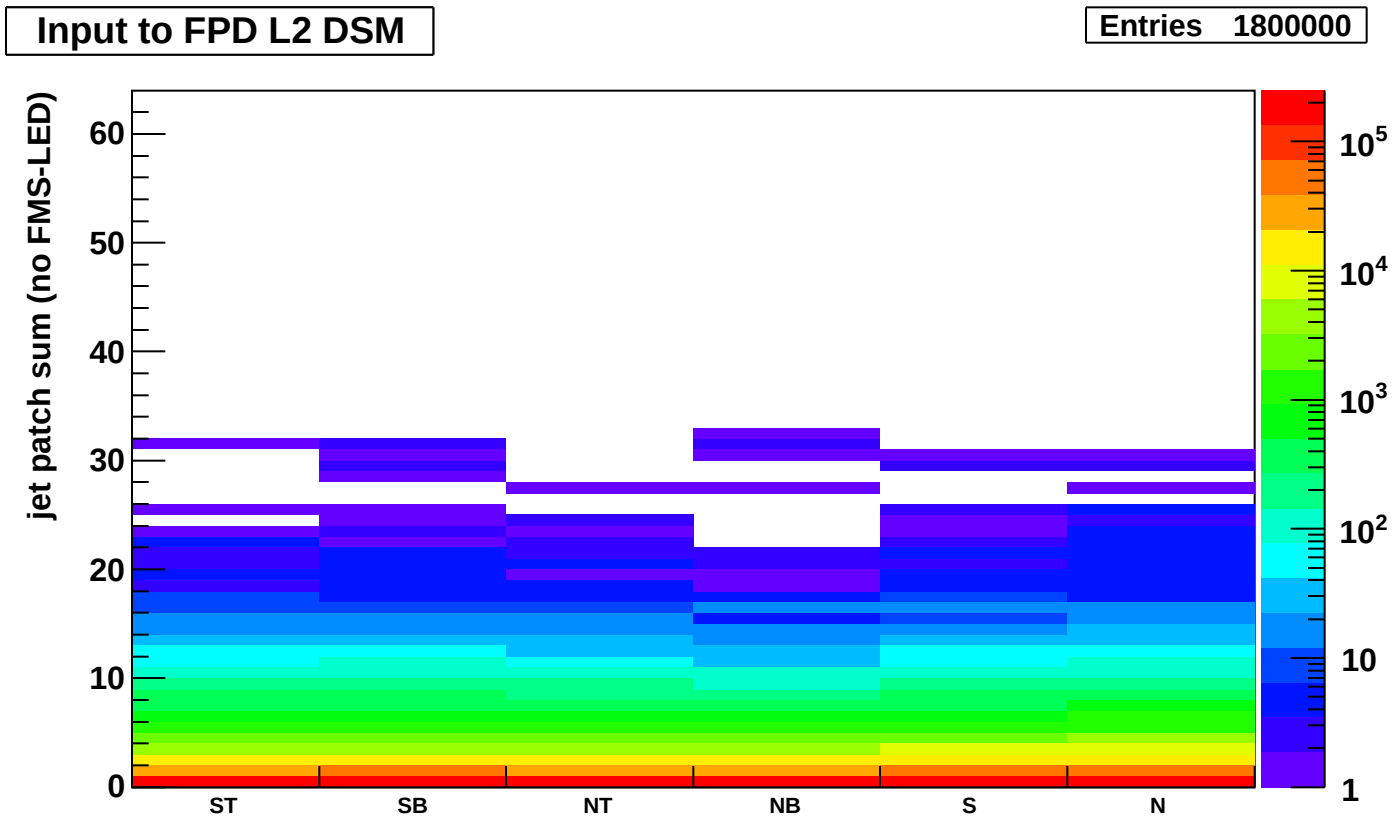

Input to FPD L2 DSM
Entries 9
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

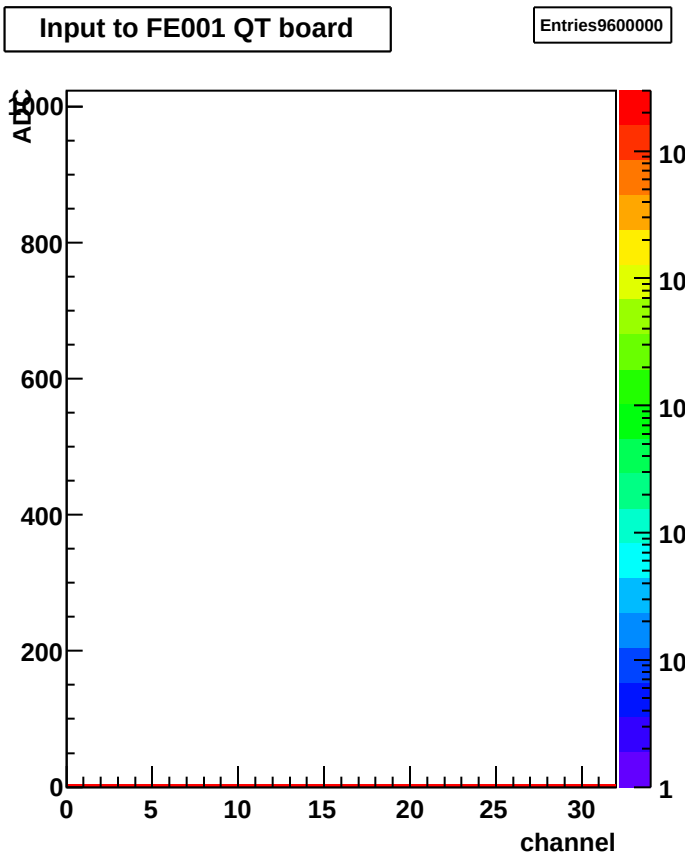

Input to FPD L2 DSM
Entries 900000
CL bits
5
3
62
22
17
2235
925
797
297703
299048
299183
SMALL
LARGE-S
LARGE-N

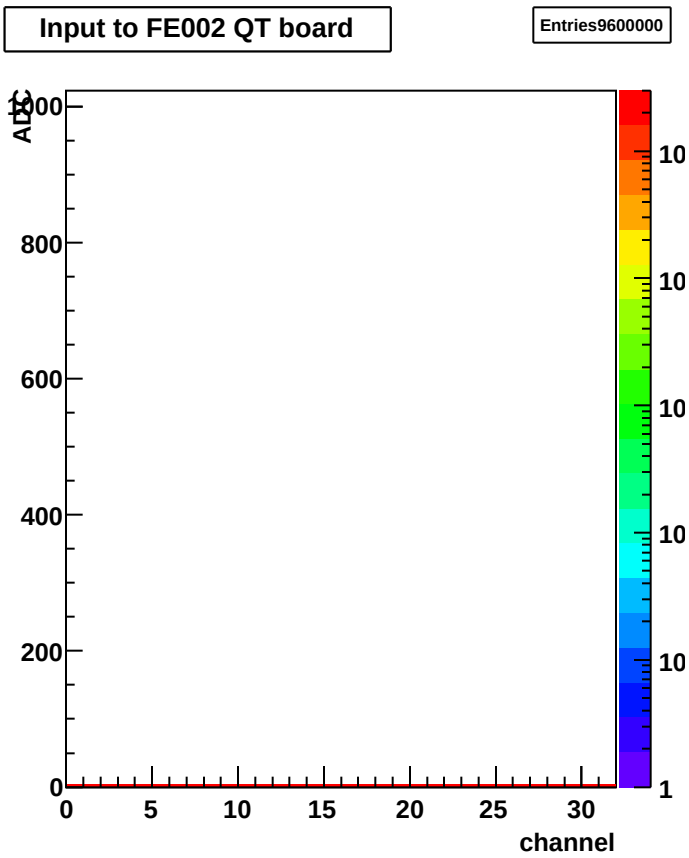

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

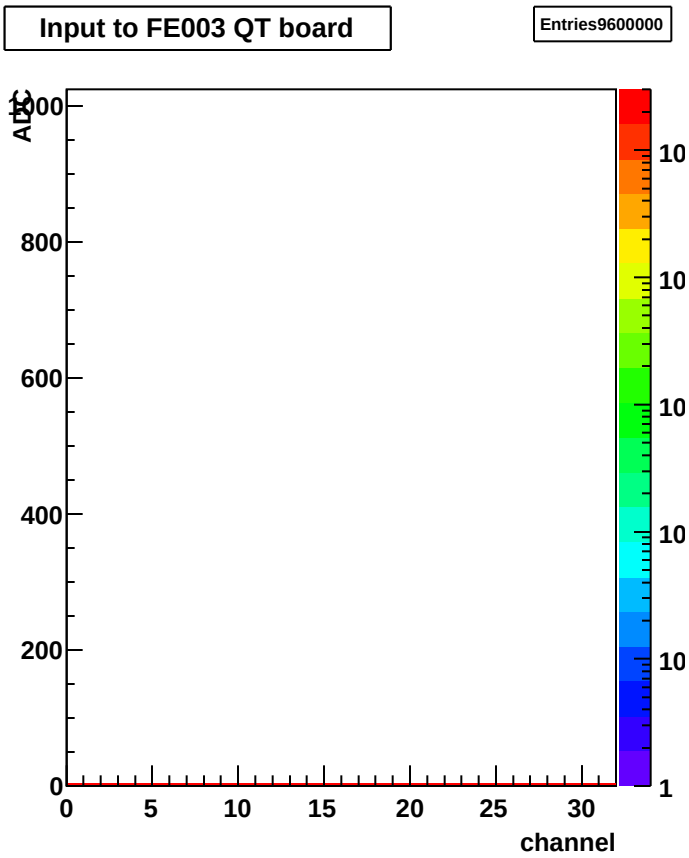

Input to FPD L2 DSM
Entries 900000
HT bits
2437
172
117
5751
16224
1132
839
275588
298696
299044
SMALL
LARGE-S
LARGE-N

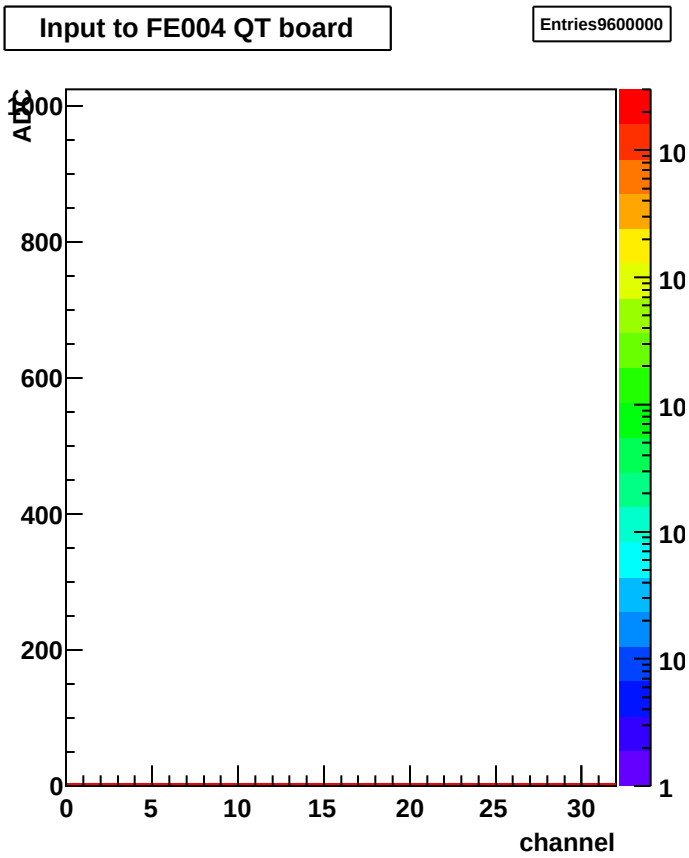

Input to FPD L2 DSM
Entries 1
HT bits - simulated
1
SMALL
LARGE-S
LARGE-N

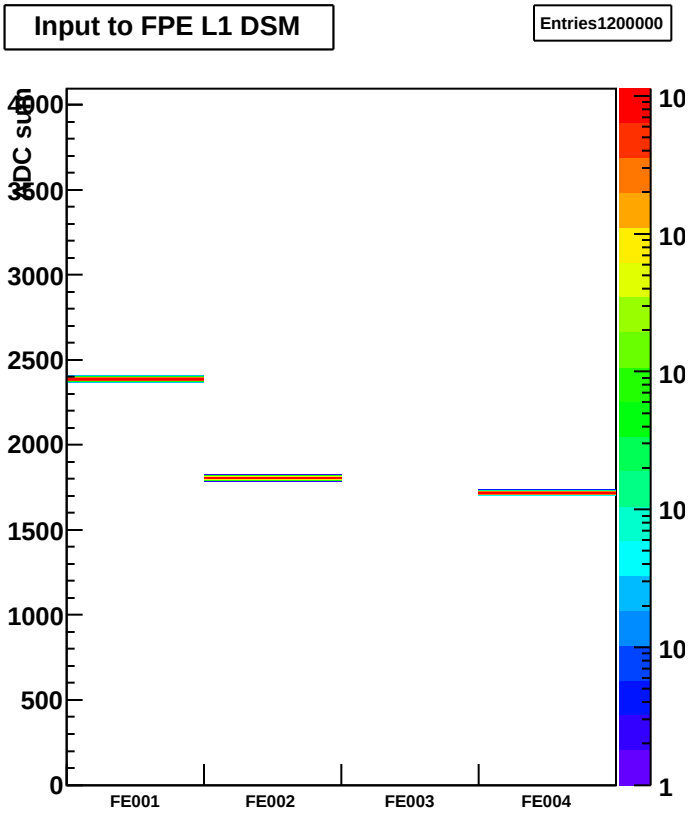

Input to FPD L2 DSM
Entries 1800000
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

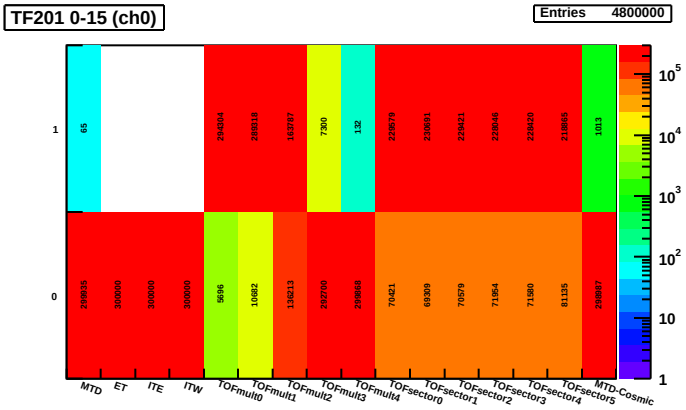

Input to FE001 QT board
Entries9600000
ADC
channel

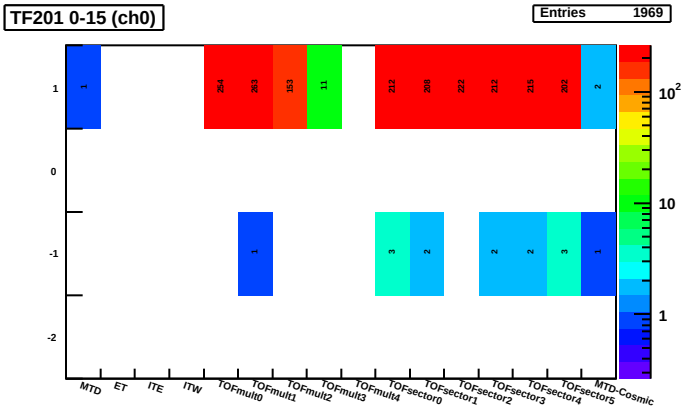

Input to FE002 QT board
Entries9600000
ADC
channel

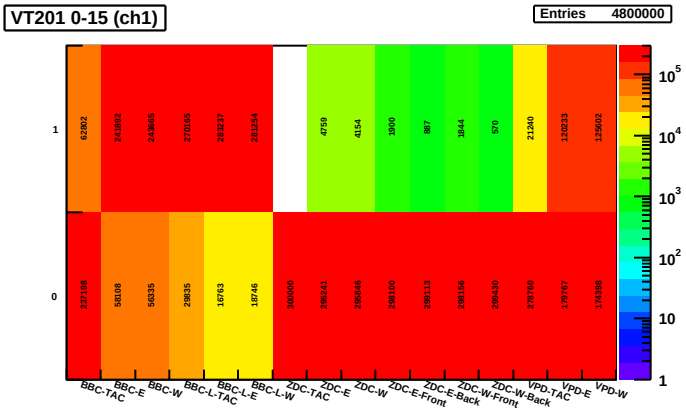

Input to FE003 QT board
Entries9600000
ADC
channel

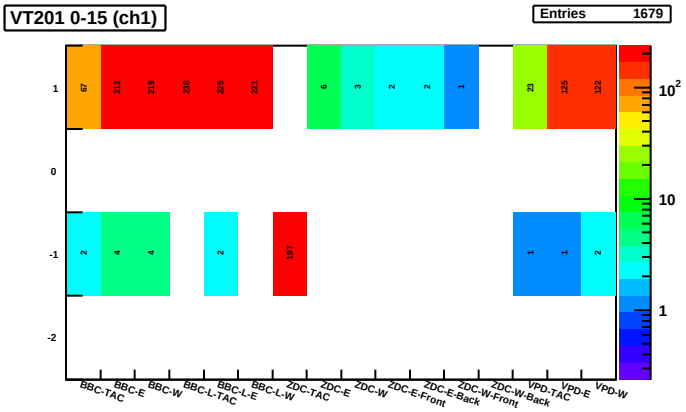

Input to FE004 QT board
Entries9600000
ADC
channel

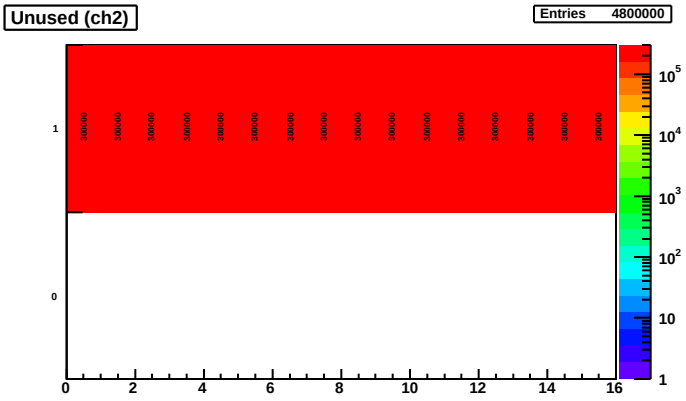

Input to FPE L1 DSM
Entries1200000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

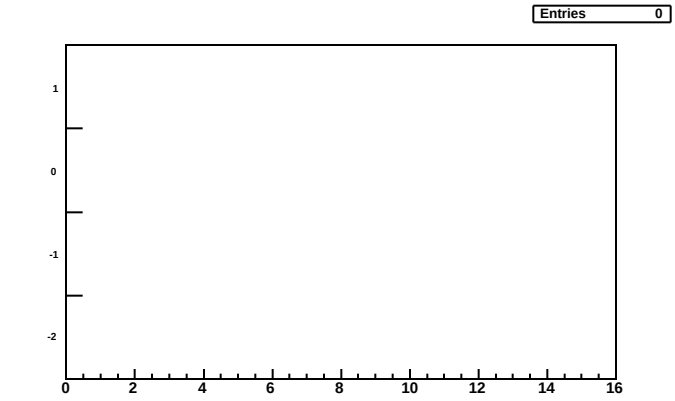

TF201 0-15 (ch0)
Entries 4800000
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

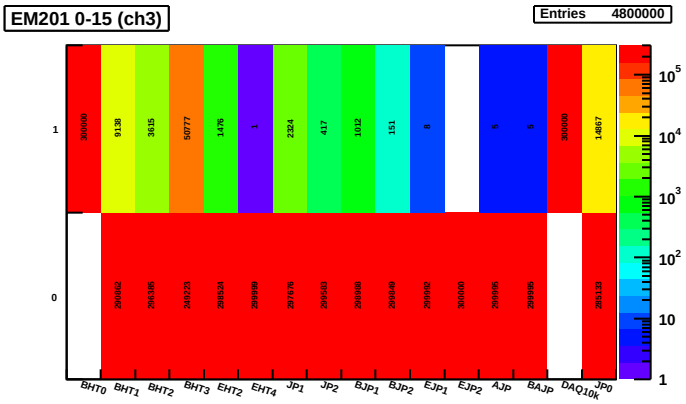

TF201 0-15 (ch0)
Entries 1969
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 4800000
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1679
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 4800000


Entries 0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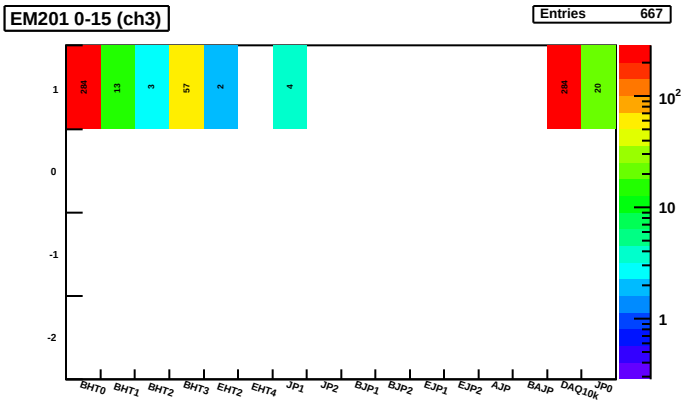
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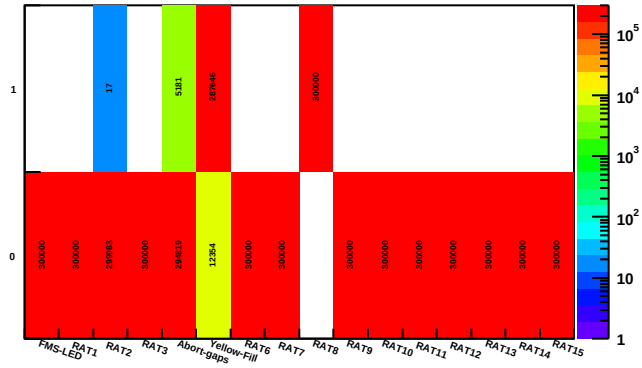
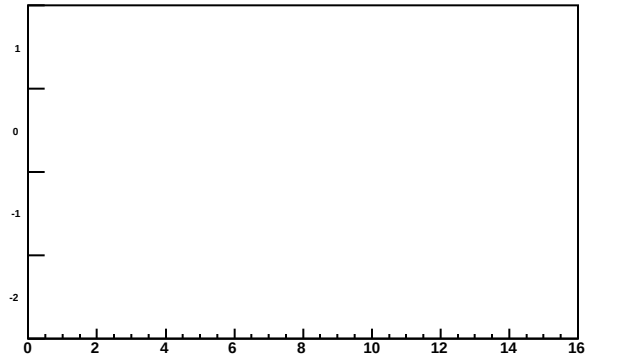
RAT board (ch4)





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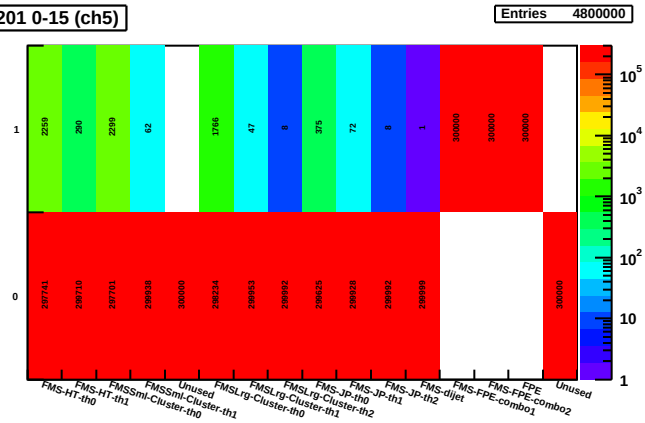

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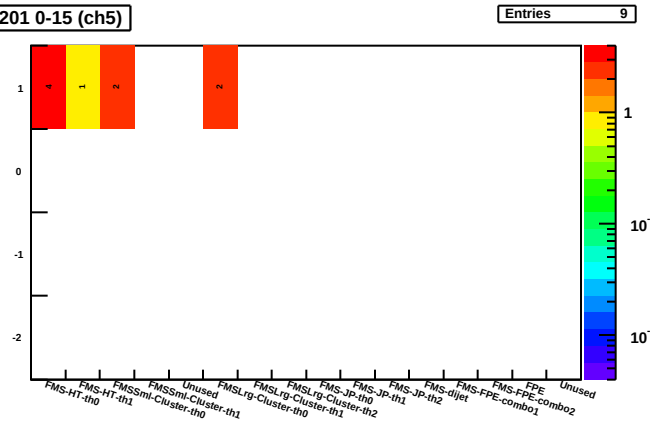

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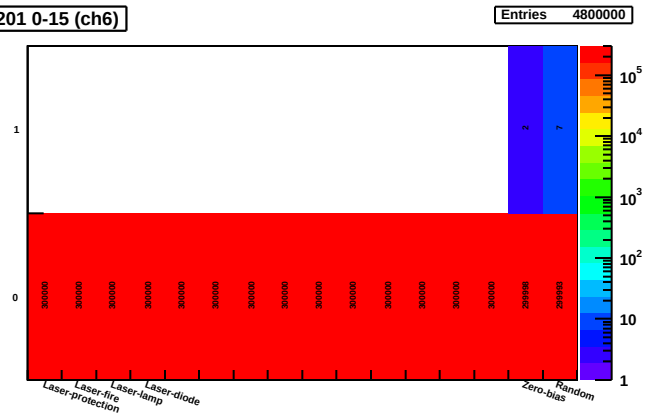

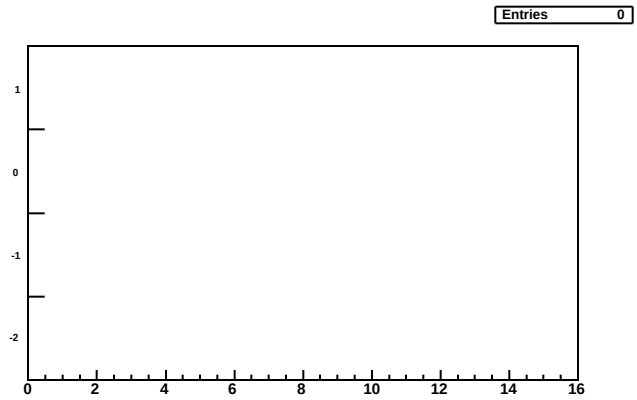
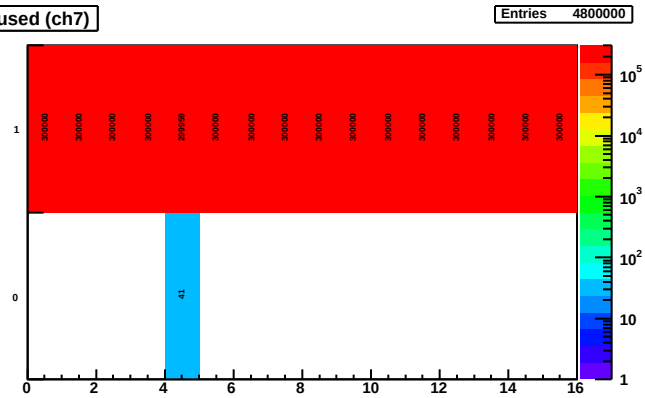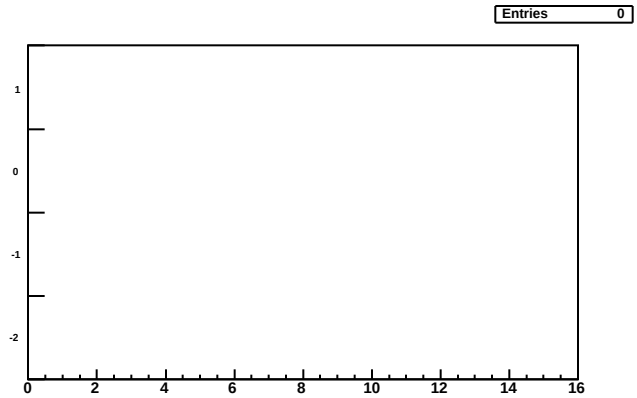
Unused (ch7)