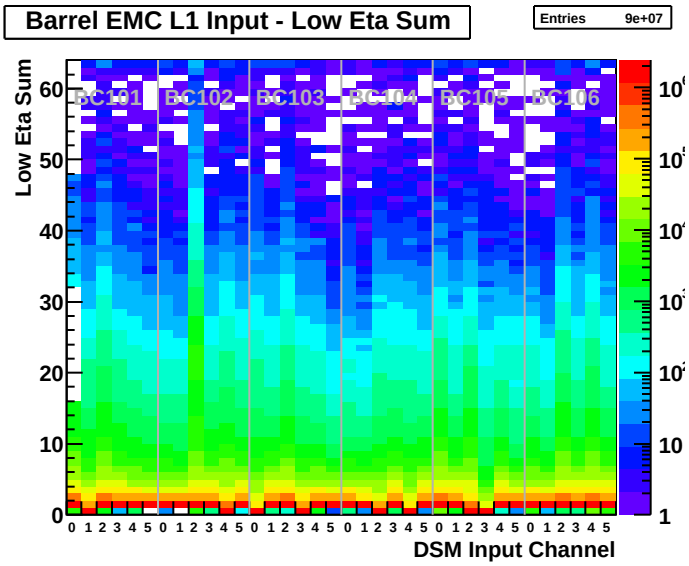
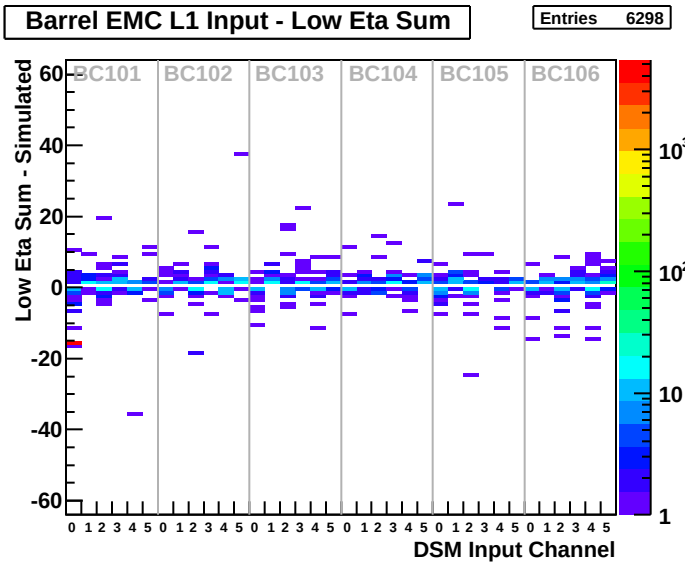
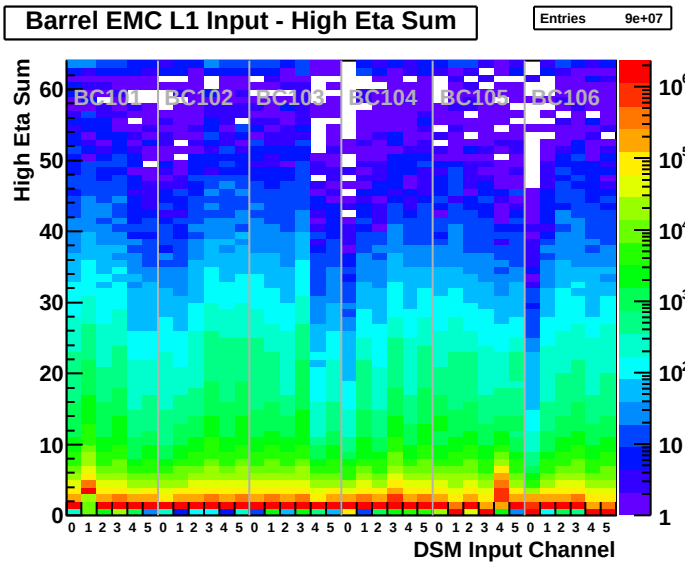
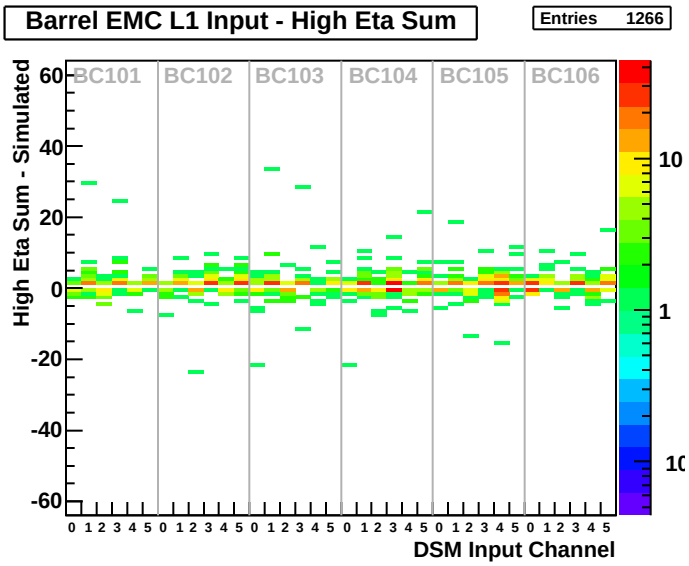
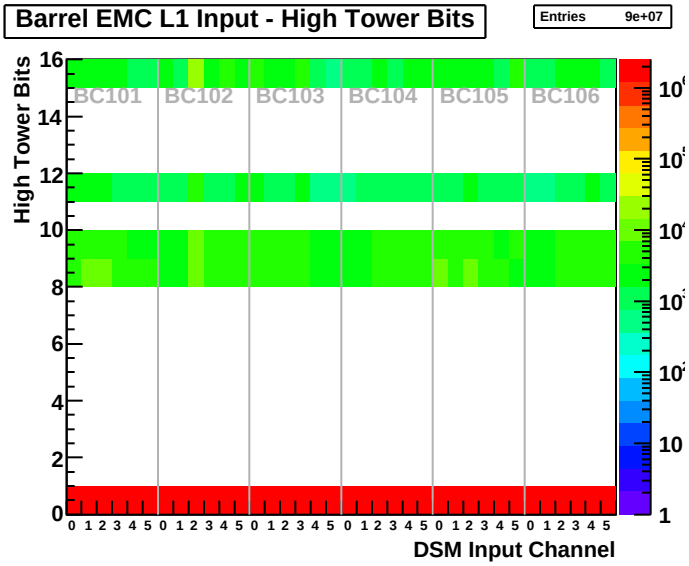
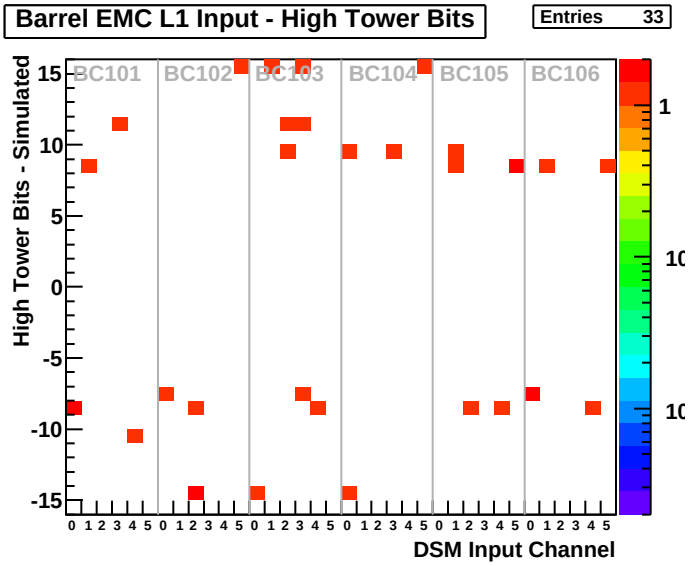

Barrel EMC L1 Input - Low Eta Sum
Entries 9e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

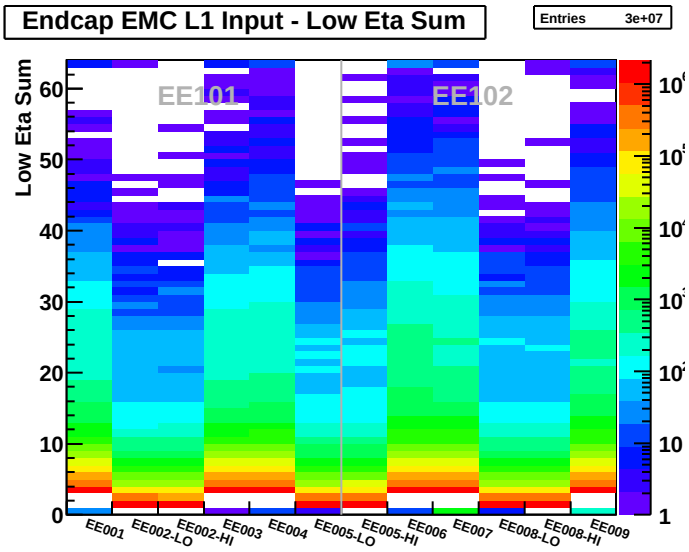

Barrel EMC L1 Input - Low Eta Sum
Entries 6298
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

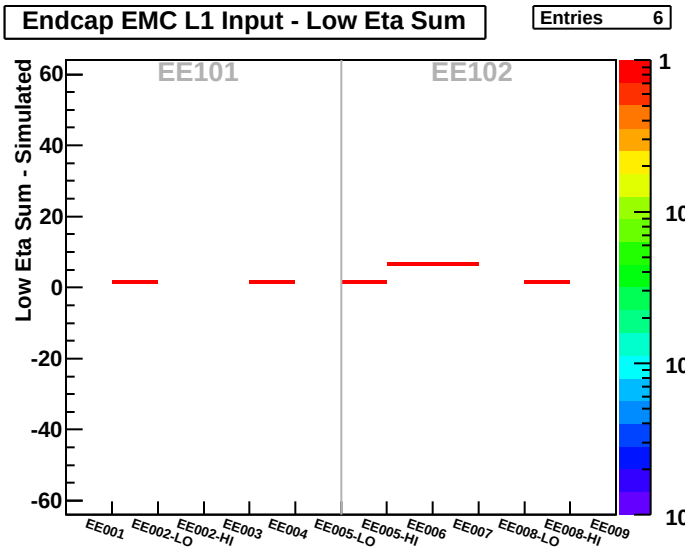

Barrel EMC L1 Input - High Eta Sum
Entries 9e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

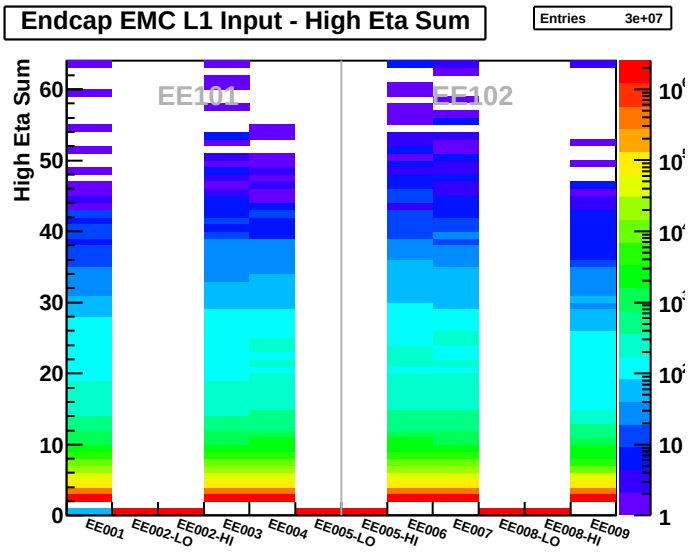

Barrel EMC L1 Input - High Eta Sum
Entries 1266
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

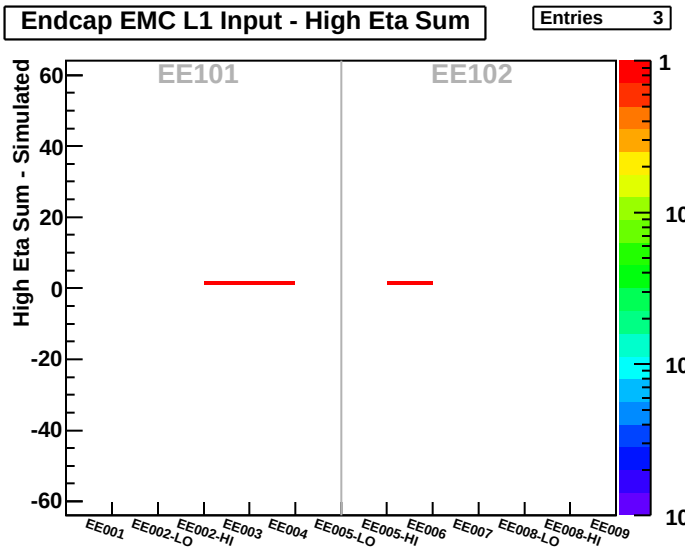

Barrel EMC L1 Input - High Tower Bits
Entries 9e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

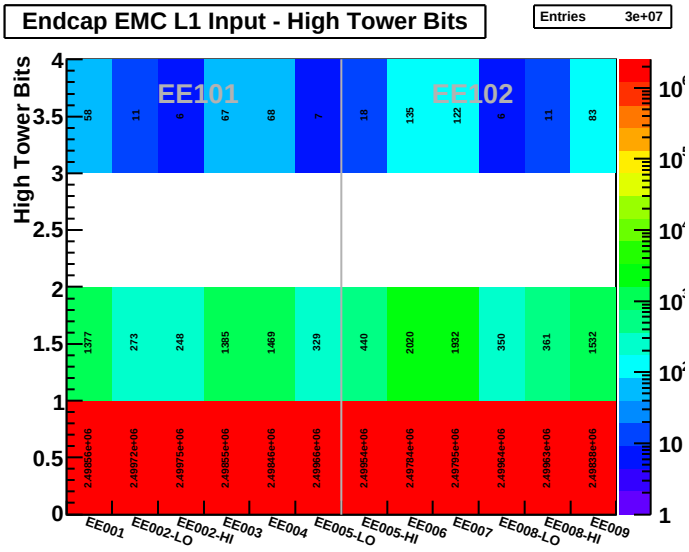

Barrel EMC L1 Input - High Tower Bits
Entries 33
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

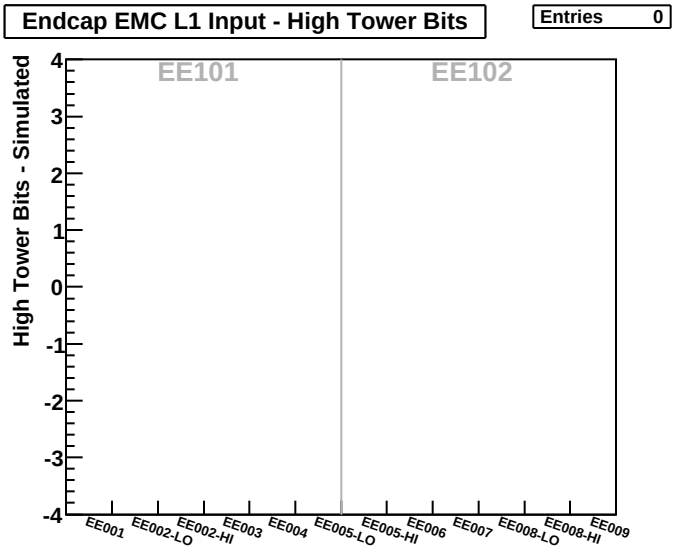

Endcap EMC L1 Input - Low Eta Sum
Entries 3e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

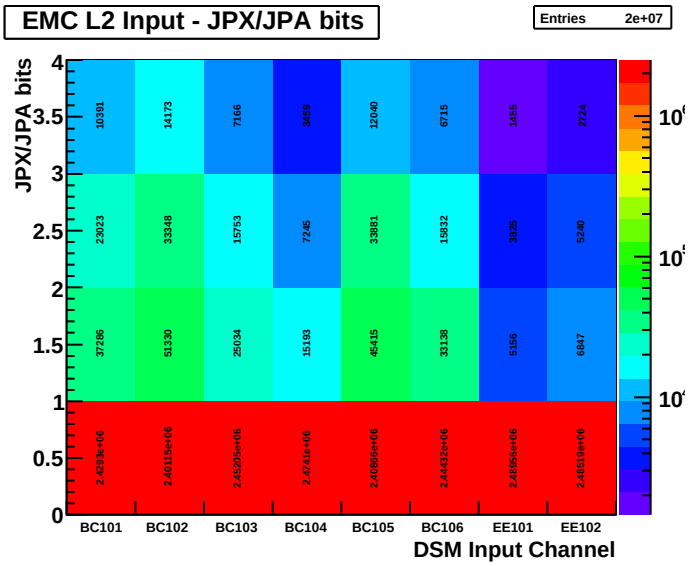

Endcap EMC L1 Input - Low Eta Sum
Entries 6
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

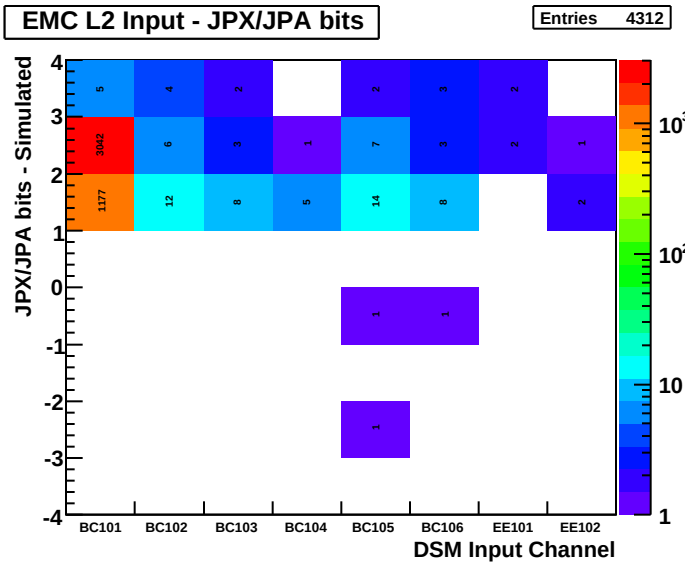

Endcap EMC L1 Input - High Eta Sum
Entries 3e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

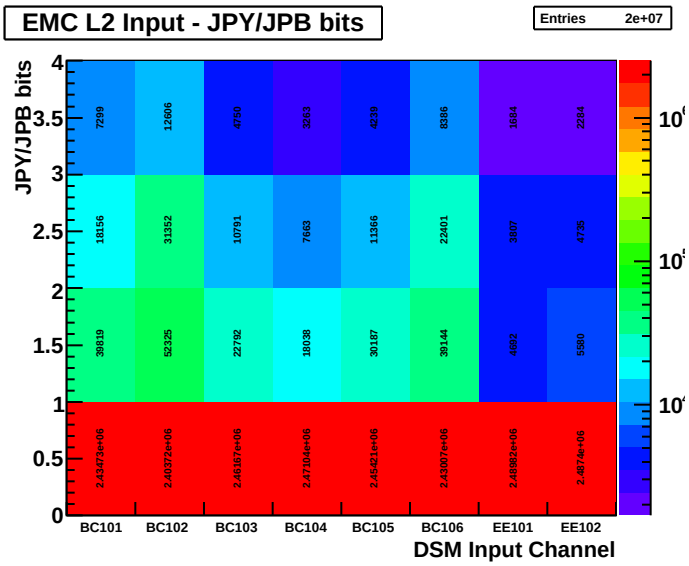

Endcap EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

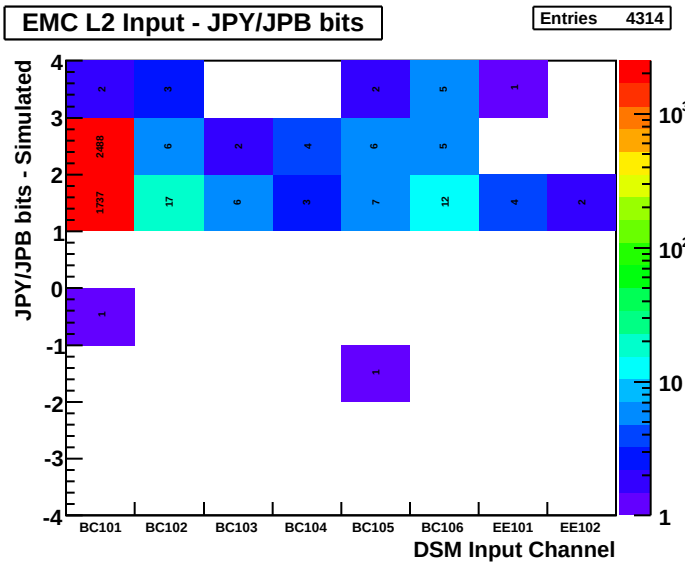

Endcap EMC L1 Input - High Tower Bits
Entries 3e+07
High Tower Bits
EE101
EE102
58 11 6 67 68 7 18 135 122 6 11 83
1377 273 248 1385 1469 329 440 2020 1932 350 361 1532
2.49855e+06 2.49872e+06 2.49975e+06 2.49855e+06 2.49846e+06 2.49966e+06 2.49954e+06 2.49784e+06 2.49795e+06 2.49964e+06 2.49963e+06 2.49839e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

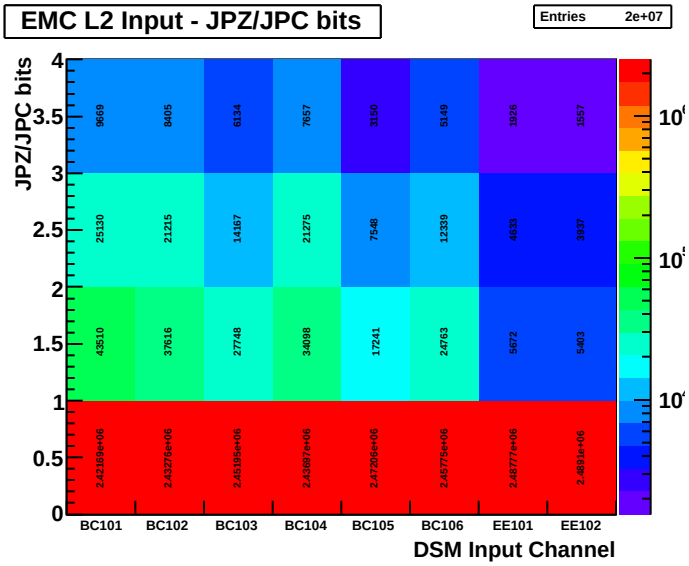

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

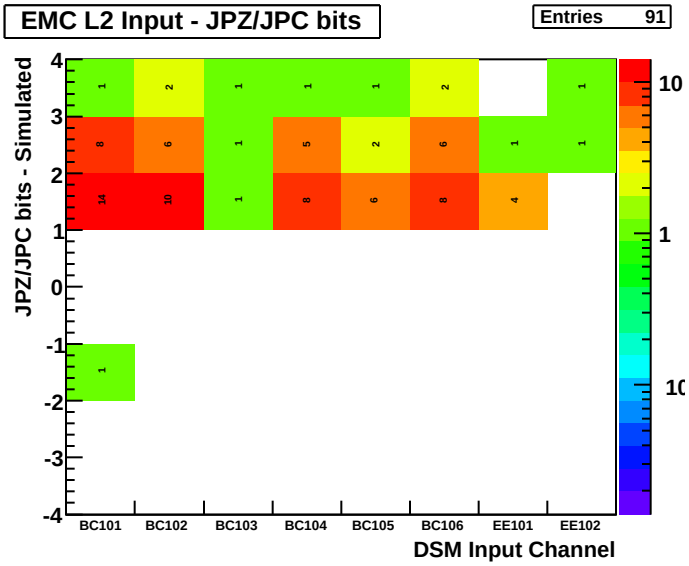

EMC L2 Input - JPX/JPA bits
Entries 2e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

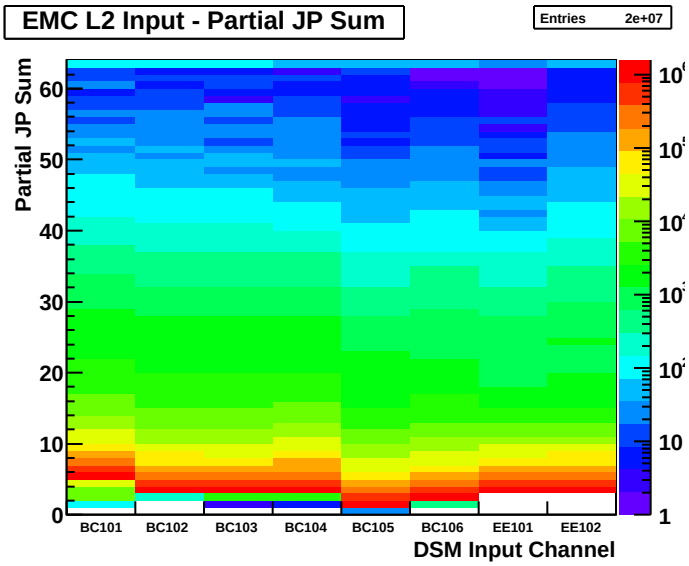

EMC L2 Input - JPX/JPA bits
Entries 4312
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

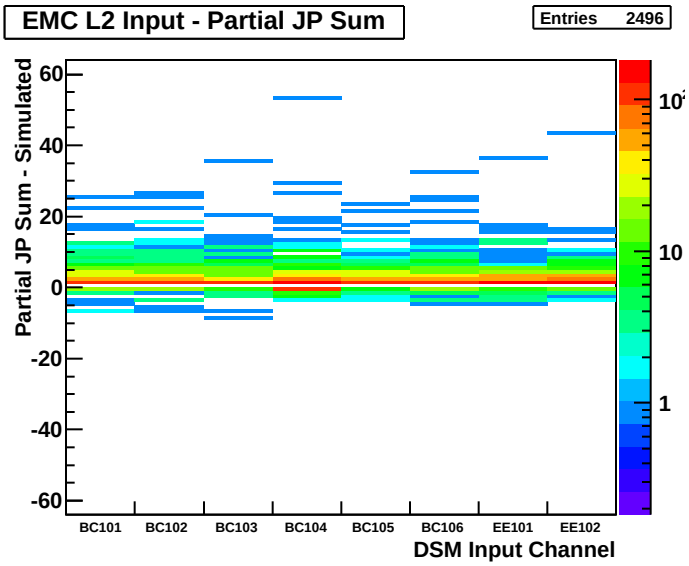

EMC L2 Input - JPY/JPB bits
Entries 2e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

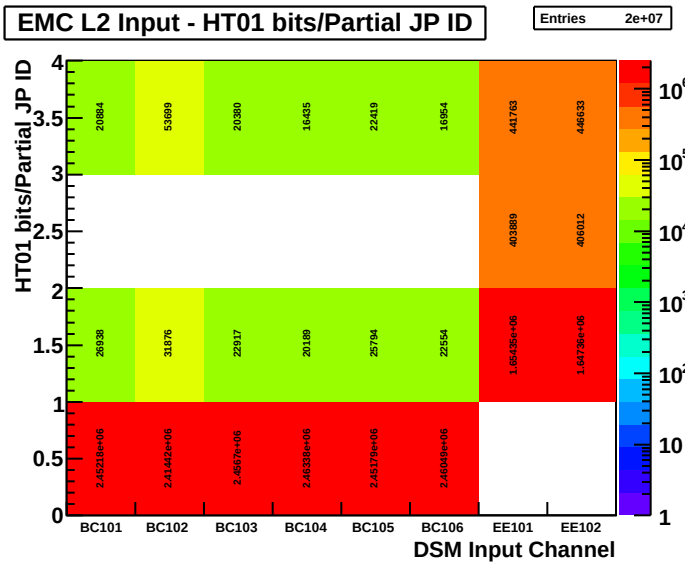

EMC L2 Input - JPY/JPB bits
Entries 4314
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

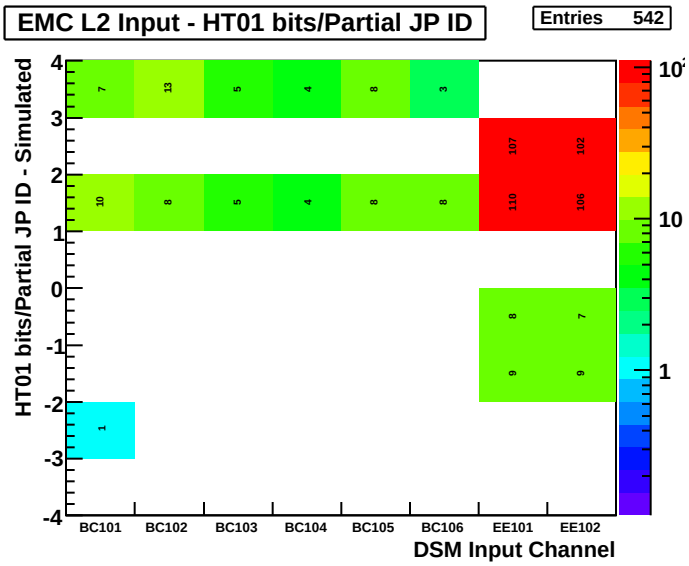

EMC L2 Input - JPZ/JPC bits
Entries 2e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

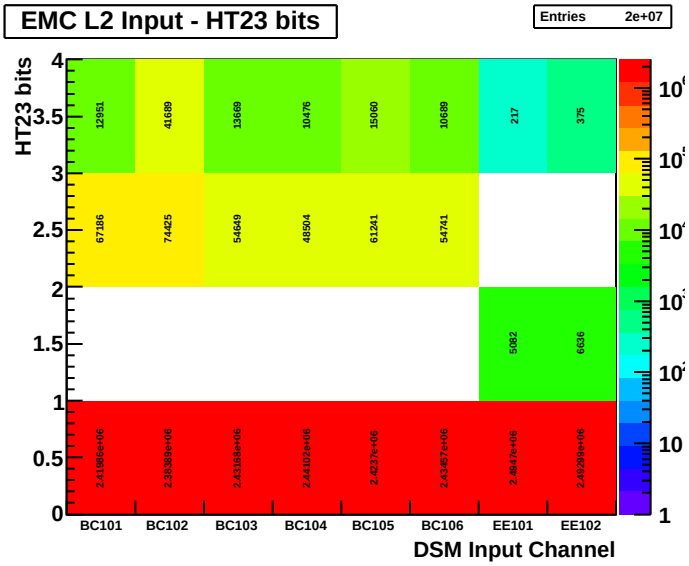

EMC L2 Input - JPZ/JPC bits
Entries 91
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

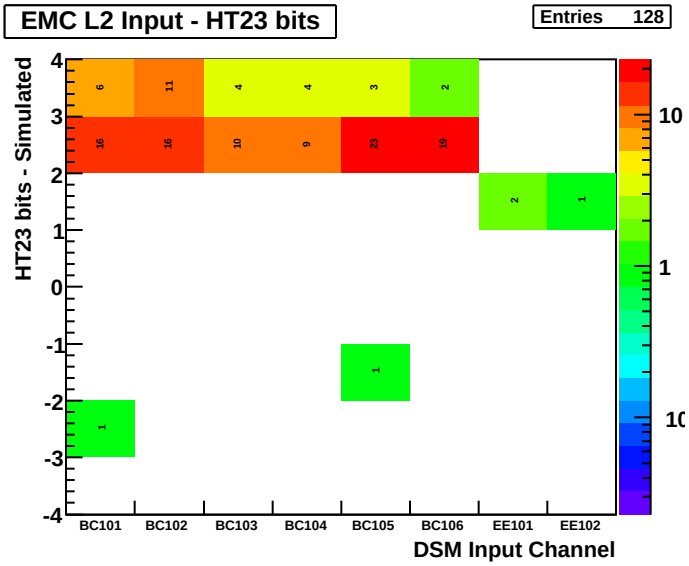

EMC L2 Input - Partial JP Sum
Entries 2e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

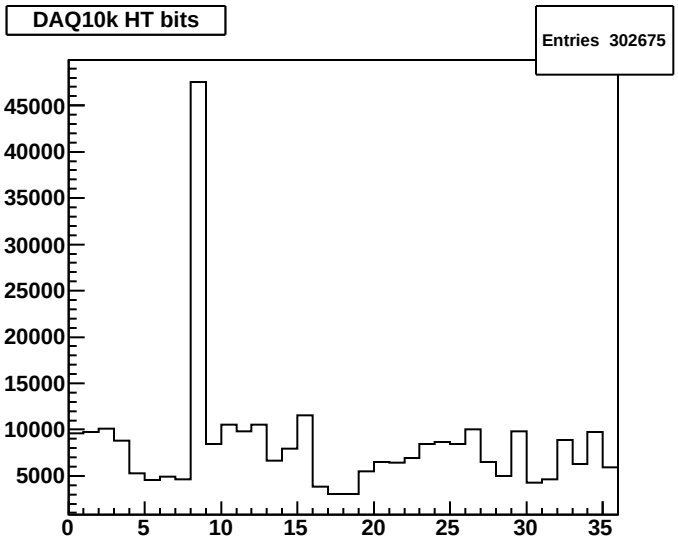

EMC L2 Input - Partial JP Sum
Entries 2496
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

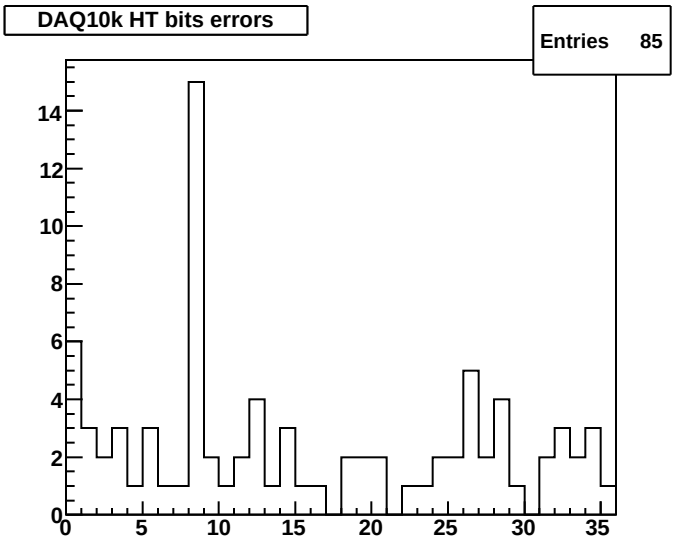

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

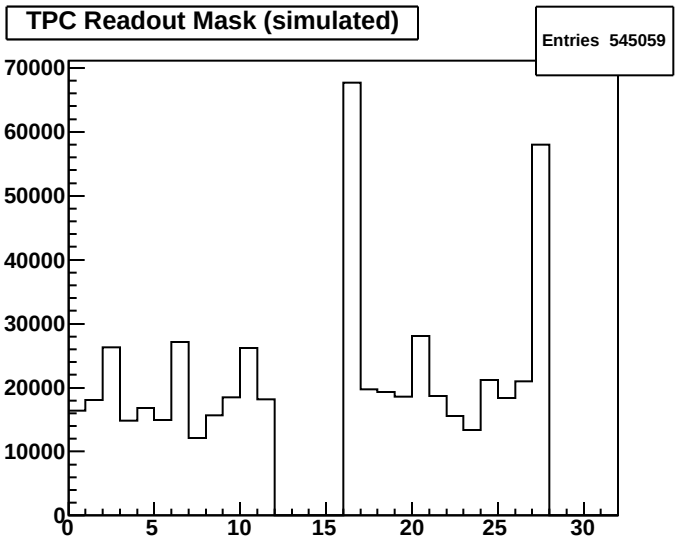

EMC L2 Input - HT01 bits/Partial JP ID
Entries 542
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

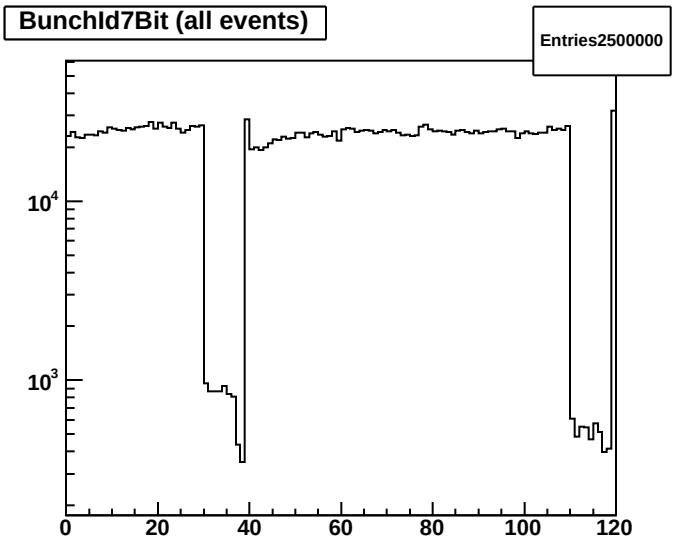

EMC L2 Input - HT23 bits
Entries 2e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

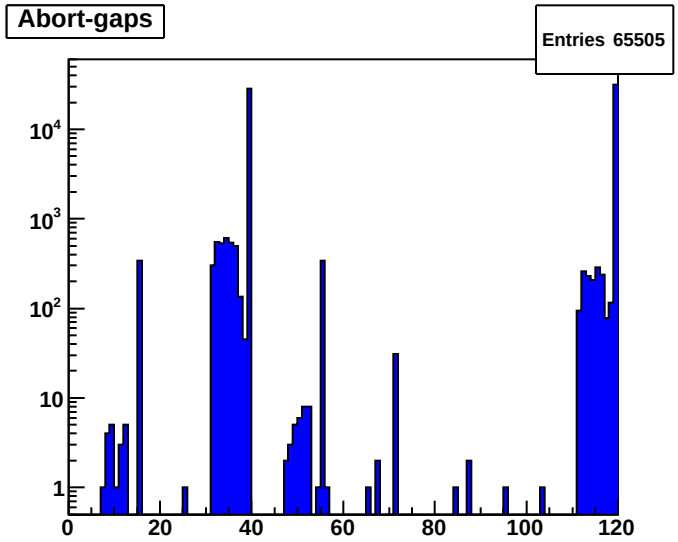

EMC L2 Input - HT23 bits
Entries 128
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

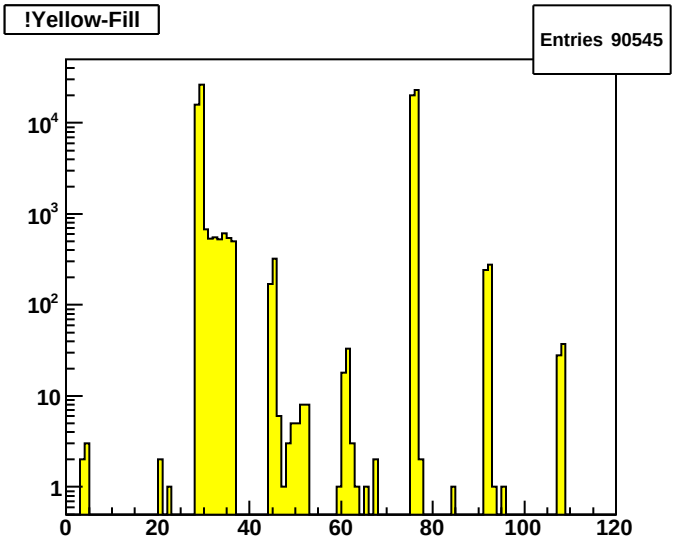

DAQ10k HT bits
Entries 302675

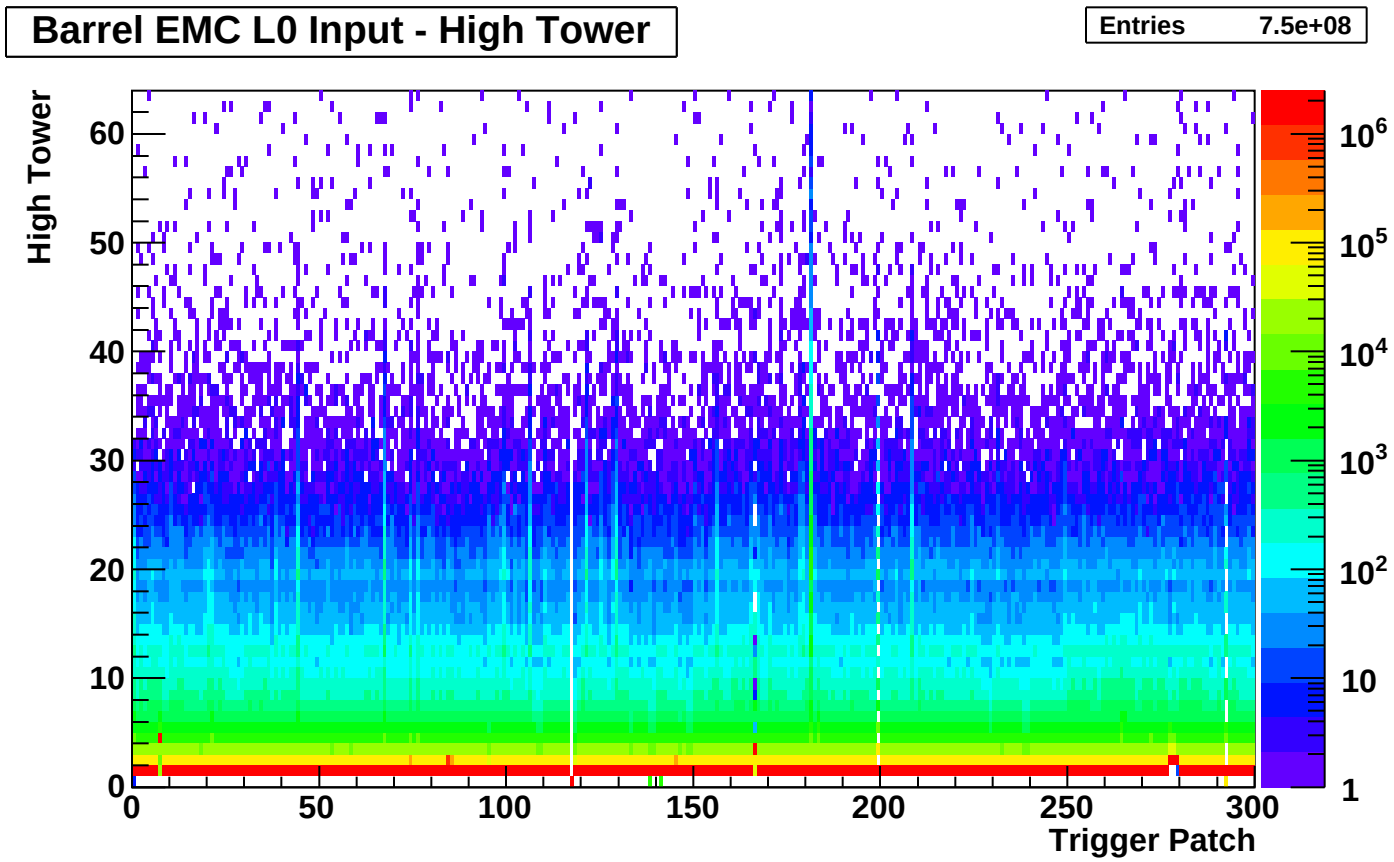

DAQ10k HT bits errors
Entries 85

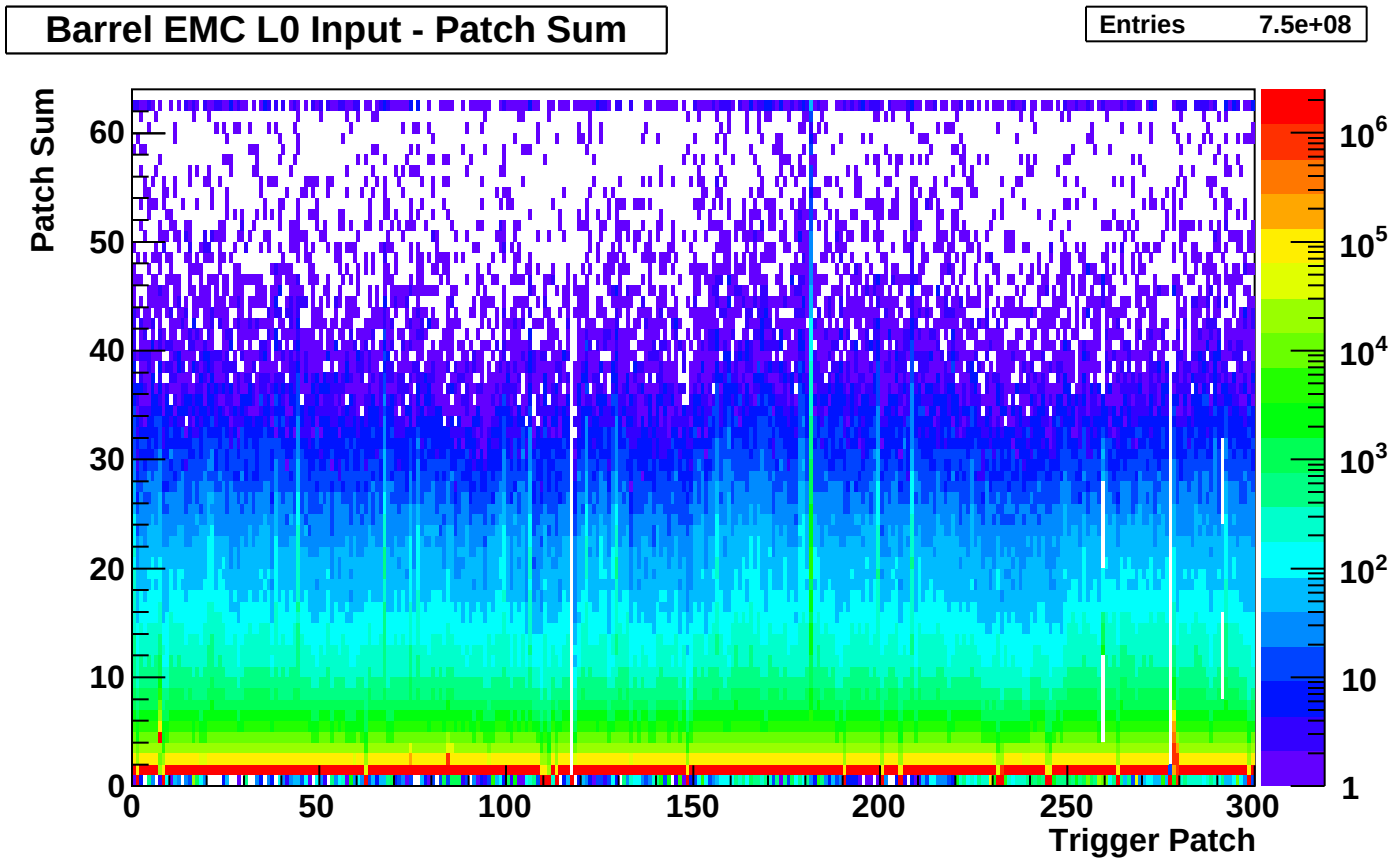

TPC Readout Mask (simulated)
Entries 545059

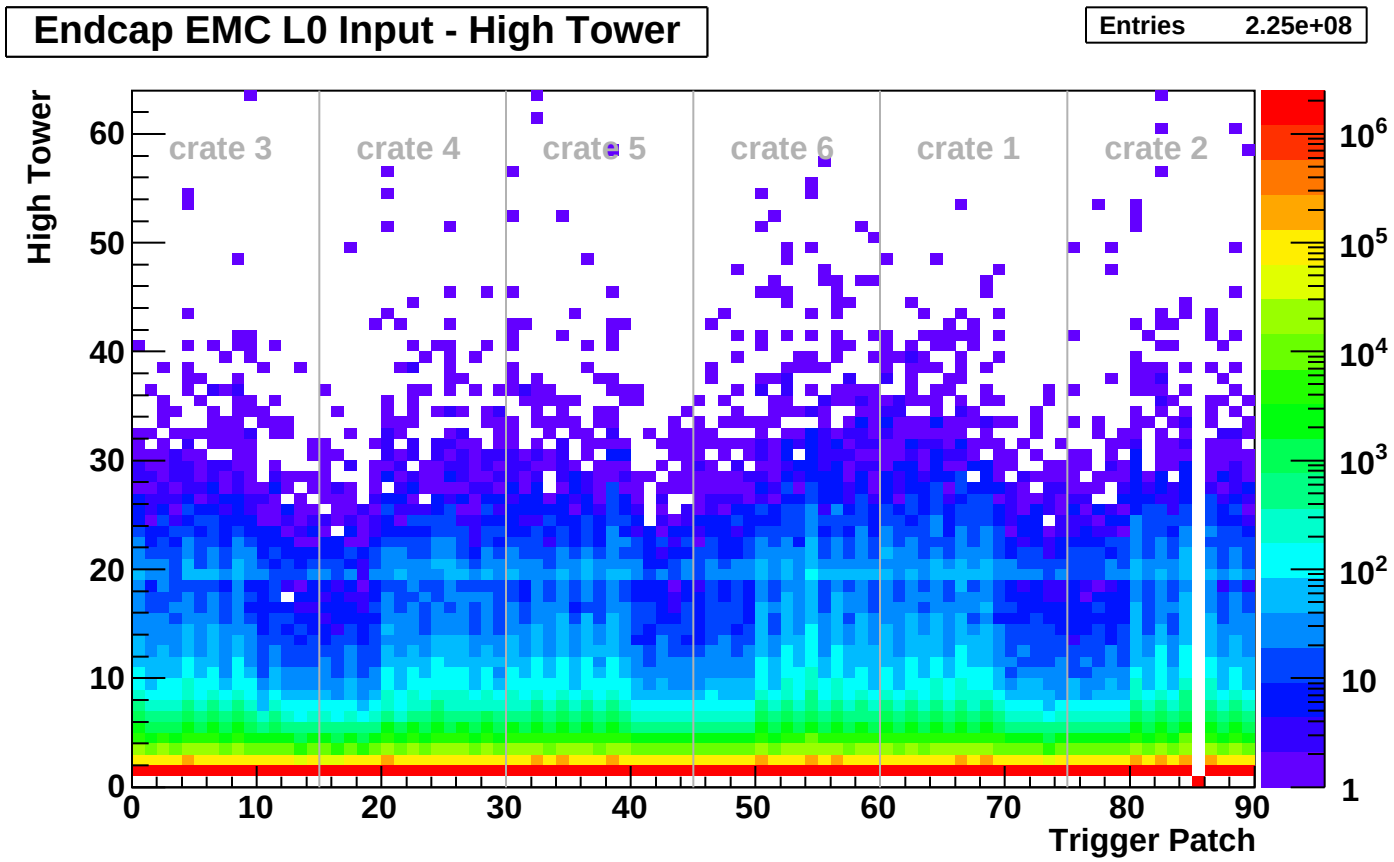

BunchId7Bit (all events)
Entries2500000

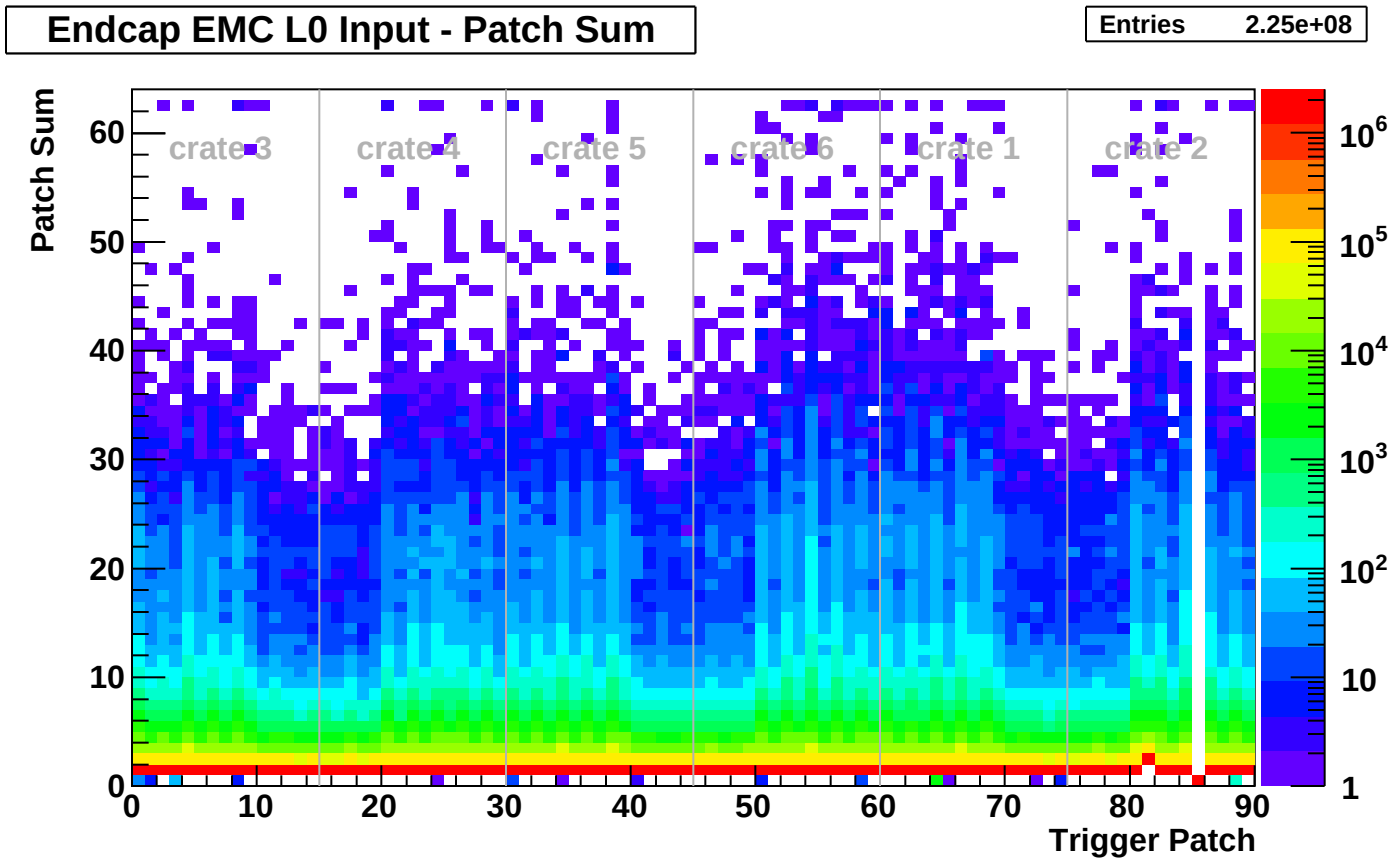

Abort-gaps
Entries 65505

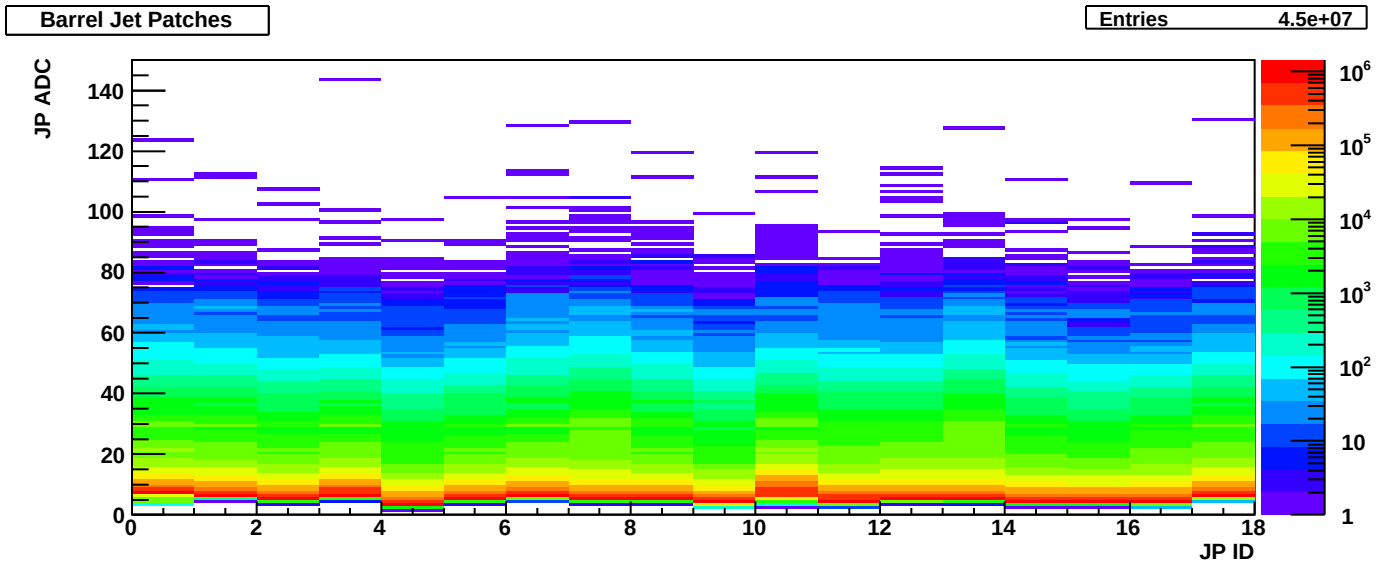

!Yellow-Fill
Entries 90545

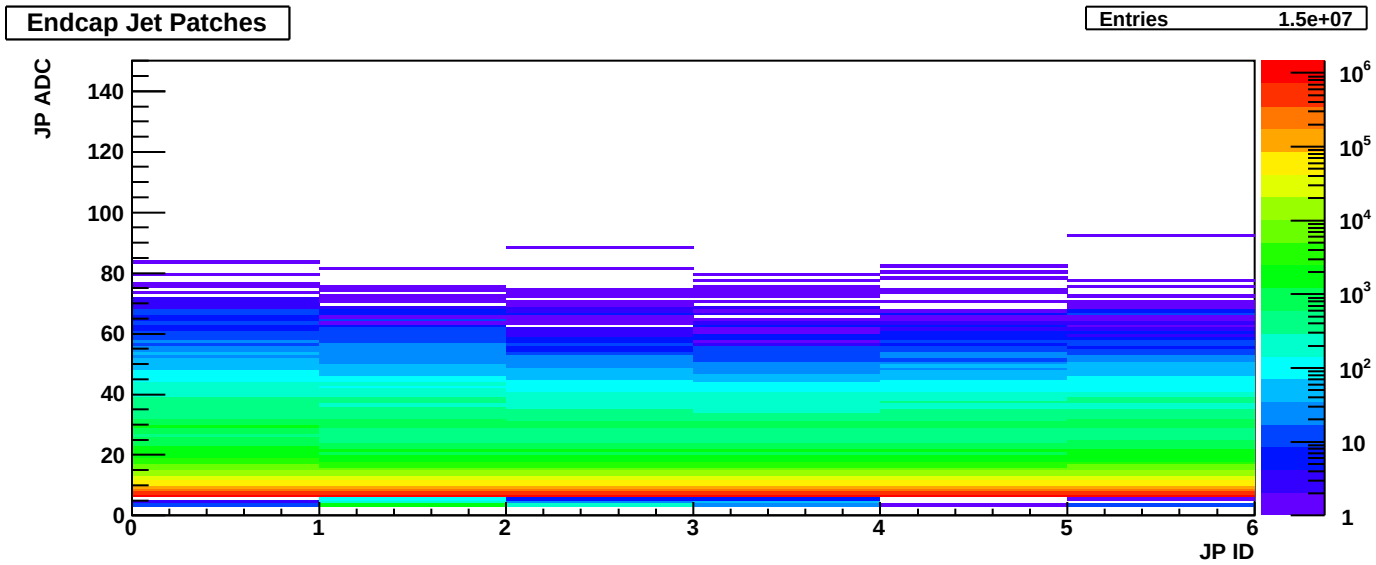

Barrel EMC L0 Input - High Tower
Entries 7.5e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

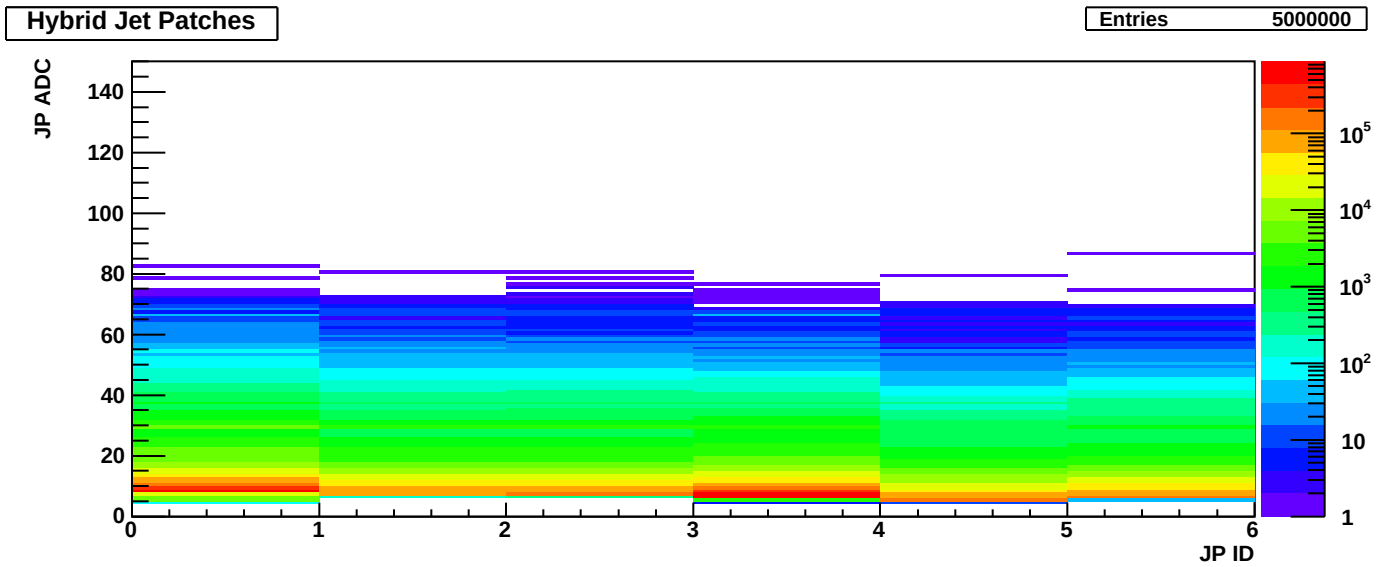

Barrel EMC L0 Input - Patch Sum
Entries 7.5e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

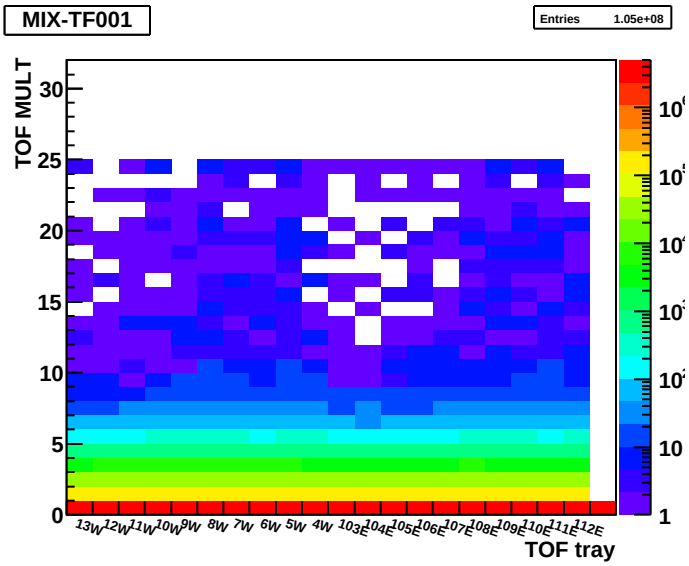

Endcap EMC L0 Input - High Tower
Entries 2.25e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

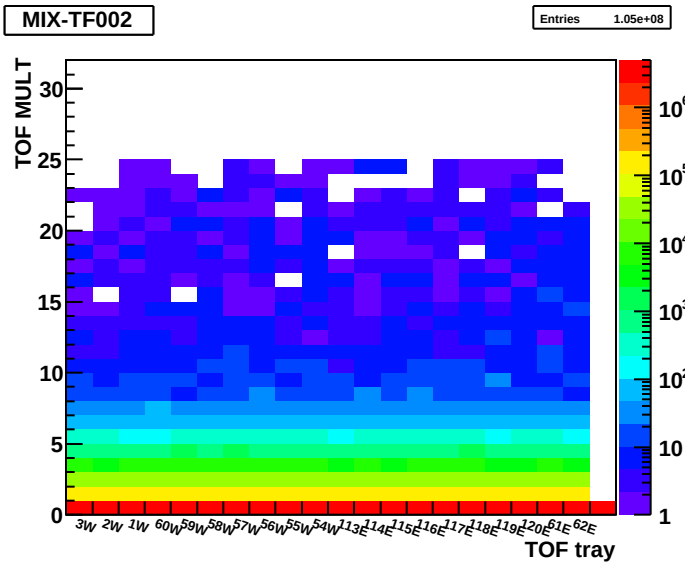

Endcap EMC L0 Input - Patch Sum
Entries 2.25e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

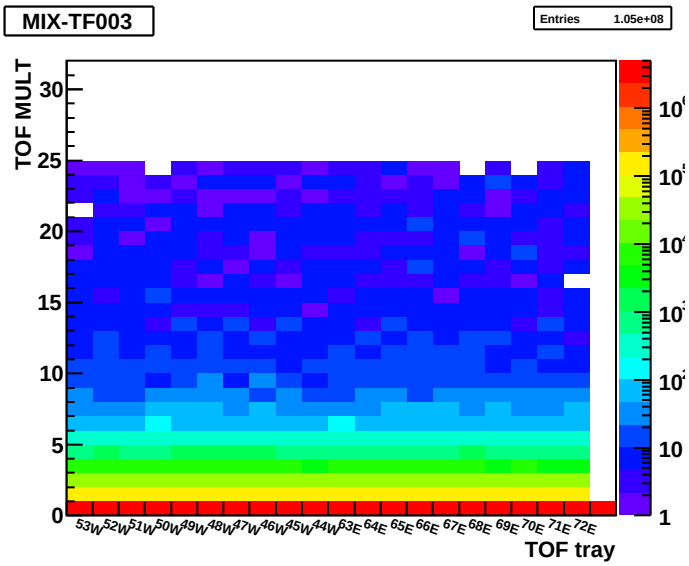

Barrel Jet Patches
Entries 4.5e+07
JP ADC
JP ID

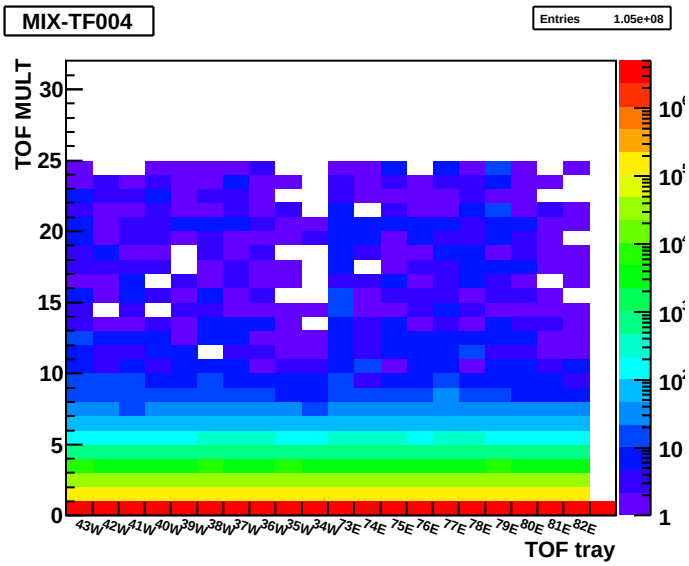

Endcap Jet Patches
Entries 1.5e+07
JP ADC
JP ID

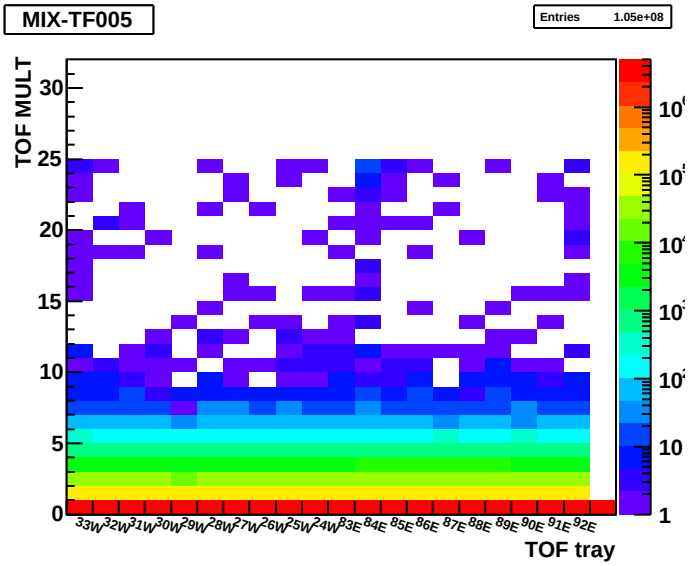

Hybrid Jet Patches
Entries 5000000
JP ADC
JP ID

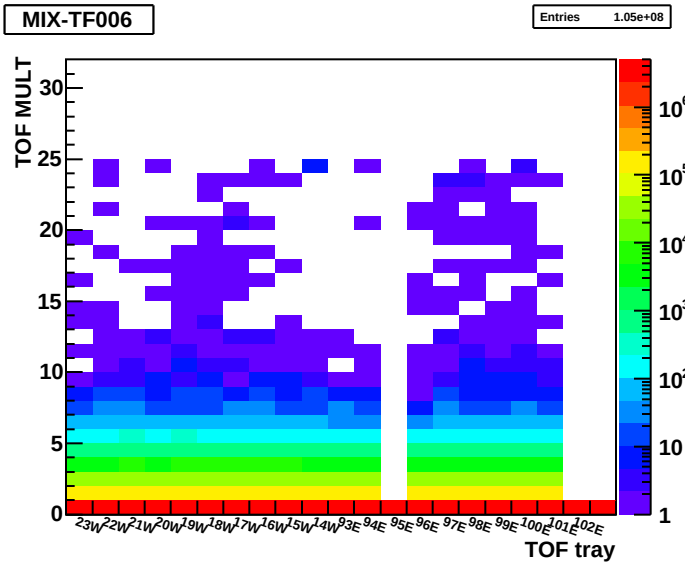

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

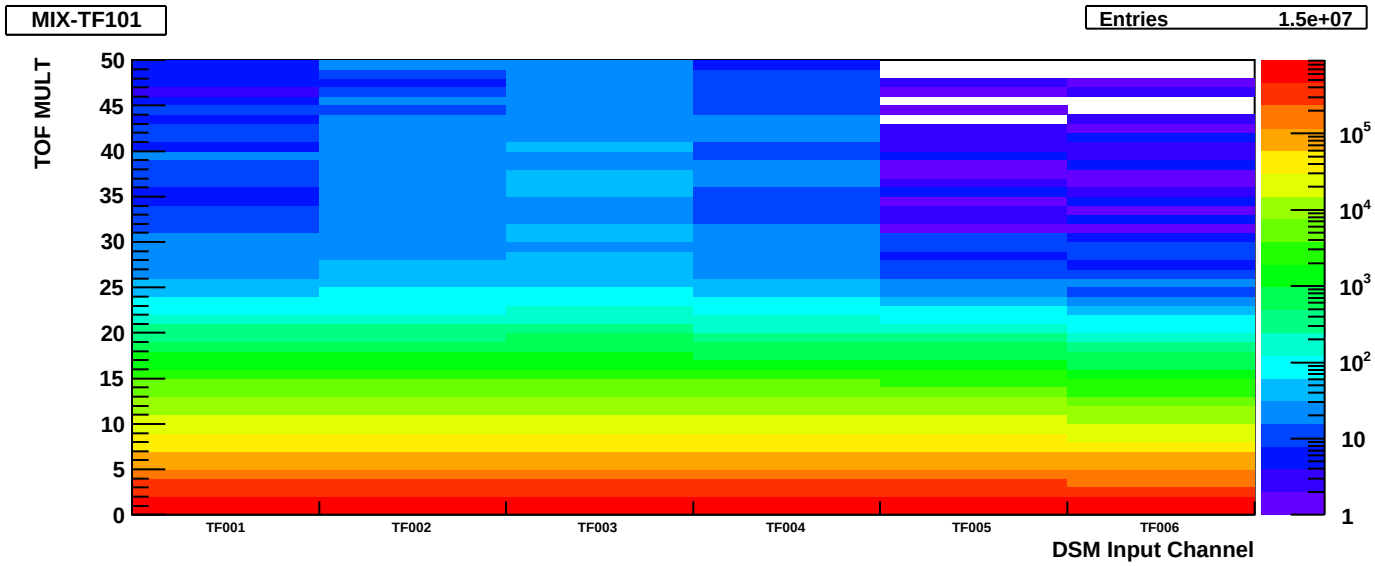

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

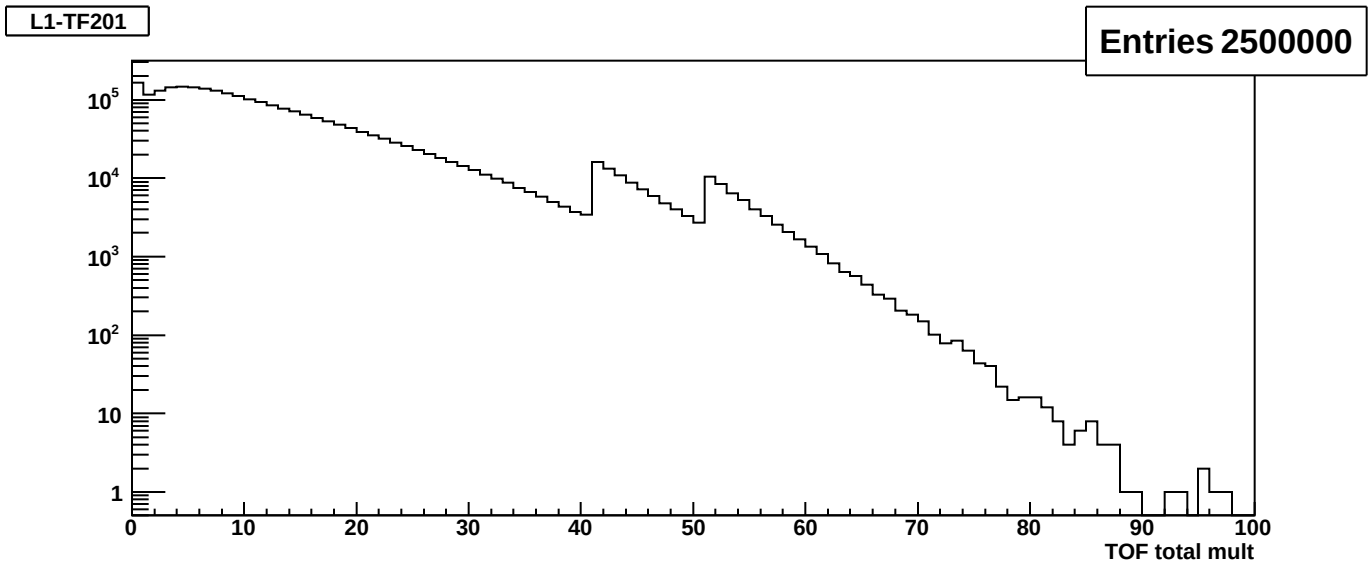

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

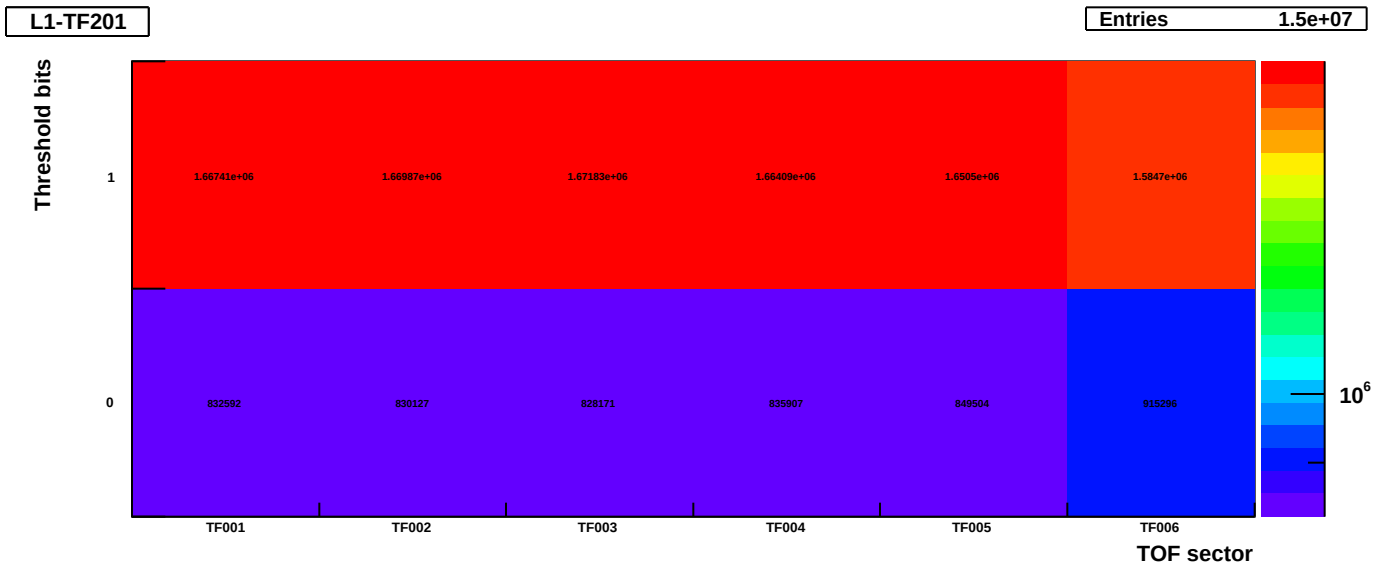

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

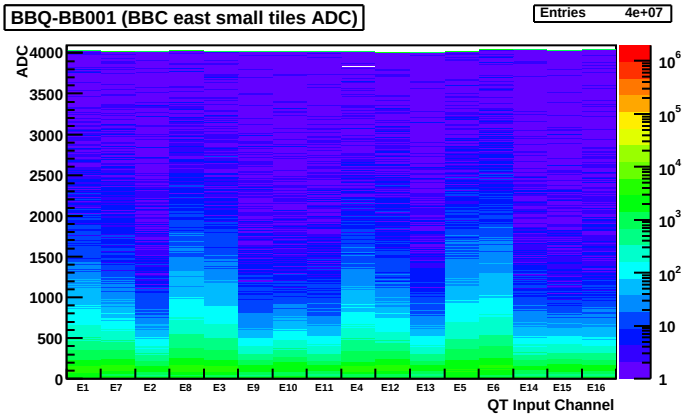

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

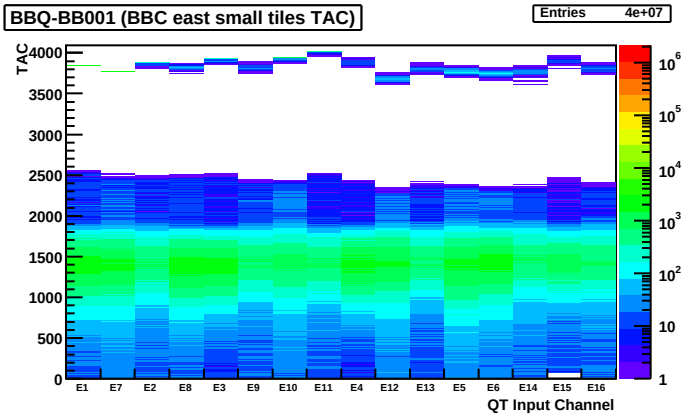

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

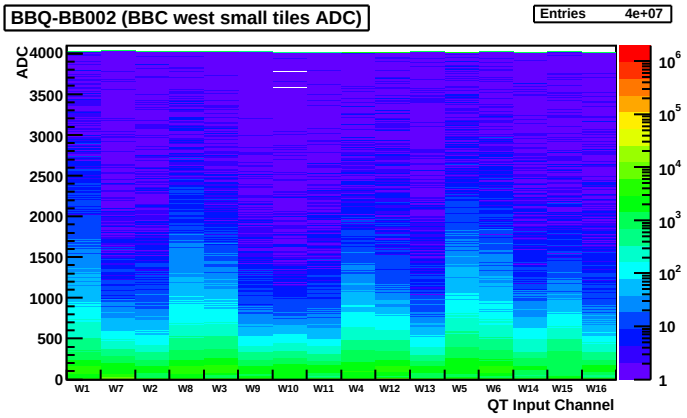

MIX-TF101
Entries 1.5e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

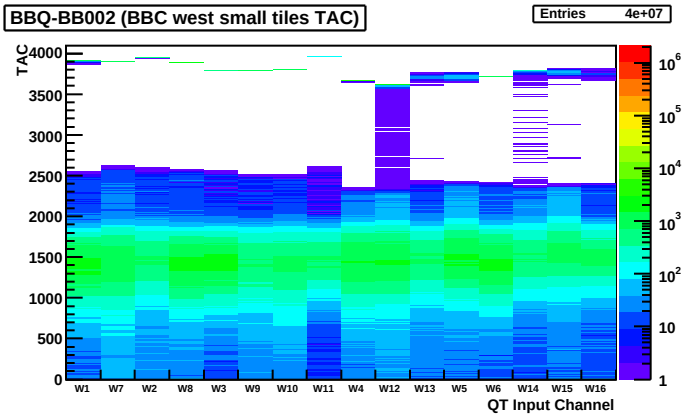

L1-TF201
Entries 2500000
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

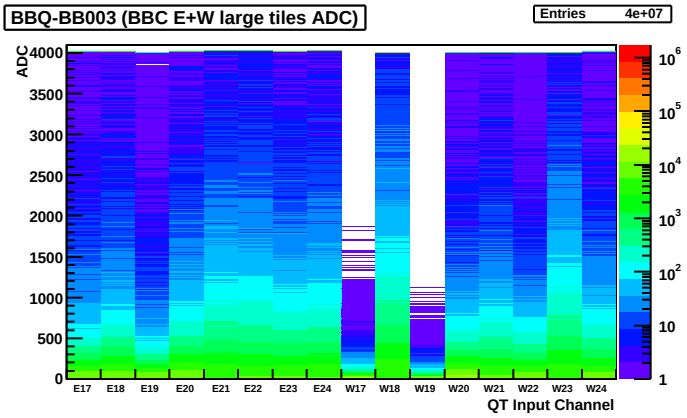

L1-TF201
Entries 1.5e+07
Threshold bits
1
0
1.66741e+06
1.66987e+06
1.67183e+06
1.66409e+06
1.6505e+06
1.5847e+06
832592
830127
828171
835907
849504
915296
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

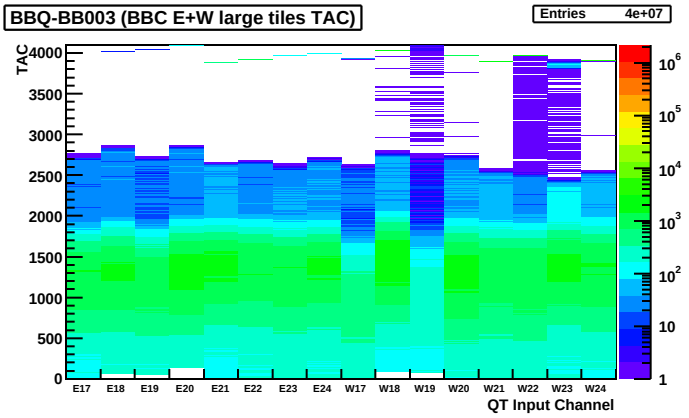

BBQ-BB001 (BBC east small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

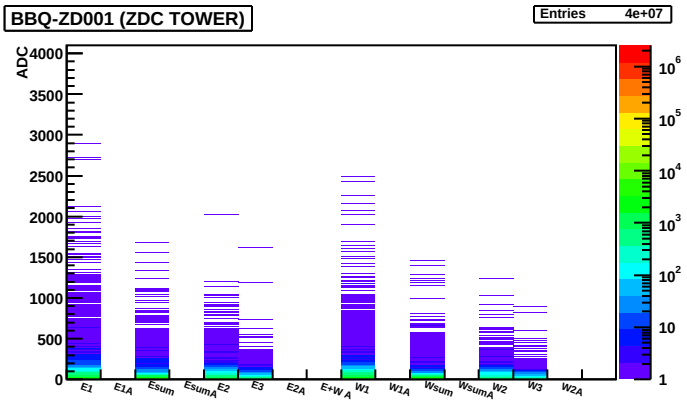

BBQ-BB001 (BBC east small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

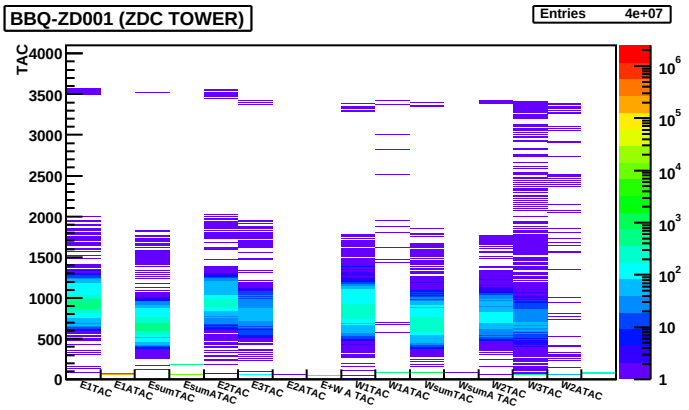

BBQ-BB002 (BBC west small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

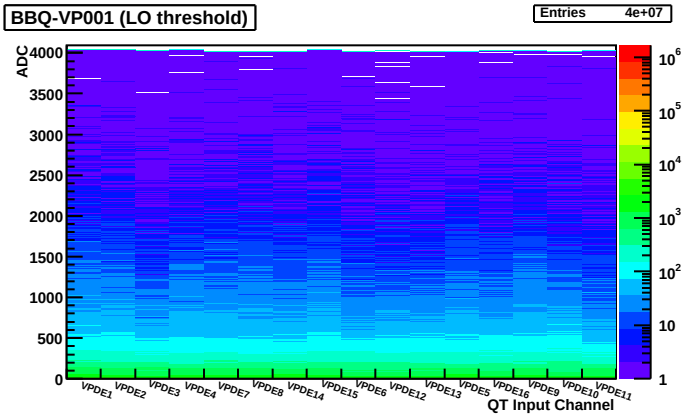

BBQ-BB002 (BBC west small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

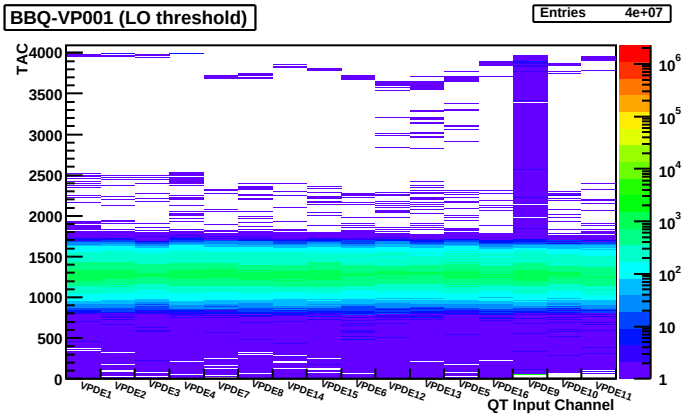

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4e+07
ADC
QT Input Channel

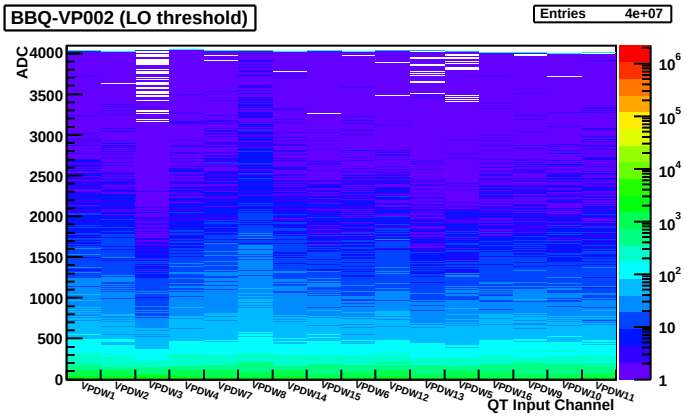

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4e+07
TAC
QT Input Channel

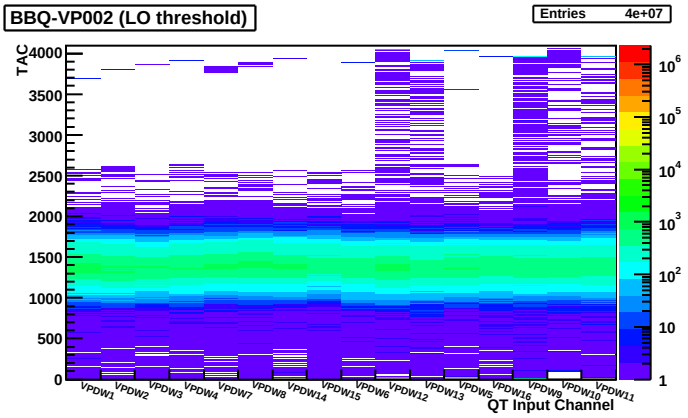

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
ADC

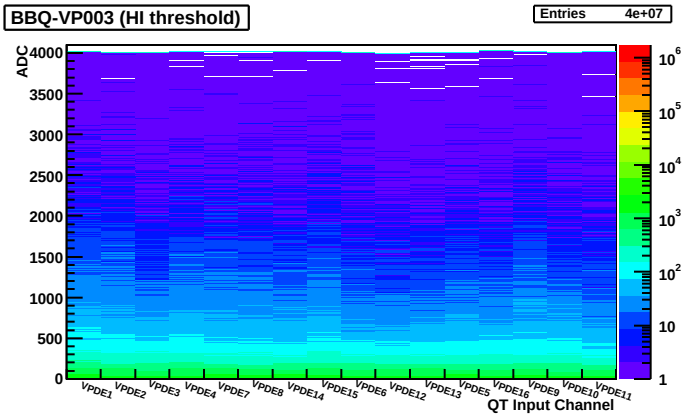

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
TAC

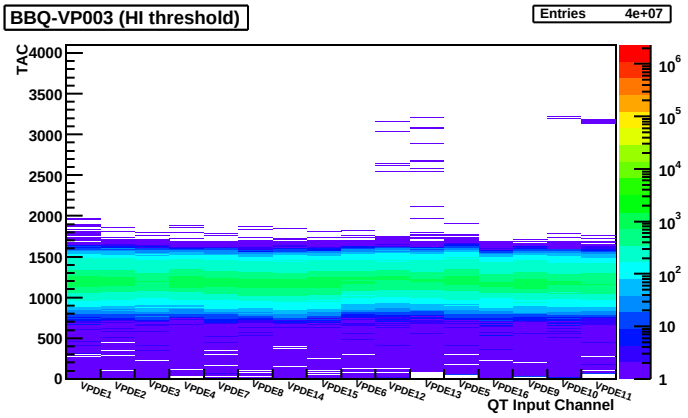

BBQ-VP001 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

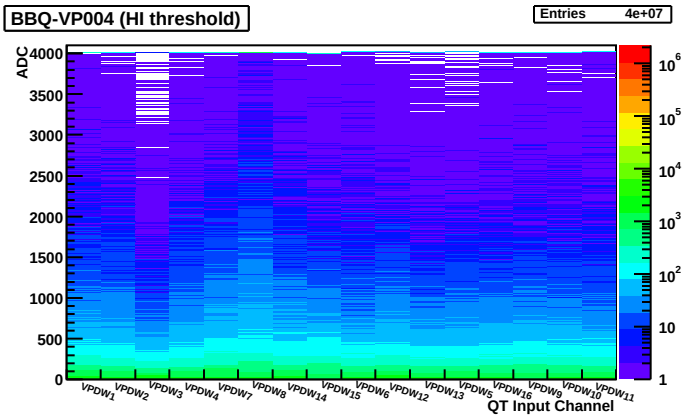

BBQ-VP001 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

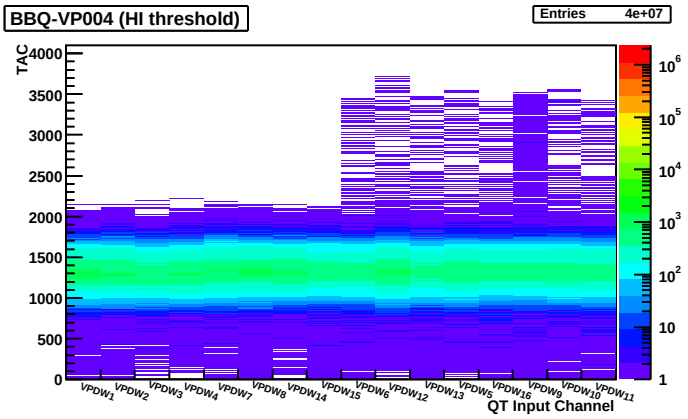

BBQ-VP002 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

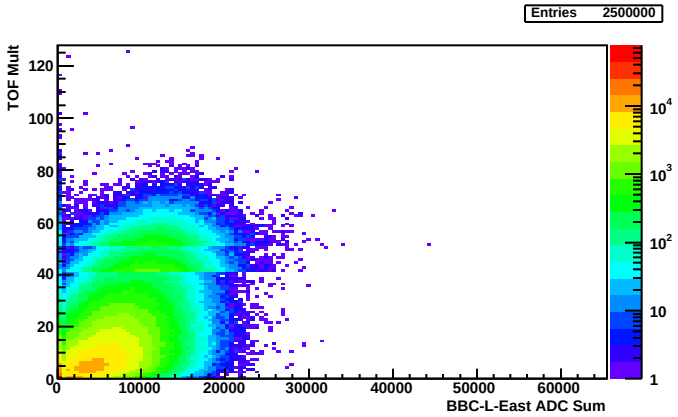

BBQ-VP002 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

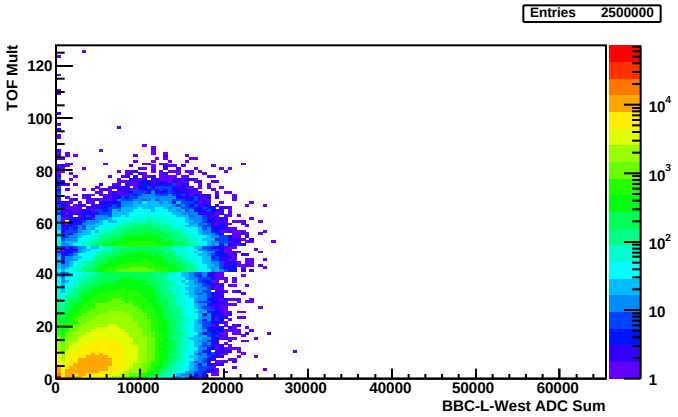

BBQ-VP003 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

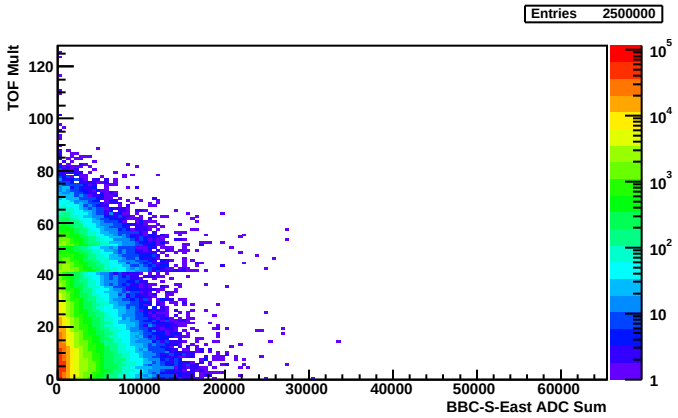

BBQ-VP003 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

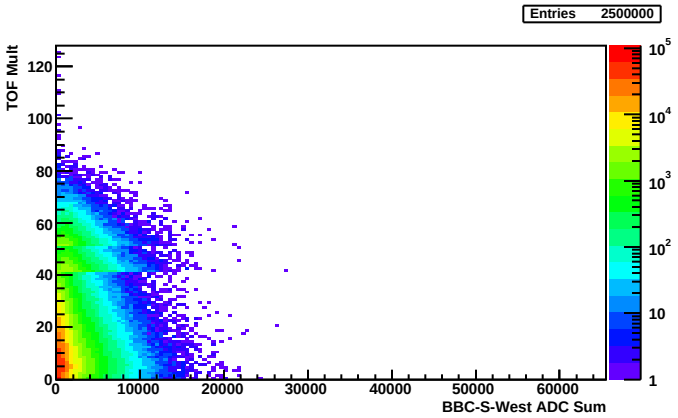

BBQ-VP004 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

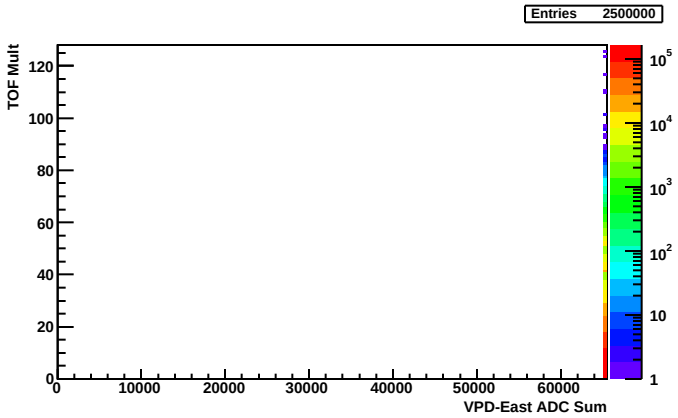

BBQ-VP004 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

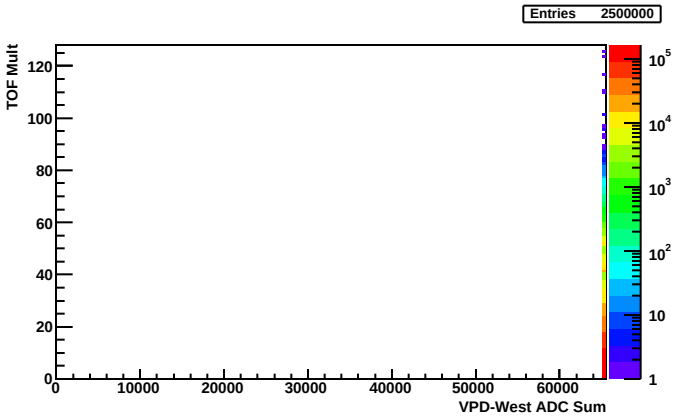

Entries 2500000
TOF Mult
BBC-L-East ADC Sum

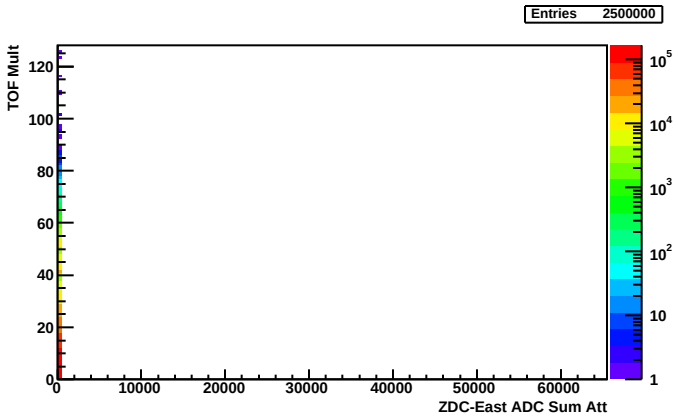

Entries 2500000
TOF Mult
BBC-L-West ADC Sum

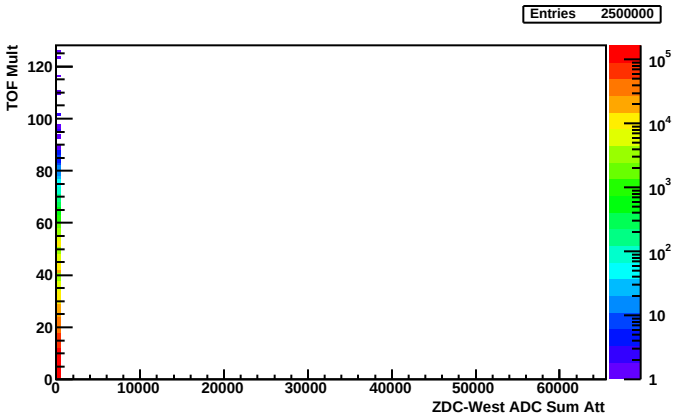

Entries 2500000
TOF Mult
BBC-S-East ADC Sum

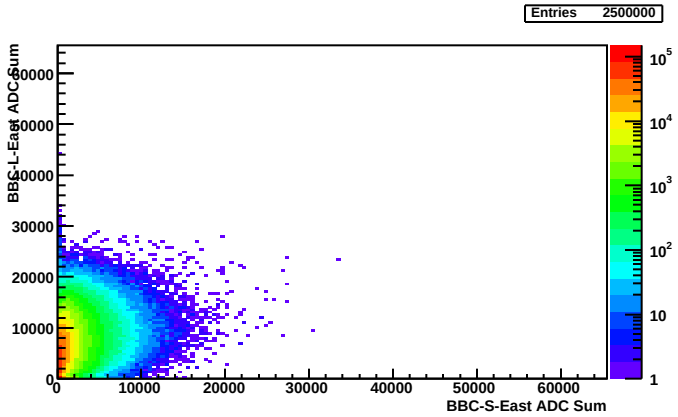

Entries 2500000
TOF Mult
BBC-S-West ADC Sum

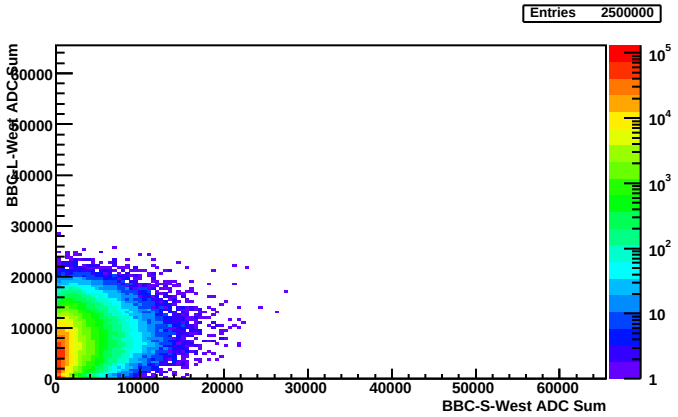

Entries 2500000
TOF Mult
VPD-East ADC Sum

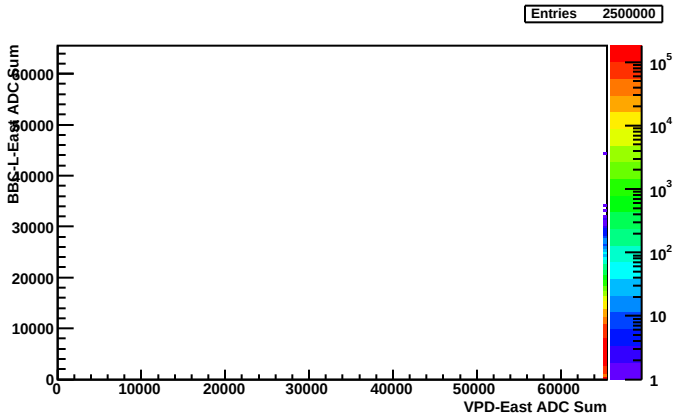

Entries 2500000
TOF Mult
VPD-West ADC Sum

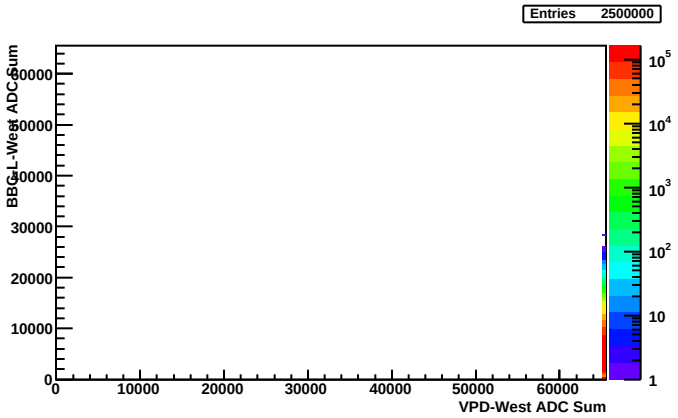

Entries 2500000
TOF Mult
ZDC-East ADC Sum Att

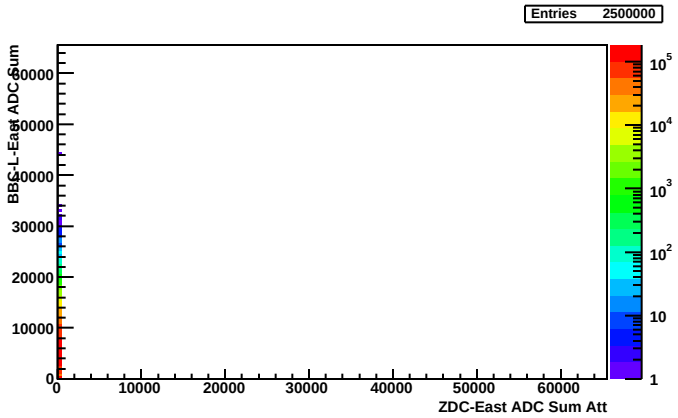

Entries 2500000
TOF Mult
ZDC-West ADC Sum Att

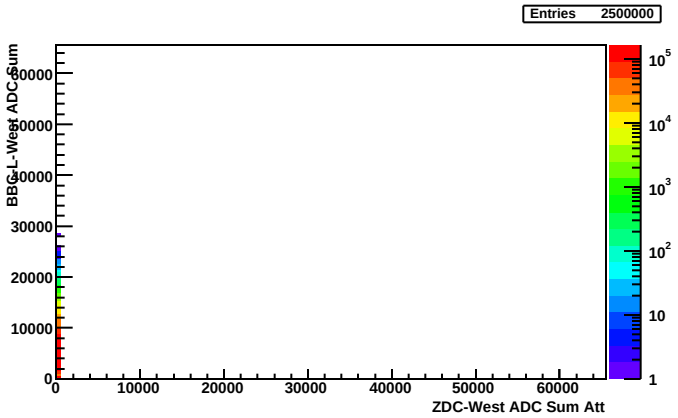

Entries 2500000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

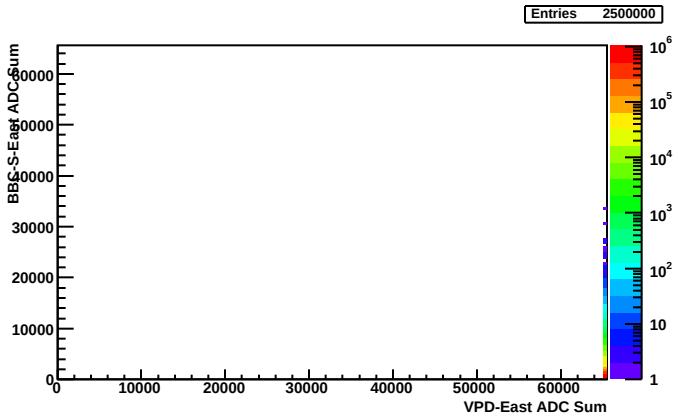

Entries 2500000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

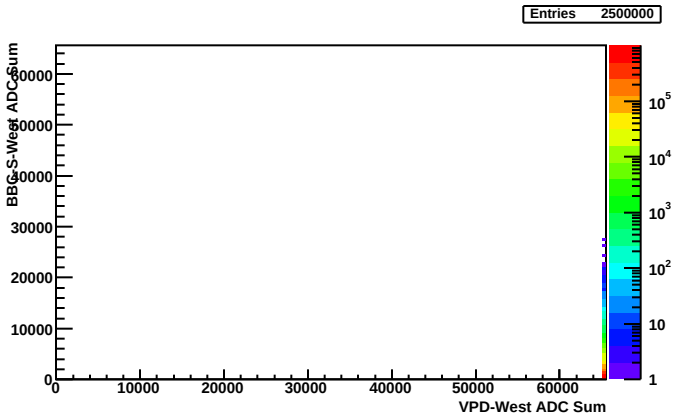

Entries 2500000
BBC-L-East ADC Sum
VPD-East ADC Sum

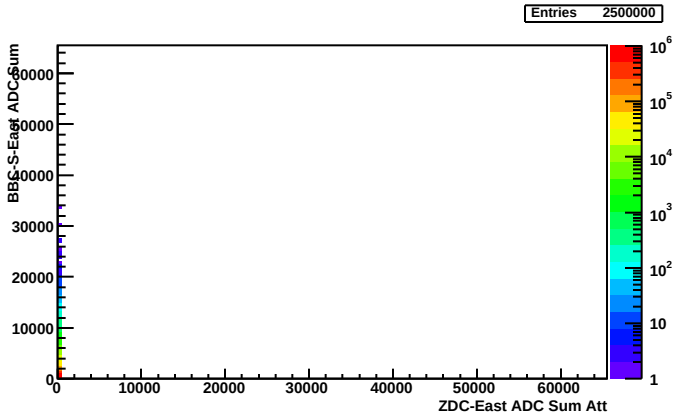

Entries 2500000
BBC-L-West ADC Sum
VPD-West ADC Sum

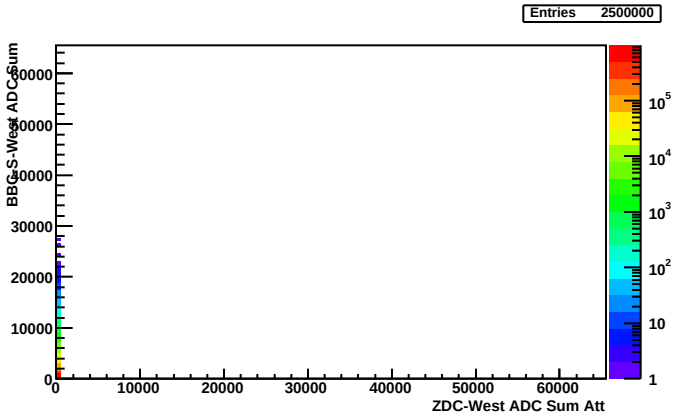

Entries 2500000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

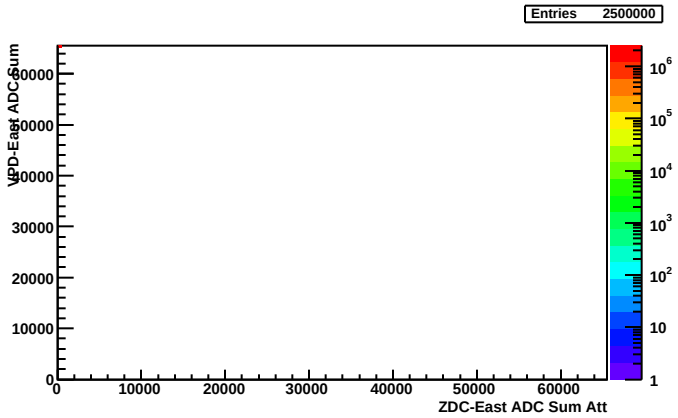

Entries 2500000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

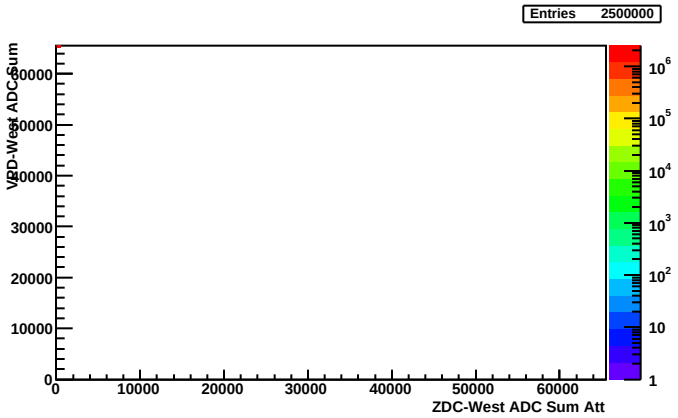

Entries 2500000
BBC-S-East ADC Sum
VPD-East ADC Sum

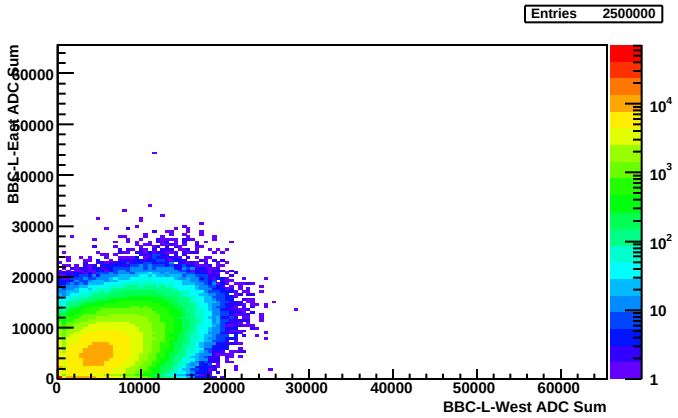

Entries 2500000
BBC-S-West ADC Sum
VPD-West ADC Sum

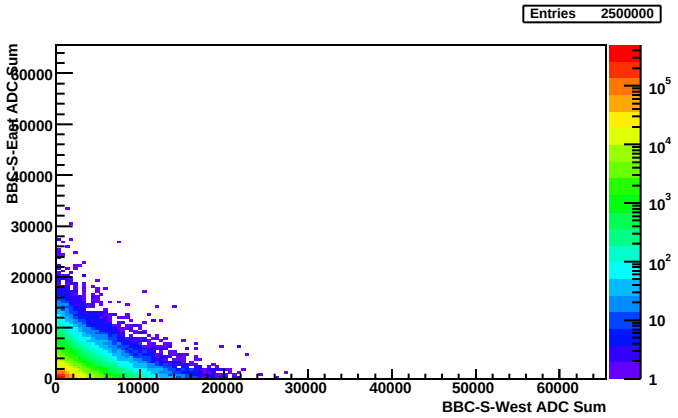

Entries 2500000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

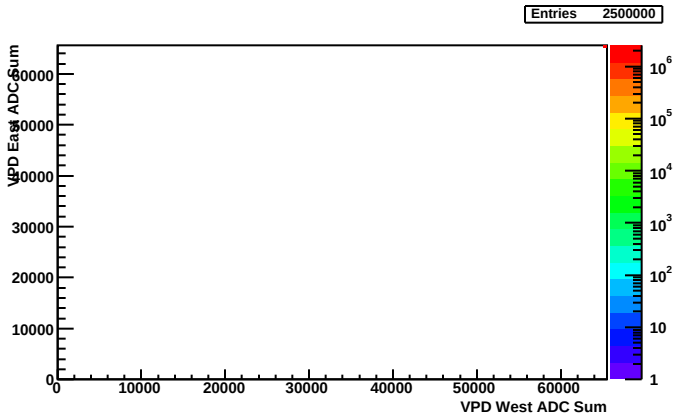

Entries 2500000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

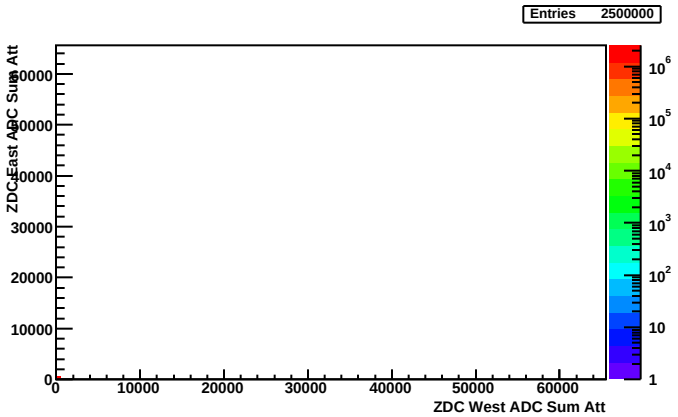

Entries 2500000
VPD-East ADC Sum
ZDC-East ADC Sum Att

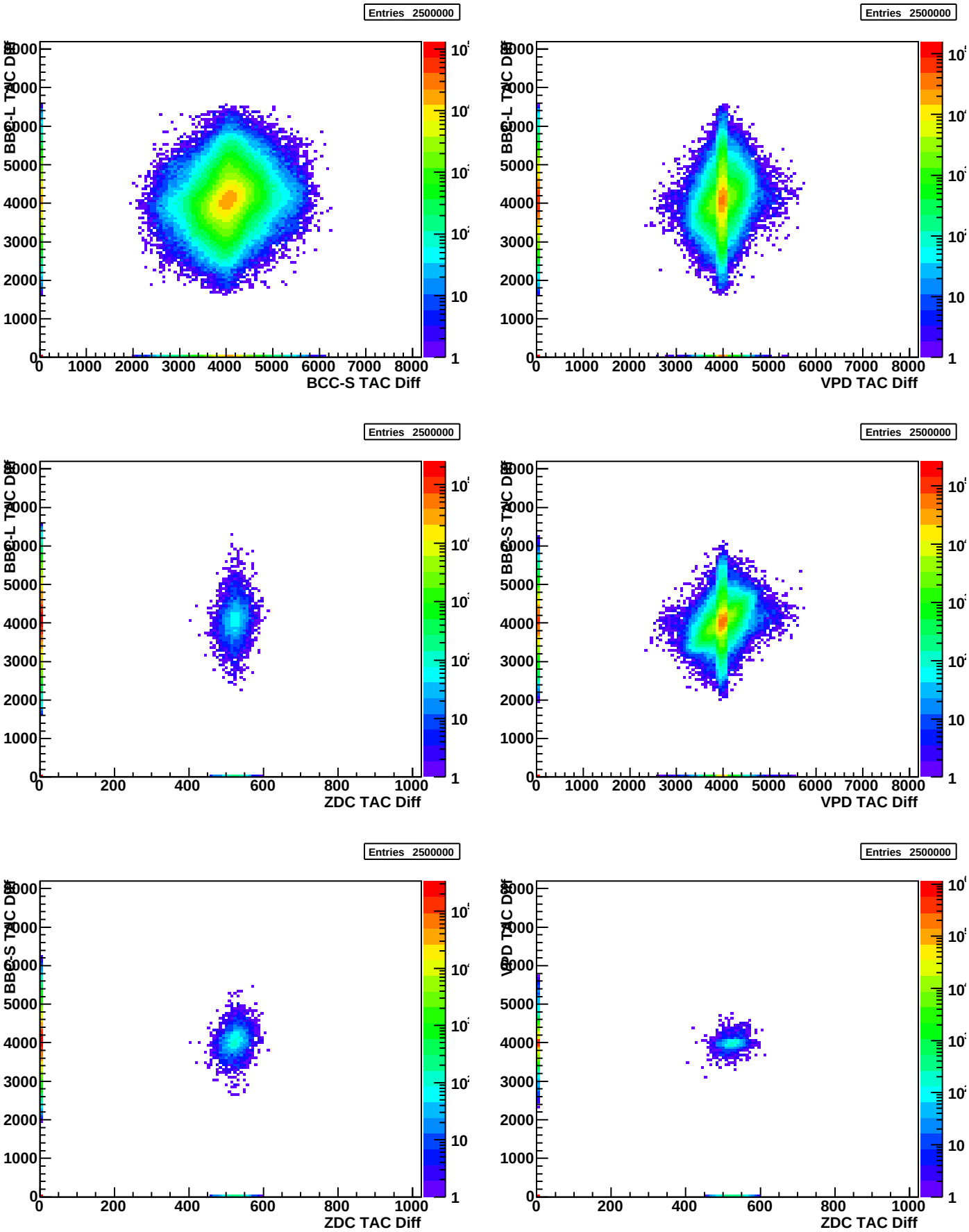

Entries 2500000
VPD-West ADC Sum
ZDC-West ADC Sum Att

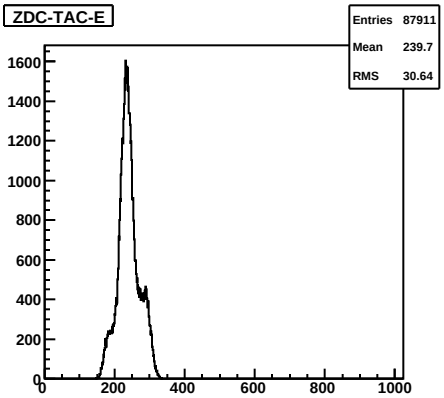

Entries 2500000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

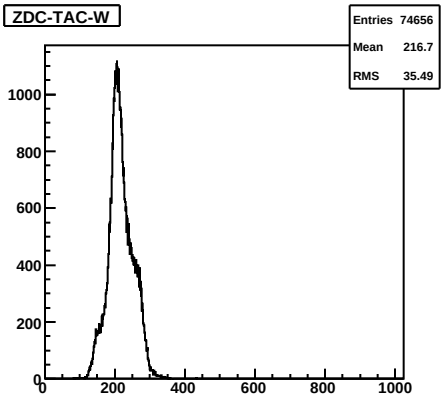

Entries 2500000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

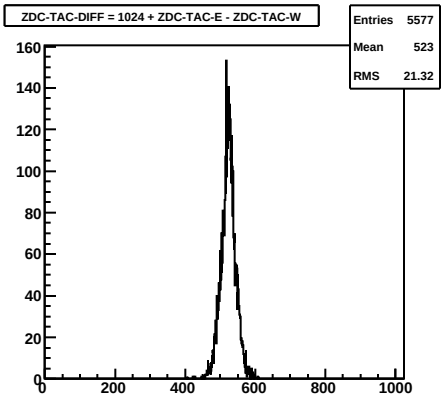

Entries 2500000
VPD East ADC Sum
VPD West ADC Sum

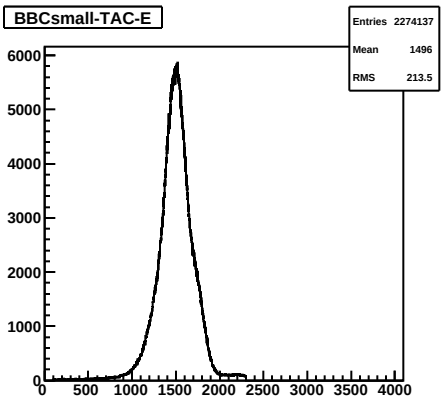

Entries 2500000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

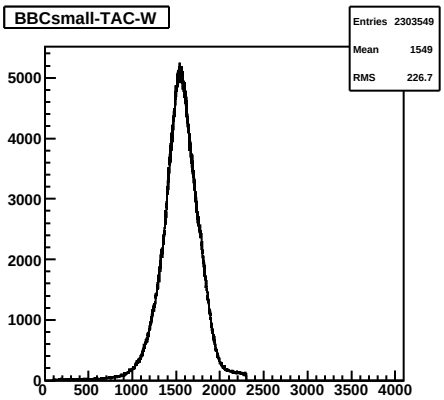

Entries 2500000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2500000
BBC-L TAC Diff
VPD TAC Diff
Entries 2500000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2500000
BBC-S TAC Diff
VPD TAC Diff
Entries 2500000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2500000
VPD TAC Diff
ZDC TAC Diff

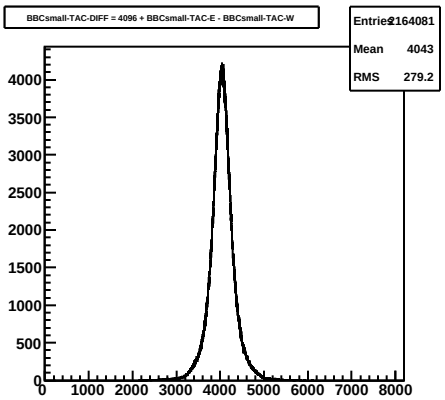

ZDC-TAC-E
Entries 87911
Mean 239.7
RMS 30.64

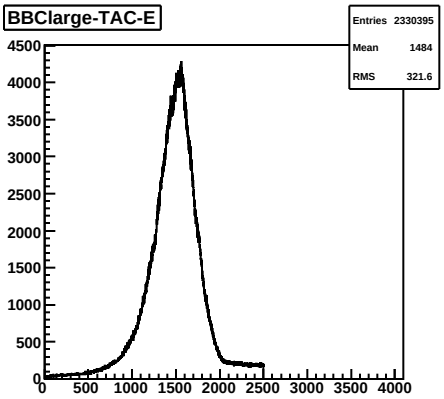

ZDC-TAC-W
Entries 74656
Mean 216.7
RMS 35.49

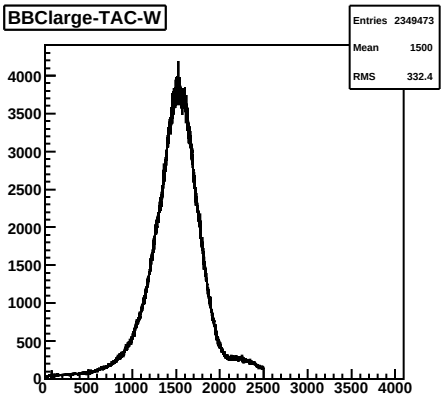

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5577
Mean 523
RMS 21.32

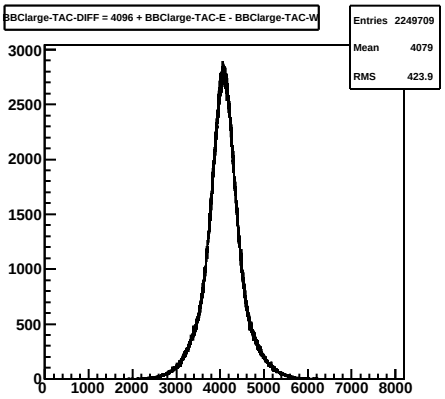

BBCsmall-TAC-E
Entries 2274137
Mean 1496
RMS 213.5

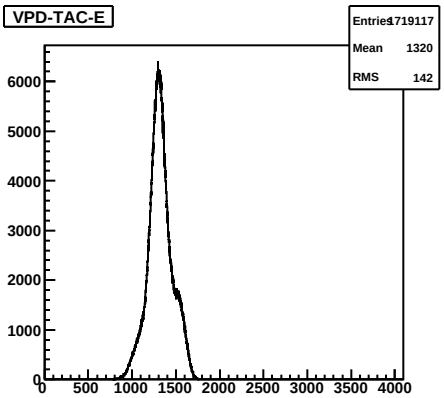

BBCsmall-TAC-W
Entries 2303549
Mean 1549
RMS 226.7

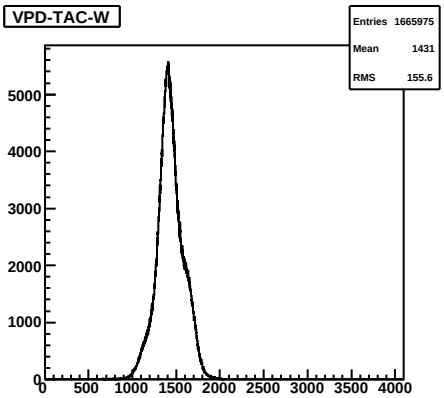

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2164081
Mean 4043
RMS 279.2

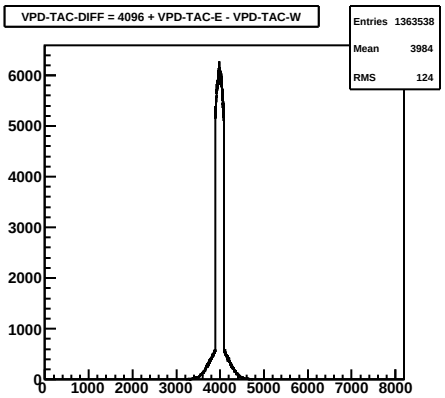

BBClarge-TAC-E
Entries 2330395
Mean 1484
RMS 321.6

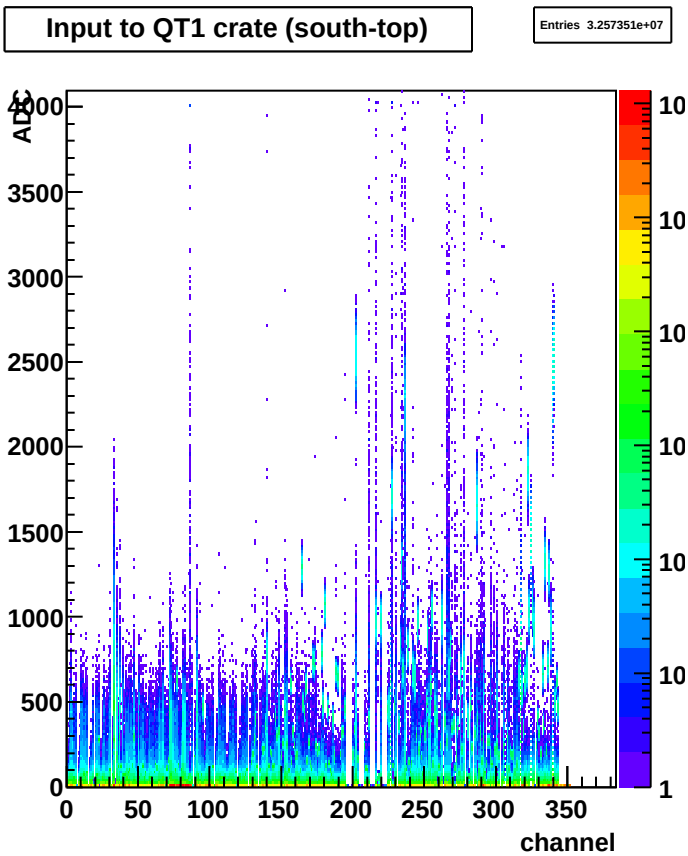

BBClarge-TAC-W
Entries 2349473
Mean 1500
RMS 332.4

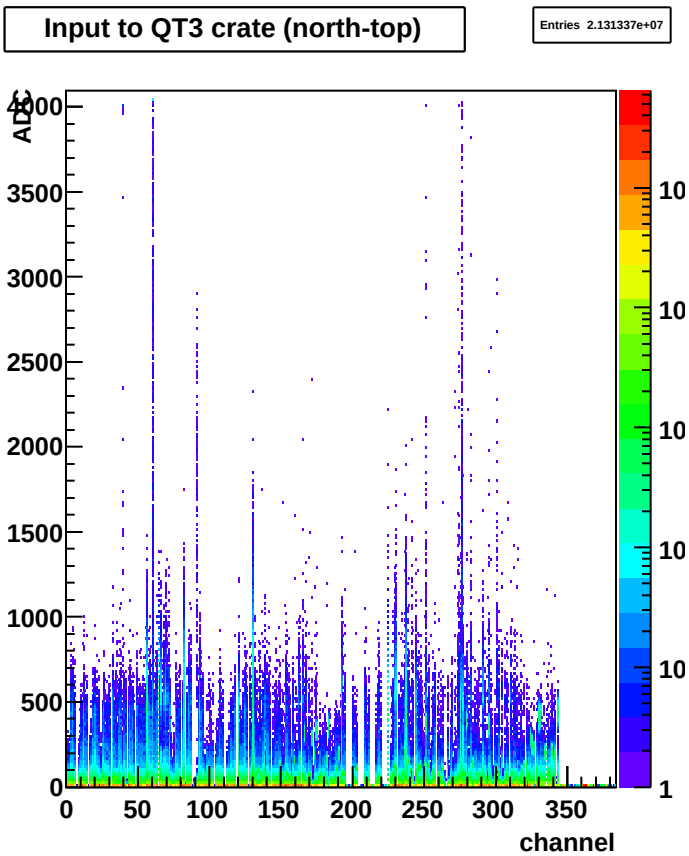

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2249709
Mean 4079
RMS 423.9

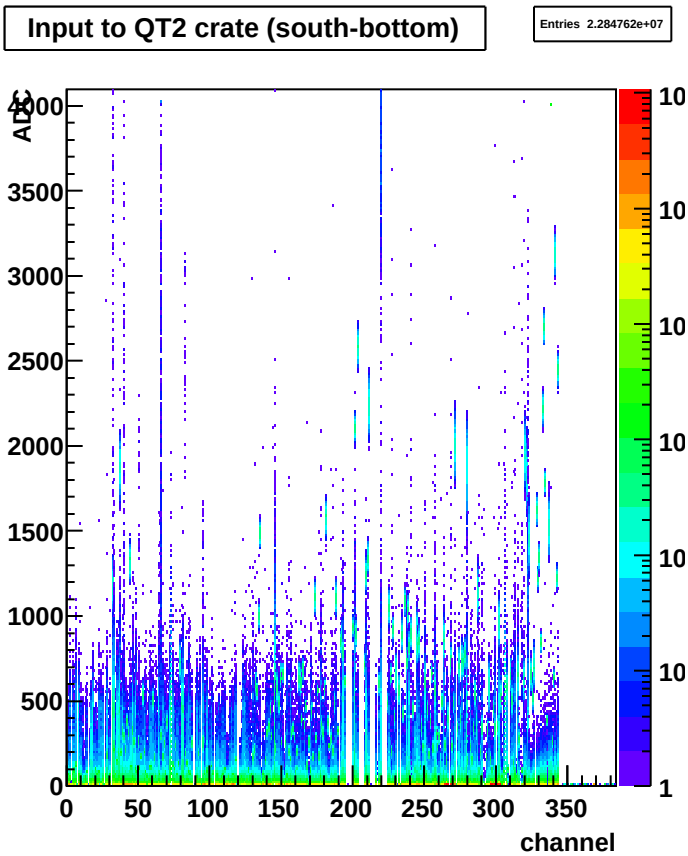

VPD-TAC-E
Entries 1719117
Mean 1320
RMS 142

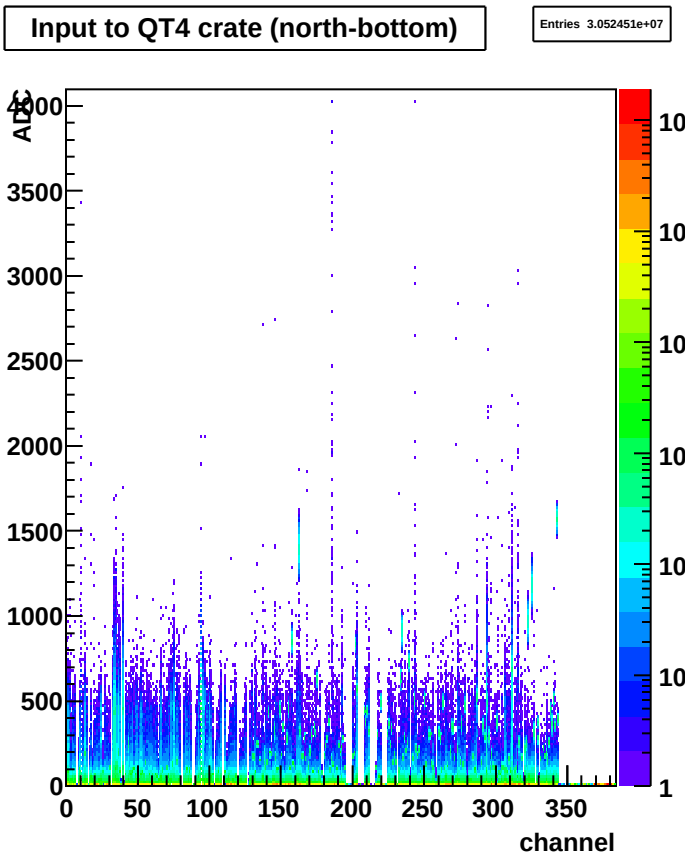

VPD-TAC-W
Entries 1665975
Mean 1431
RMS 155.6

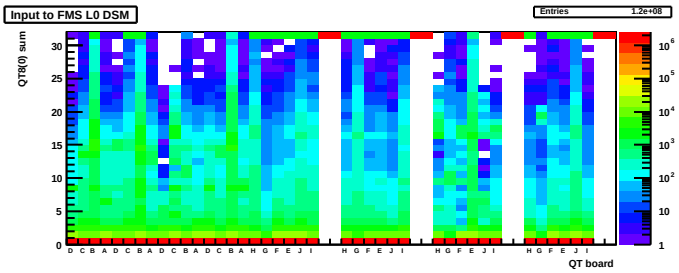

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1363538
Mean 3984
RMS 124

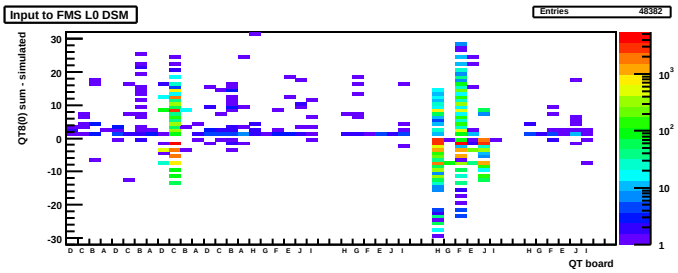

Input to QT1 crate (south-top)
Entries 3.257351e+07
ADC
channel

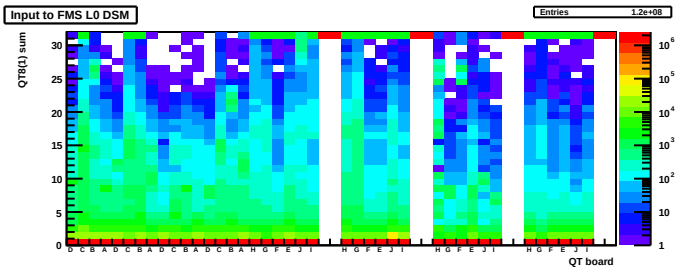

Input to QT3 crate (north-top)
Entries 2.131337e+07
ADC
channel

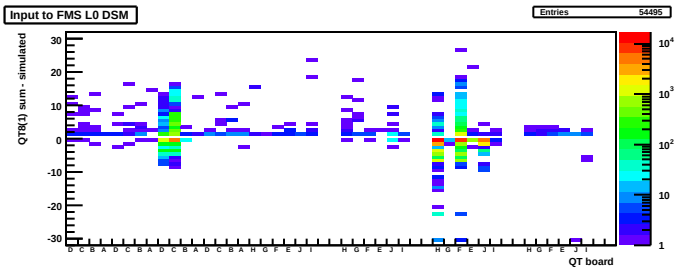

Input to QT2 crate (south-bottom)
Entries 2.284762e+07
ADC
channel

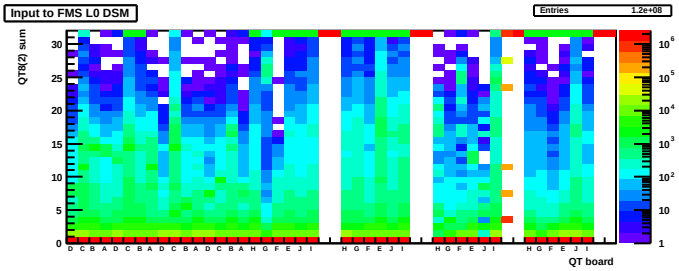

Input to QT4 crate (north-bottom)
Entries 3.052451e+07
ADC
channel

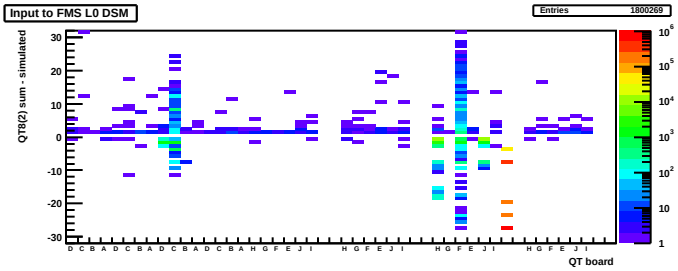

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

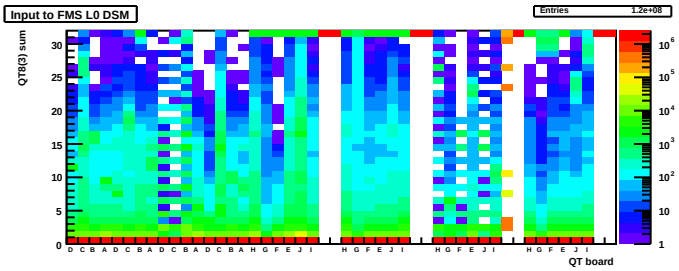

Input to FMS L0 DSM
Entries 48382
QT8(0) sum - simulated
QT board

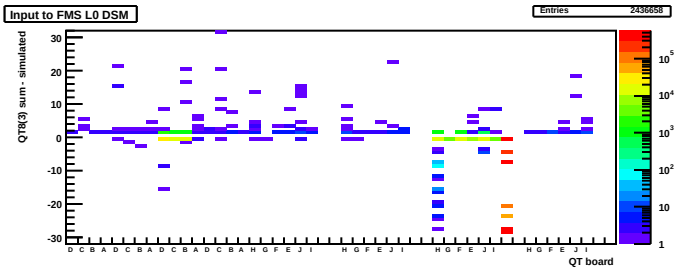

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

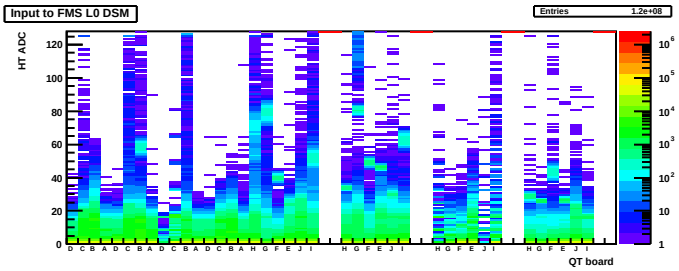

Input to FMS L0 DSM
Entries 54495
QT8(1) sum - simulated
QT board

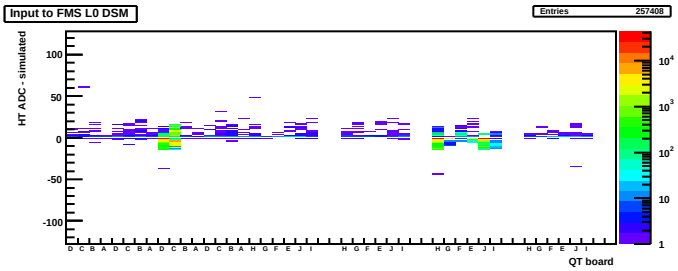

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

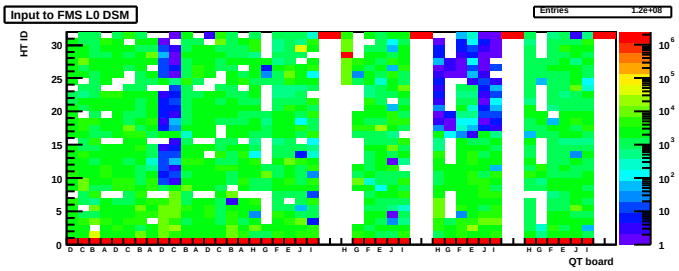

Input to FMS L0 DSM
Entries 1800269
QT8(2) sum - simulated
QT board

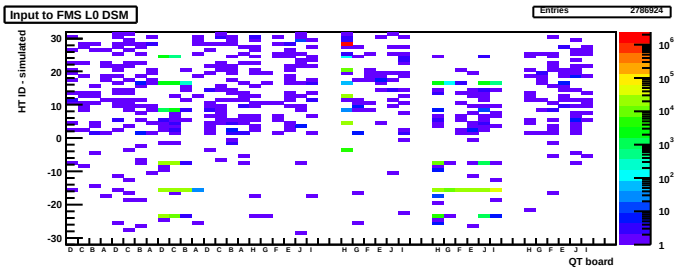

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

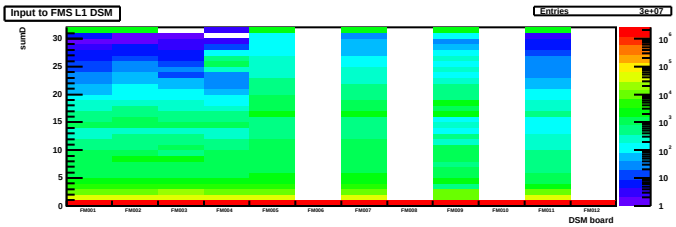

Input to FMS L0 DSM
Entries 2436658
QT8(3) sum - simulated
QT board

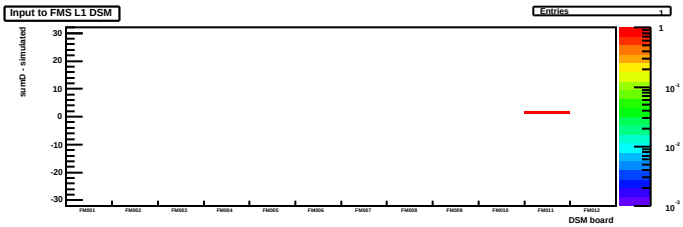

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

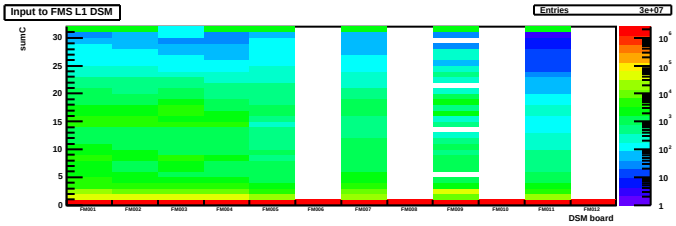

Input to FMS L0 DSM
Entries 257408
HT ADC - simulated
QT board

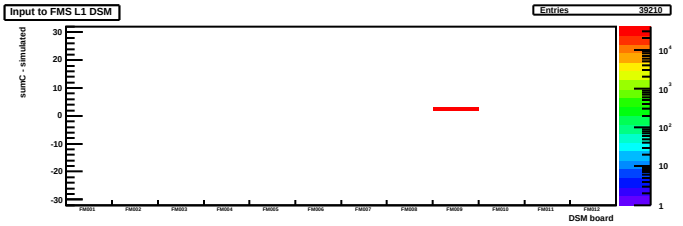

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

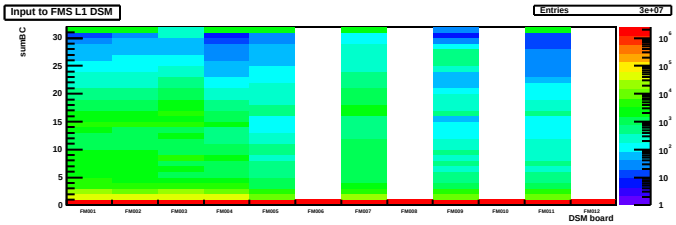

Input to FMS L0 DSM
Entries 2786924
HT ID - simulated
QT board

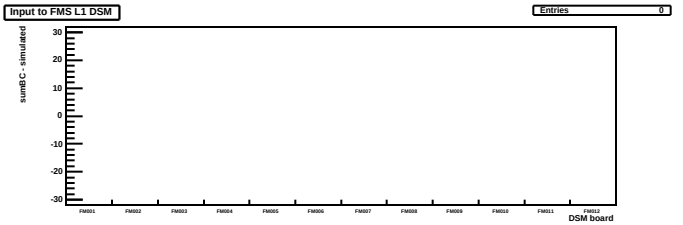

Input to FMS L1 DSM
Entries 3e+07
sumD
DSM board

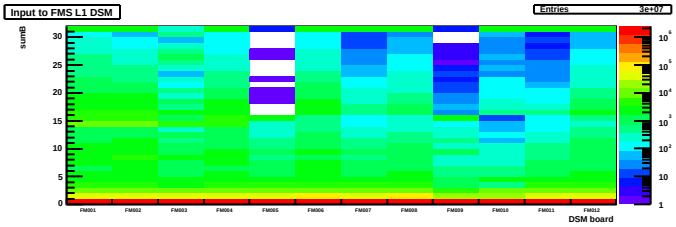

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

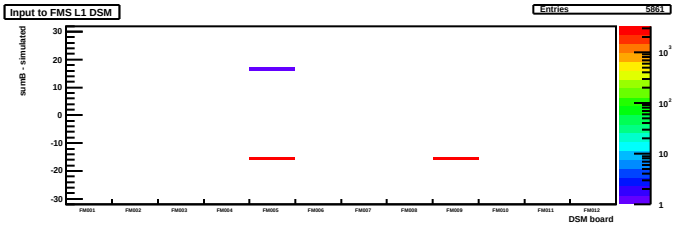

Input to FMS L1 DSM
Entries 3e+07
sumC
DSM board

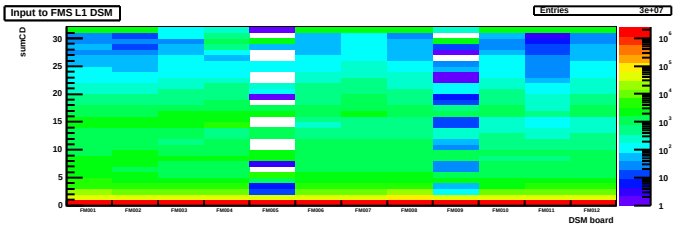

Input to FMS L1 DSM
Entries 39210
sumC - simulated
DSM board

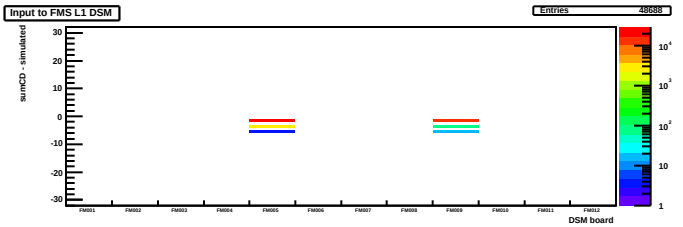

Input to FMS L1 DSM
Entries 3e+07
sumBC
DSM board

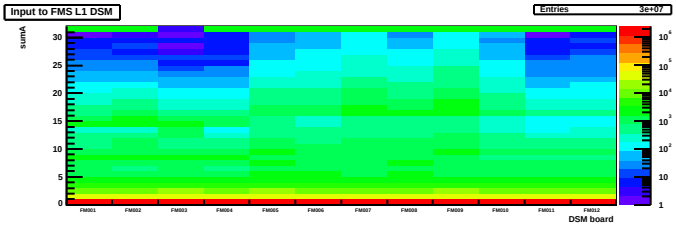

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

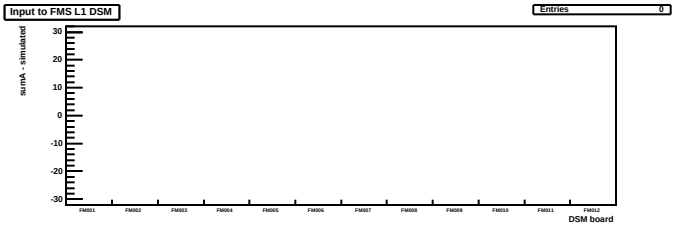

Input to FMS L1 DSM
Entries 3e+07
sumB
DSM board

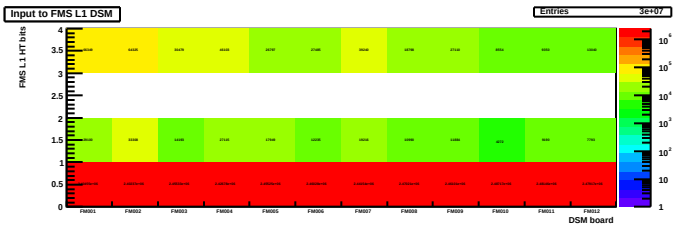

Input to FMS L1 DSM
Entries 5861
sumB - simulated
DSM board

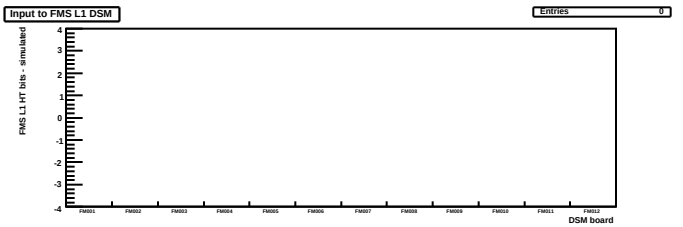

Input to FMS L1 DSM
Entries 3e+07
sumCD
DSM board

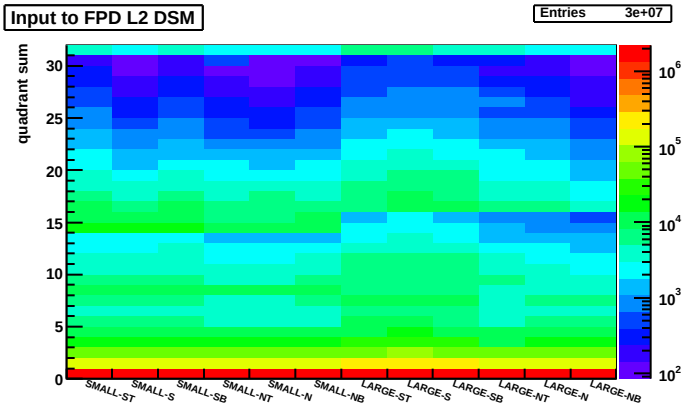

Input to FMS L1 DSM
Entries 48688
sumCD - simulated
DSM board

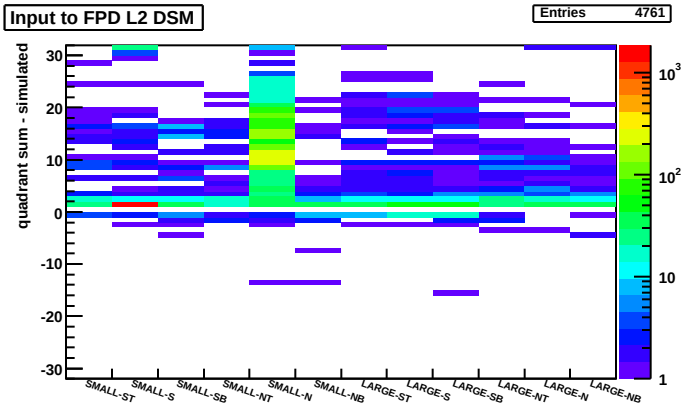

Input to FMS L1 DSM
Entries 3e+07
sumA
DSM board

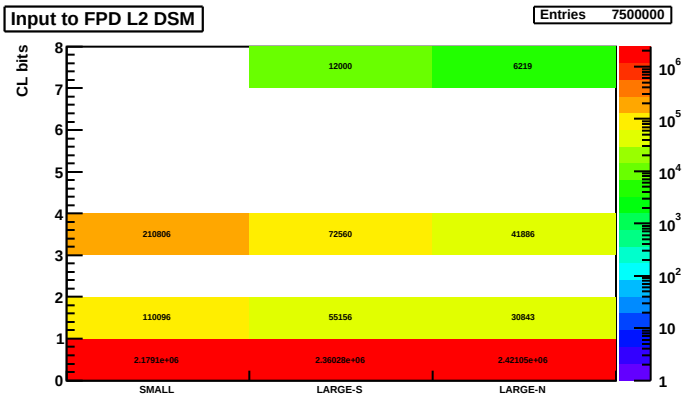

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

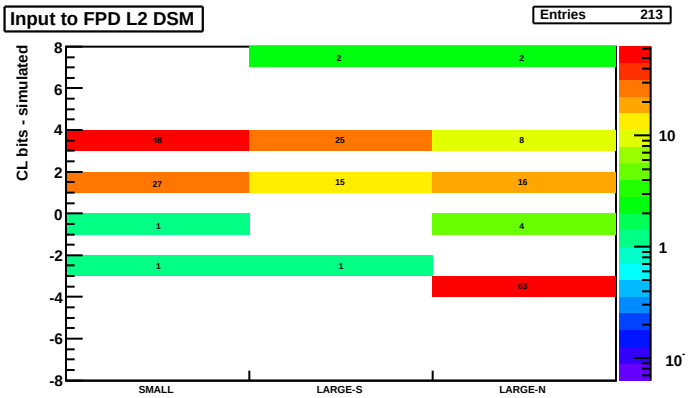

Input to FMS L1 DSM
Entries 3e+07
FMS L1 HT bits
DSM board

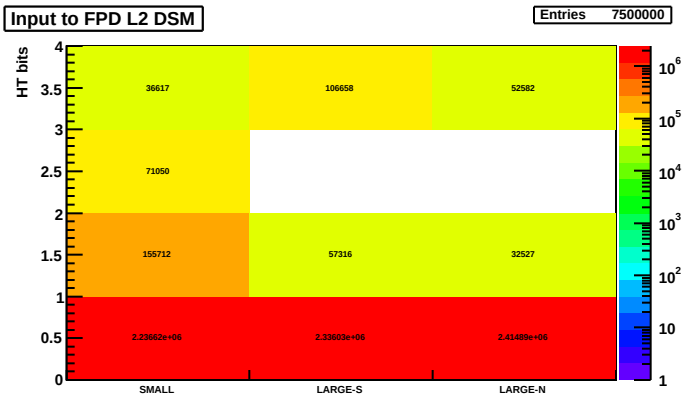

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

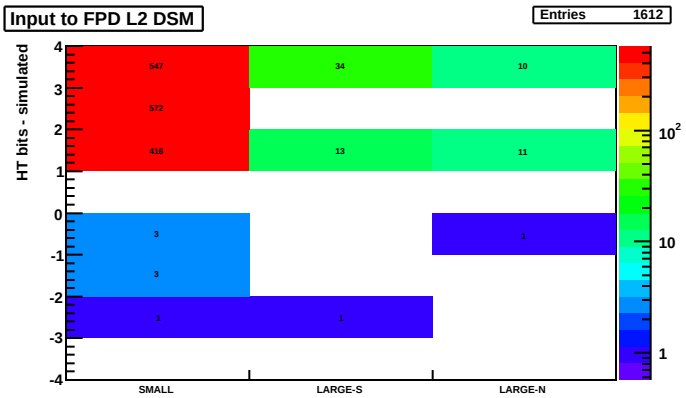

Input to FPD L2 DSM
Entries 3e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

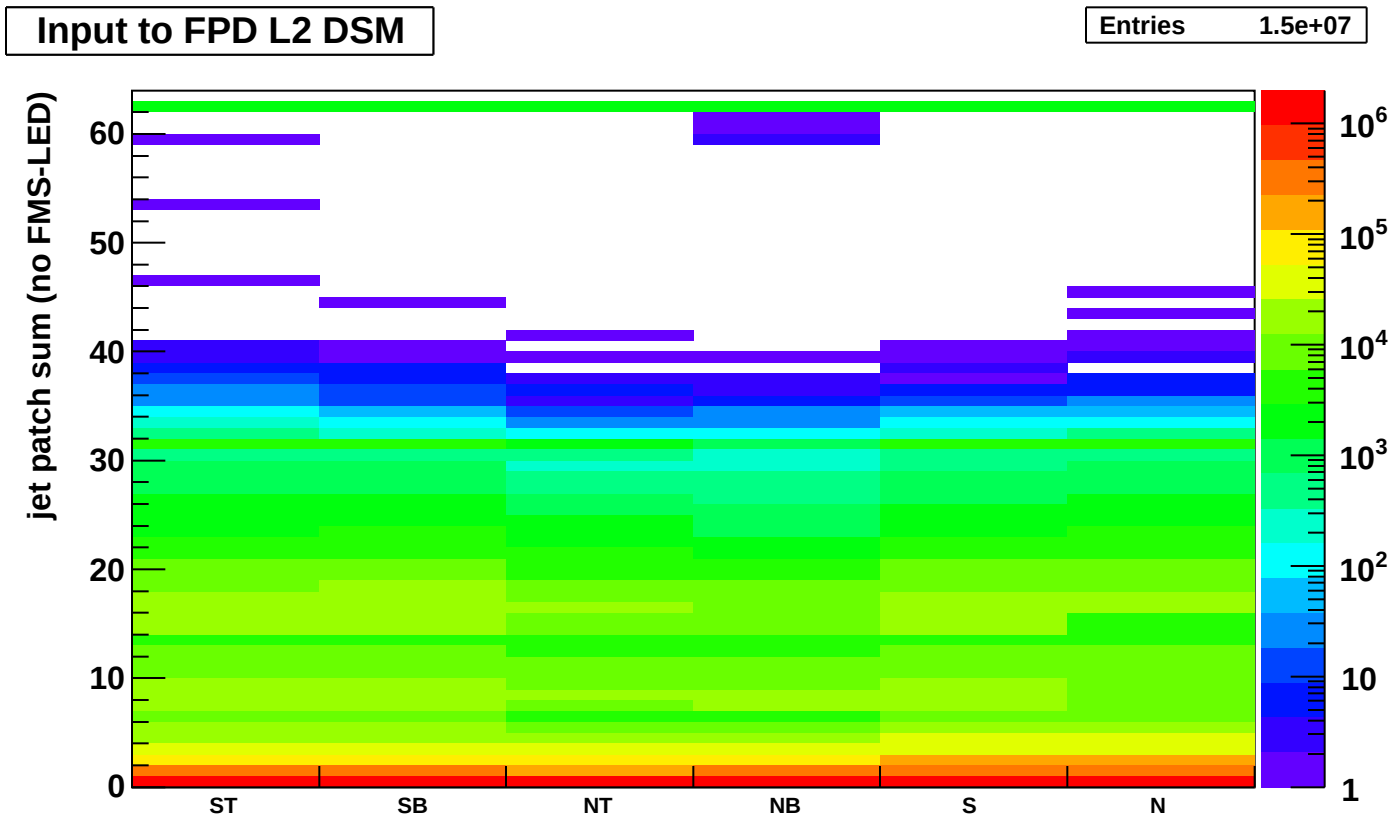

Input to FPD L2 DSM
Entries 4761
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

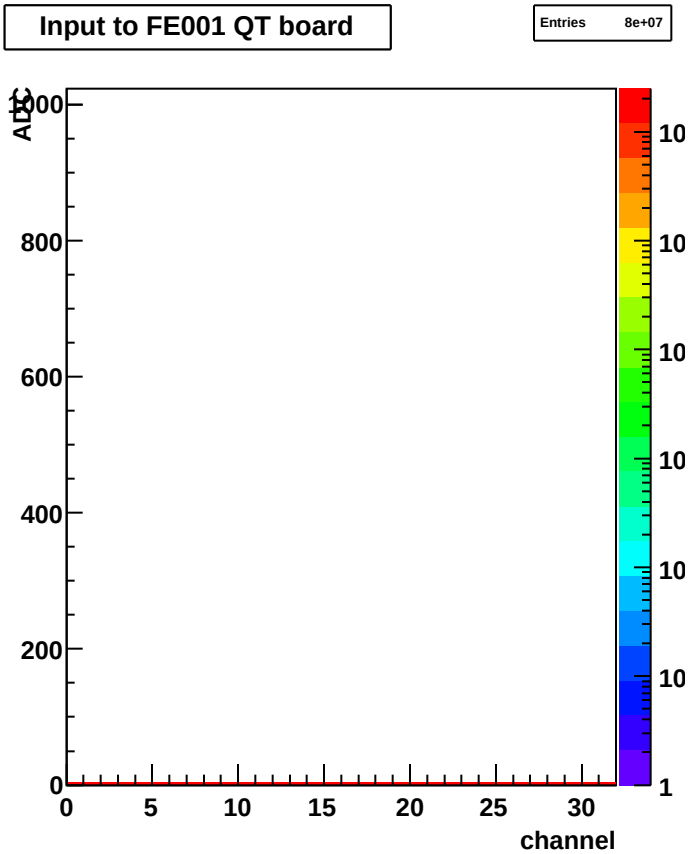

Input to FPD L2 DSM
Entries 7500000
CL bits
12000
6219
210806
72560
41886
110096
55156
30843
2.1791e+06
2.36028e+06
2.42105e+06
SMALL
LARGE-S
LARGE-N

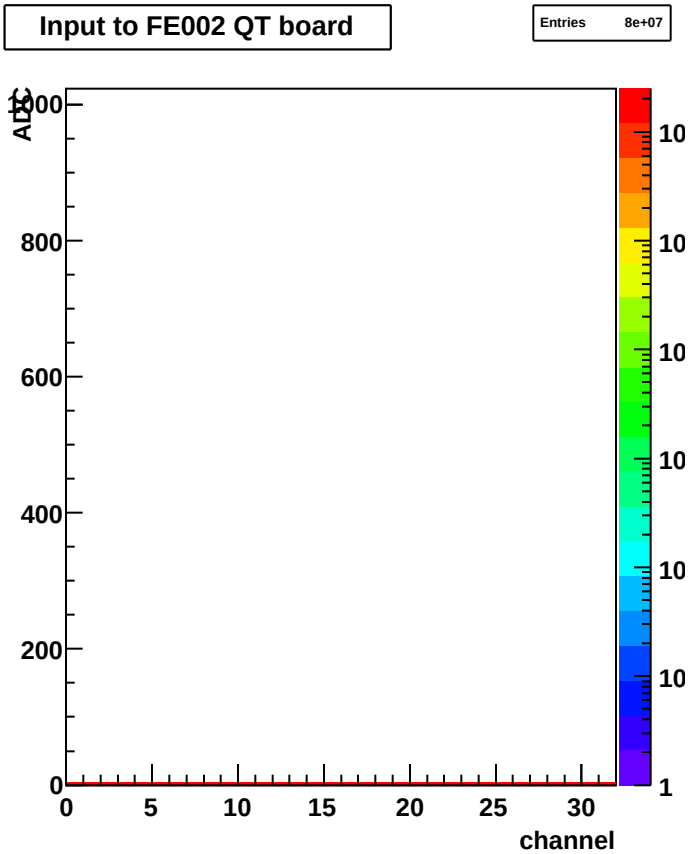

Input to FPD L2 DSM
Entries 213
CL bits - simulated
2
2
48
25
8
27
15
16
1
4
1
1
63
SMALL
LARGE-S
LARGE-N

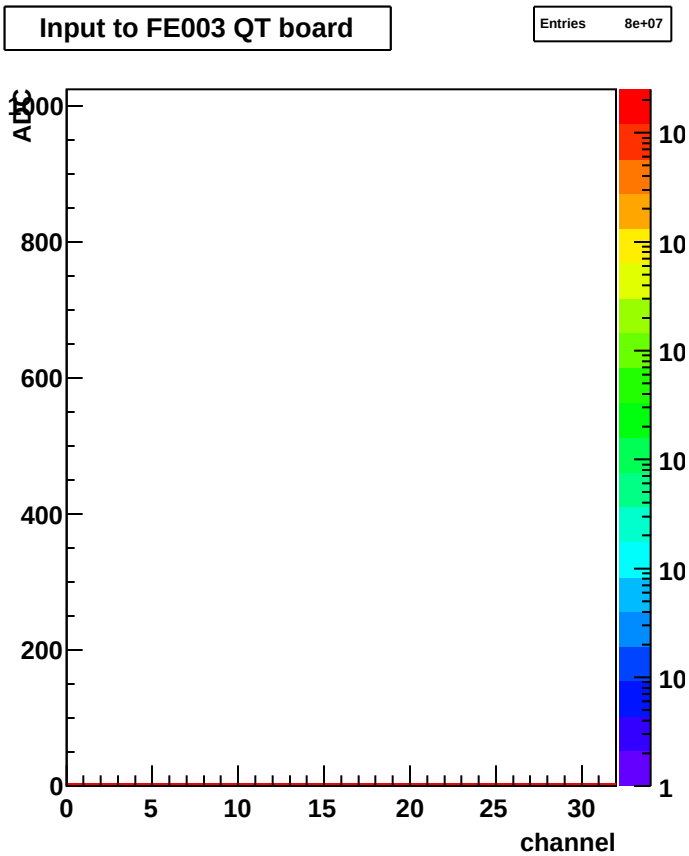

Input to FPD L2 DSM
Entries 7500000
HT bits
36617
106658
52582
71050
155712
57316
32527
2.23662e+06
2.33603e+06
2.41489e+06
SMALL
LARGE-S
LARGE-N

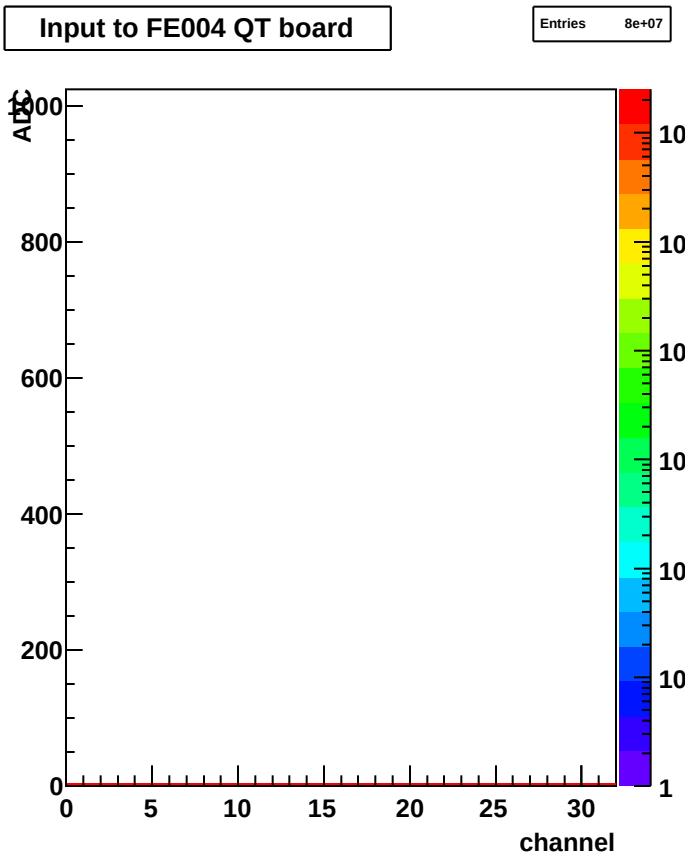

Input to FPD L2 DSM
Entries 1612
HT bits - simulated
547
34
10
572
416
13
11
3
1
3
1
1
SMALL
LARGE-S
LARGE-N

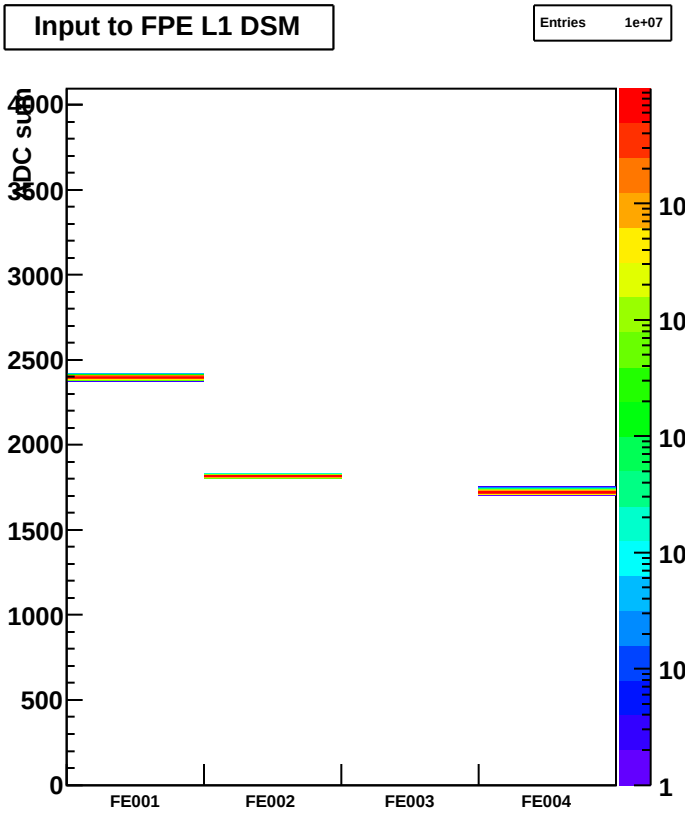

Input to FPD L2 DSM
Entries 1.5e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

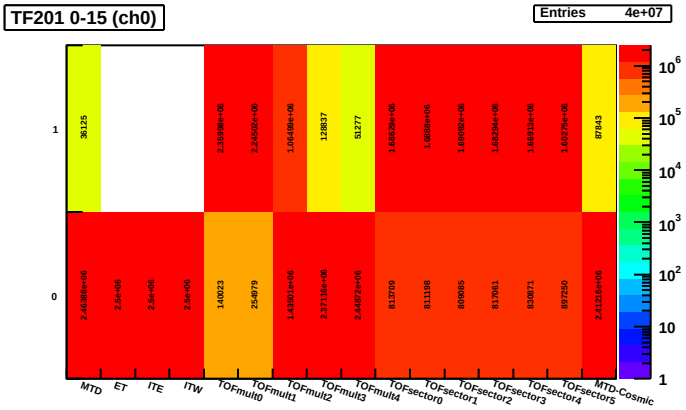

Input to FE001 QT board
Entries 8e+07
ADC
channel

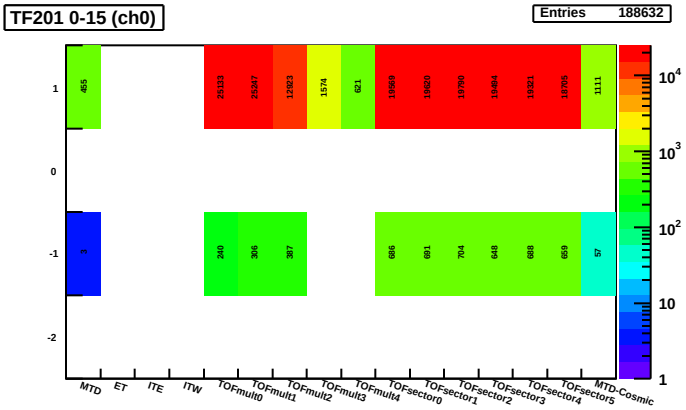

Input to FE002 QT board
Entries 8e+07
ADC
channel

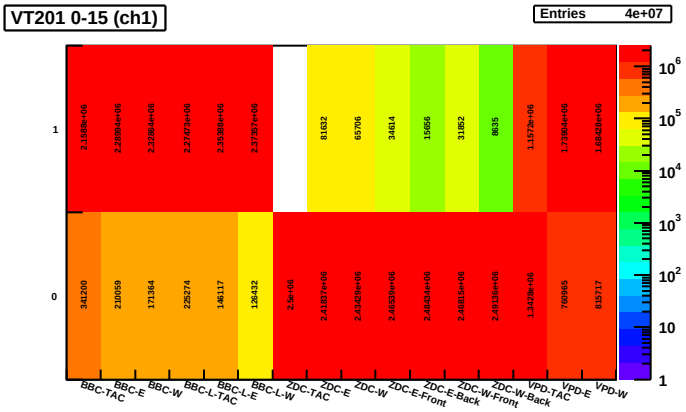

Input to FE003 QT board
Entries 8e+07
ADC
channel

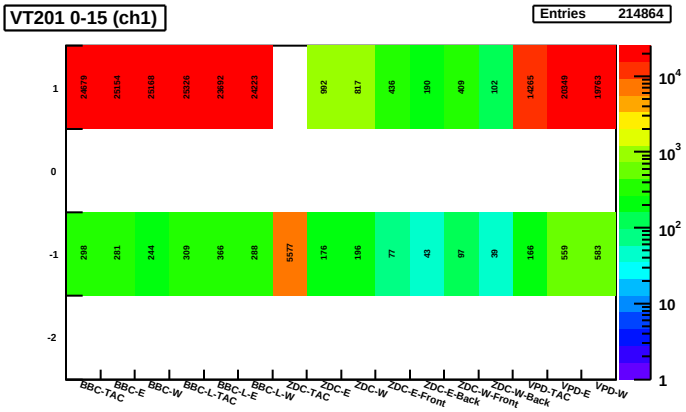

Input to FE004 QT board
Entries 8e+07
ADC
channel


Input to FPE L1 DSM
Entries 1e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 4e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 188632
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 4e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 214864
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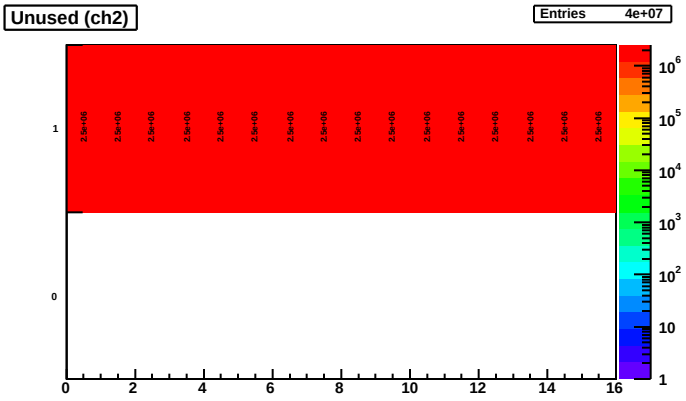
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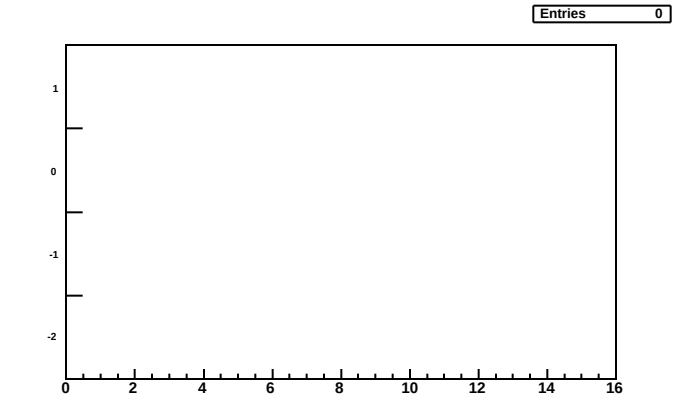
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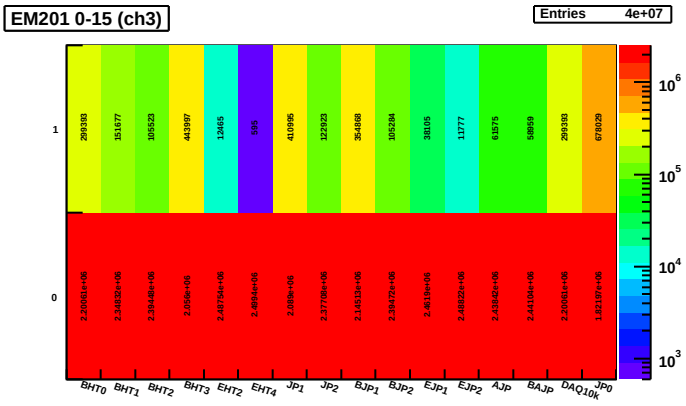
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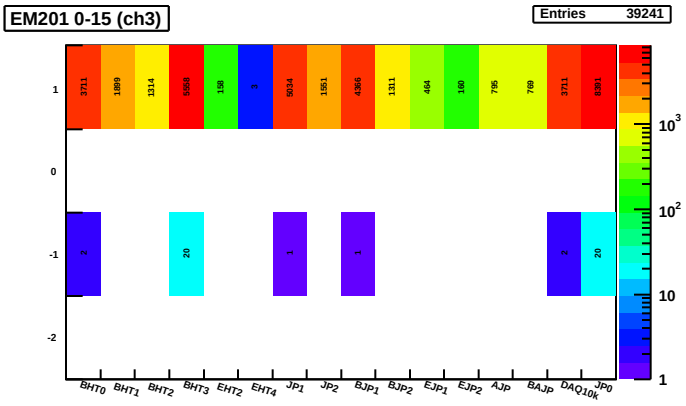
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Entries 39241
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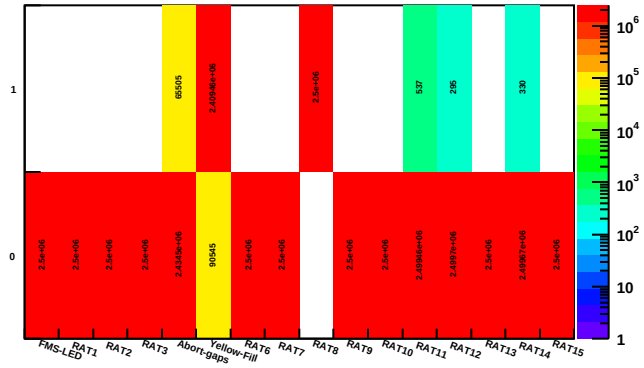
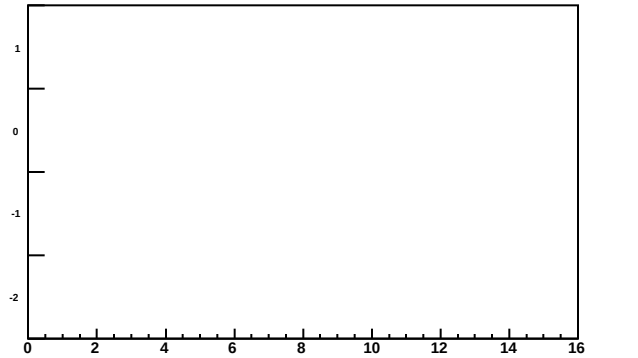
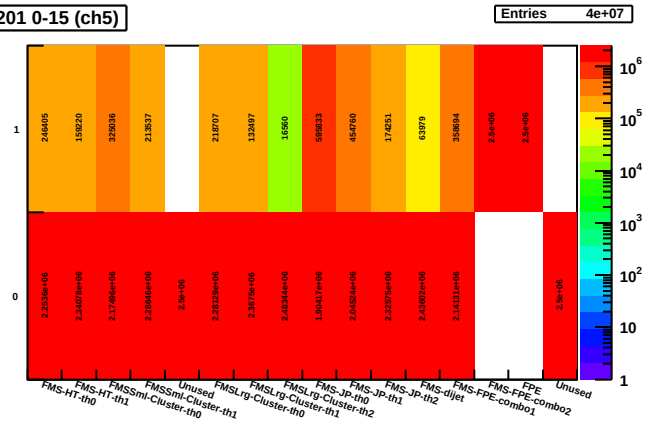
RAT board (ch4)





FP201 0-15 (ch5)



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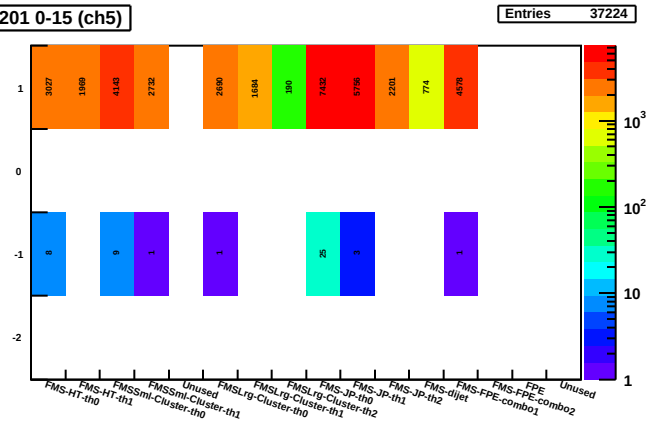
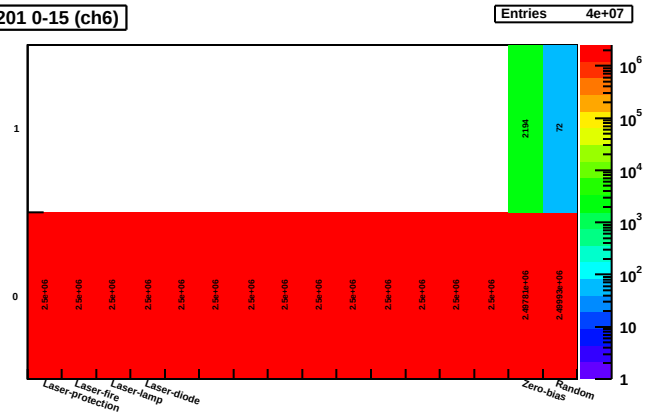
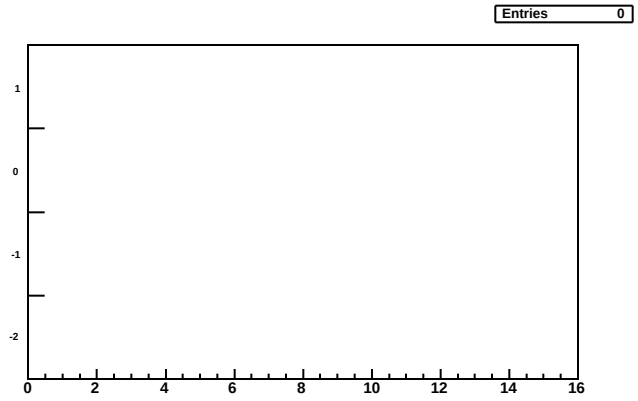
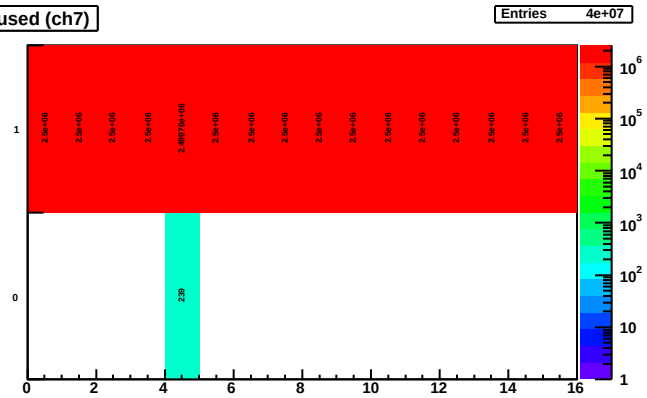
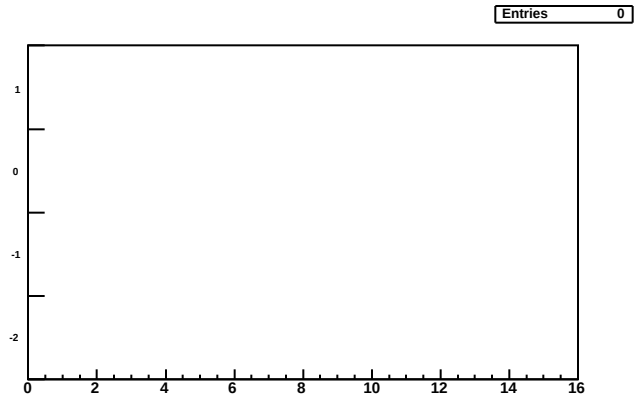


ST201 0-15 (ch6)





Unused (ch7)