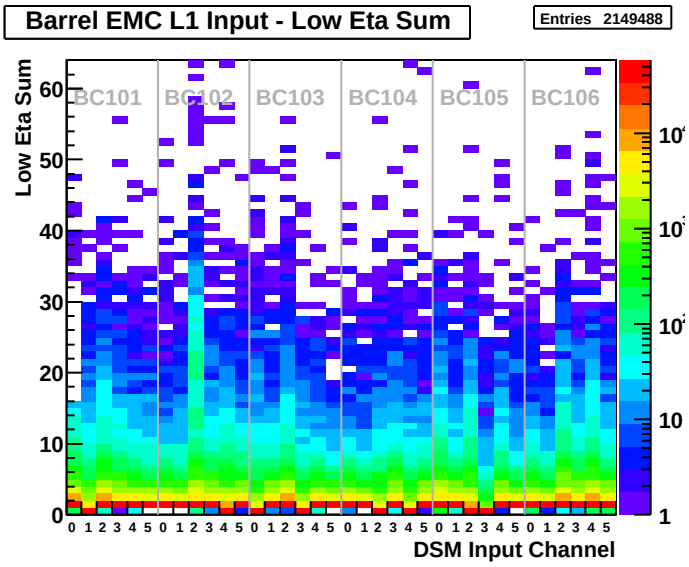
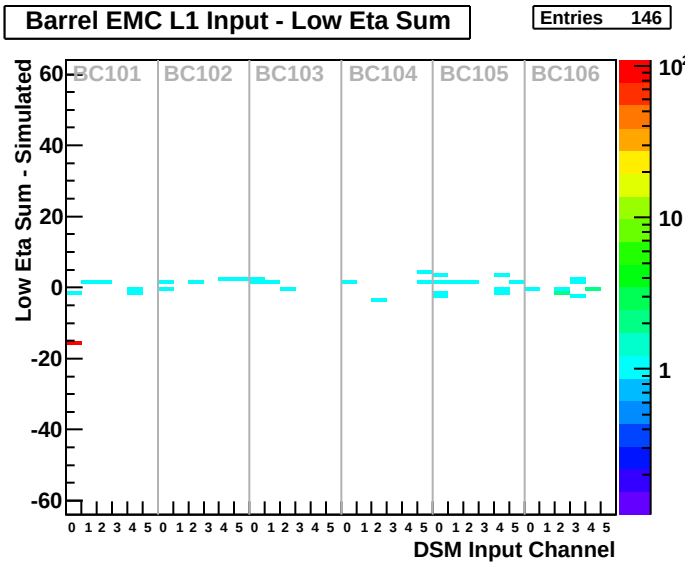
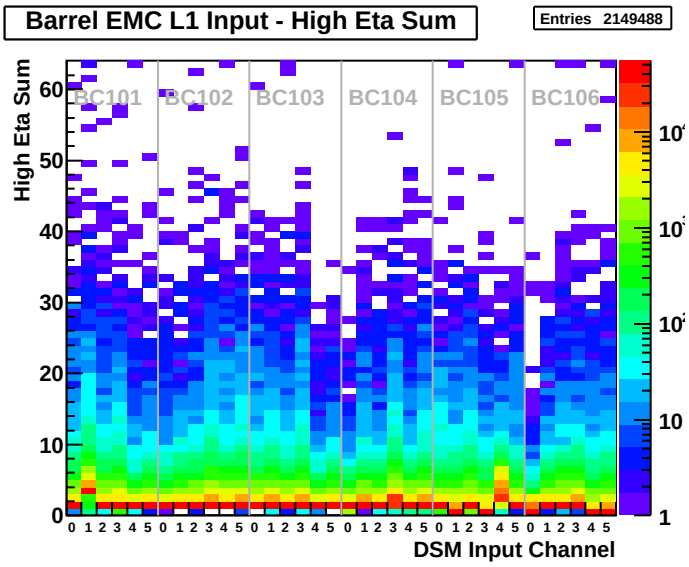
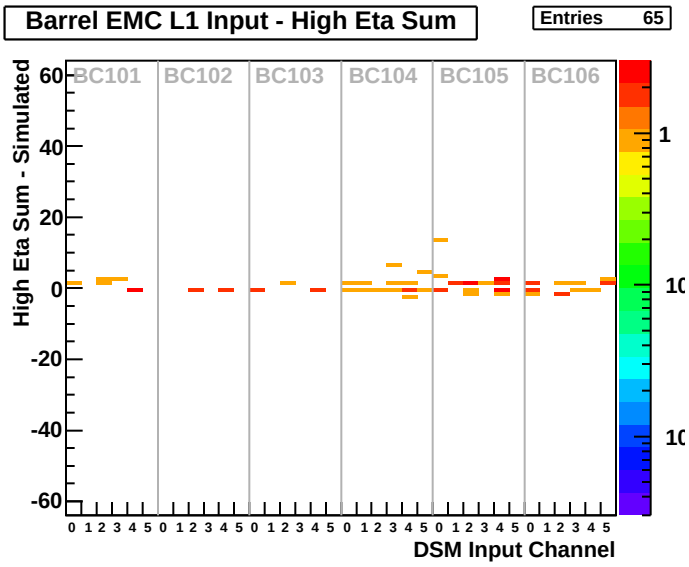
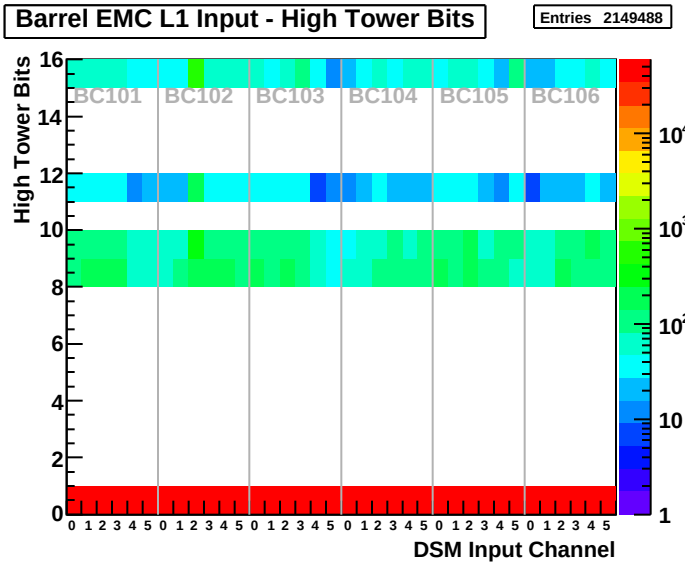
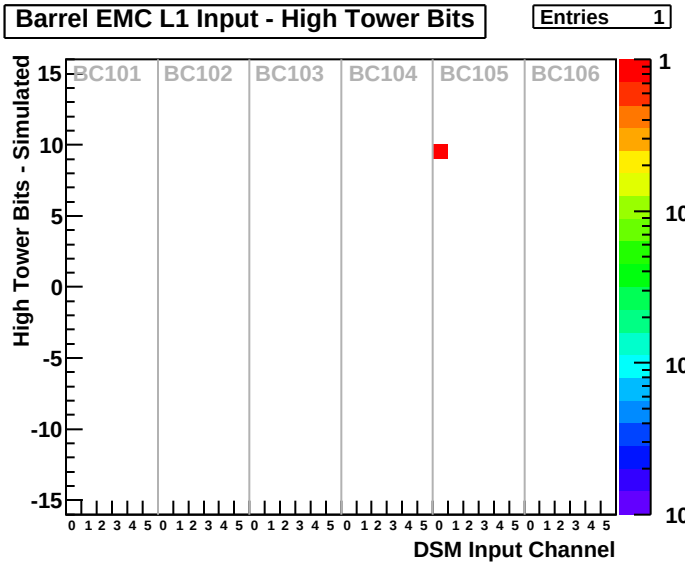

Barrel EMC L1 Input - Low Eta Sum
Entries 2149488
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

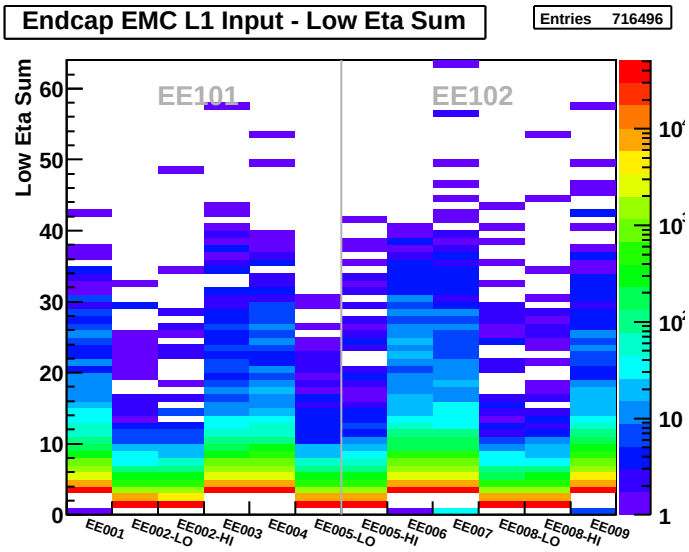

Barrel EMC L1 Input - Low Eta Sum
Entries 146
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

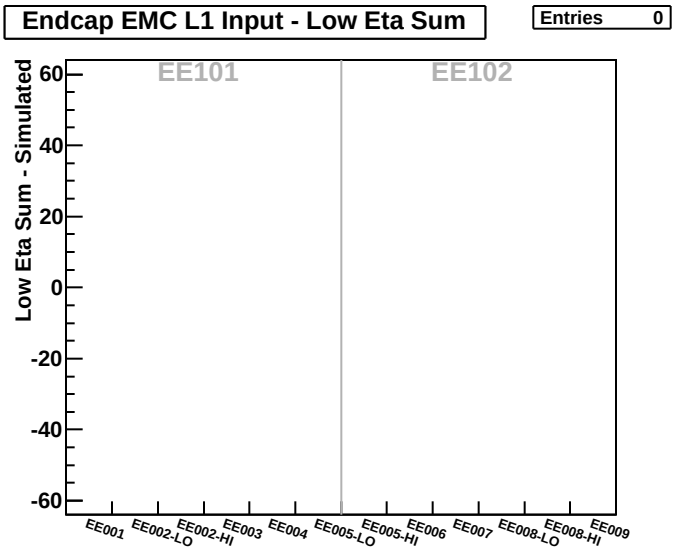

Barrel EMC L1 Input - High Eta Sum
Entries 2149488
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

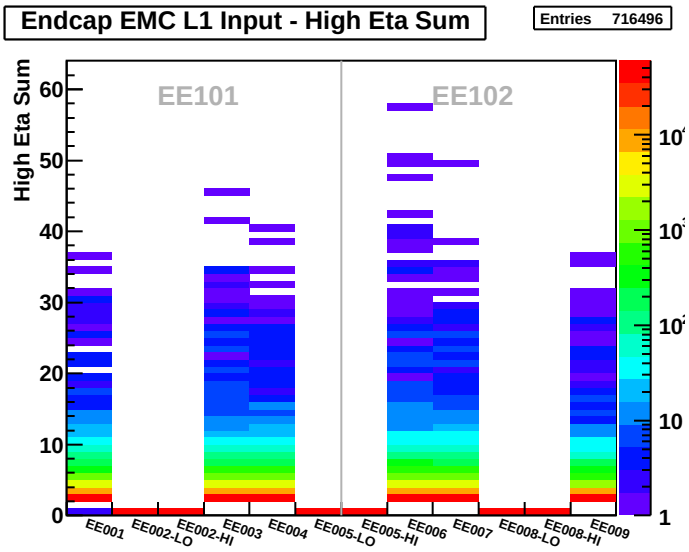

Barrel EMC L1 Input - High Eta Sum
Entries 65
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

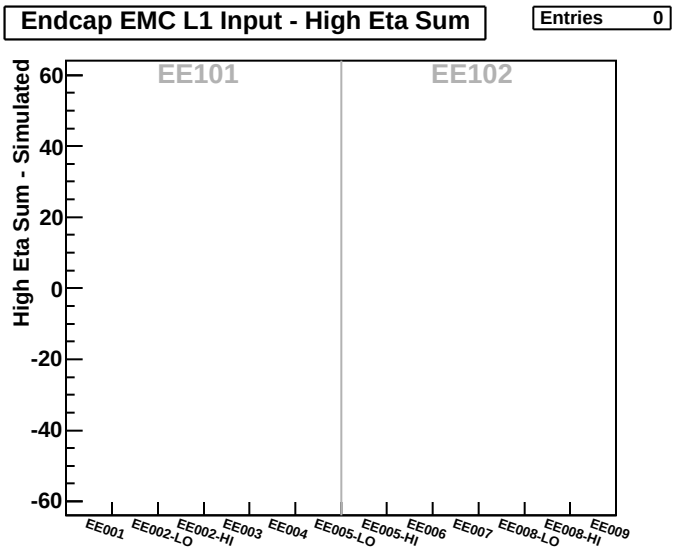

Barrel EMC L1 Input - High Tower Bits
Entries 2149488
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

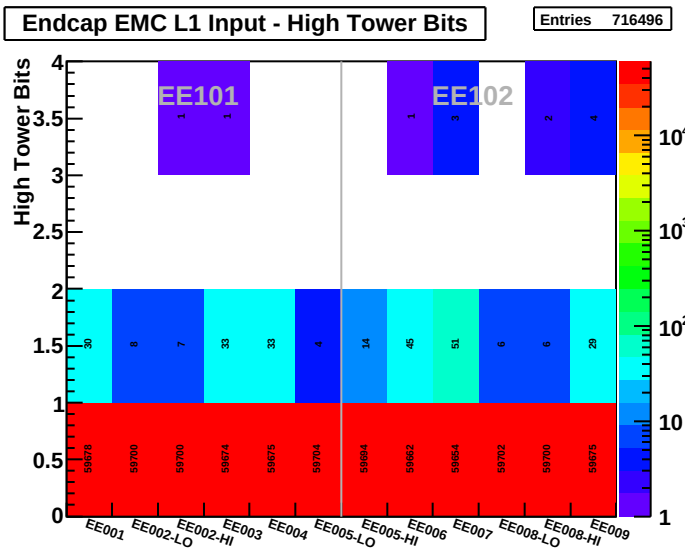

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

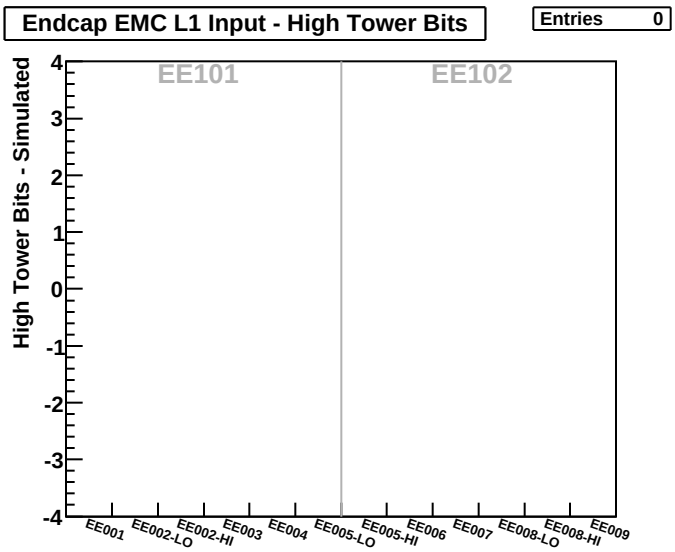

Endcap EMC L1 Input - Low Eta Sum
Entries 716496
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

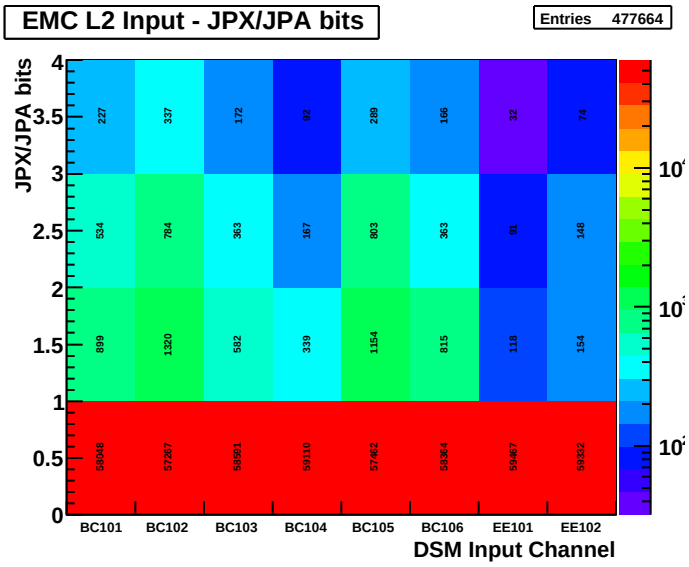

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

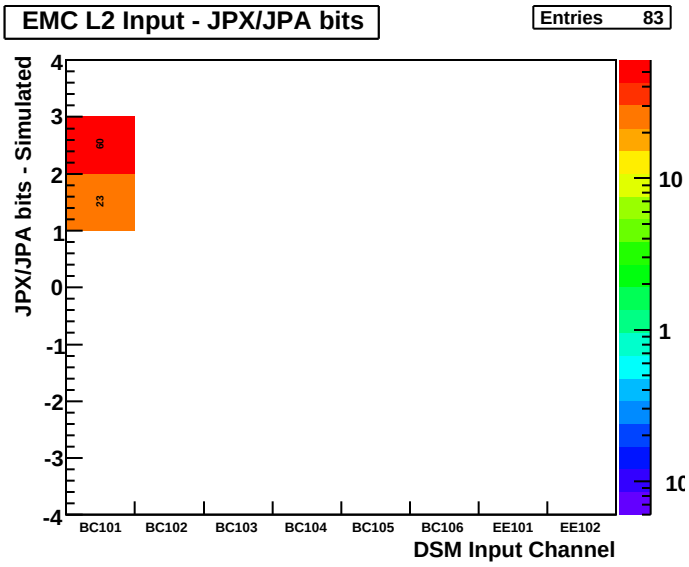

Endcap EMC L1 Input - High Eta Sum
Entries 716496
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

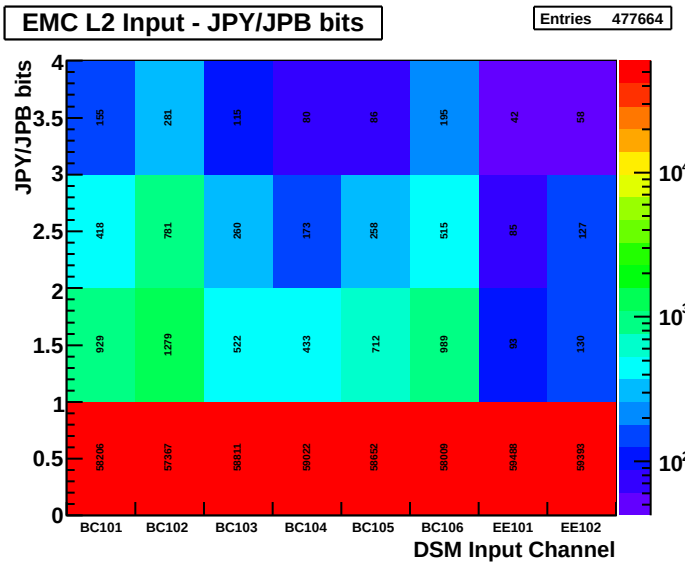

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

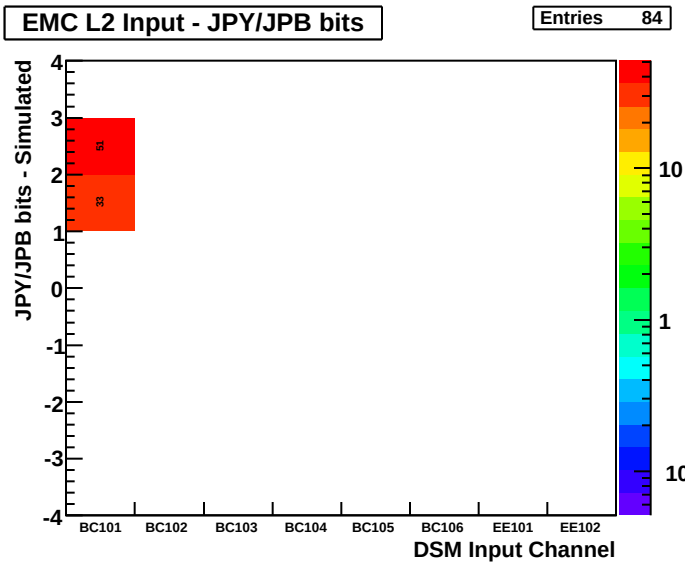

Endcap EMC L1 Input - High Tower Bits
Entries 716496
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

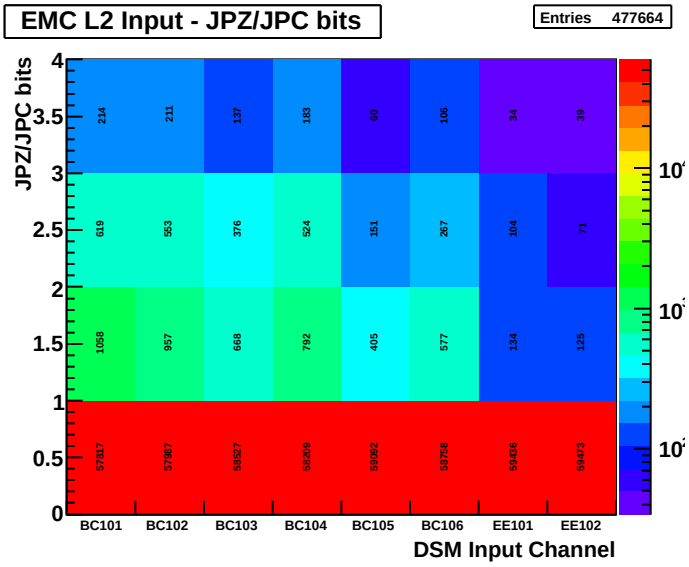

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

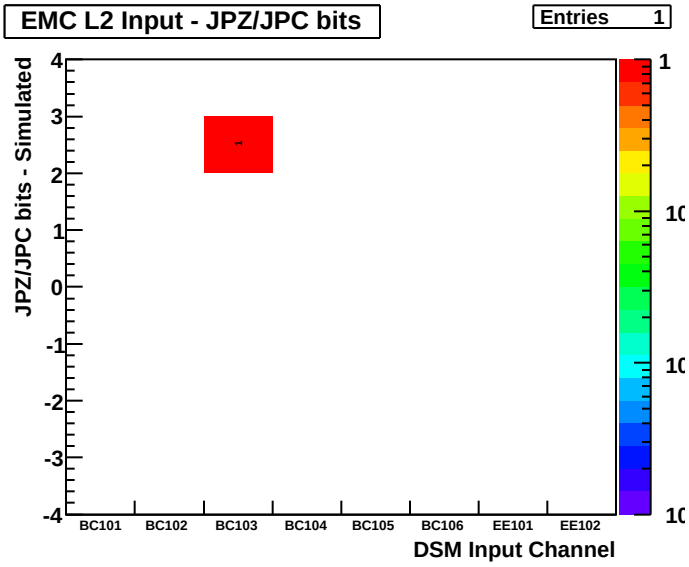

EMC L2 Input - JPX/JPA bits
Entries 477664
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
227 337 172 92 289 166 32 74
534 784 363 167 803 363 91 148
899 1320 582 339 1154 815 118 154
58048 57267 58591 59110 57462 58364 59467 59332

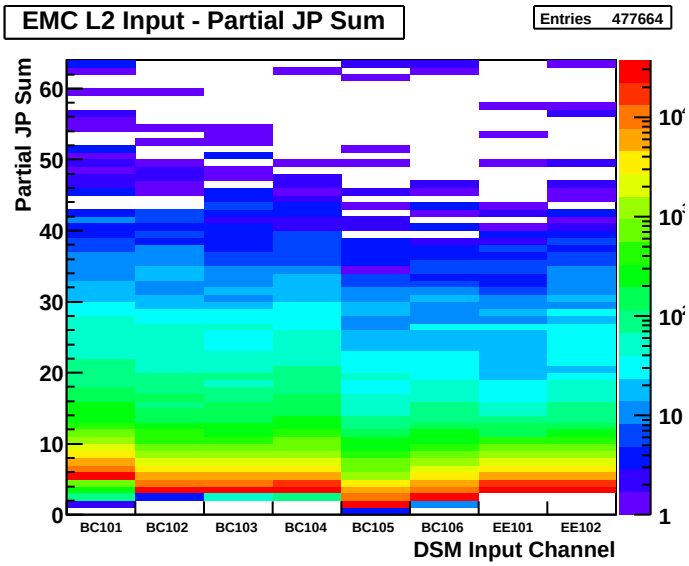

EMC L2 Input - JPX/JPA bits
Entries 83
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
60
23

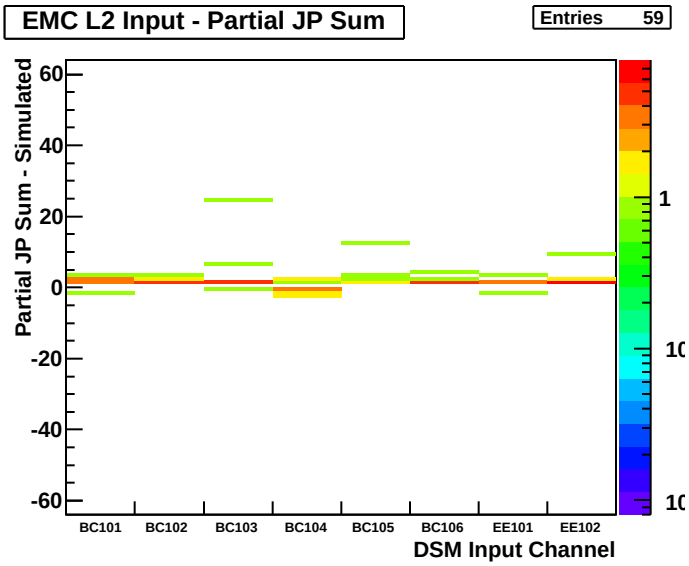

EMC L2 Input - JPY/JPB bits
Entries 477664
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
155 281 115 80 86 195 42 58
418 781 260 173 258 515 85 127
929 1279 522 433 712 989 93 130
58206 57367 58811 59022 58652 58009 59488 59393

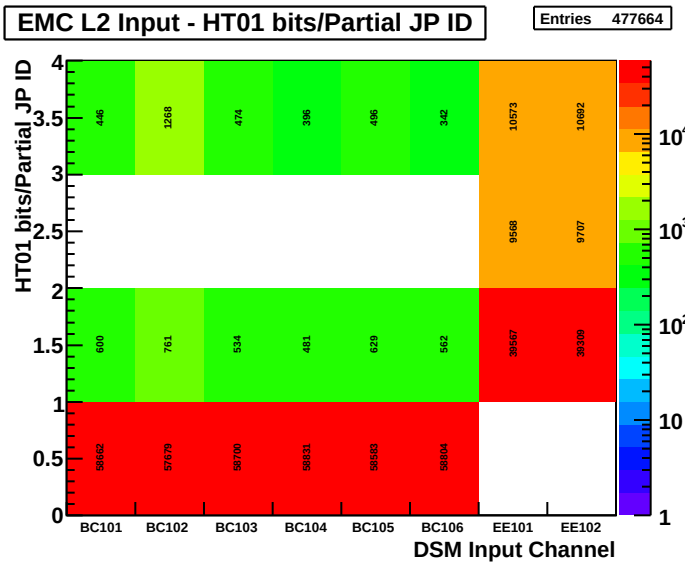

EMC L2 Input - JPY/JPB bits
Entries 84
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
51
33

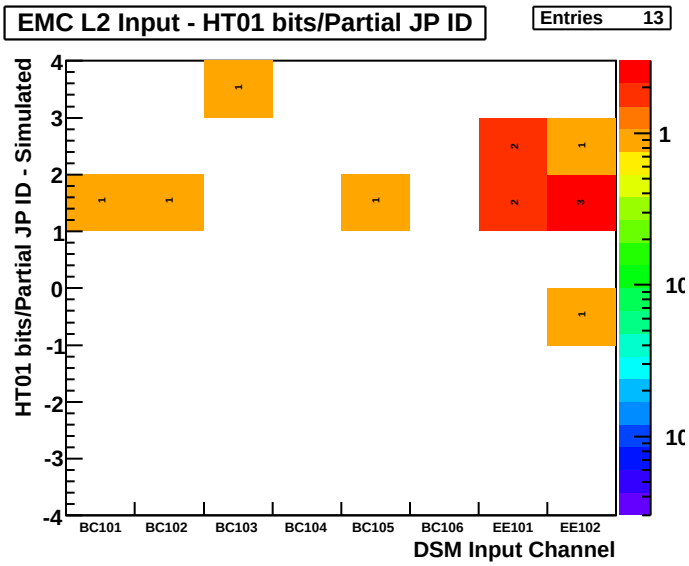

EMC L2 Input - JPZ/JPC bits
Entries 477664
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
214 211 137 183 60 106 34 39
619 553 376 524 151 267 104 71
1068 957 668 792 405 577 134 125
57817 57887 58827 58209 59092 58758 59436 59473

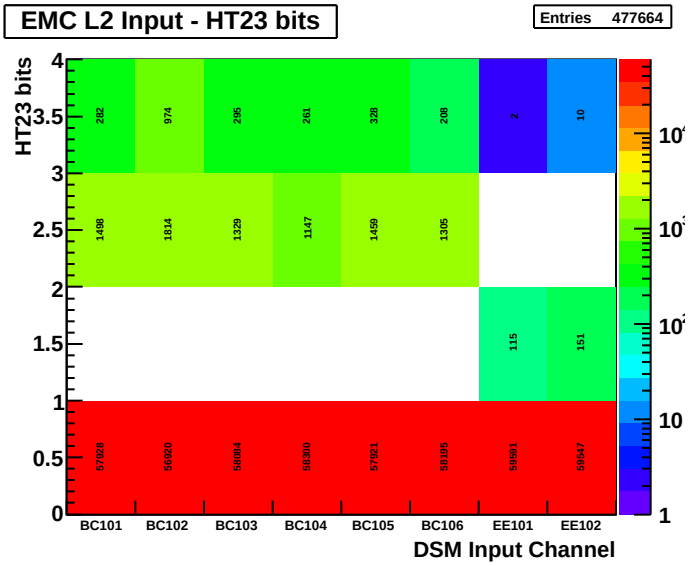

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1

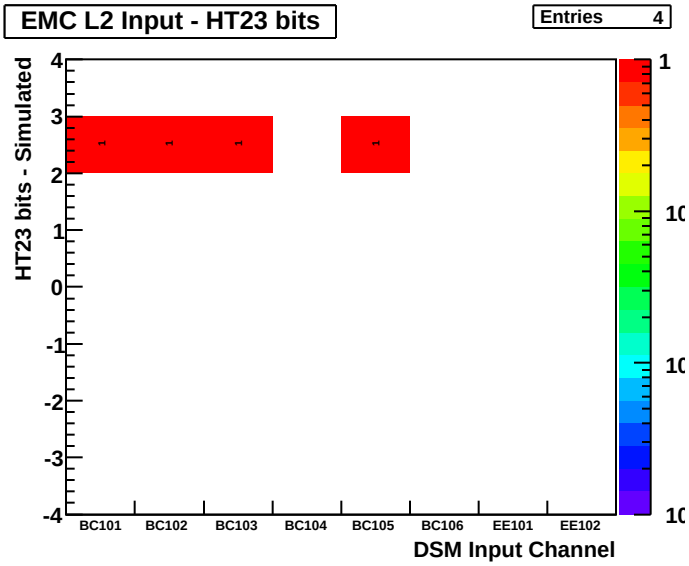

EMC L2 Input - Partial JP Sum
Entries 477664
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

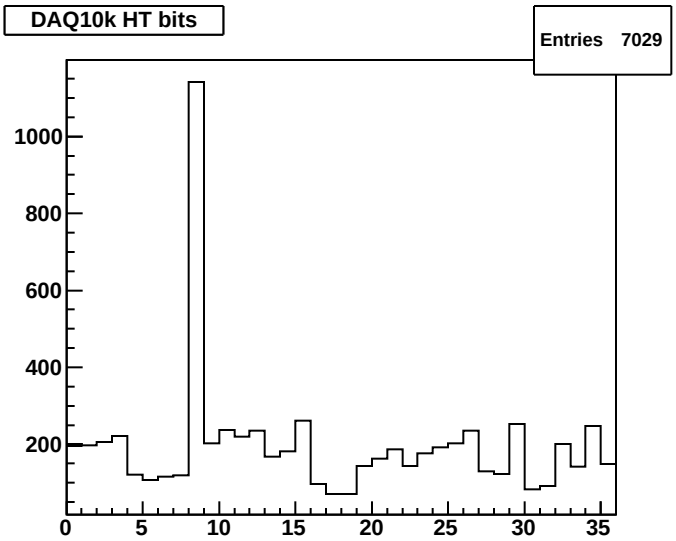

EMC L2 Input - Partial JP Sum
Entries 59
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

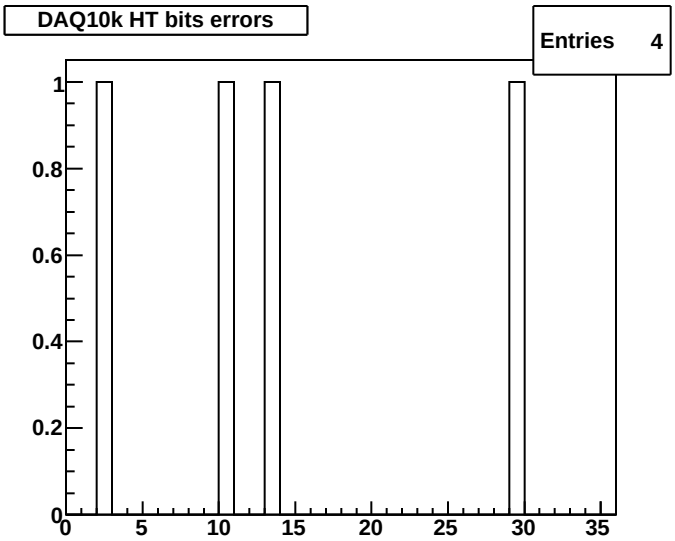

EMC L2 Input - HT01 bits/Partial JP ID
Entries 477664
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

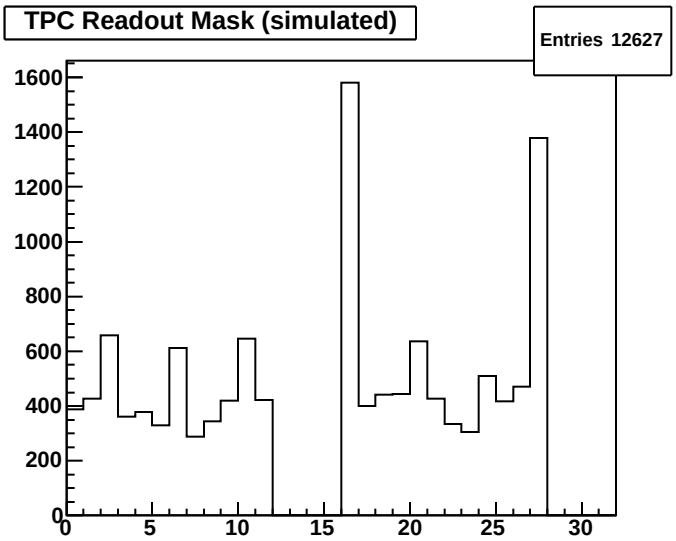

EMC L2 Input - HT01 bits/Partial JP ID
Entries 13
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

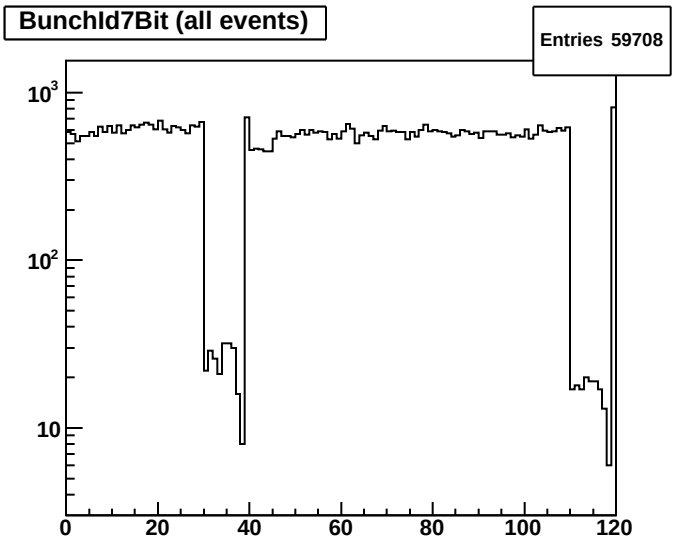

EMC L2 Input - HT23 bits
Entries 477664
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

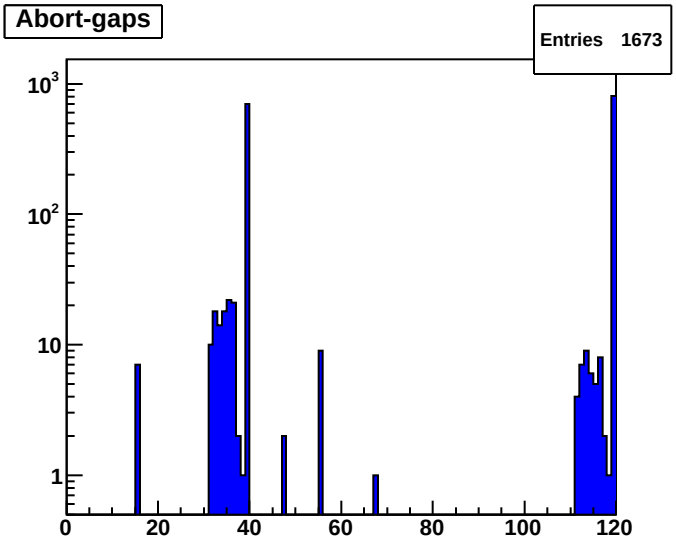

EMC L2 Input - HT23 bits
Entries 4
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

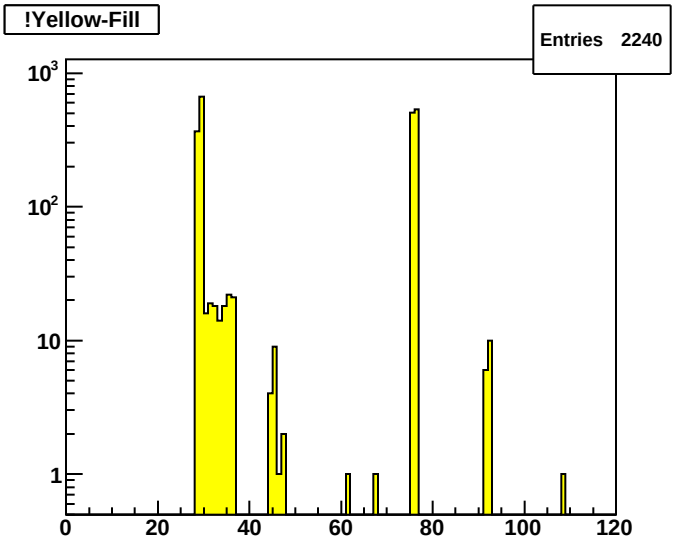

DAQ10k HT bits
Entries 7029

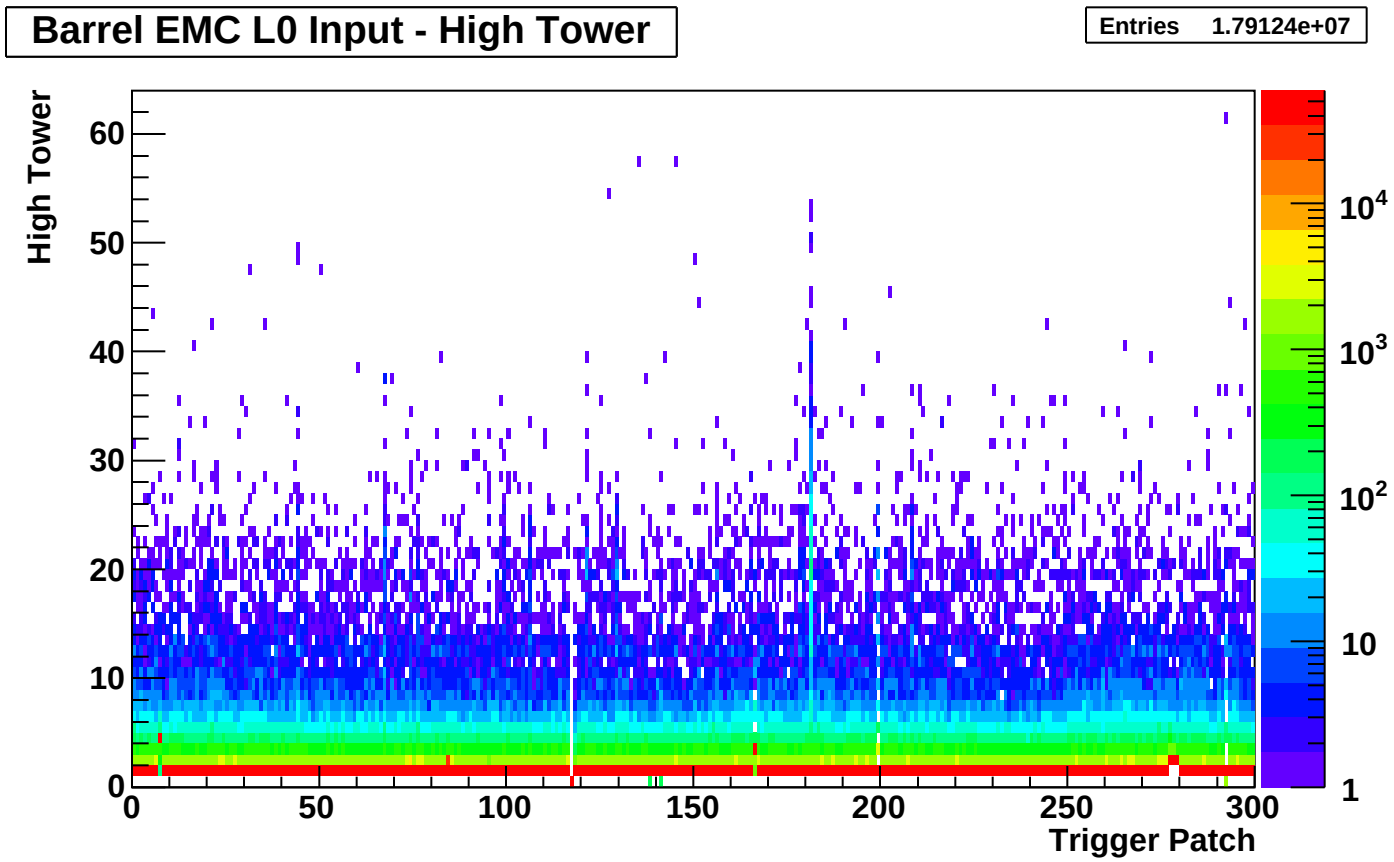

DAQ10k HT bits errors
Entries 4

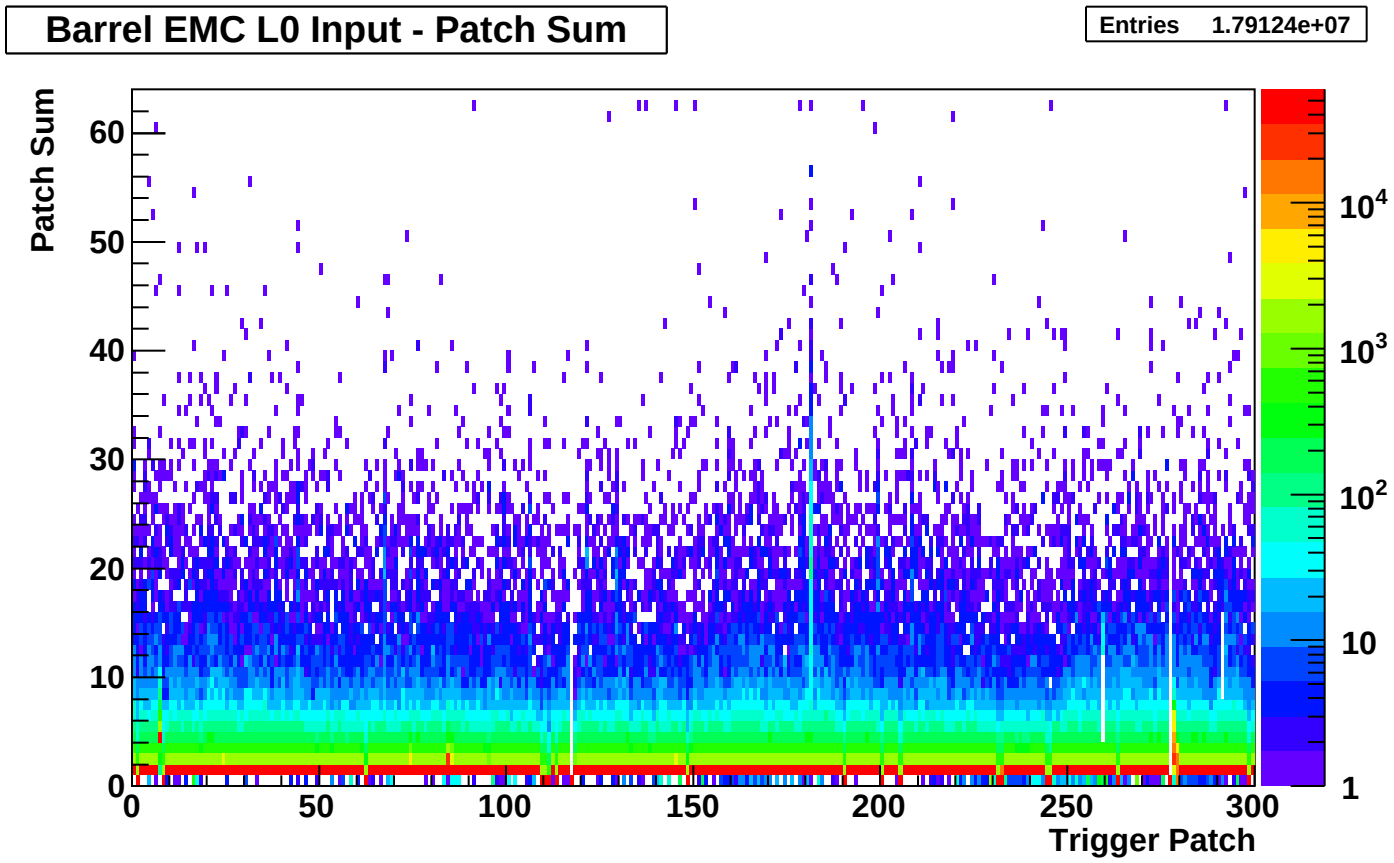

TPC Readout Mask (simulated)
Entries 12627

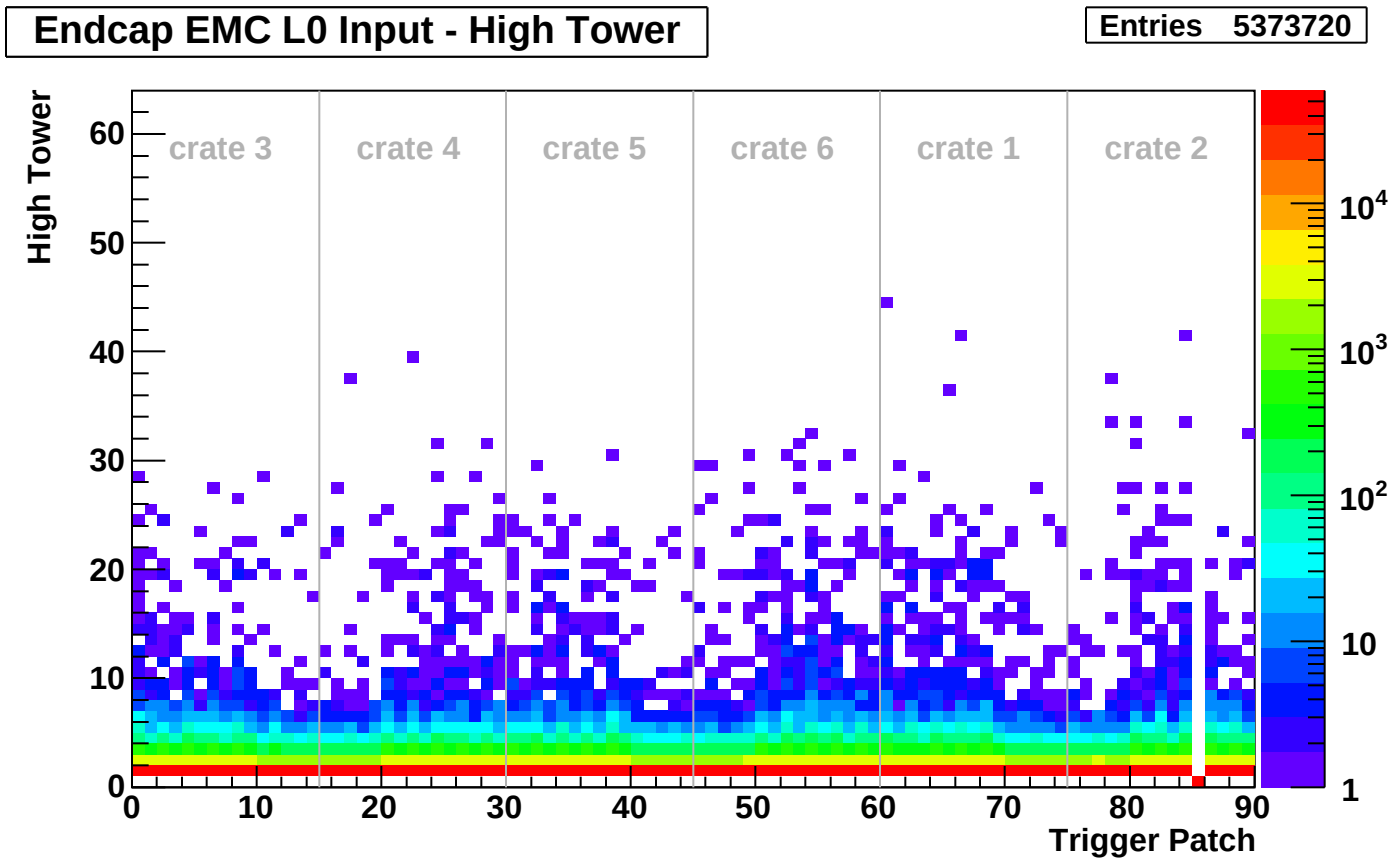

BunchId7Bit (all events)
Entries 59708

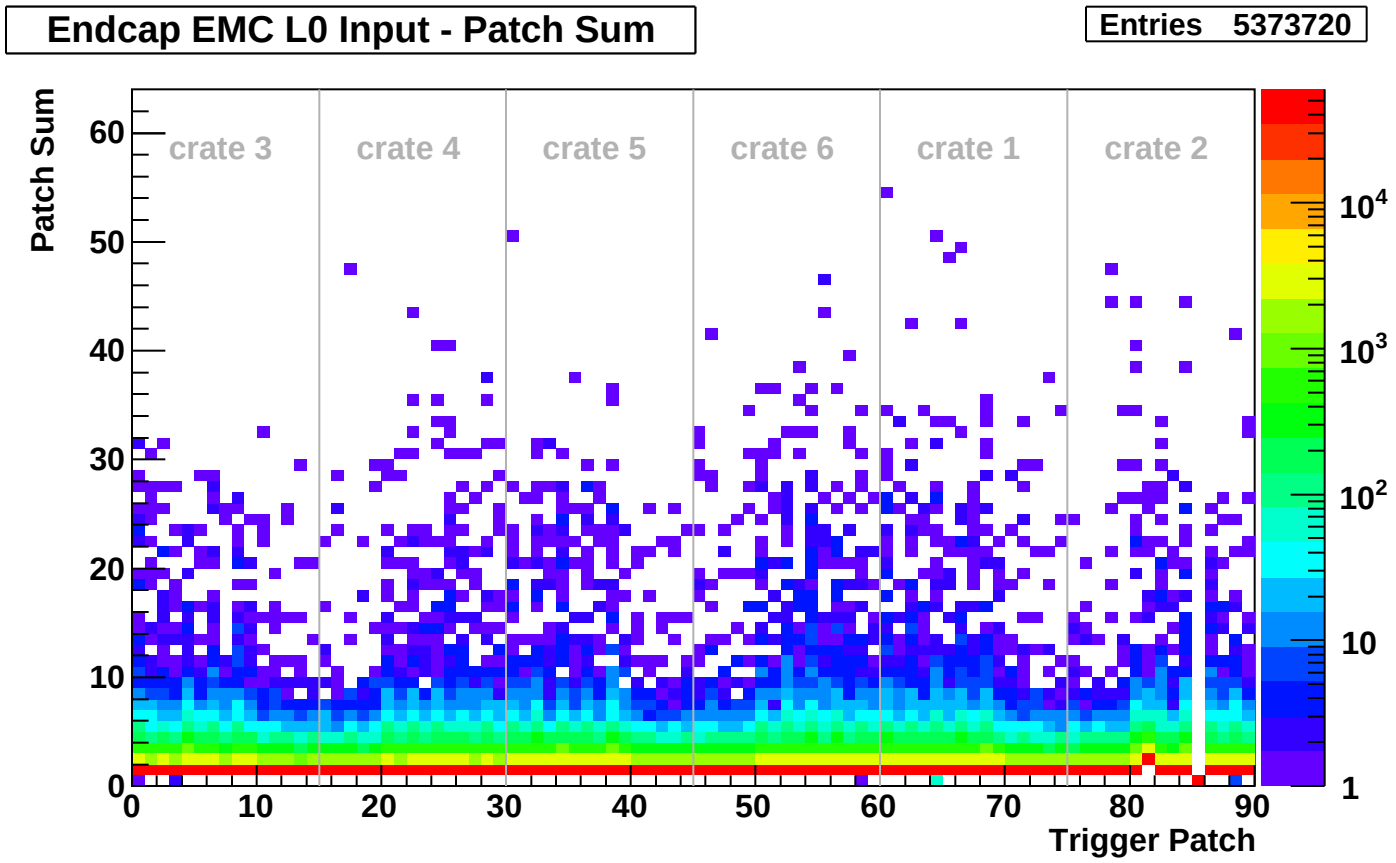

Abort-gaps
Entries 1673

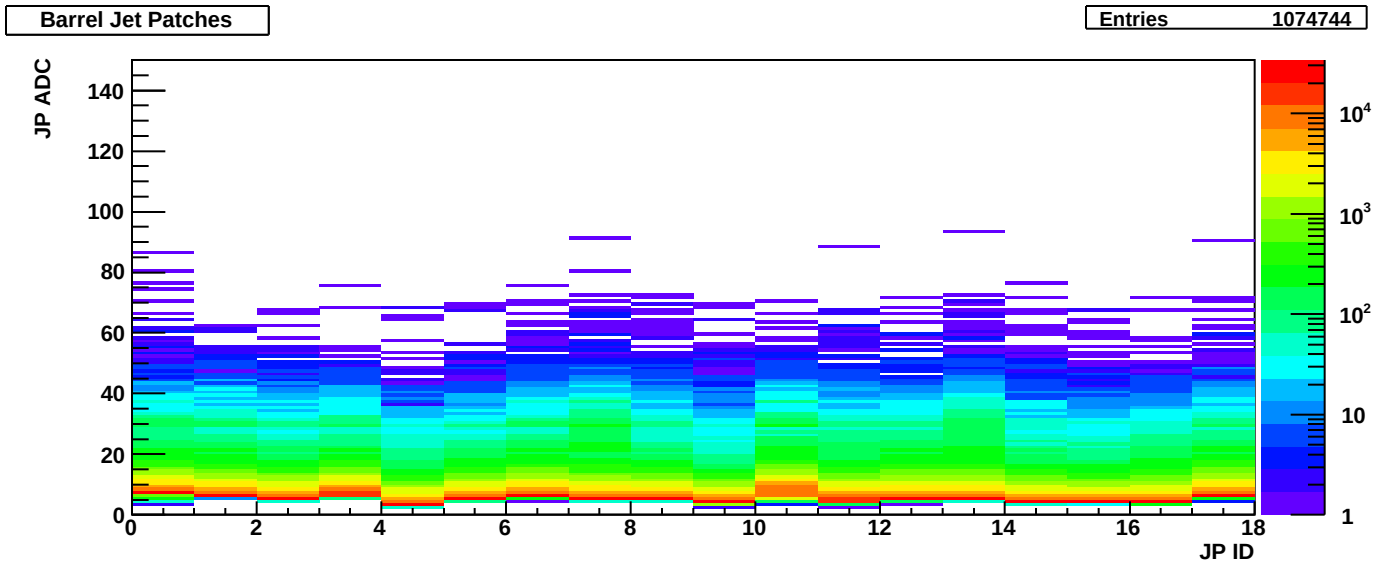

!Yellow-Fill
Entries 2240

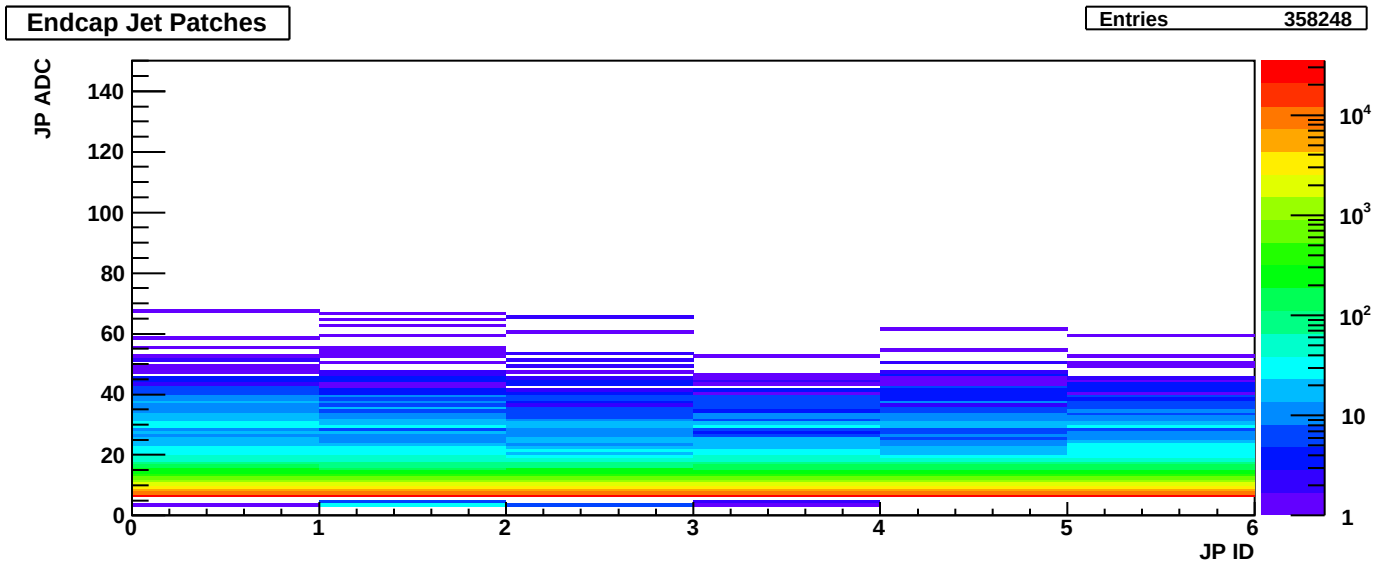

Barrel EMC L0 Input - High Tower
Entries 1.79124e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

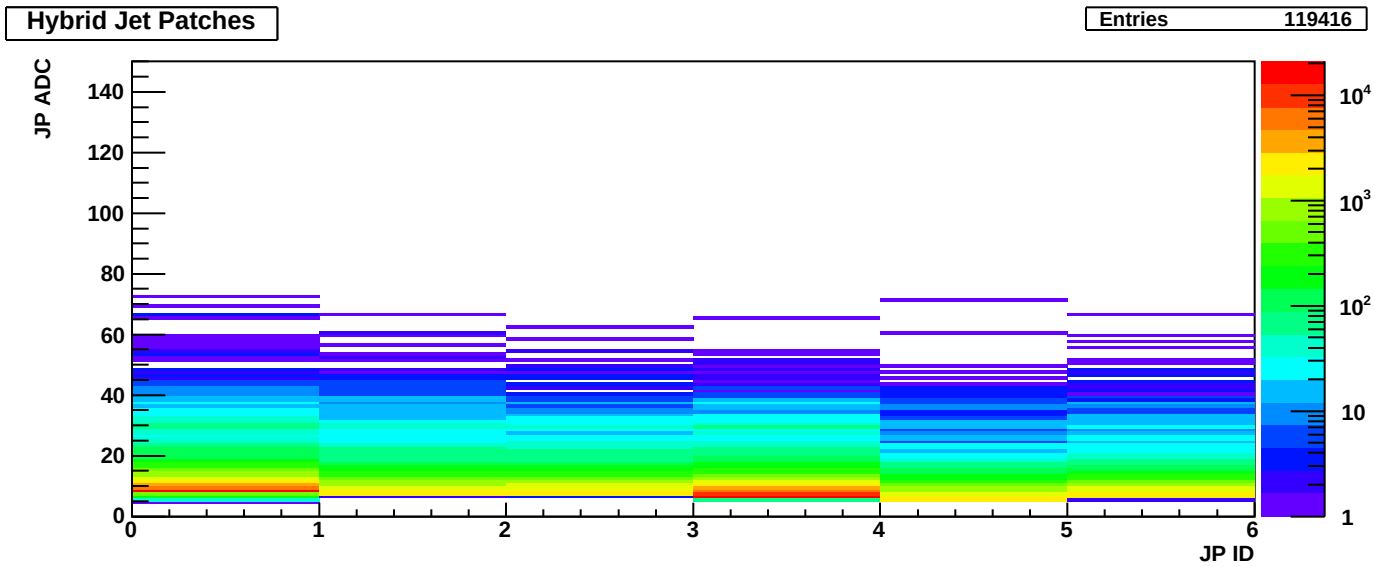

Barrel EMC L0 Input - Patch Sum
Entries 1.79124e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

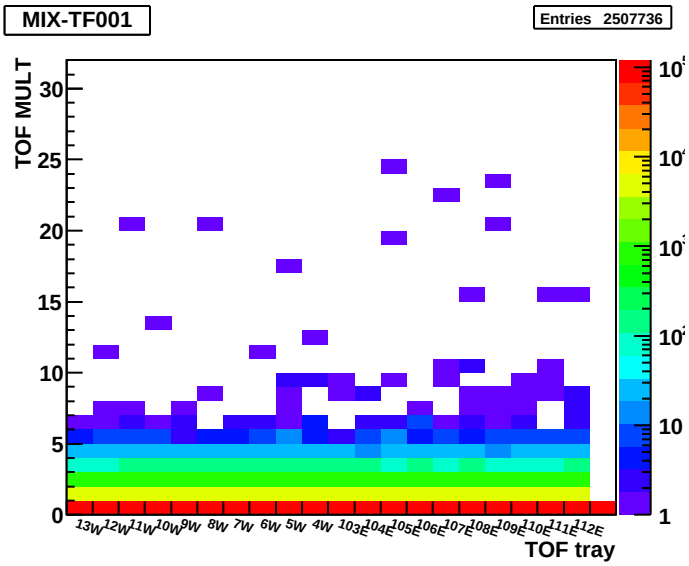

Endcap EMC L0 Input - High Tower
Entries 5373720
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

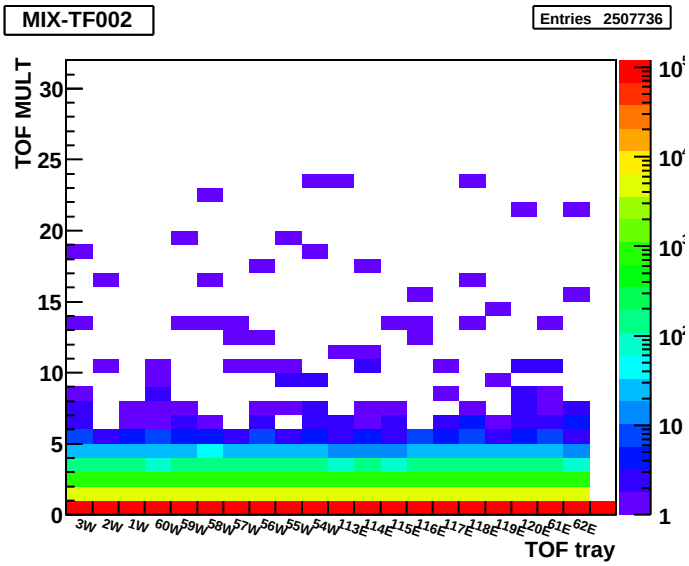

Endcap EMC L0 Input - Patch Sum
Entries 5373720
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

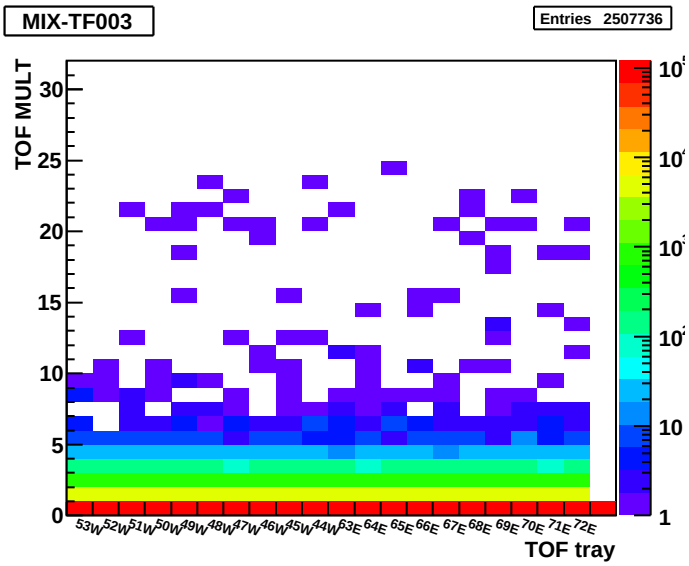

Barrel Jet Patches
Entries 1074744
JP ADC
JP ID

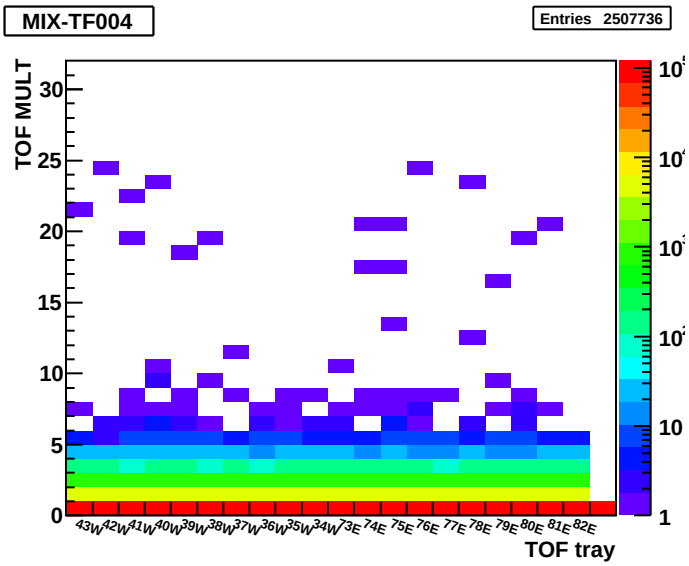

Endcap Jet Patches
Entries 358248
JP ADC
JP ID

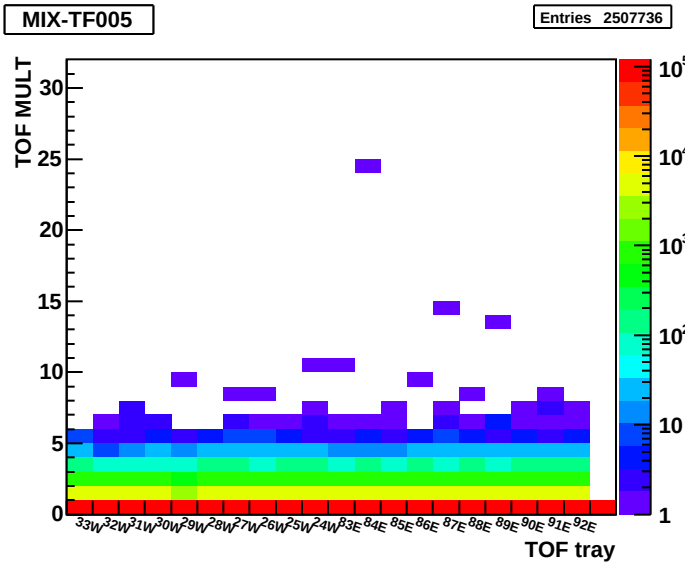

Hybrid Jet Patches
Entries 119416
JP ADC
JP ID

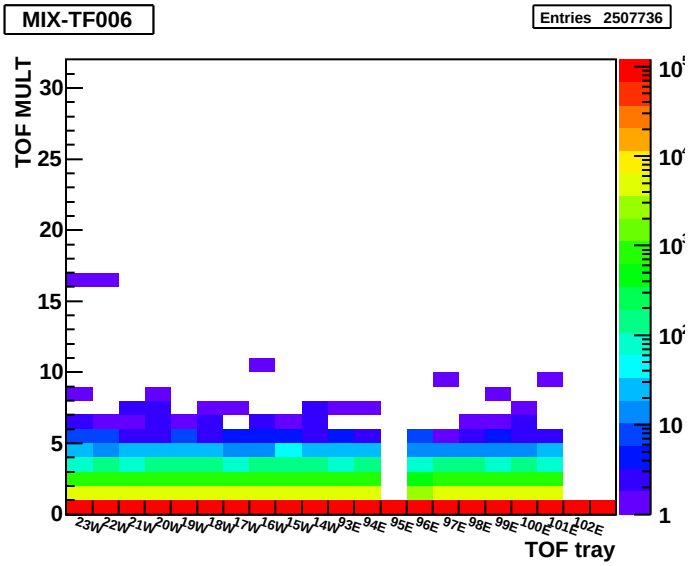

MIX-TF001
Entries 2507736
TOF MULT
TOF tray

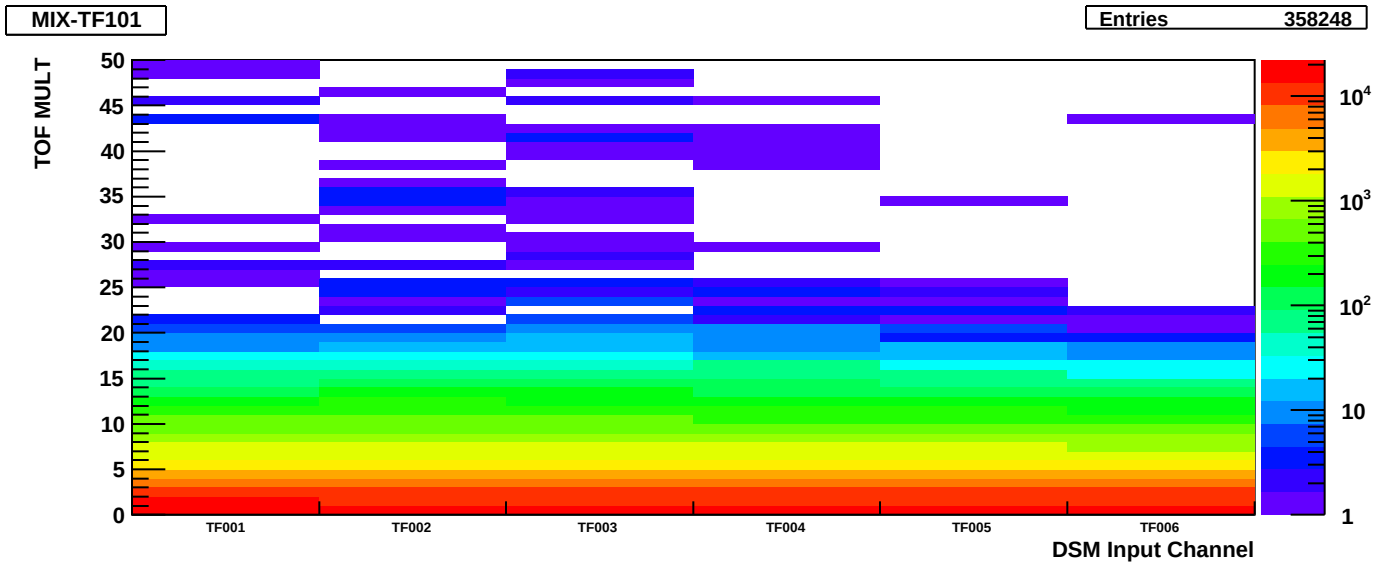

MIX-TF002
Entries 2507736
TOF MULT
TOF tray

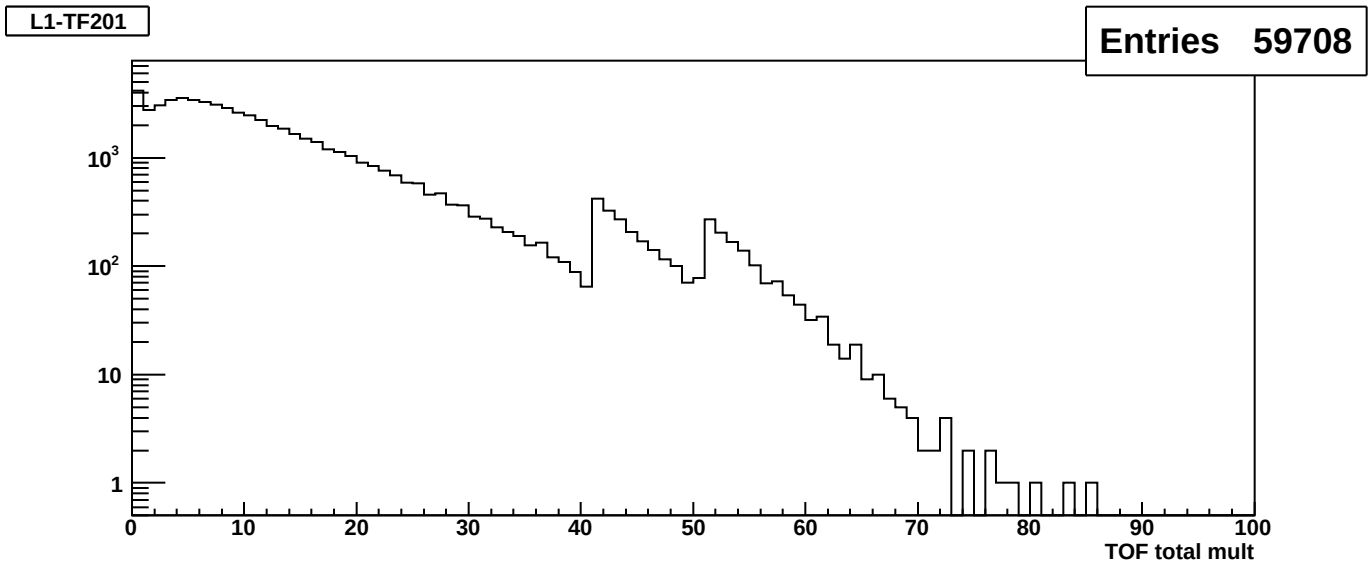

MIX-TF003
Entries 2507736
TOF MULT
TOF tray

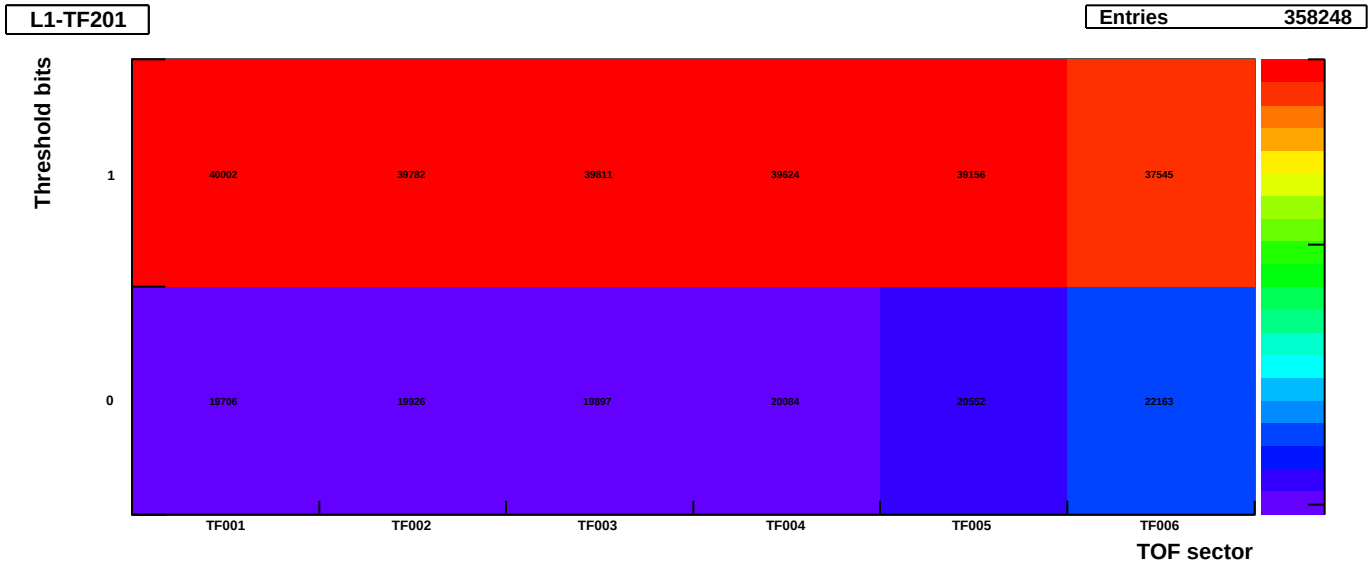

MIX-TF004
Entries 2507736
TOF MULT
TOF tray

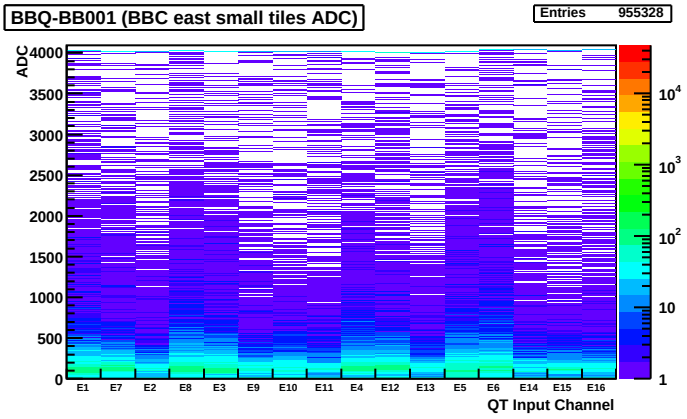

MIX-TF005
Entries 2507736
TOF MULT
TOF tray

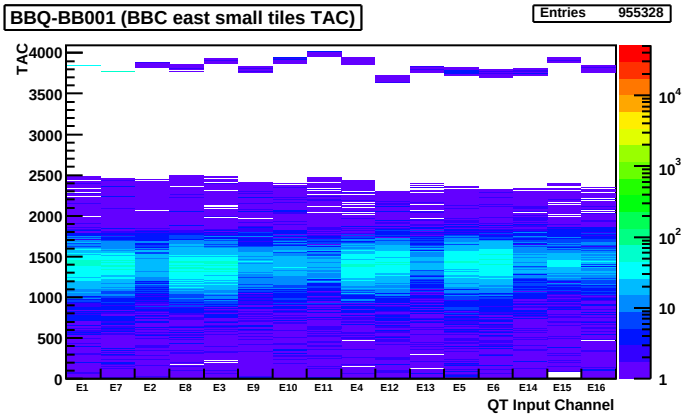

MIX-TF006
Entries 2507736
TOF MULT
TOF tray

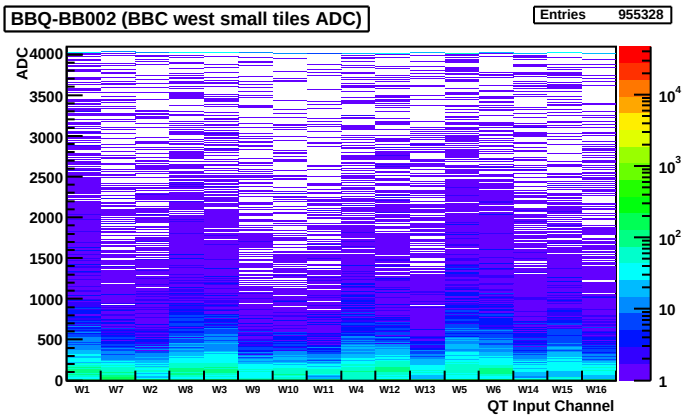

MIX-TF101
Entries 358248
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

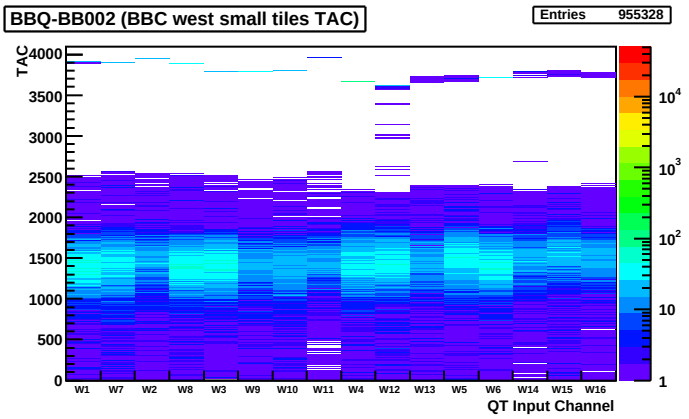

L1-TF201
Entries 59708
TOF total mult

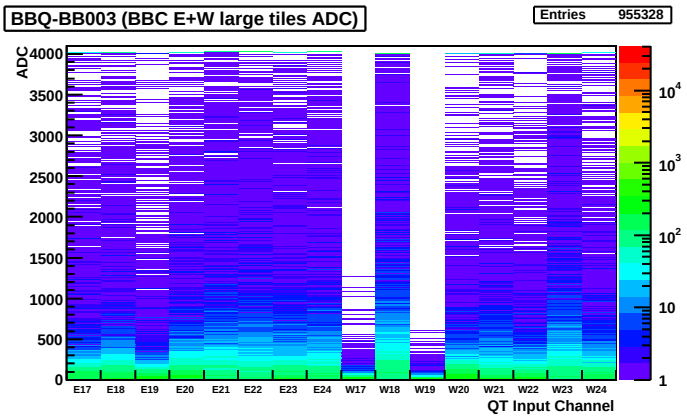

L1-TF201
Entries 358248
Threshold bits
40002
39782
39811
39624
39156
37545
19706
19926
19897
20084
20552
22163
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

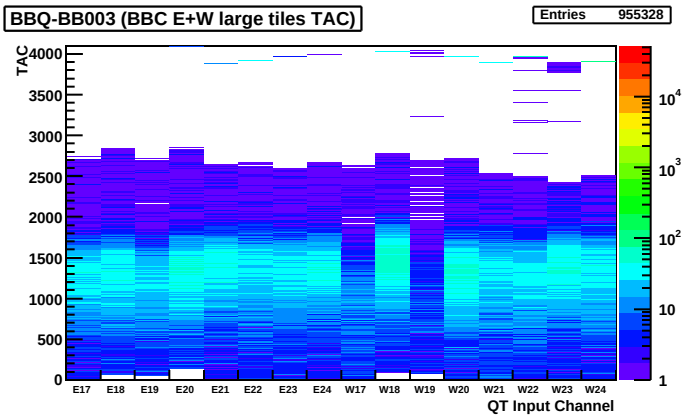

BBQ-BB001 (BBC east small tiles ADC)
Entries 955328
ADC
QT Input Channel

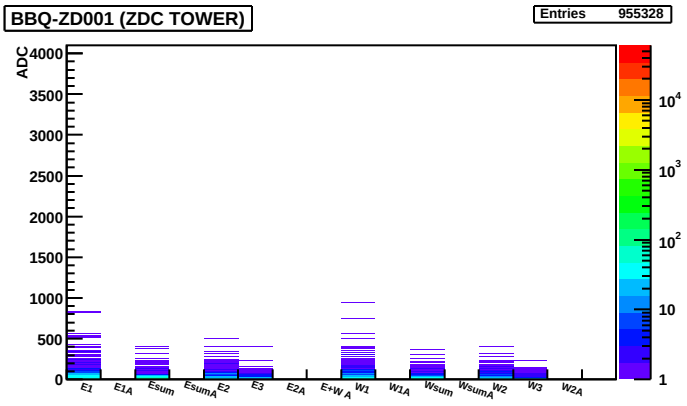

BBQ-BB001 (BBC east small tiles TAC)
Entries 955328
TAC
QT Input Channel

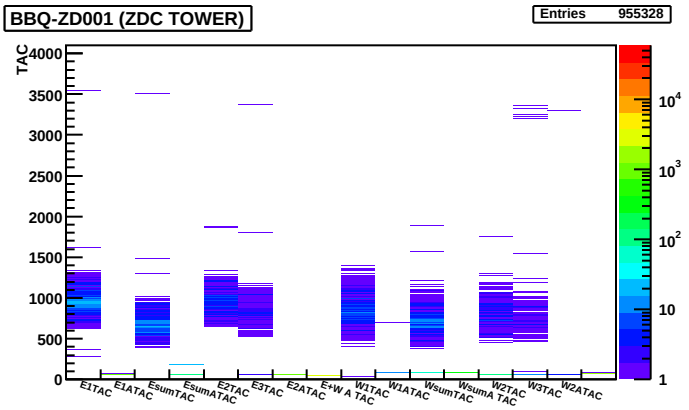

BBQ-BB002 (BBC west small tiles ADC)
Entries 955328
ADC
QT Input Channel

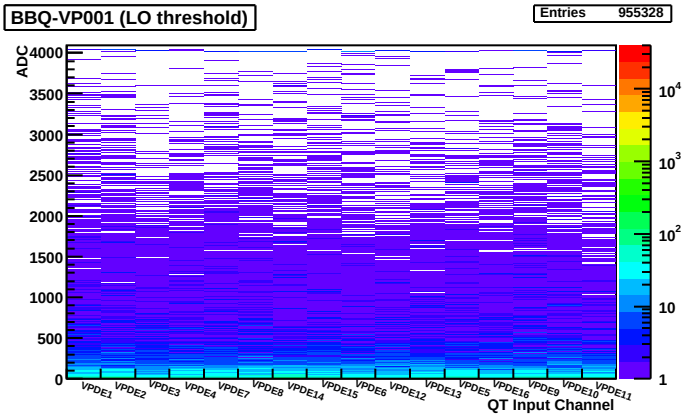

BBQ-BB002 (BBC west small tiles TAC)
Entries 955328
TAC
QT Input Channel

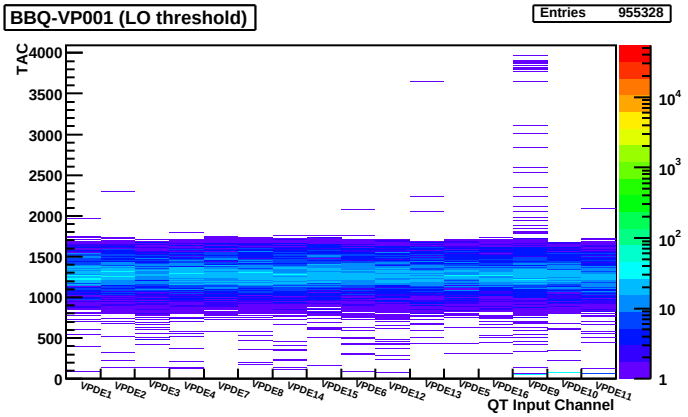

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 955328
ADC
QT Input Channel

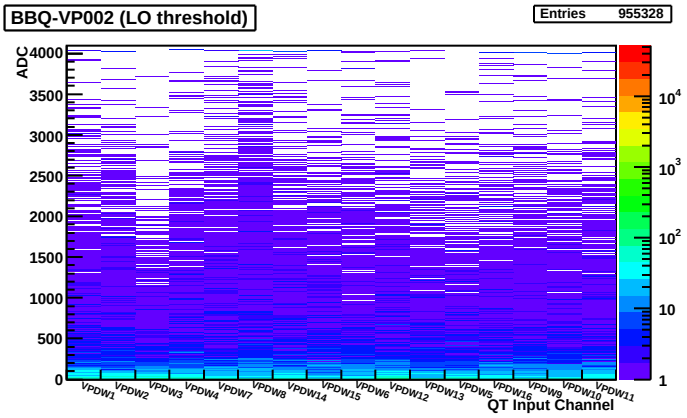

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 955328
TAC
QT Input Channel

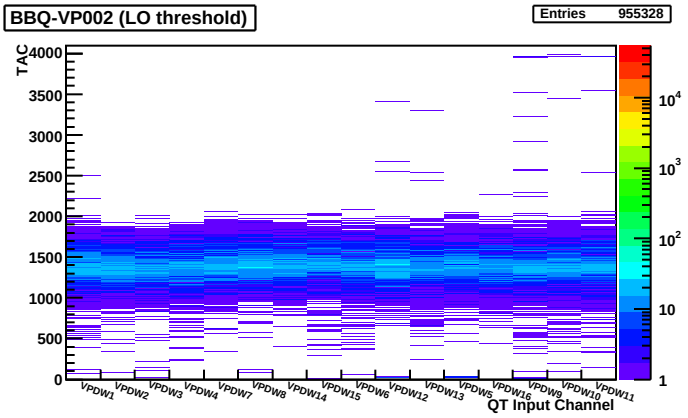

BBQ-ZD001 (ZDC TOWER)
Entries 955328
ADC

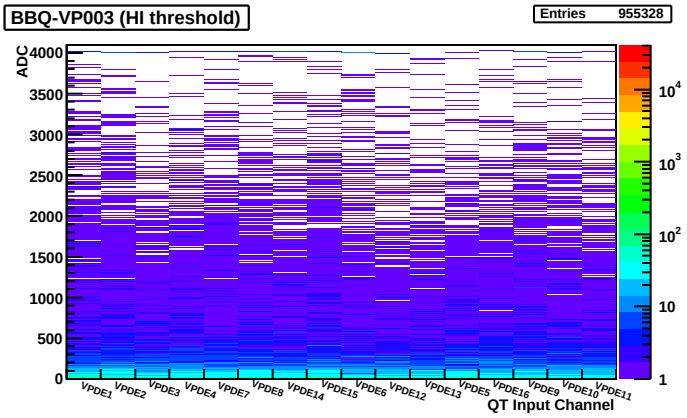

BBQ-ZD001 (ZDC TOWER)
Entries 955328
TAC

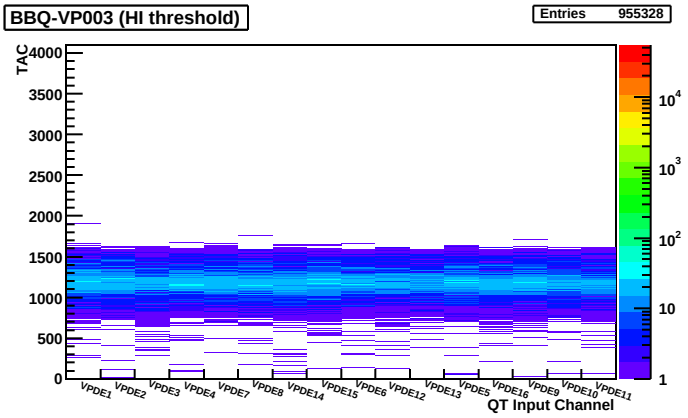

BBQ-VP001 (LO threshold)
Entries 955328
ADC
QT Input Channel

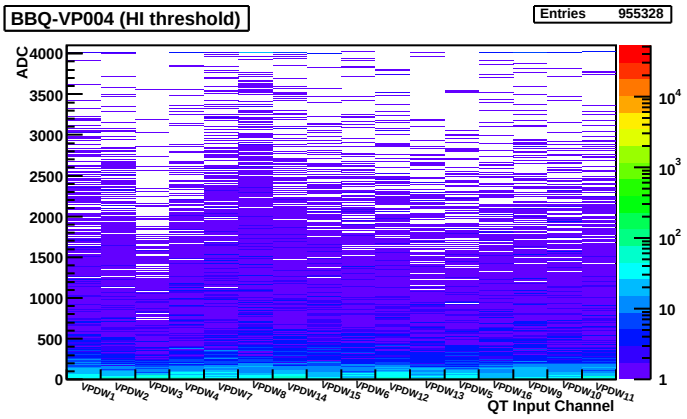

BBQ-VP001 (LO threshold)
Entries 955328
TAC
QT Input Channel

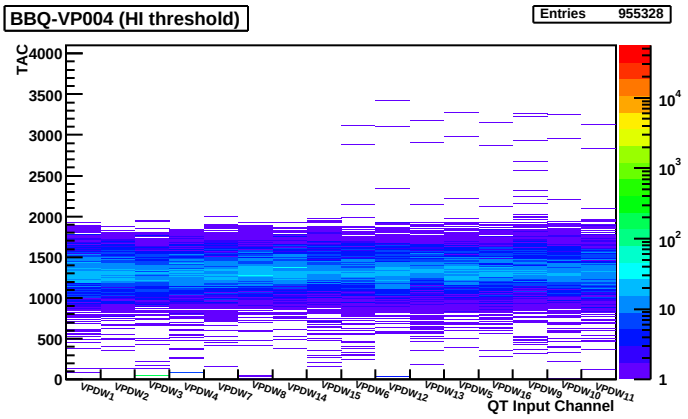

BBQ-VP002 (LO threshold)
Entries 955328
ADC
QT Input Channel

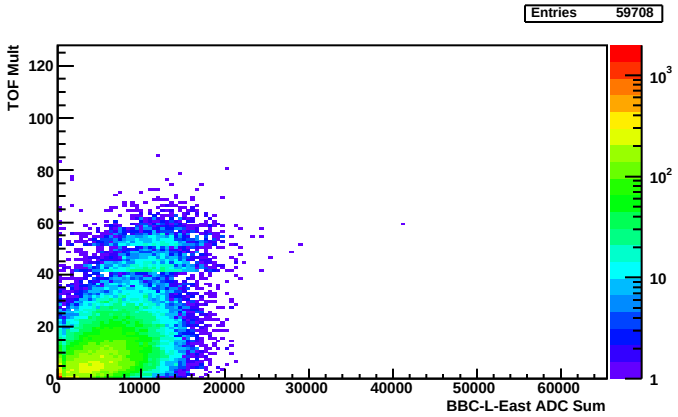

BBQ-VP002 (LO threshold)
Entries 955328
TAC
QT Input Channel

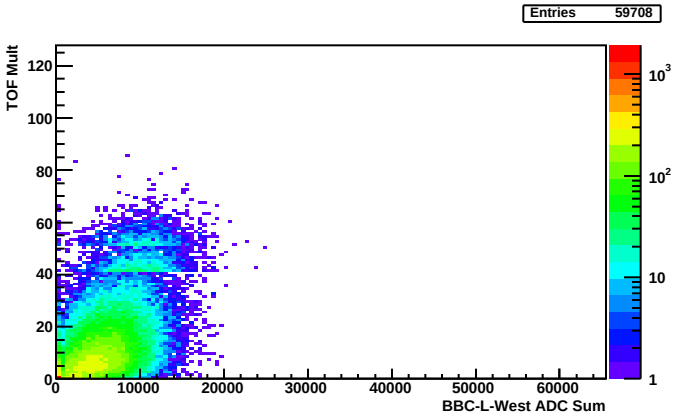

BBQ-VP003 (HI threshold)
Entries 955328
ADC
QT Input Channel

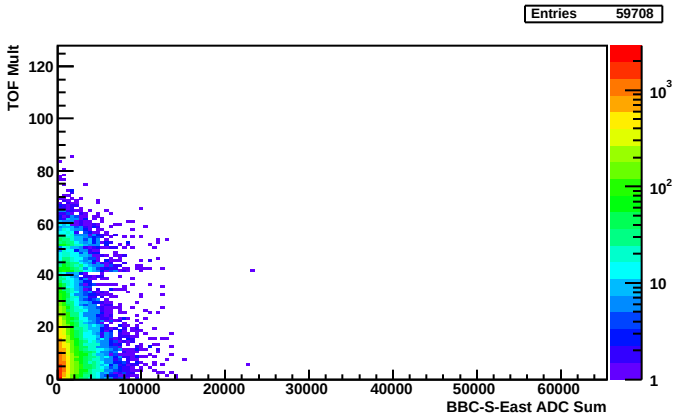

BBQ-VP003 (HI threshold)
Entries 955328
TAC
QT Input Channel

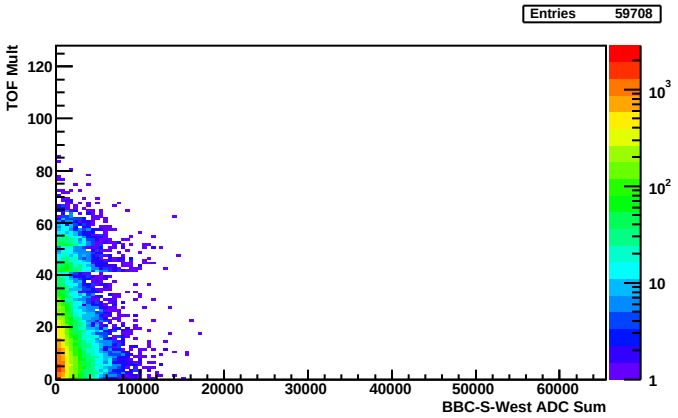

BBQ-VP004 (HI threshold)
Entries 955328
ADC
QT Input Channel

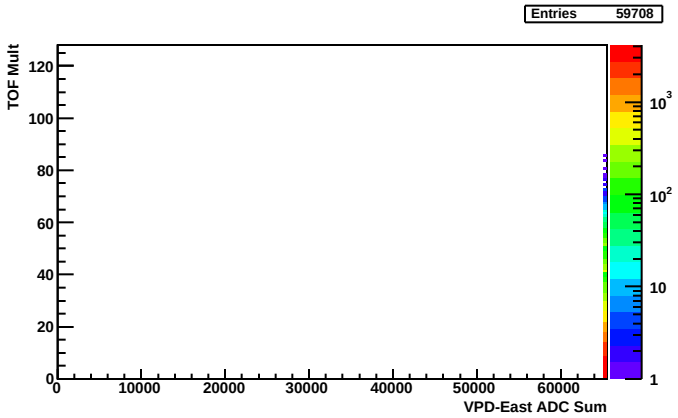

BBQ-VP004 (HI threshold)
Entries 955328
TAC
QT Input Channel

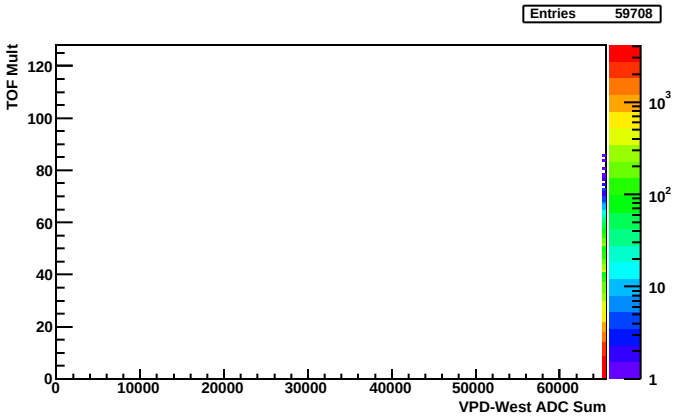

Entries 59708
TOF Mult
BBC-L-East ADC Sum

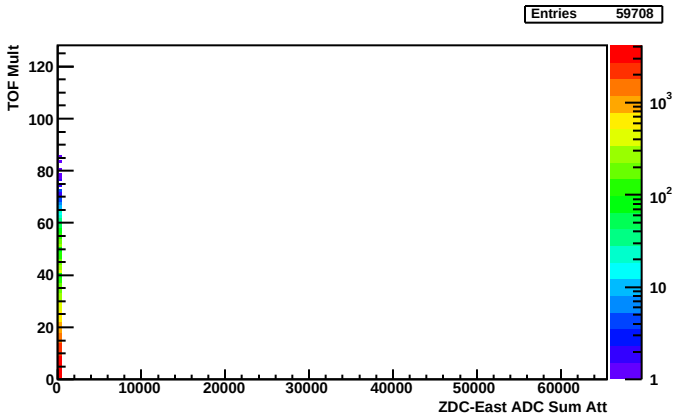

Entries 59708
TOF Mult
BBC-L-West ADC Sum

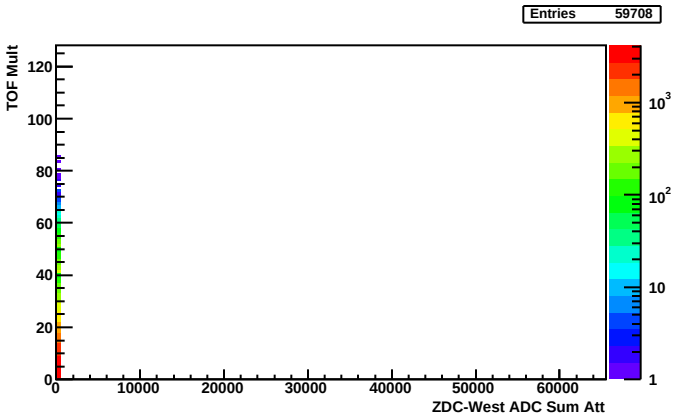

Entries 59708
TOF Mult
BBC-S-East ADC Sum

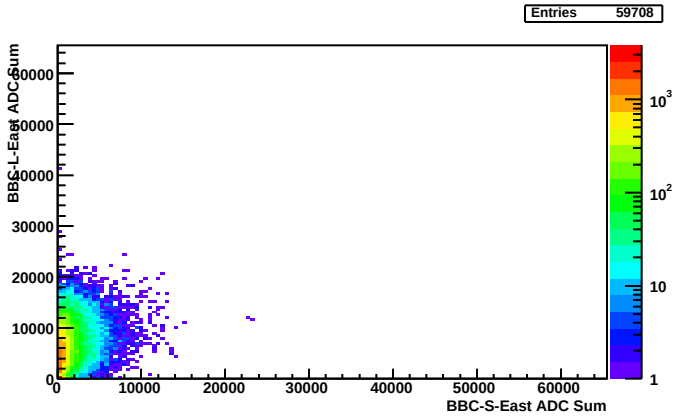

Entries 59708
TOF Mult
BBC-S-West ADC Sum

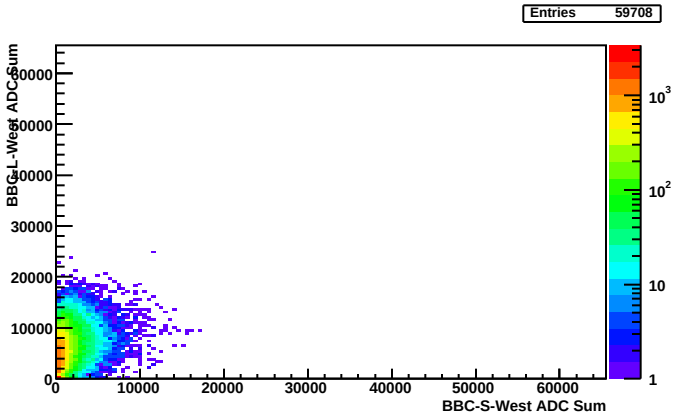

Entries 59708
TOF Mult
VPD-East ADC Sum

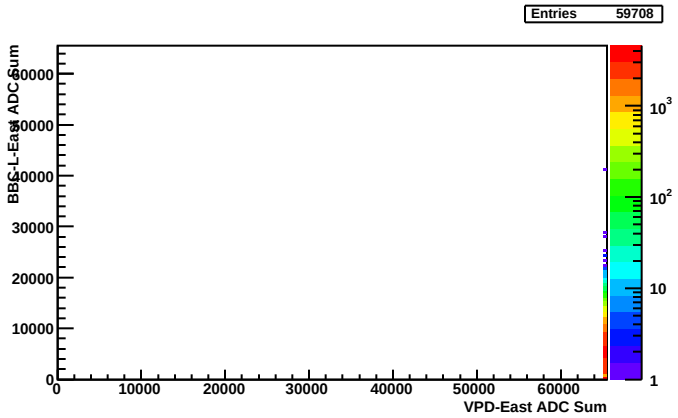

Entries 59708
TOF Mult
VPD-West ADC Sum

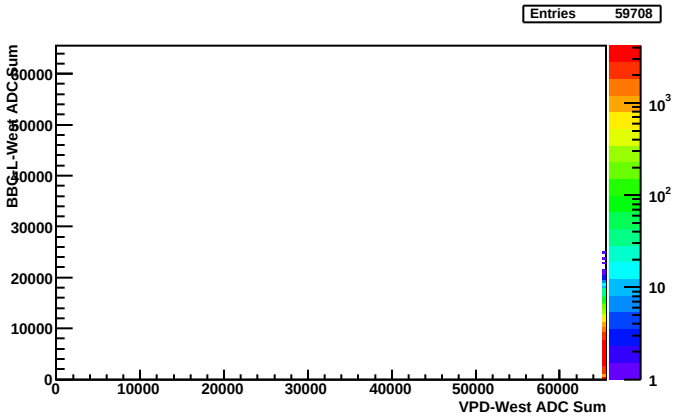

Entries 59708
TOF Mult
ZDC-East ADC Sum Att

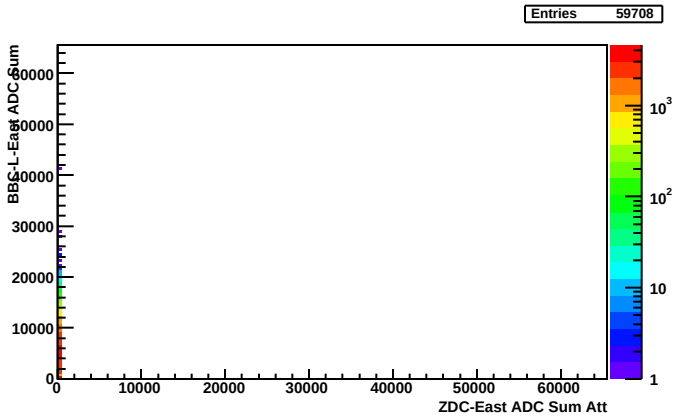

Entries 59708
TOF Mult
ZDC-West ADC Sum Att


Entries 59708
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 59708
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 59708
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 59708
BBC-L-West ADC Sum
VPD-West ADC Sum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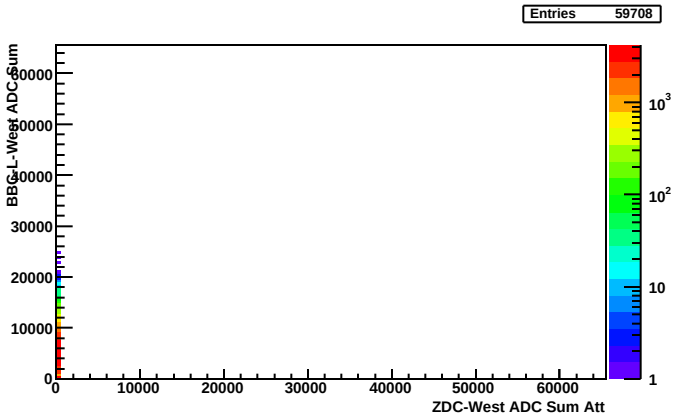
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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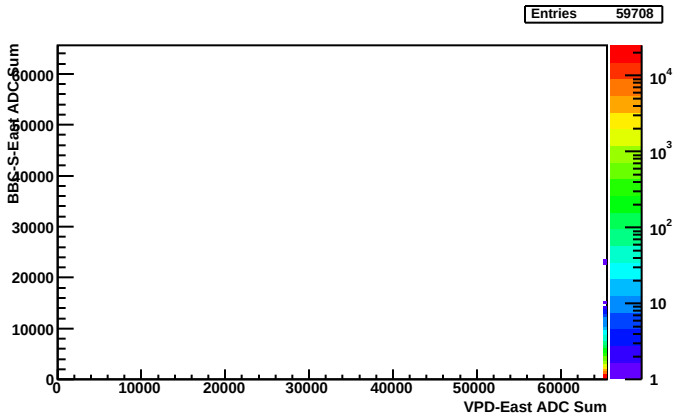
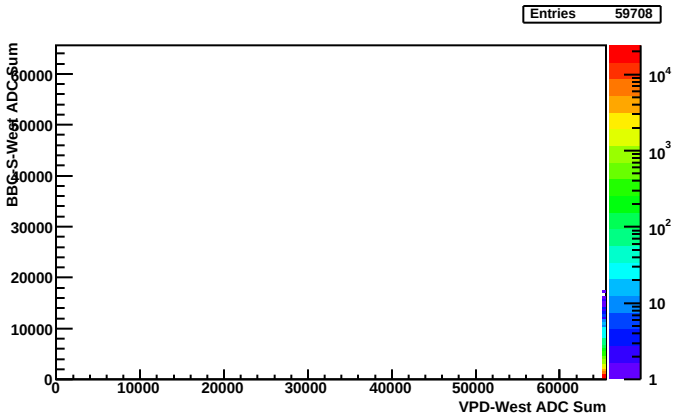
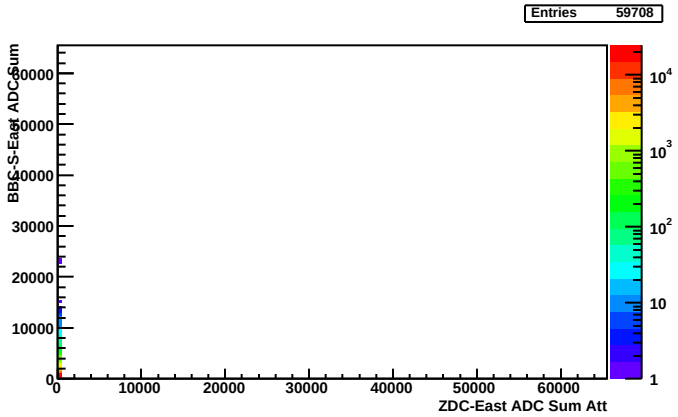
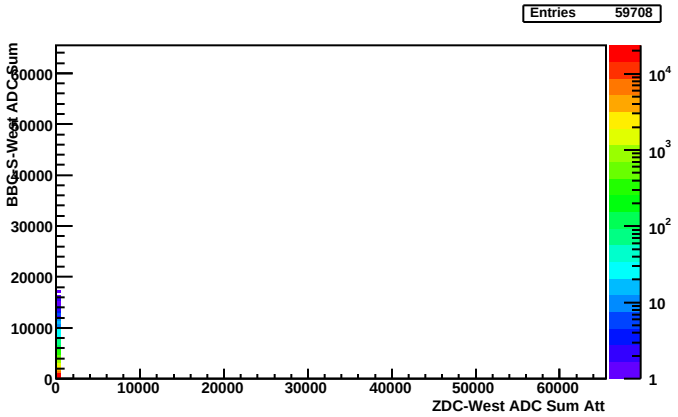
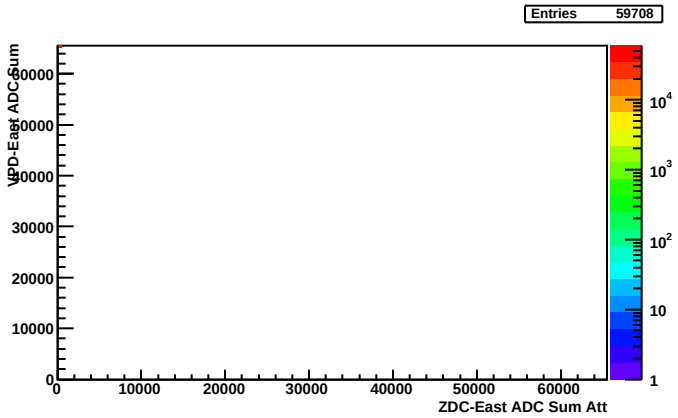
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

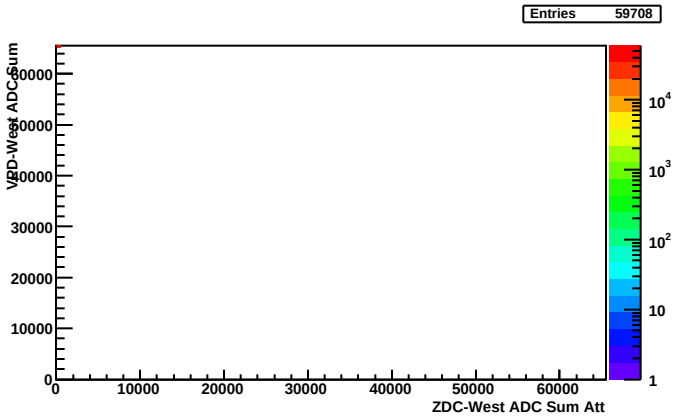

Entries 59708
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 59708
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 59708
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 59708
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 59708
VPD-East ADC Sum
ZDC-East ADC Sum Att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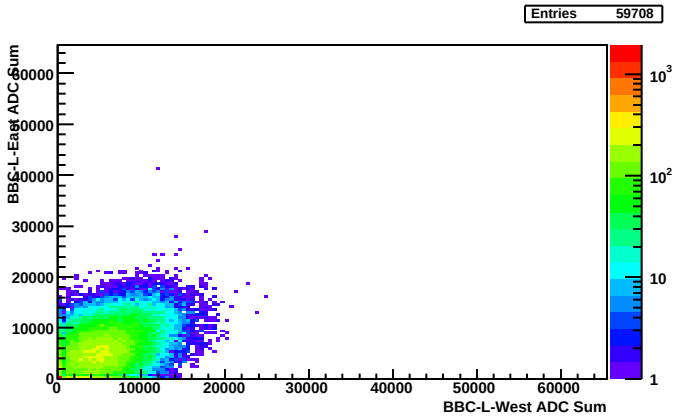
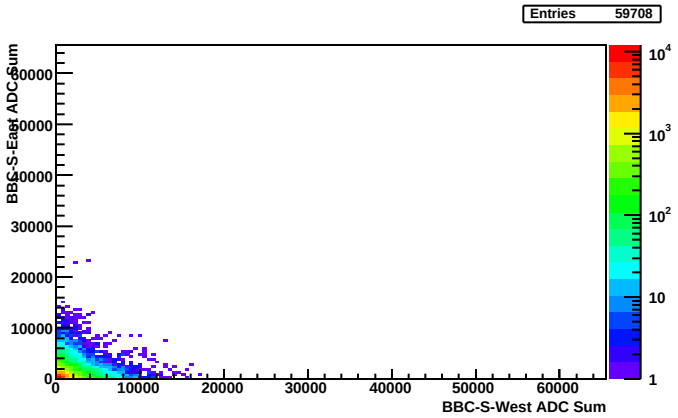
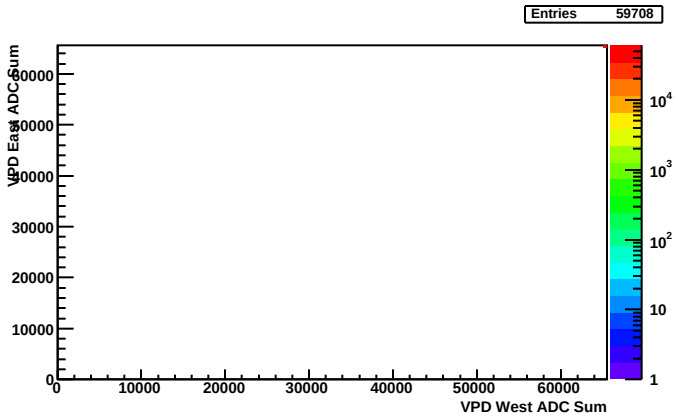
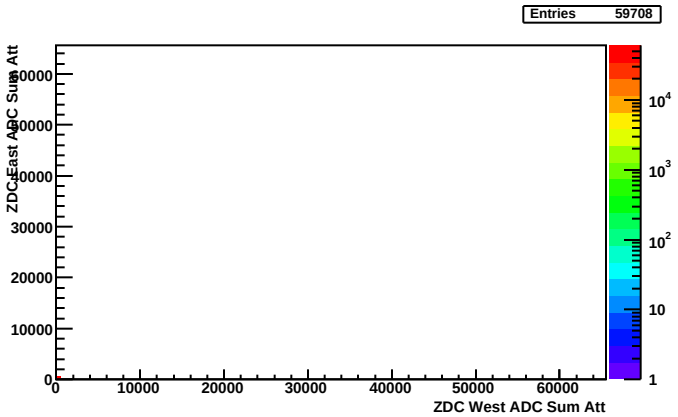
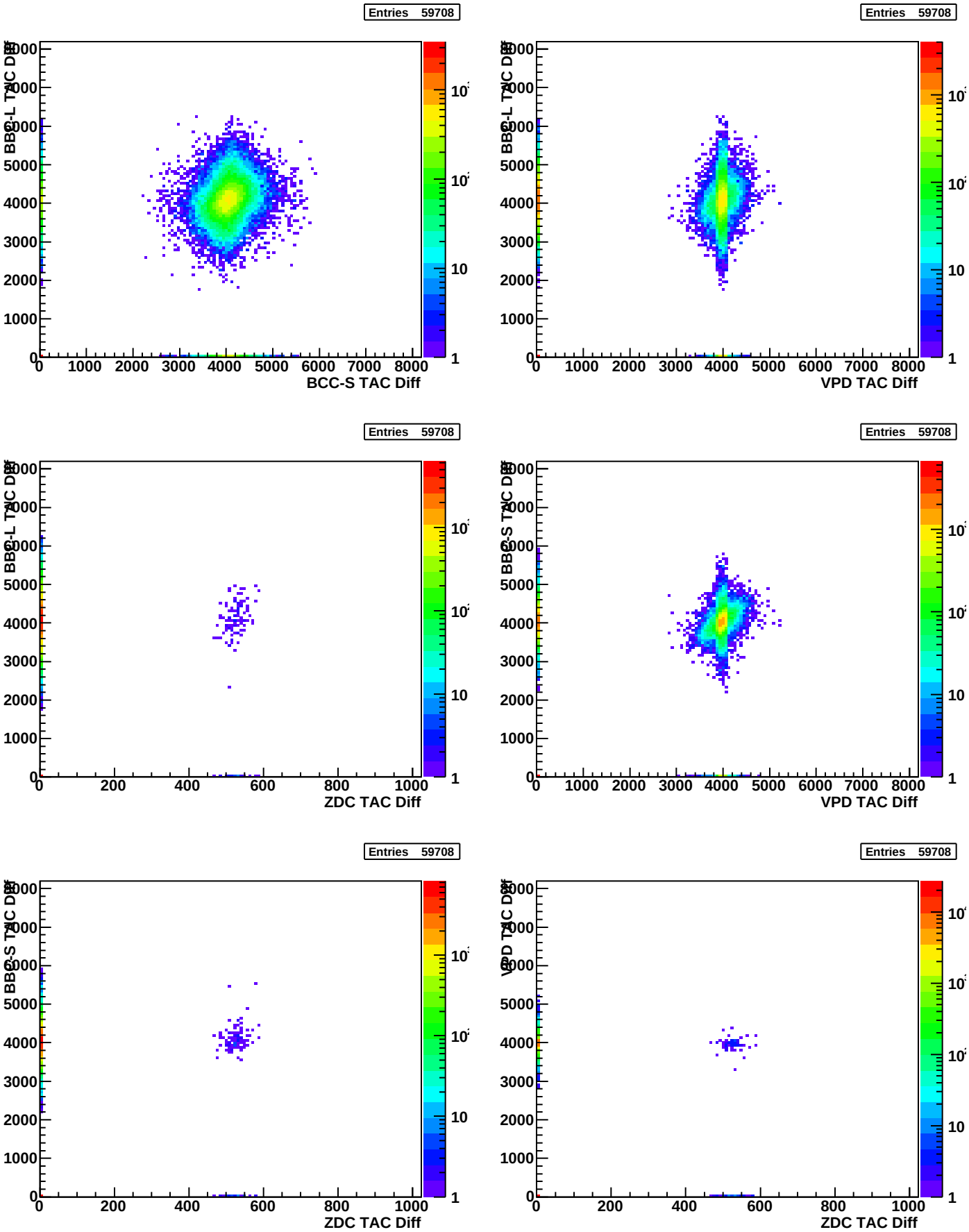
VPD-West ADC Sum
ZDC-West ADC Sum Att

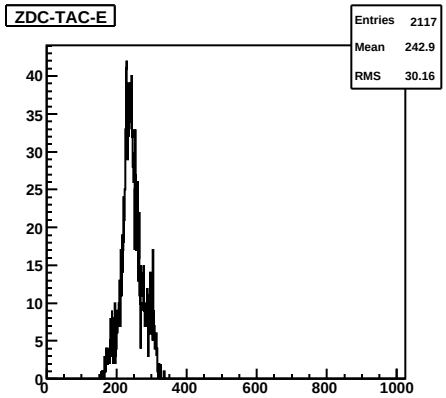

Entries 59708
BBC-L-East ADC Sum
BBC-L-West ADC Sum

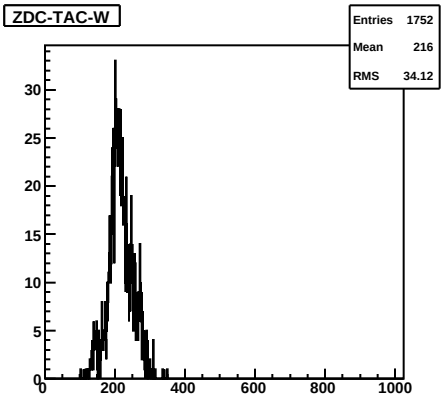

Entries 59708
BBC-S-East ADC Sum
BBC-S-West ADC Sum

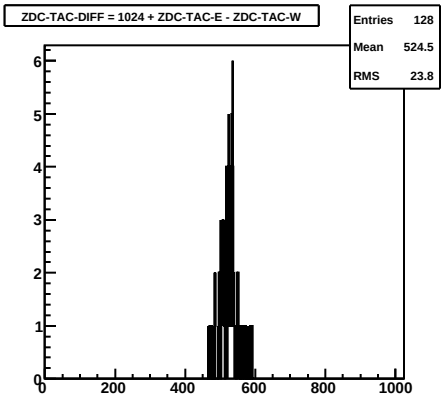

Entries 59708
VPD East ADC Sum
VPD West ADC Sum

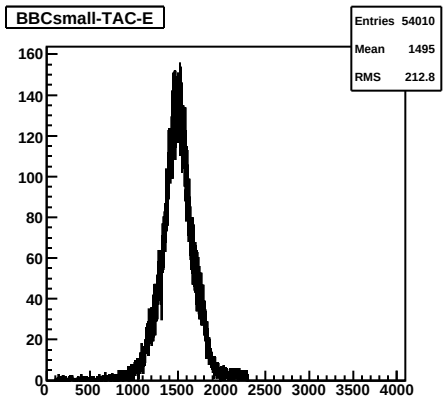

Entries 59708
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

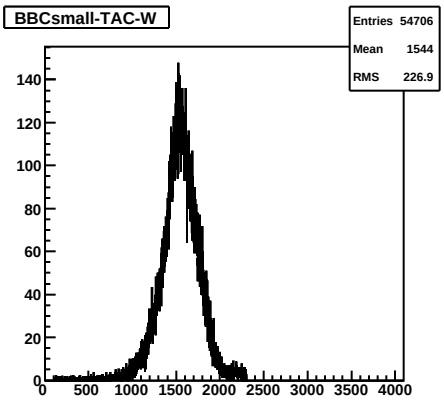

Entries 59708
BBC-L TAC Diff
BCC-S TAC Diff
Entries 59708
BBC-L TAC Diff
VPD TAC Diff
Entries 59708
BBC-L TAC Diff
ZDC TAC Diff
Entries 59708
BBC-S TAC Diff
VPD TAC Diff
Entries 59708
BBC-S TAC Diff
ZDC TAC Diff
Entries 59708
VPD TAC Diff
ZDC TAC Diff

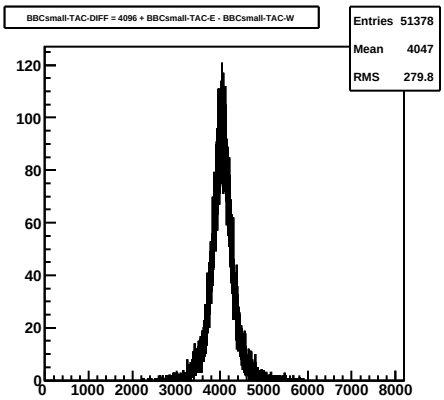

ZDC-TAC-E
Entries 2117
Mean 242.9
RMS 30.16

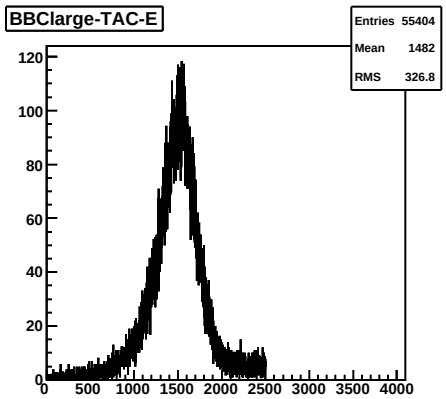

ZDC-TAC-W
Entries 1752
Mean 216
RMS 34.12

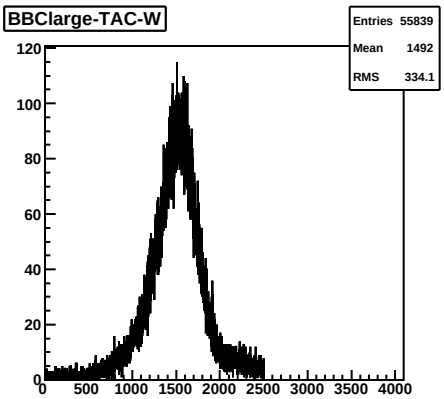

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 128
Mean 524.5
RMS 23.8

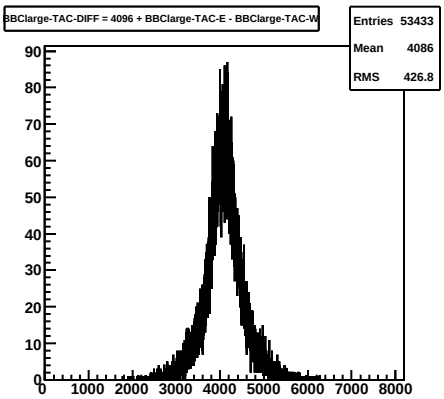

BBCsmall-TAC-E
Entries 54010
Mean 1495
RMS 212.8

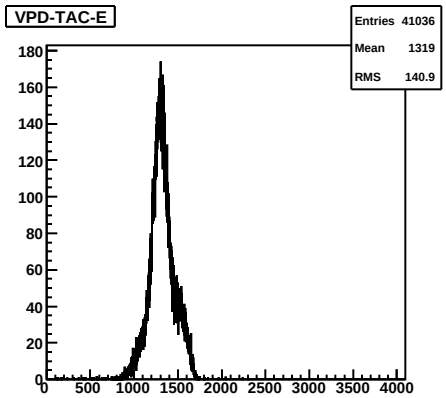

BBCsmall-TAC-W
Entries 54706
Mean 1544
RMS 226.9

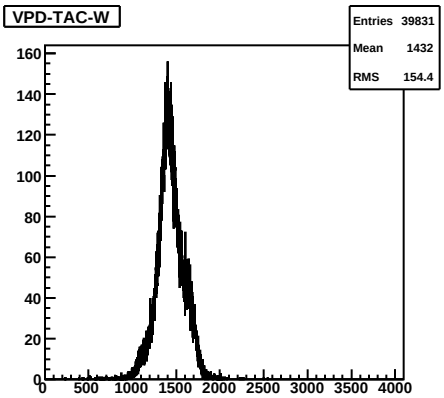

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 51378
Mean 4047
RMS 279.8

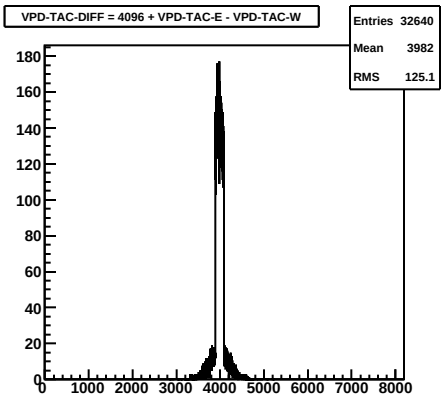

BBClarge-TAC-E
Entries 55404
Mean 1482
RMS 326.8

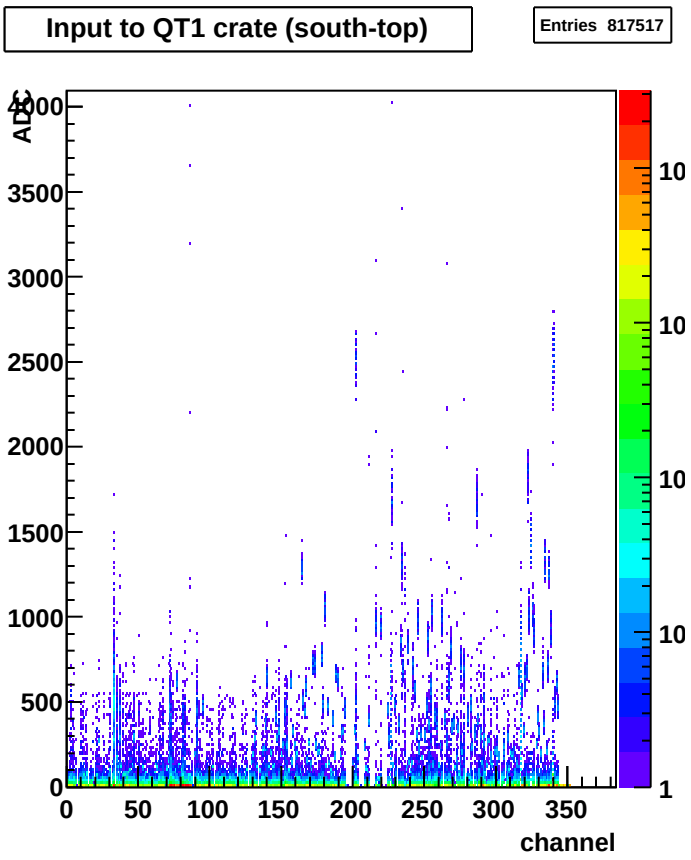

BBClarge-TAC-W
Entries 55839
Mean 1492
RMS 334.1

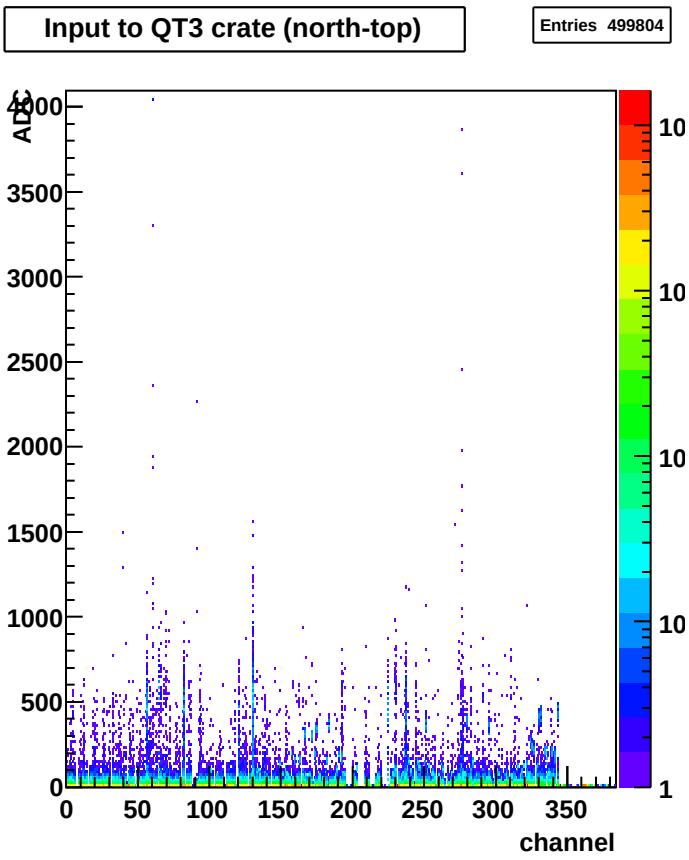

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 53433
Mean 4086
RMS 426.8

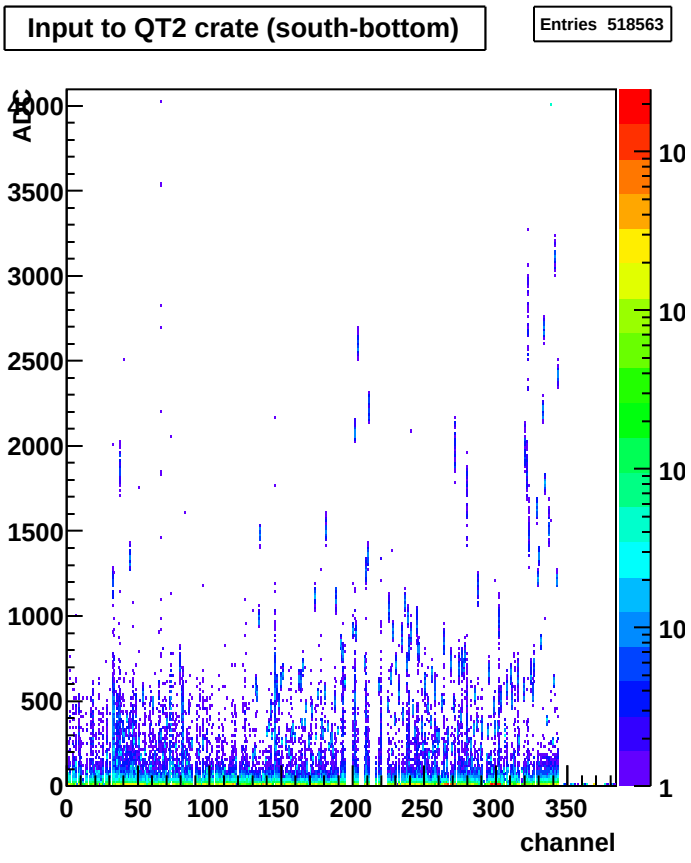

VPD-TAC-E
Entries 41036
Mean 1319
RMS 140.9

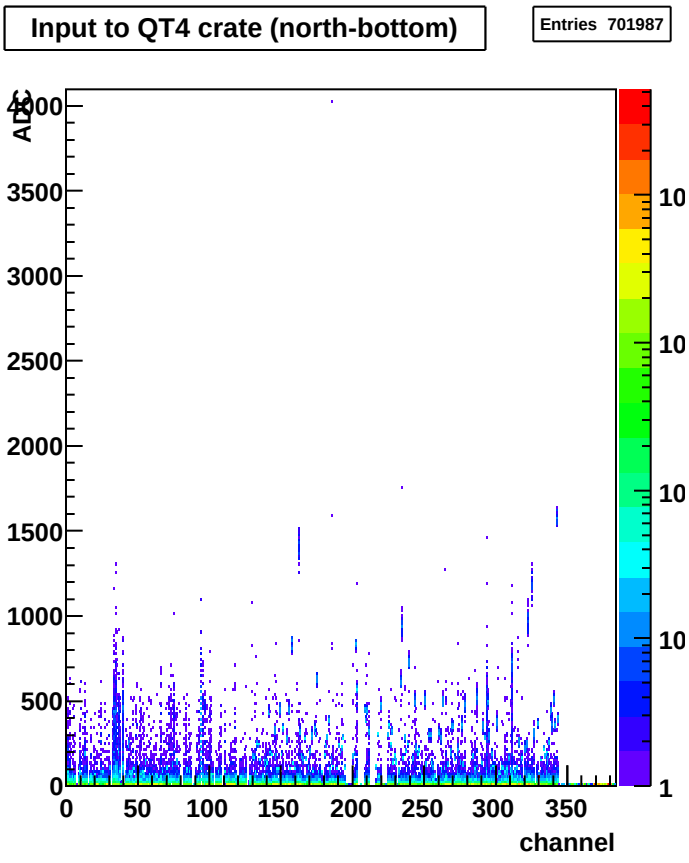

VPD-TAC-W
Entries 39831
Mean 1432
RMS 154.4

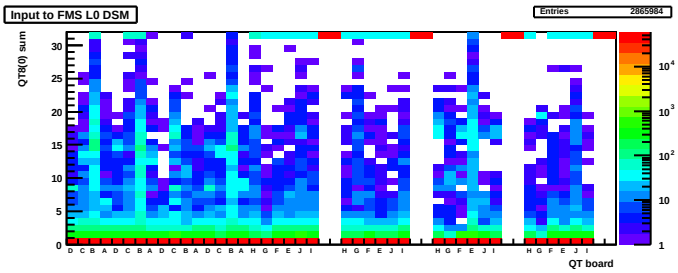

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 32640
Mean 3982
RMS 125.1

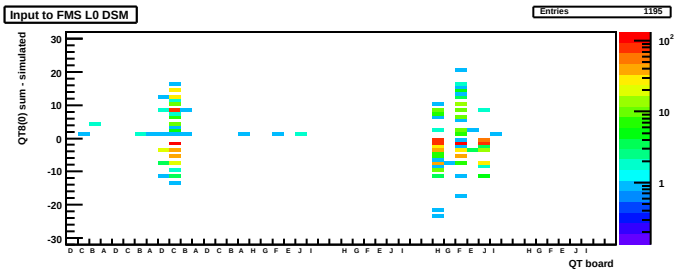

Input to QT1 crate (south-top)
Entries 817517
ADC
channel

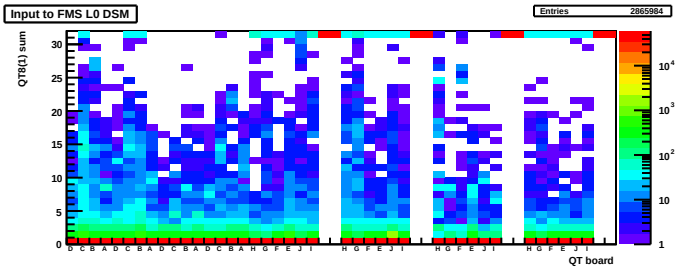

Input to QT3 crate (north-top)
Entries 499804
ADC
channel

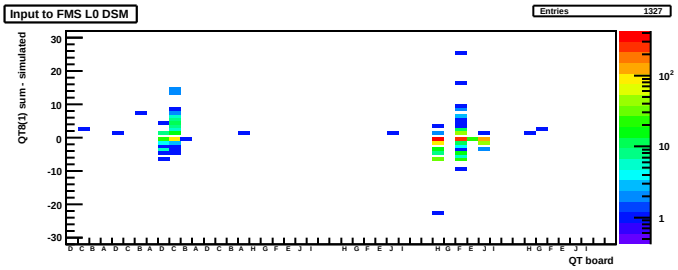

Input to QT2 crate (south-bottom)
Entries 518563
ADC
channel

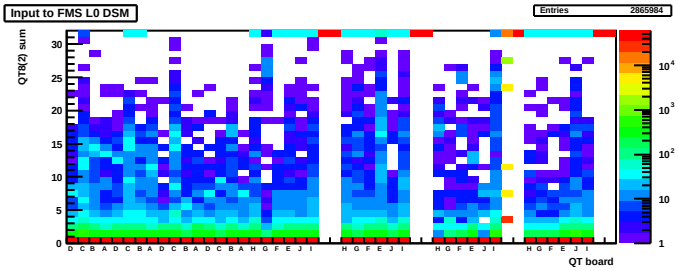

Input to QT4 crate (north-bottom)
Entries 701987
ADC
channel

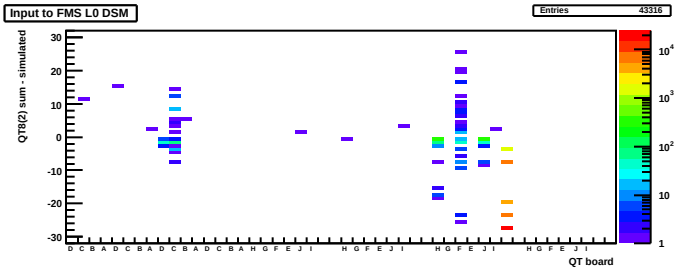

Input to FMS L0 DSM
Entries 2865984
QT8(0) sum
QT board

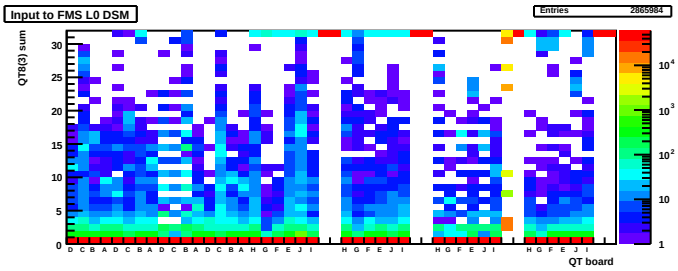

Input to FMS L0 DSM
Entries 1195
QT8(0) sum - simulated
QT board

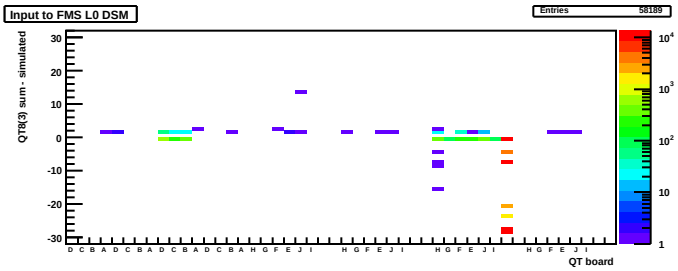

Input to FMS L0 DSM
Entries 2865984
QT8(1) sum
QT board

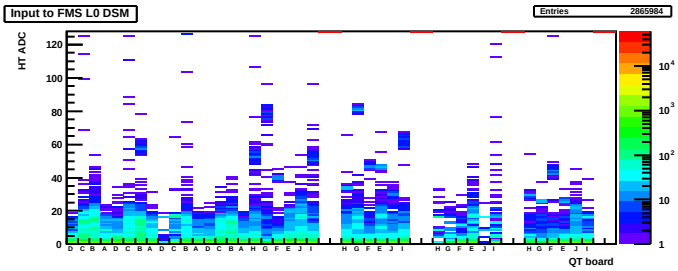

Input to FMS L0 DSM
Entries 1327
QT8(1) sum - simulated
QT board

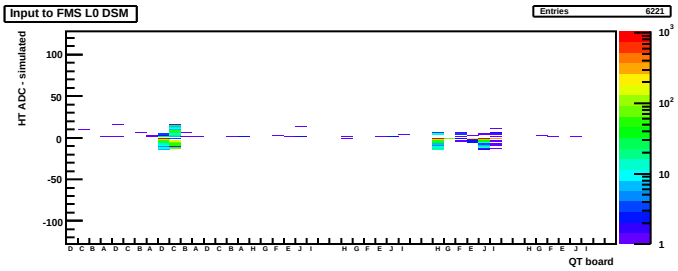

Input to FMS L0 DSM
Entries 2865984
QT8(2) sum
QT board

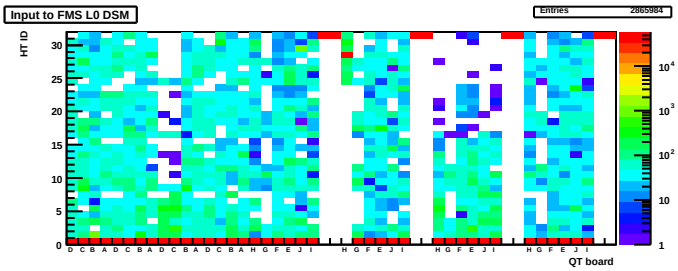

Input to FMS L0 DSM
Entries 43316
QT8(2) sum - simulated
QT board

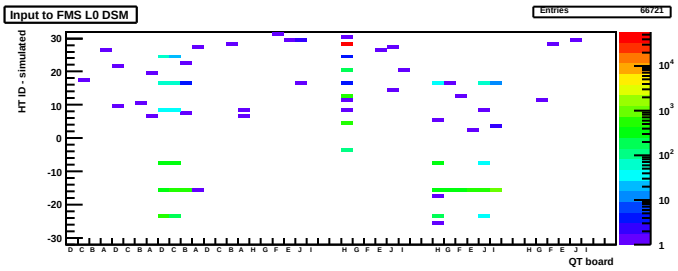

Input to FMS L0 DSM
Entries 2865984
QT8(3) sum
QT board

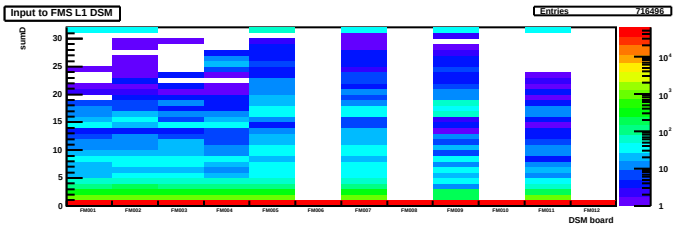

Input to FMS L0 DSM
Entries 58189
QT8(3) sum - simulated
QT board

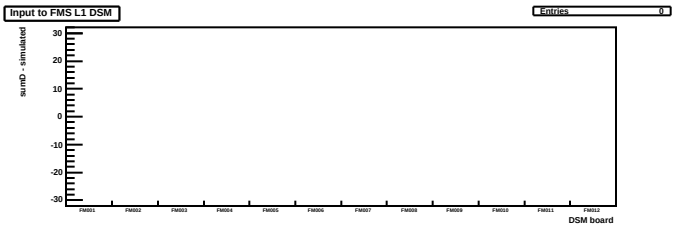

Input to FMS L0 DSM
Entries 2865984
HT ADC
QT board

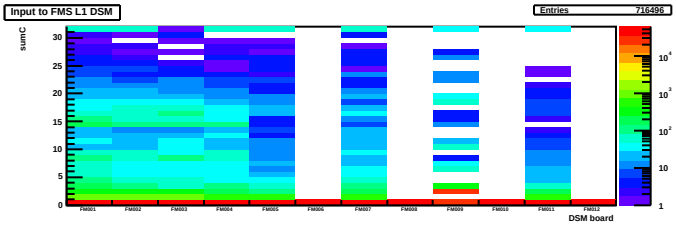

Input to FMS L0 DSM
Entries 6221
HT ADC - simulated
QT board

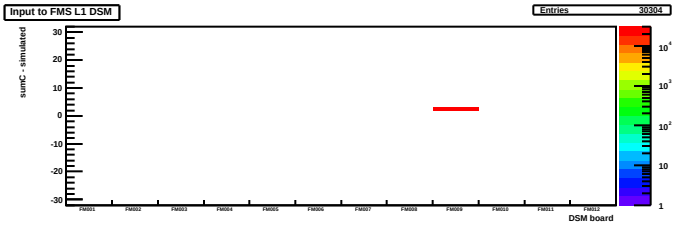

Input to FMS L0 DSM
Entries 2865984
HT ID
QT board

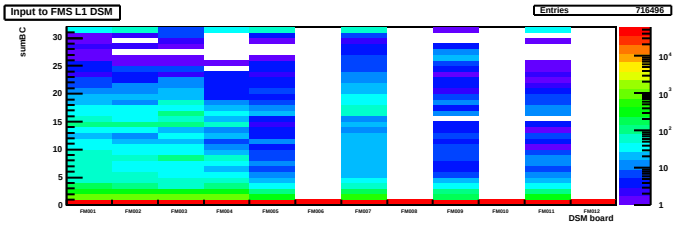

Input to FMS L0 DSM
Entries 66721
HT ID - simulated
QT board

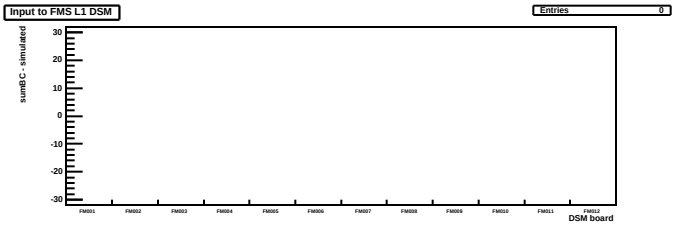

Input to FMS L1 DSM
Entries 716496
sumD
DSM board

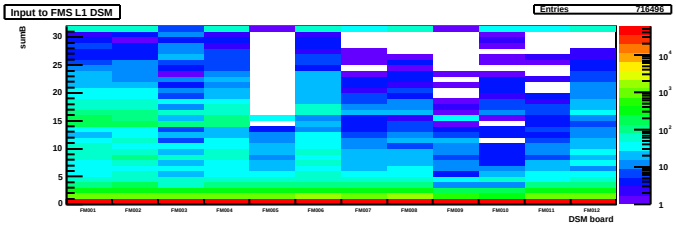

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

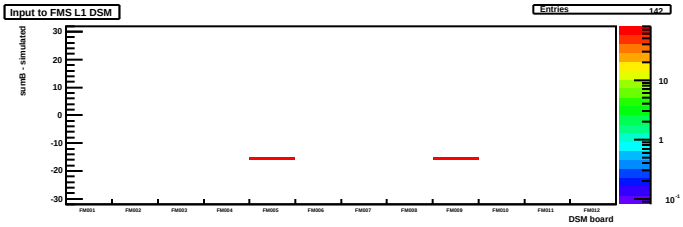

Input to FMS L1 DSM
Entries 716496
sumC
DSM board

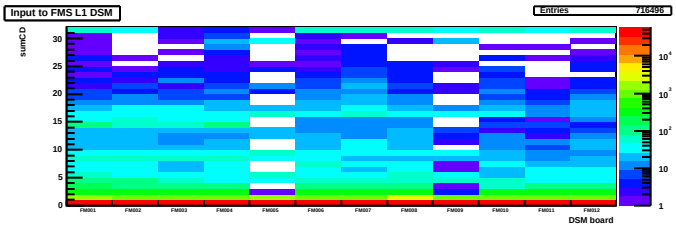

Input to FMS L1 DSM
Entries 30304
sumC - simulated
DSM board

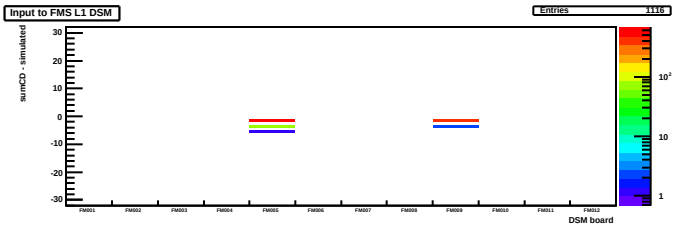

Input to FMS L1 DSM
Entries 716496
sumBC
DSM board

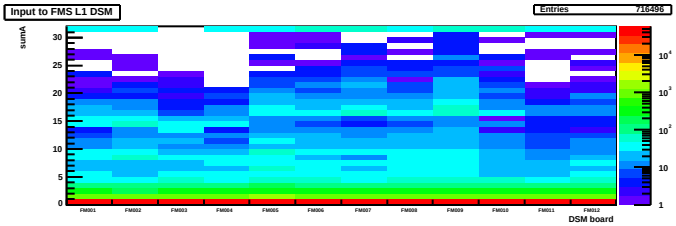

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

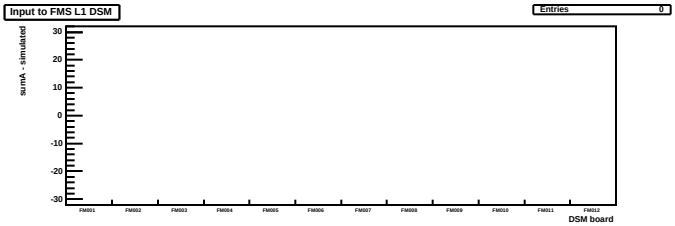

Input to FMS L1 DSM
Entries 716496
sumB
DSM board

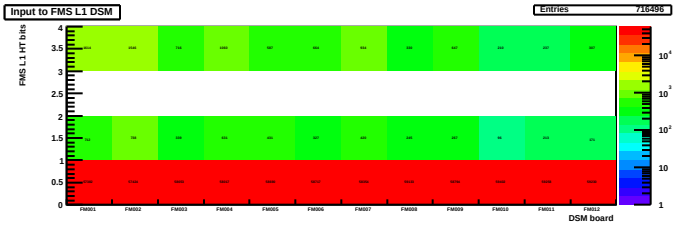

Input to FMS L1 DSM
Entries 142
sumB - simulated
DSM board

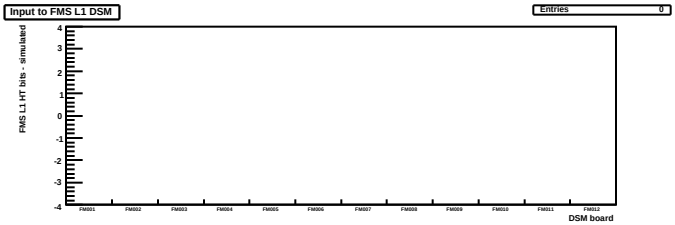

Input to FMS L1 DSM
Entries 716496
sumCD
DSM board

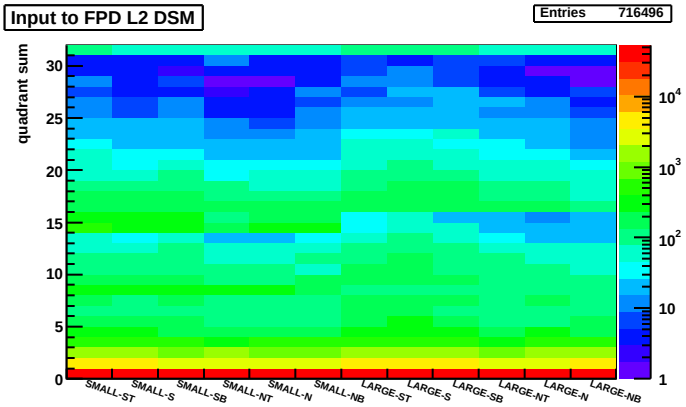

Input to FMS L1 DSM
Entries 1116
sumCD - simulated
DSM board

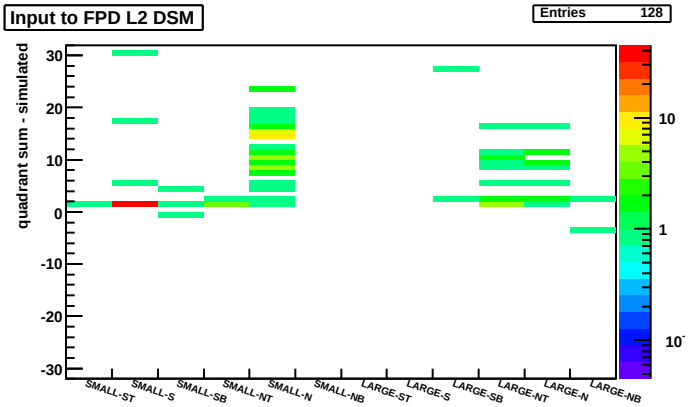

Input to FMS L1 DSM
Entries 716496
sumA
DSM board

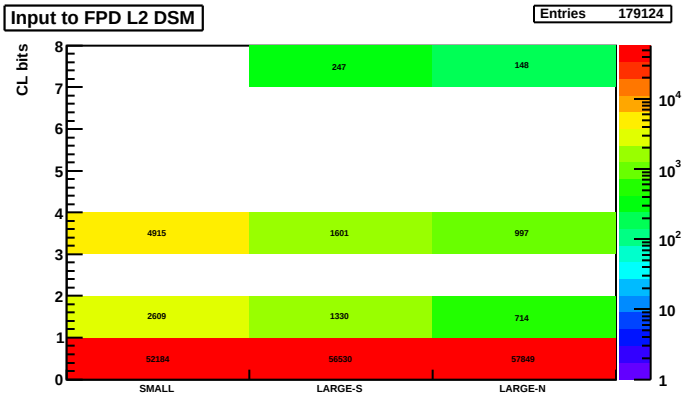

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

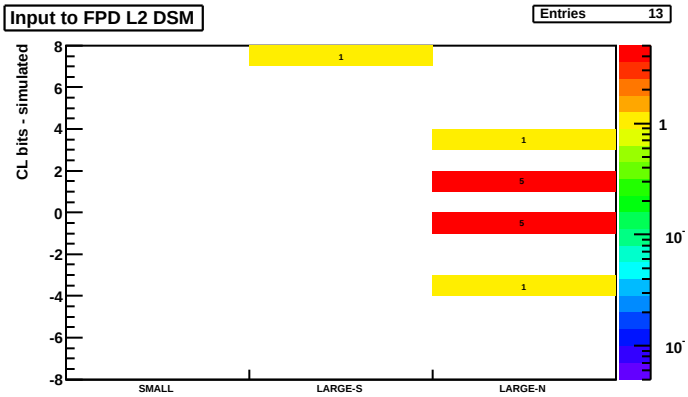

Input to FMS L1 DSM
Entries 716496
FMS L1 HT bits
DSM board

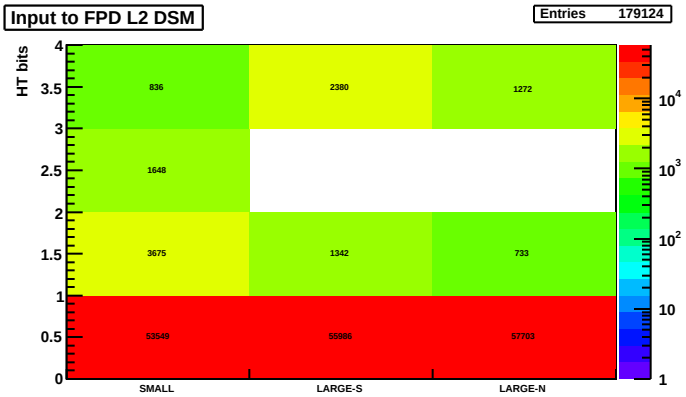

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

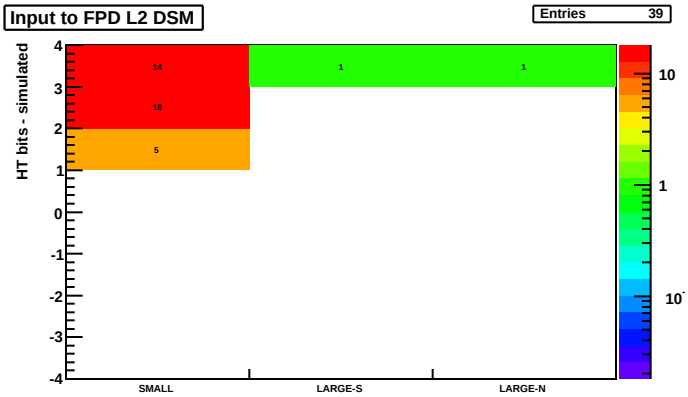

Input to FPD L2 DSM
Entries 716496
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

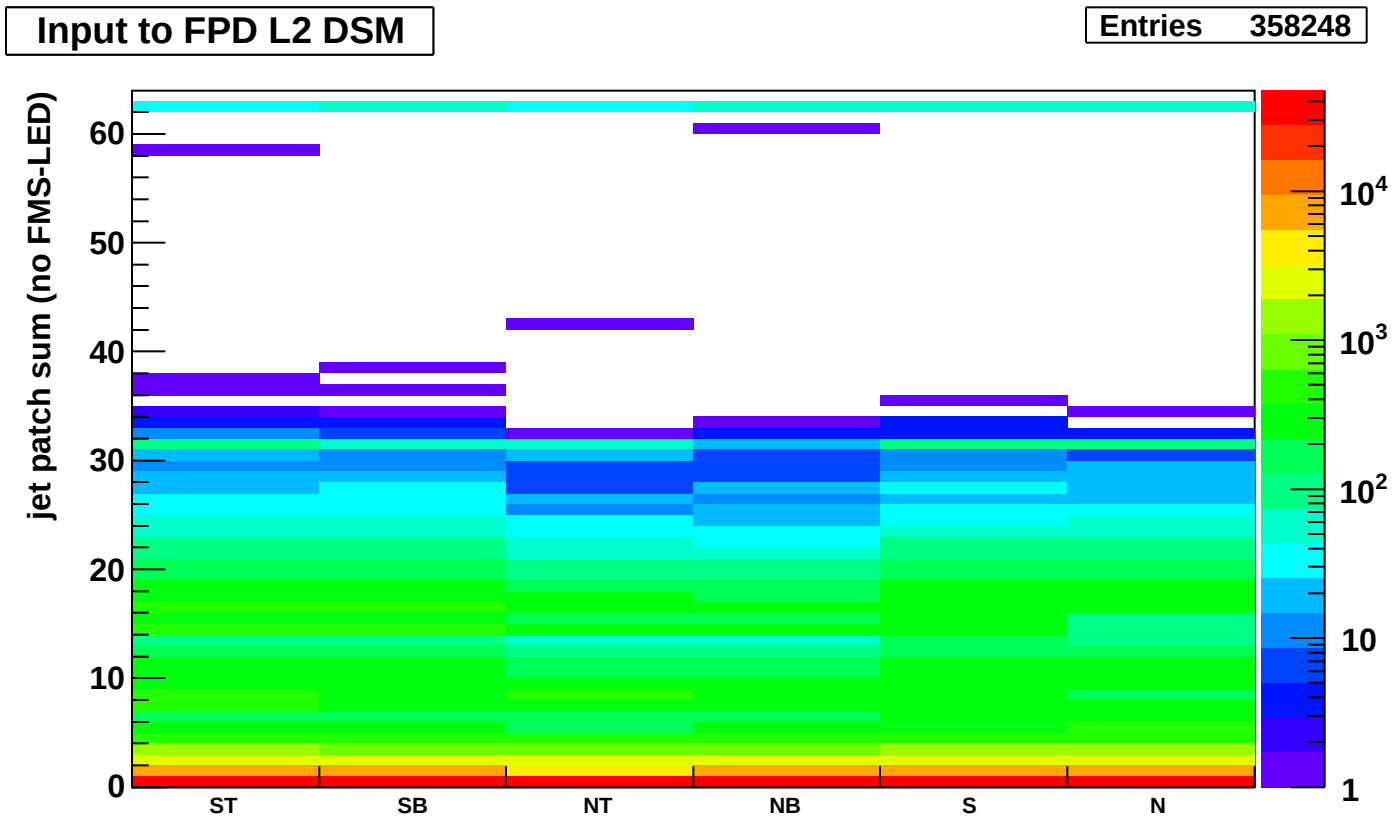

Input to FPD L2 DSM
Entries 128
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

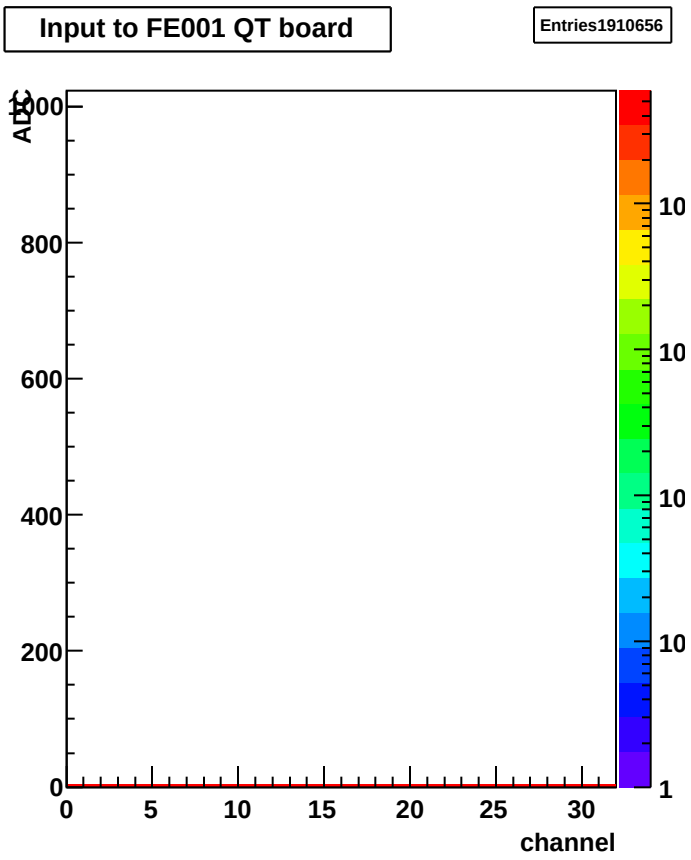

Input to FPD L2 DSM
Entries 179124
CL bits
247
148
4915
1601
997
2609
1330
714
52184
56530
57849
SMALL
LARGE-S
LARGE-N

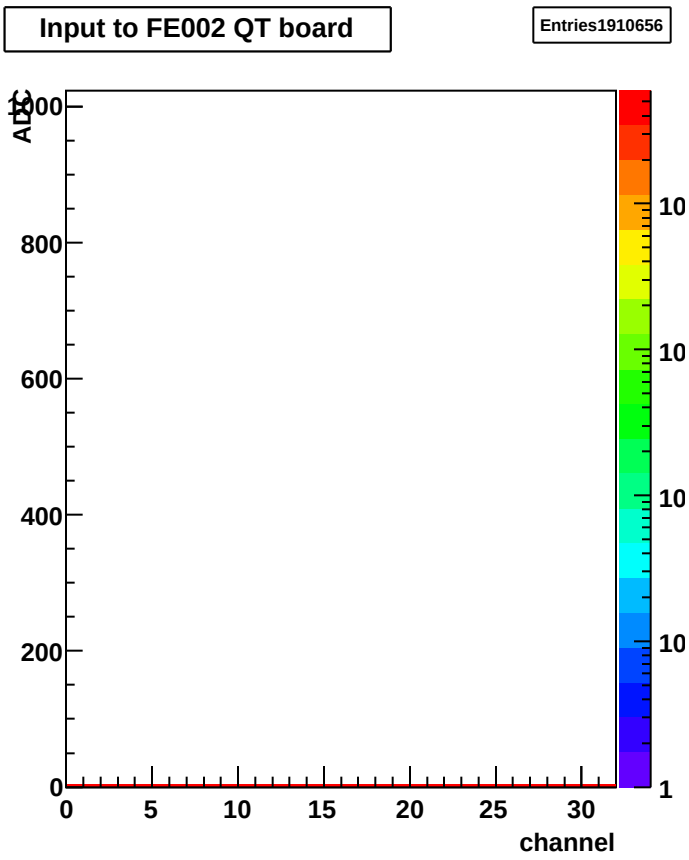

Input to FPD L2 DSM
Entries 13
CL bits - simulated
1
1
5
5
1
SMALL
LARGE-S
LARGE-N

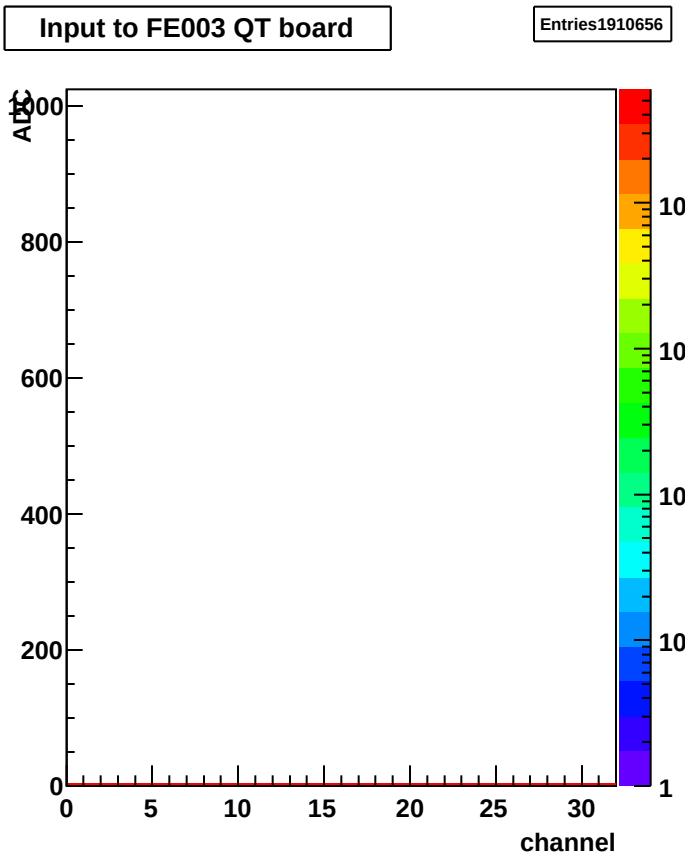

Input to FPD L2 DSM
Entries 179124
HT bits
836
2380
1272
1648
3675
1342
733
53549
55986
57703
SMALL
LARGE-S
LARGE-N

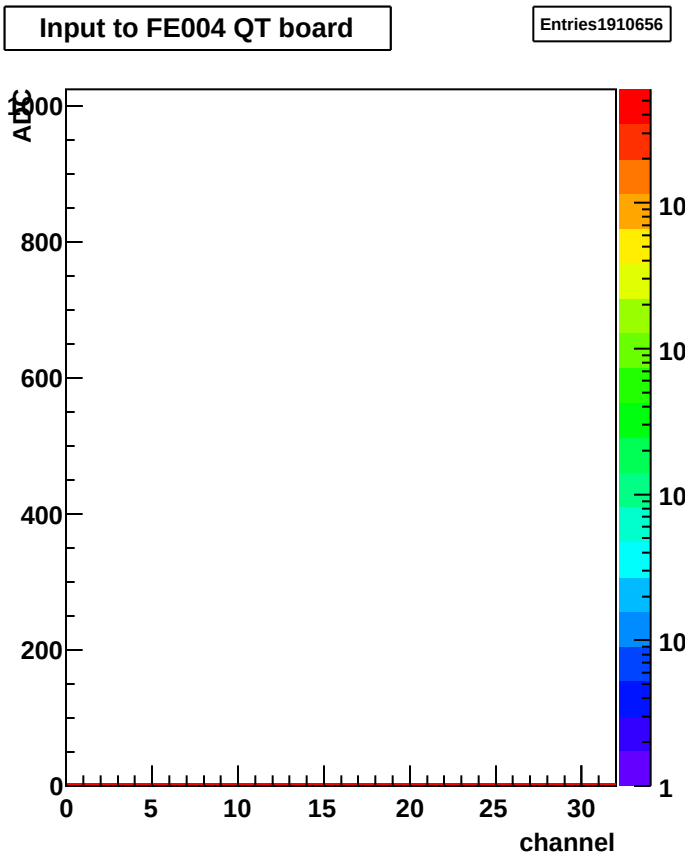

Input to FPD L2 DSM
Entries 39
HT bits - simulated
14
1
1
18
5
SMALL
LARGE-S
LARGE-N

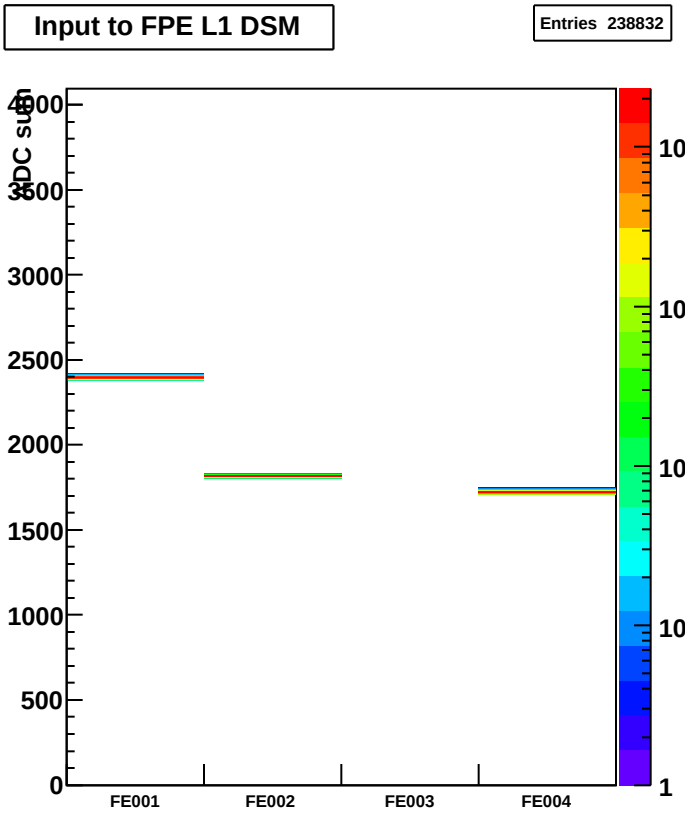

Input to FPD L2 DSM
Entries 358248
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
$10^2$
$10^3$
$10^4$


Input to FE001 QT board
Entries1910656
ADC
channel

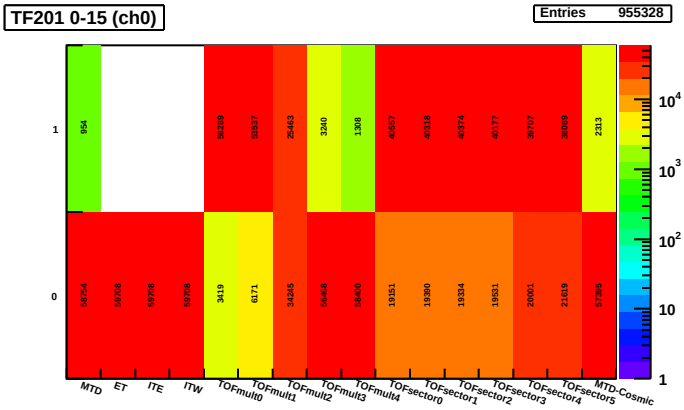

Input to FE002 QT board
Entries1910656
ADC
channel


Input to FE003 QT board
Entries1910656
ADC
channel

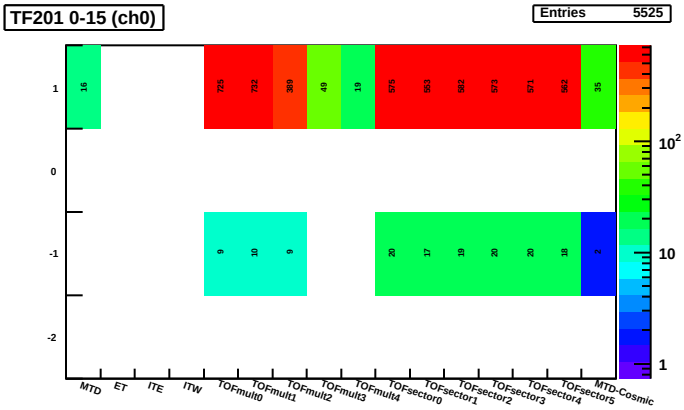

Input to FE004 QT board
Entries1910656
ADC
channel


Input to FPE L1 DSM
Entries 238832
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

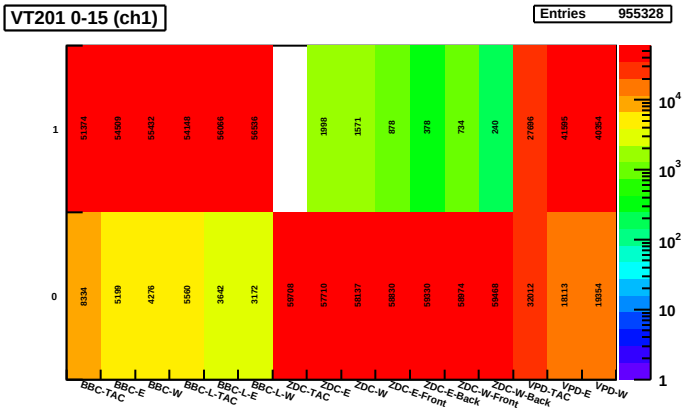
TF201 0-15 (ch0)



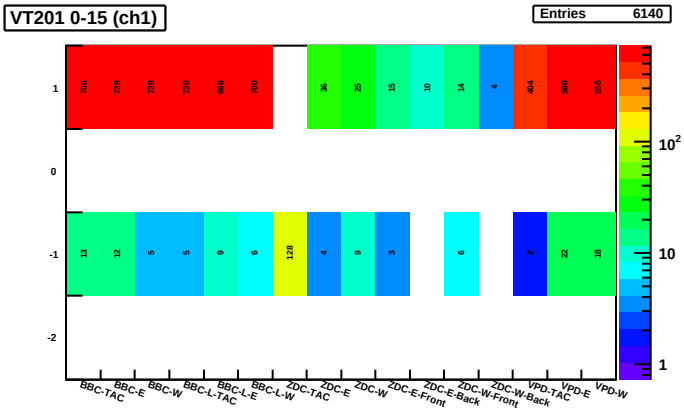

TF201 0-15 (ch0)



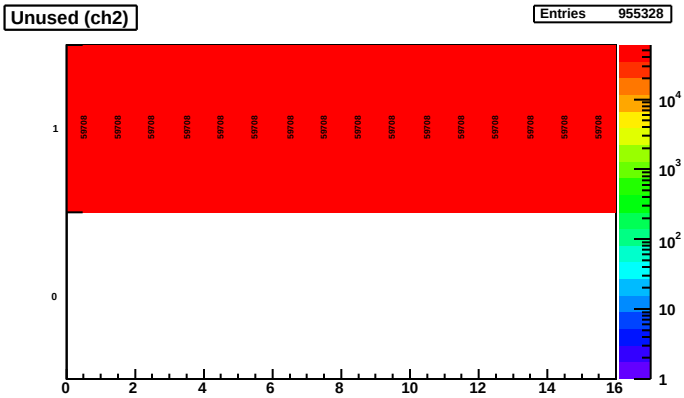

VT201 0-15 (ch1)

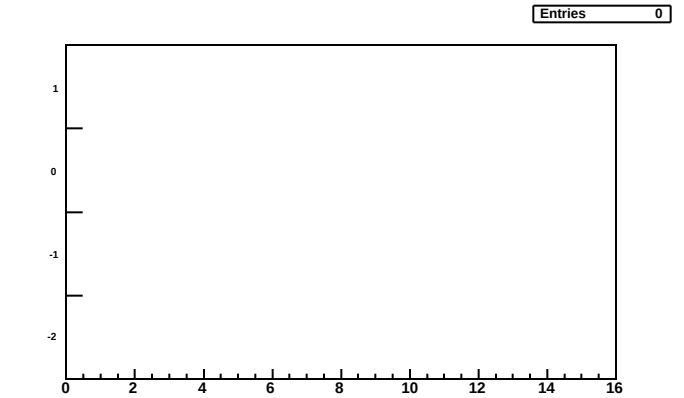



VT201 0-15 (ch1)

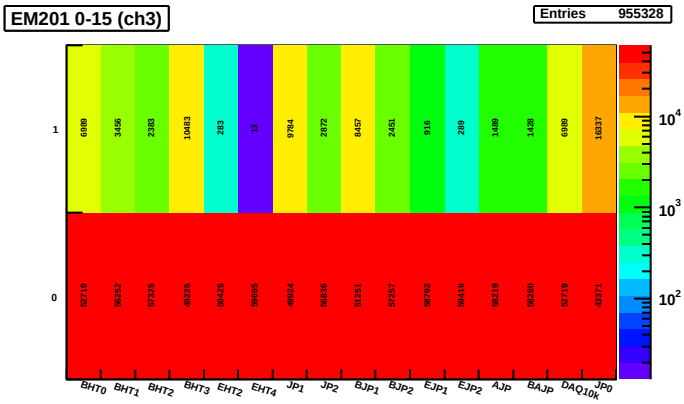



Unused (ch2)

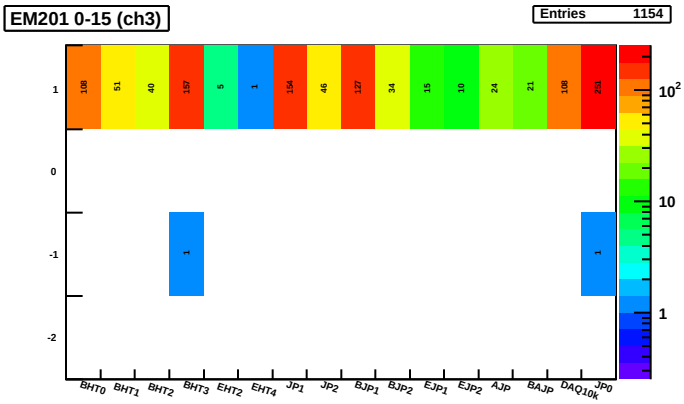


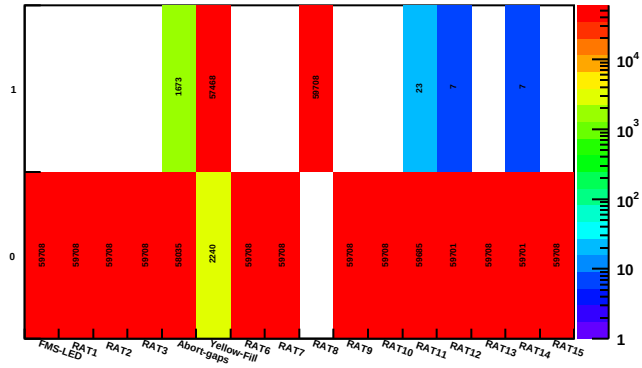

EM201 0-15 (ch3)

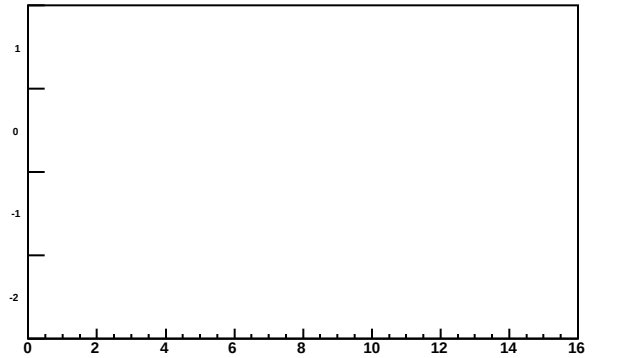



EM201 0-15 (ch3)

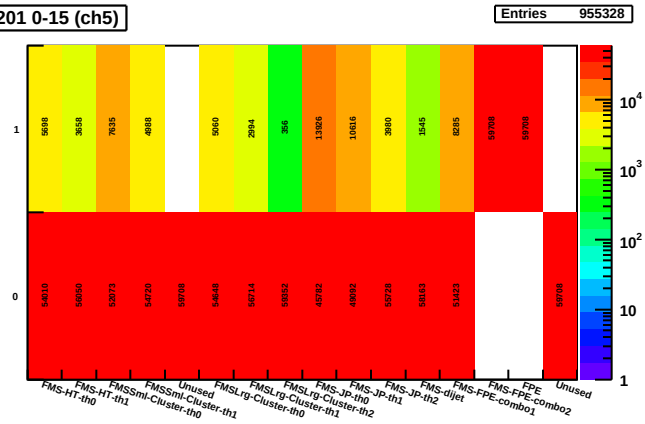


**RAT board (ch4)**

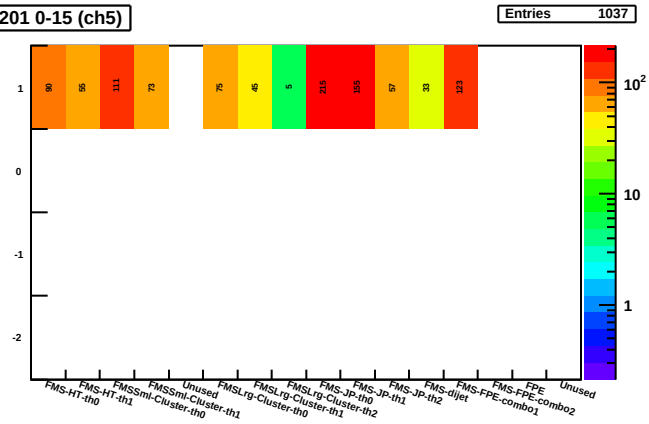


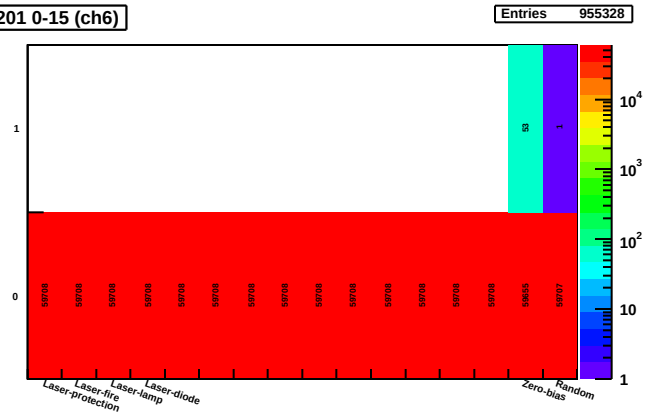

**FP201 0-15 (ch5)**

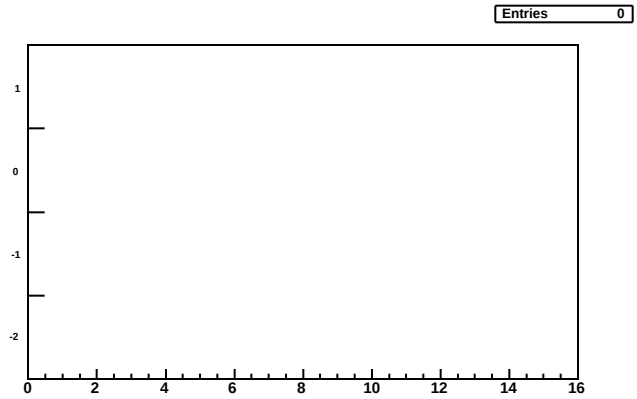


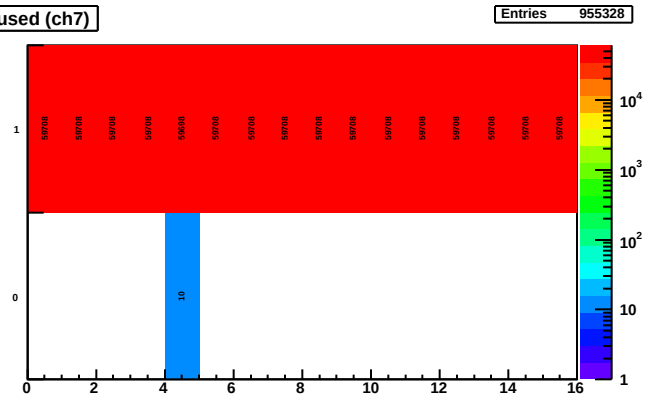
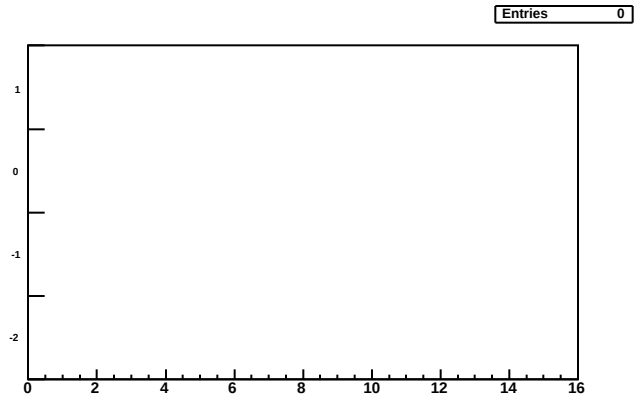

**FP201 0-15 (ch5)**




**ST201 0-15 (ch6)**




**Unused (ch7)**