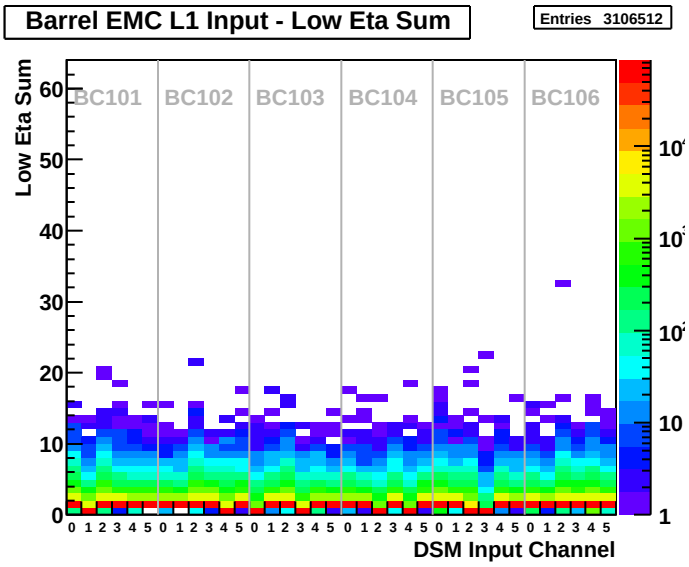
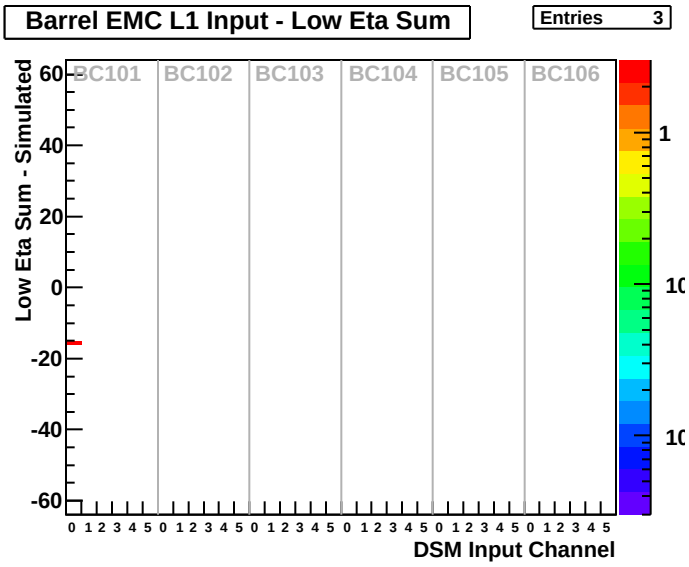
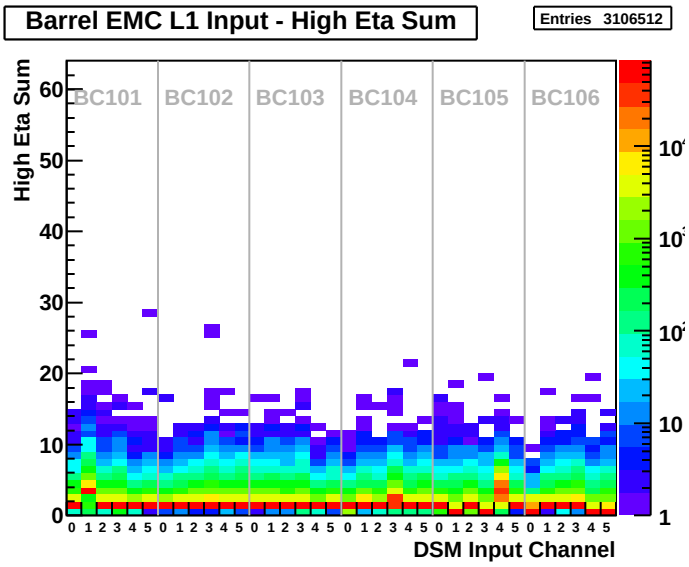
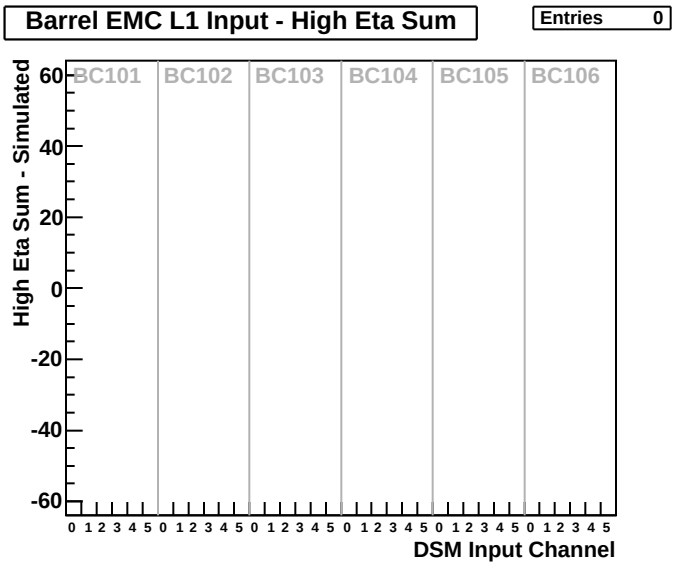
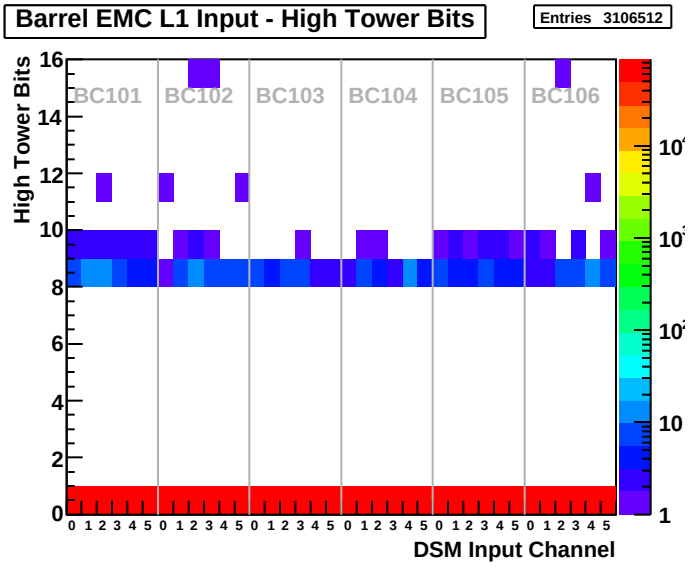
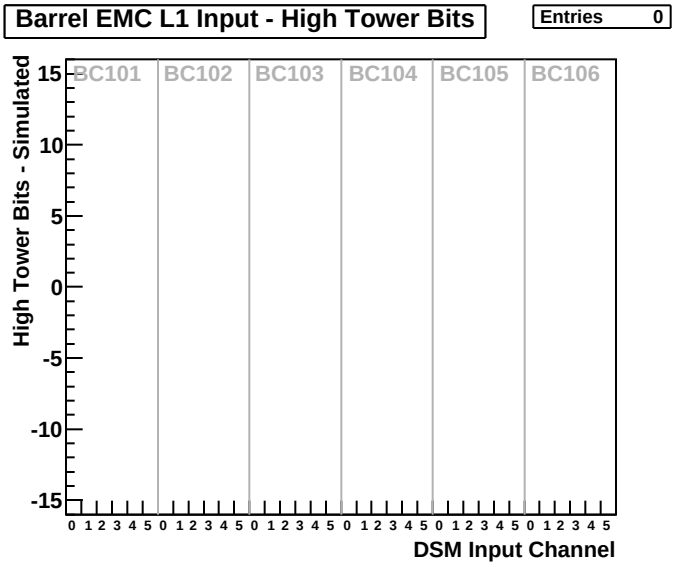

Barrel EMC L1 Input - Low Eta Sum
Entries 3106512
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

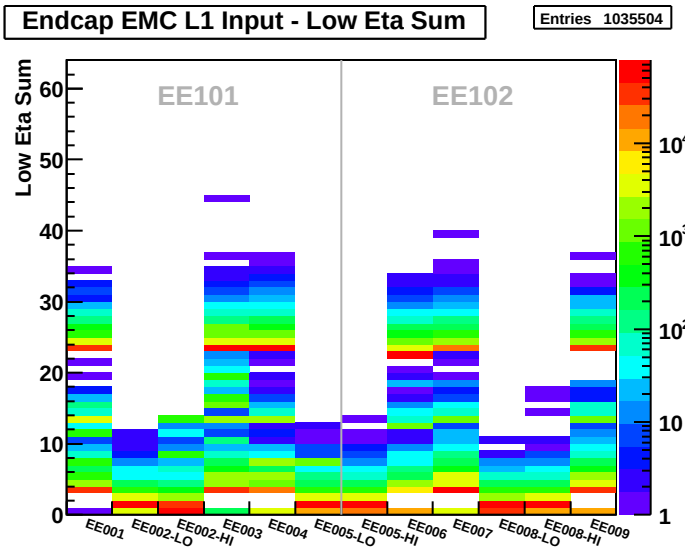

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

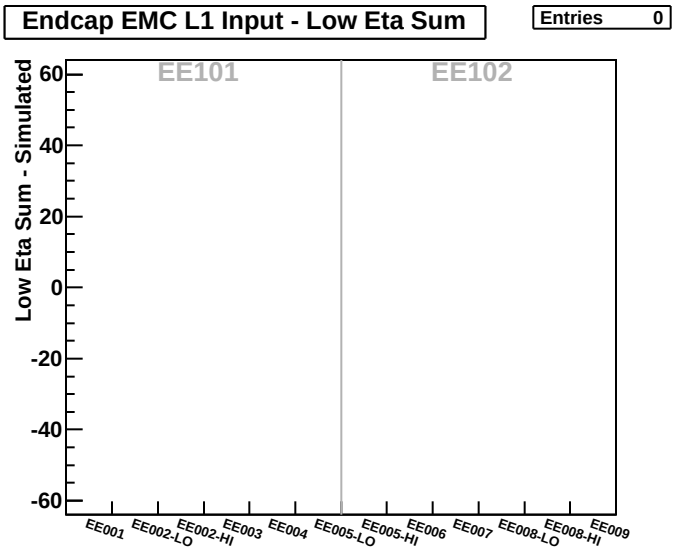

Barrel EMC L1 Input - High Eta Sum
Entries 3106512
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

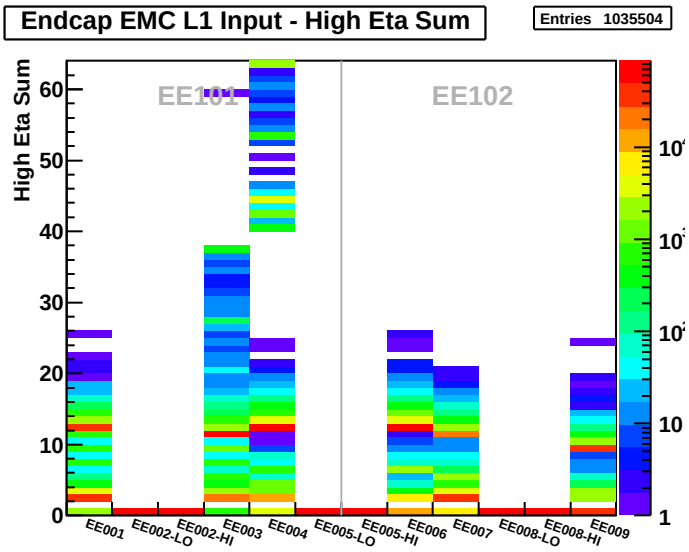

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

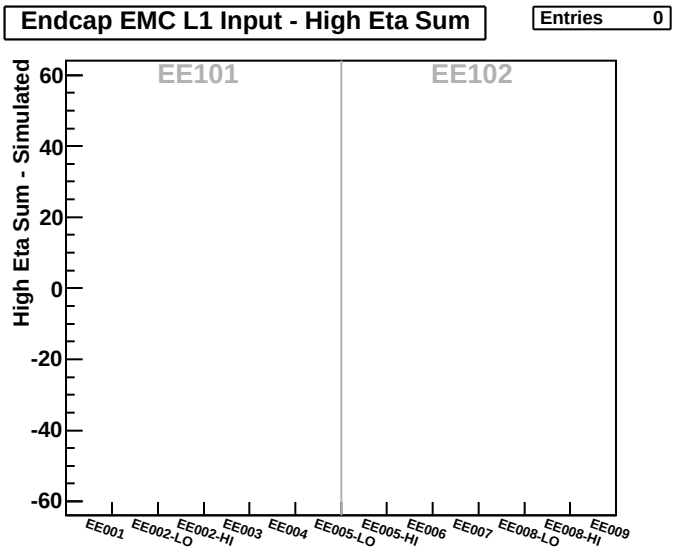

Barrel EMC L1 Input - High Tower Bits
Entries 3106512
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

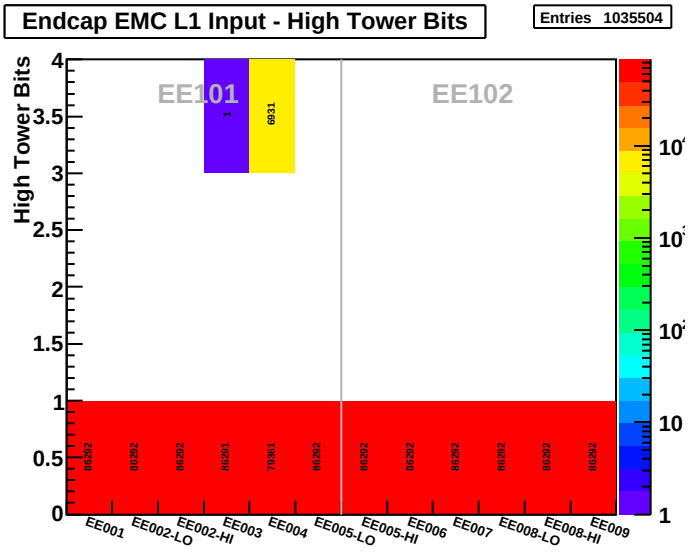

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

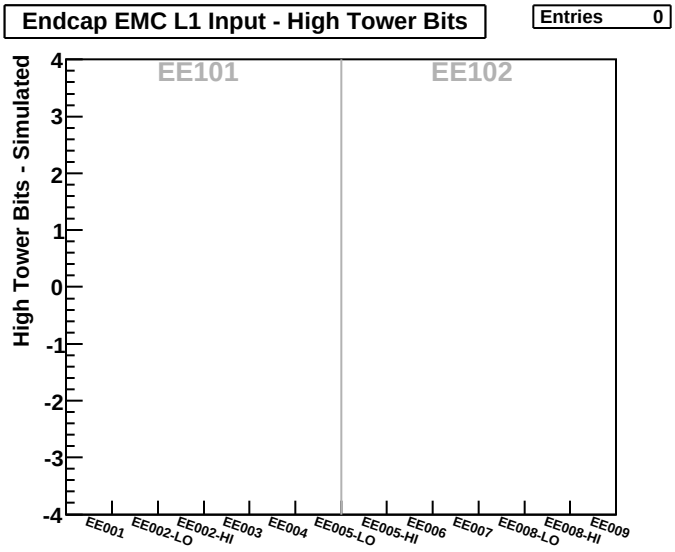

Endcap EMC L1 Input - Low Eta Sum
Entries 1035504
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

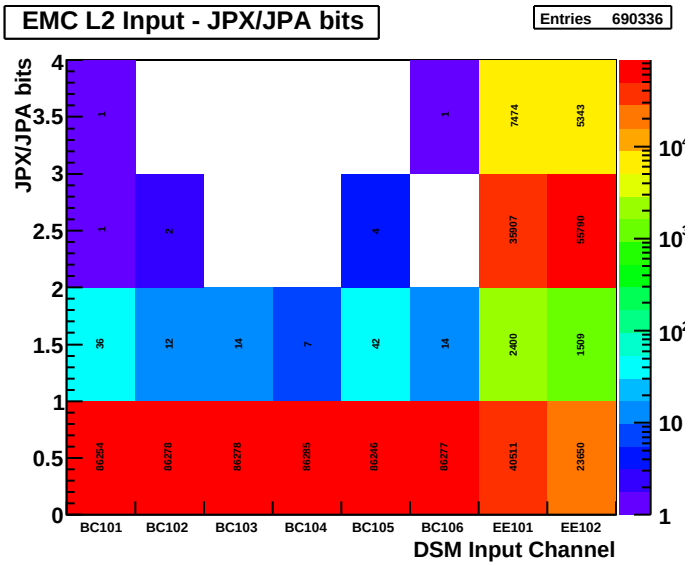

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

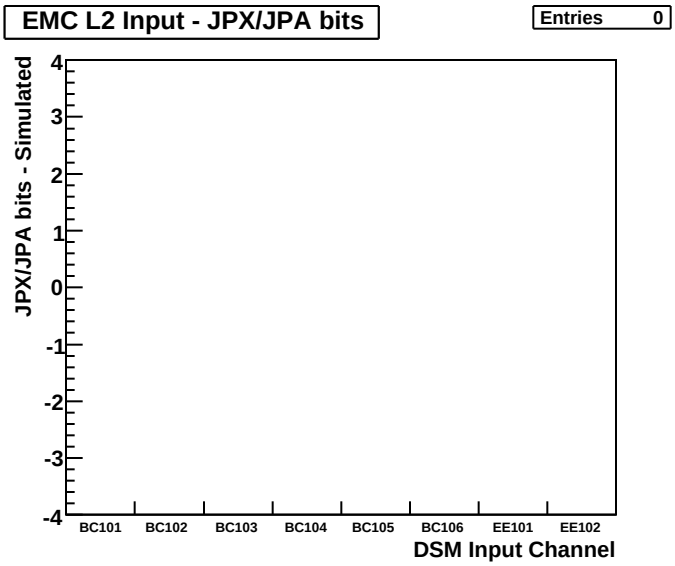

Endcap EMC L1 Input - High Eta Sum
Entries 1035504
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

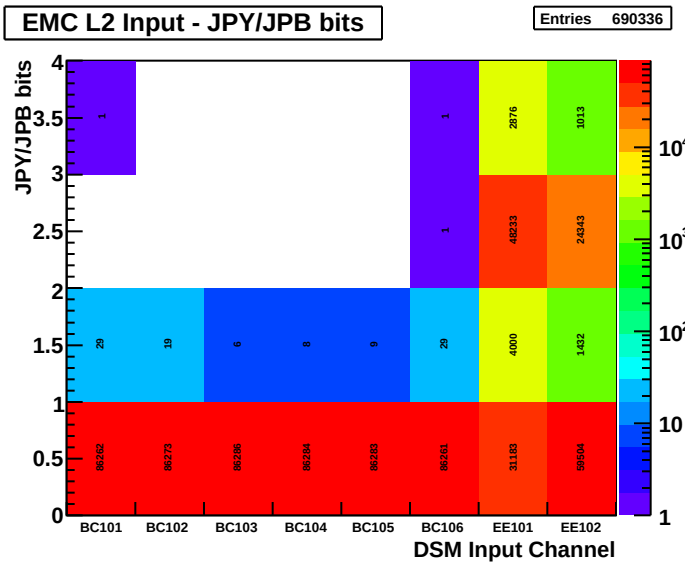

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

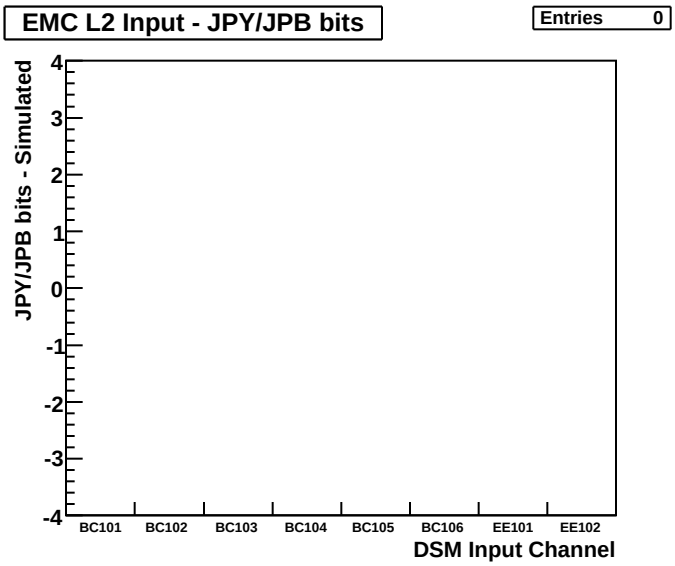

Endcap EMC L1 Input - High Tower Bits
Entries 1035504
High Tower Bits
EE101
EE102
1
6931
86292 86292 86292 86291 79361 86292 86292 86292 86292 86292 86292
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

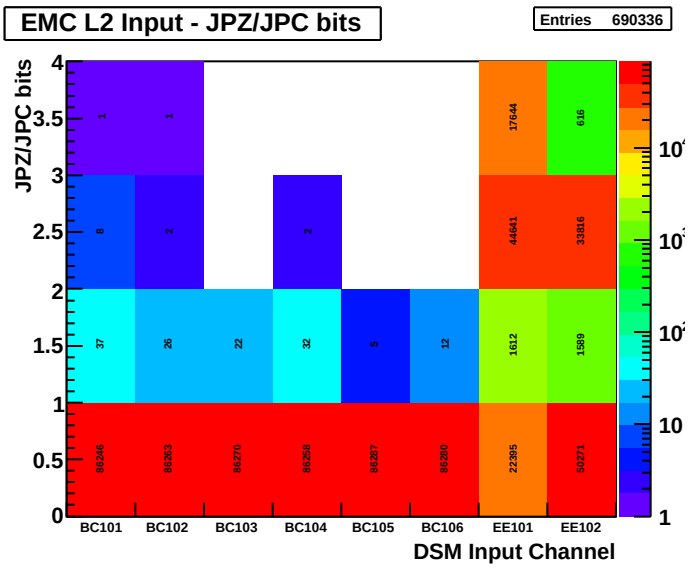

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

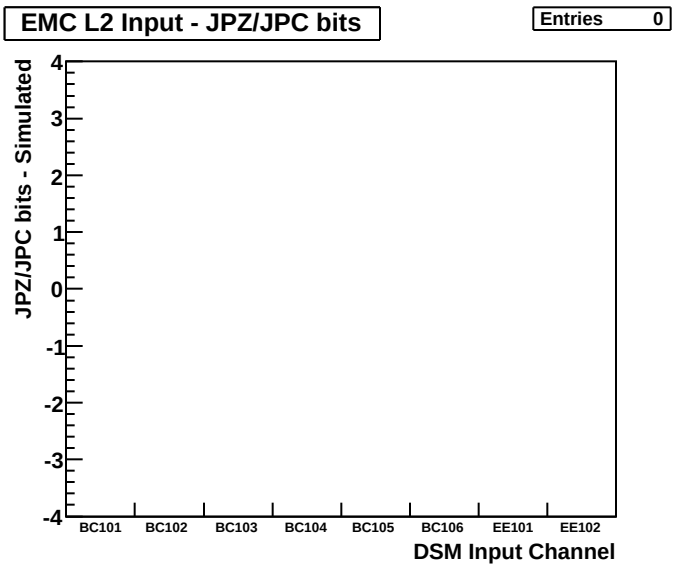

EMC L2 Input - JPX/JPA bits
Entries 690336
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

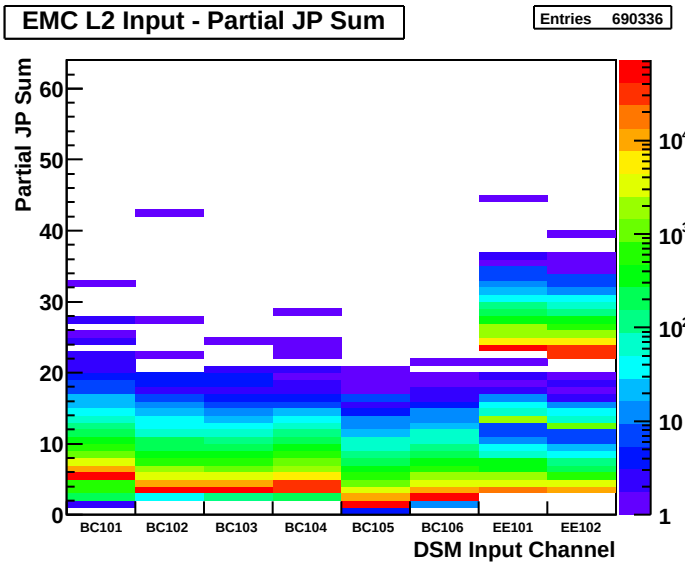

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

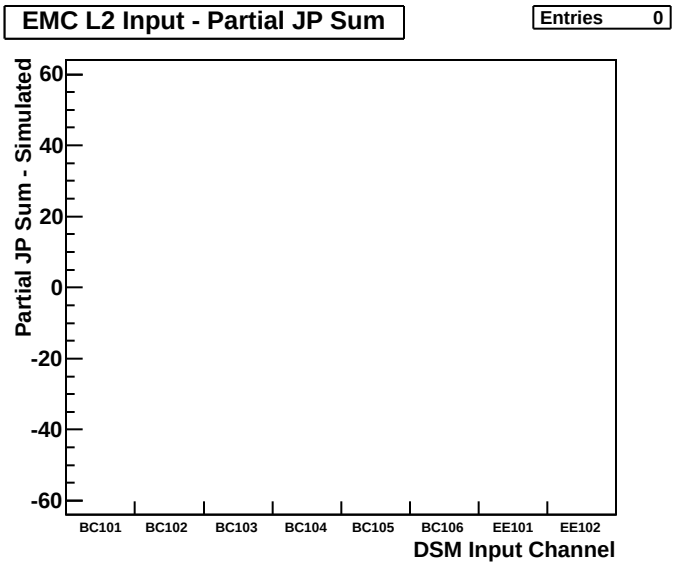

EMC L2 Input - JPY/JPB bits
Entries 690336
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

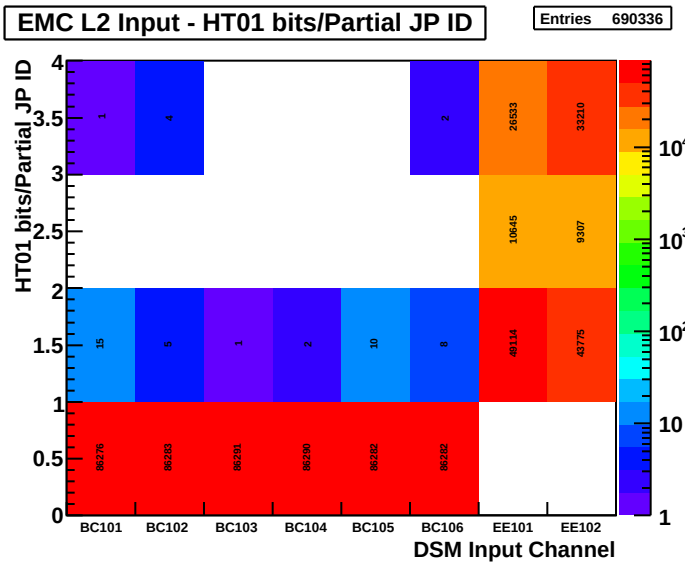

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

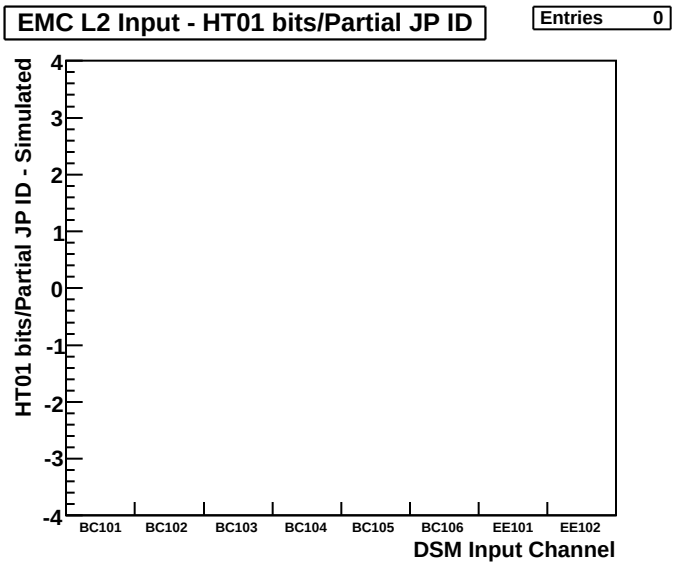

EMC L2 Input - JPZ/JPC bits
Entries 690336
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

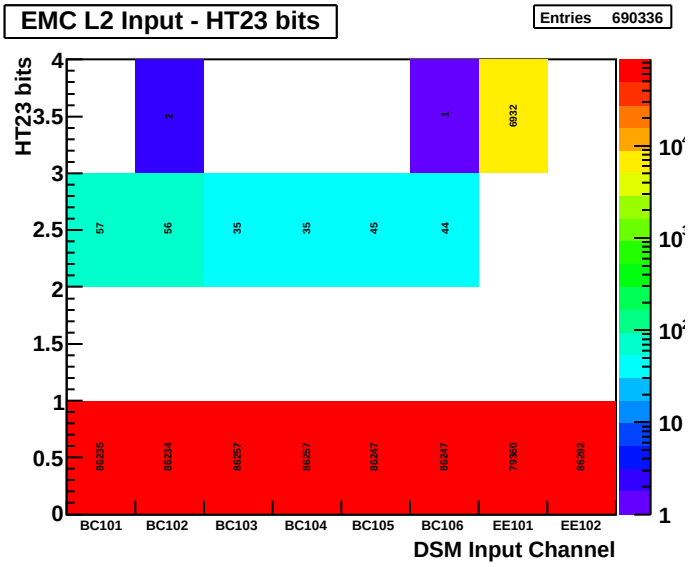

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

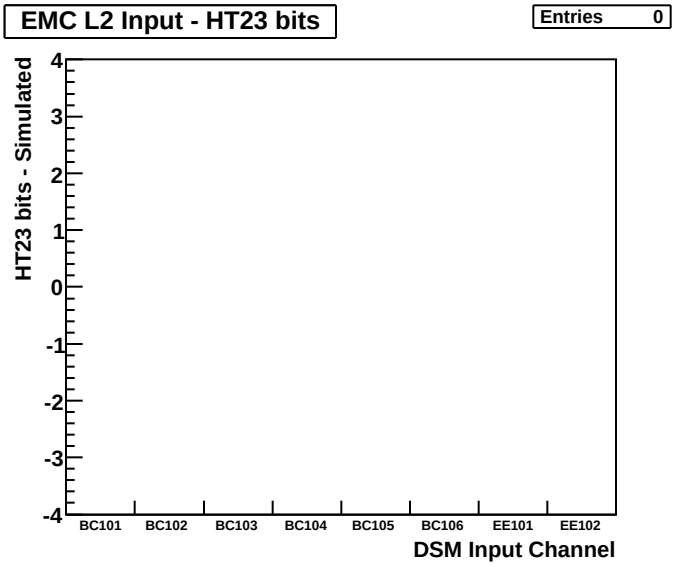

EMC L2 Input - Partial JP Sum
Entries 690336
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

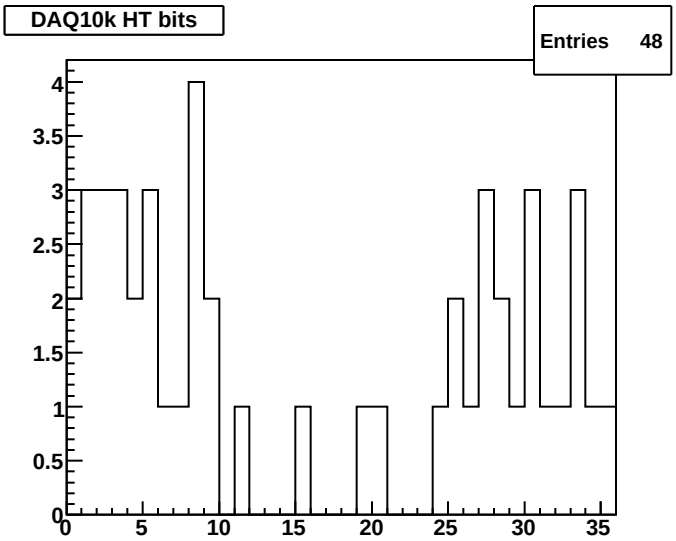

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

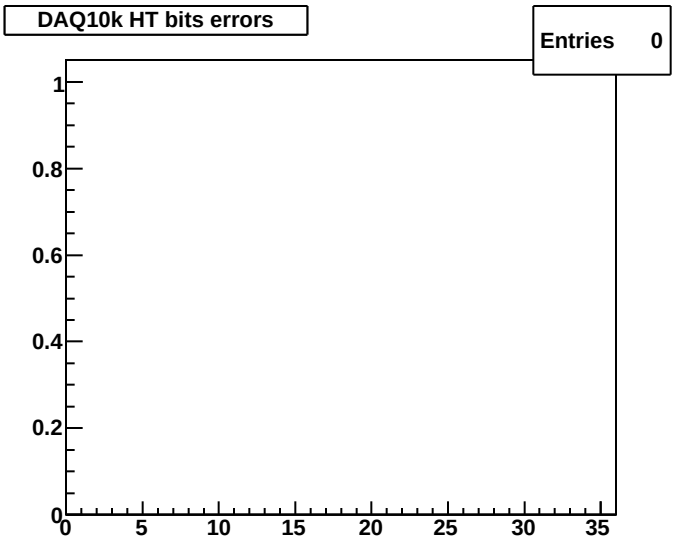

EMC L2 Input - HT01 bits/Partial JP ID
Entries 690336
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

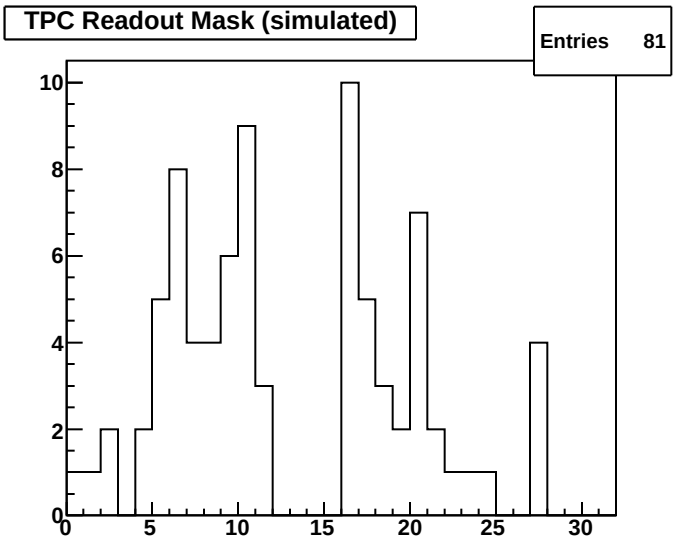

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

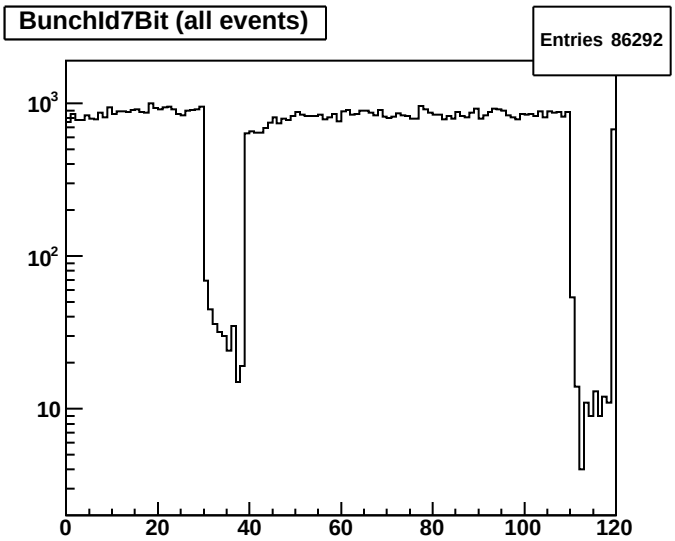

EMC L2 Input - HT23 bits
Entries 690336
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

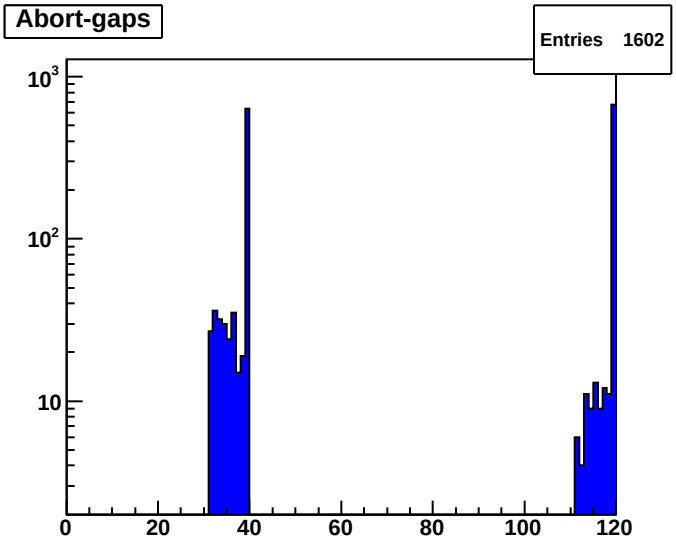

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

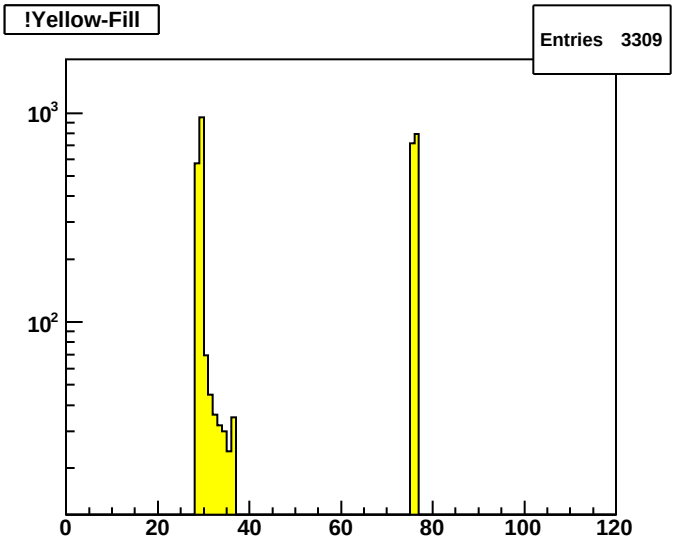

DAQ10k HT bits
Entries 48

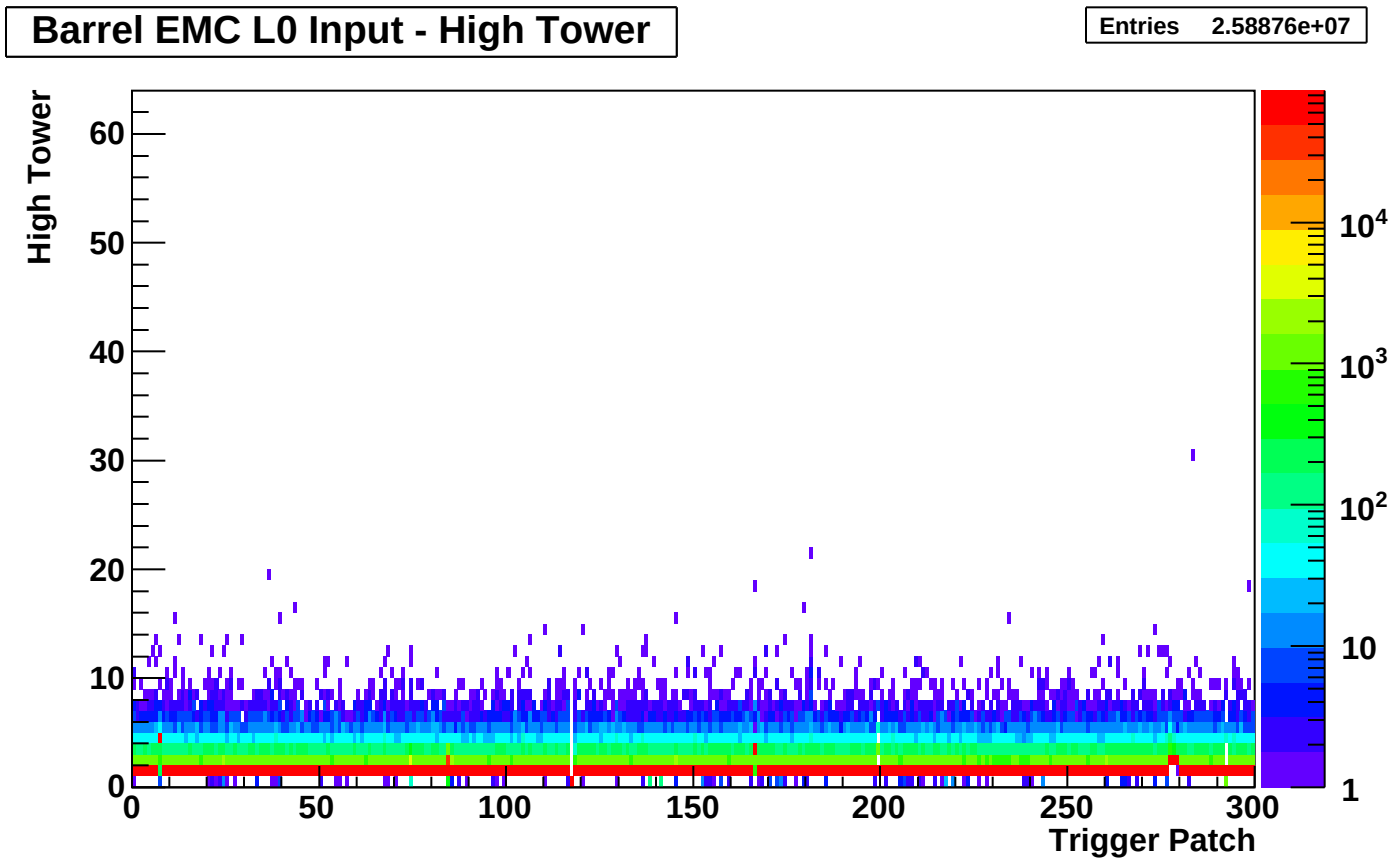

DAQ10k HT bits errors
Entries 0

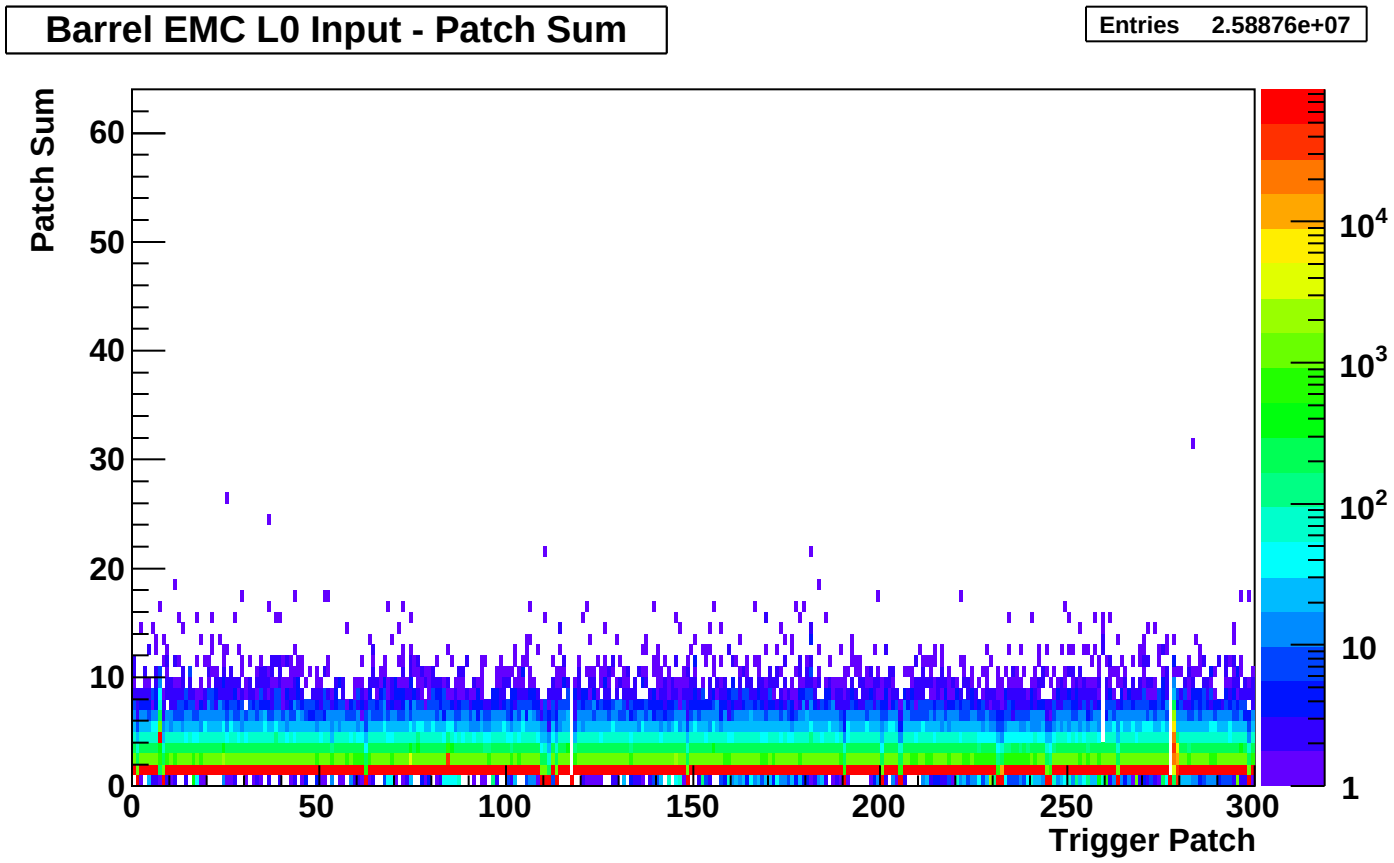

TPC Readout Mask (simulated)
Entries 81

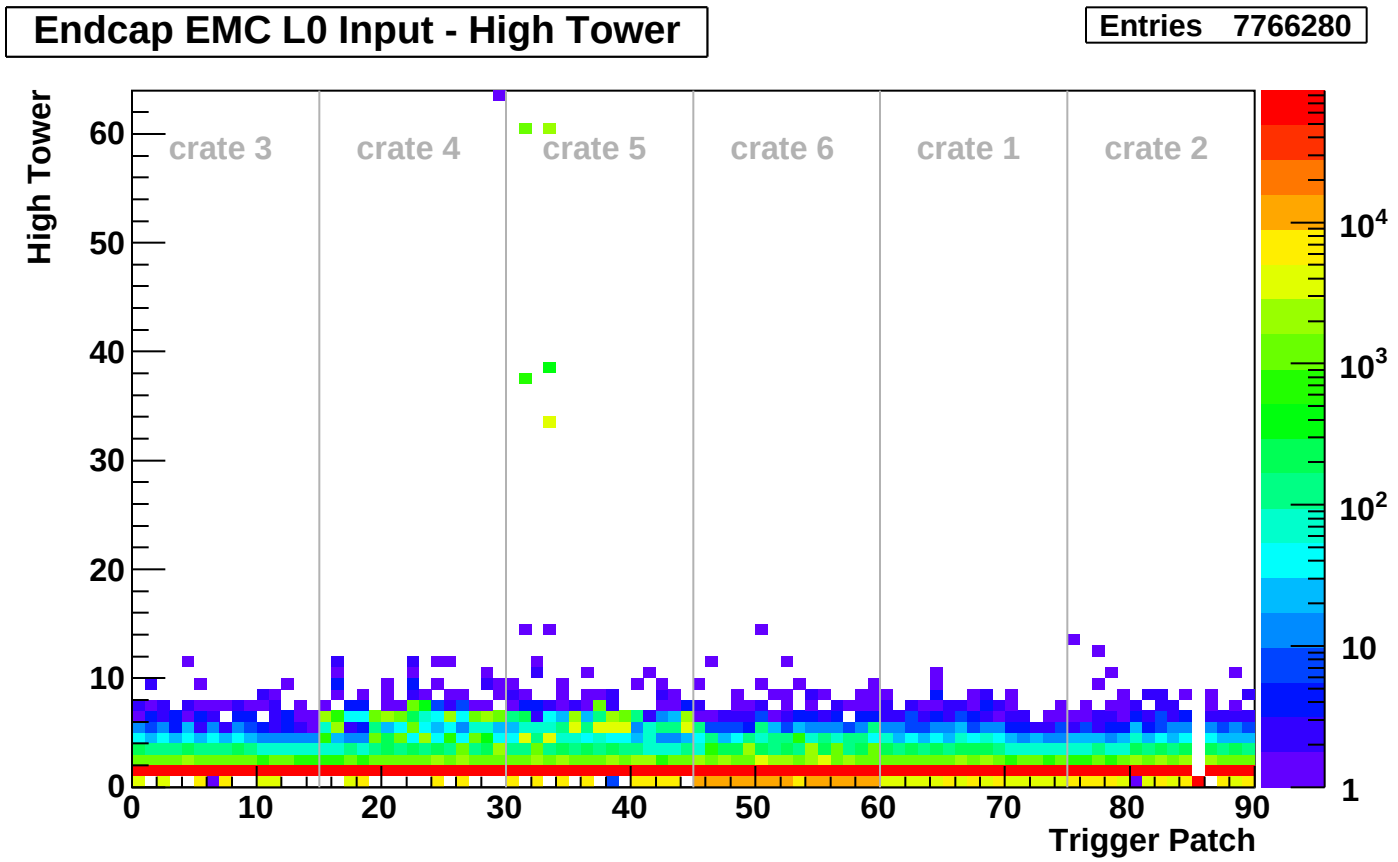

BunchId7Bit (all events)
Entries 86292

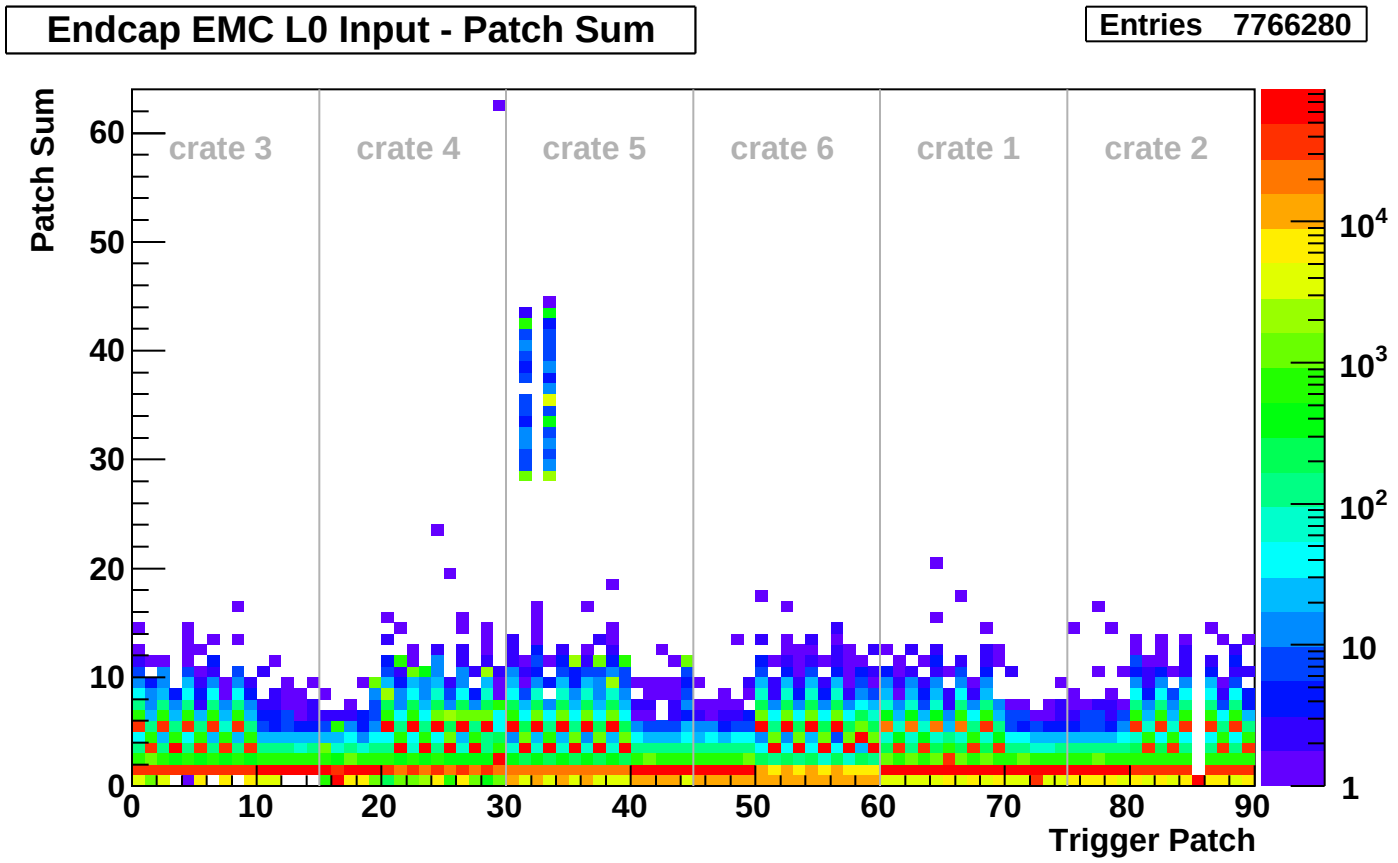

Abort-gaps
Entries 1602

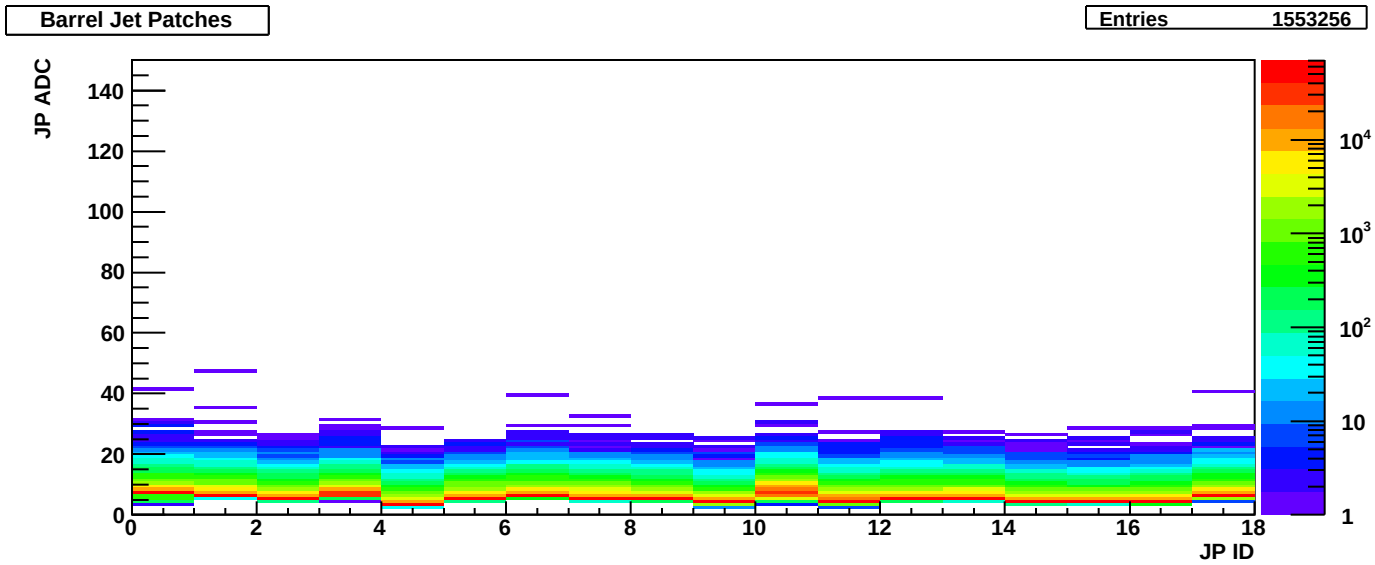

!Yellow-Fill
Entries 3309

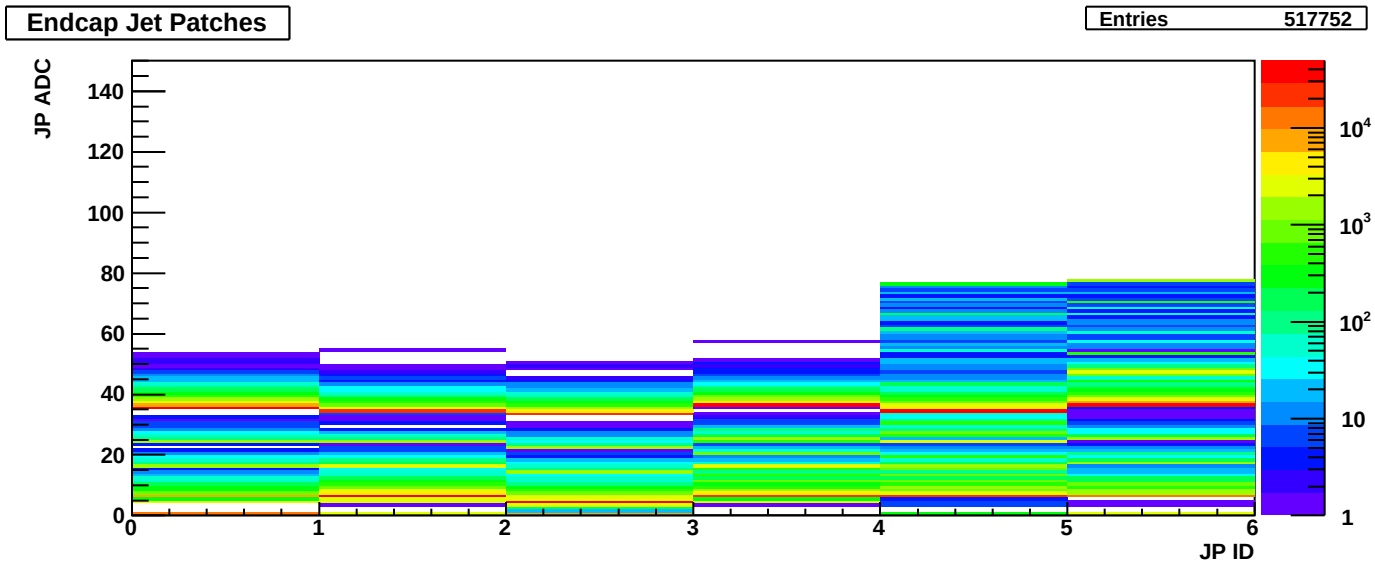

Barrel EMC L0 Input - High Tower
Entries 2.58876e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

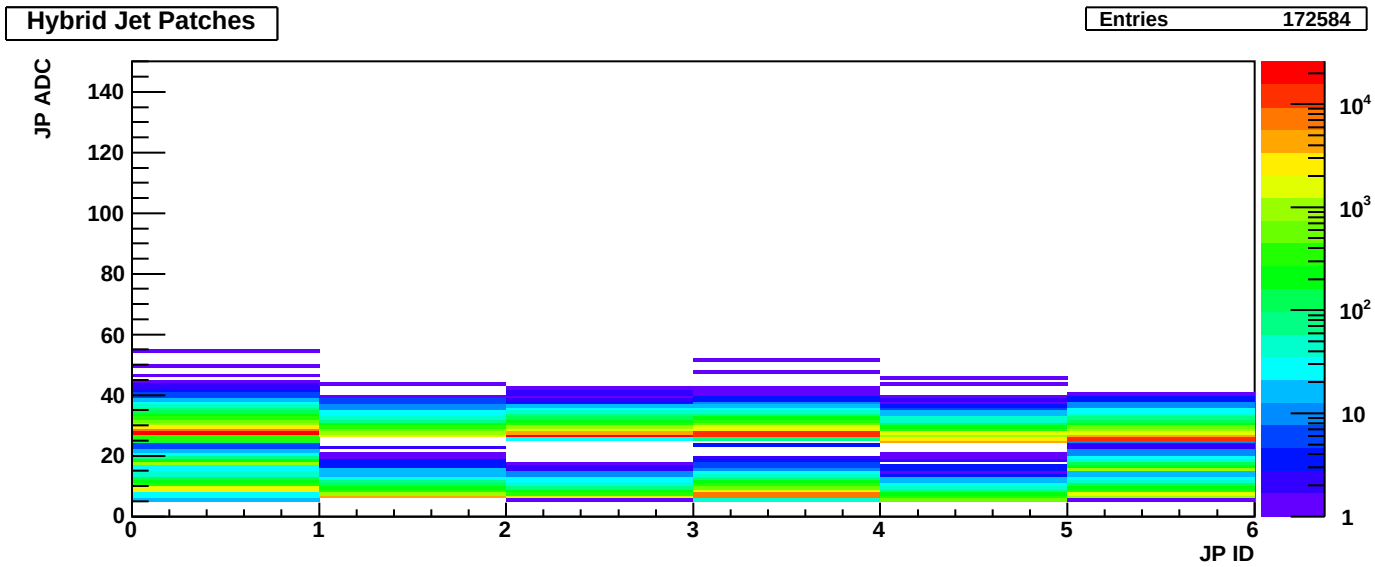

Barrel EMC L0 Input - Patch Sum
Entries 2.58876e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

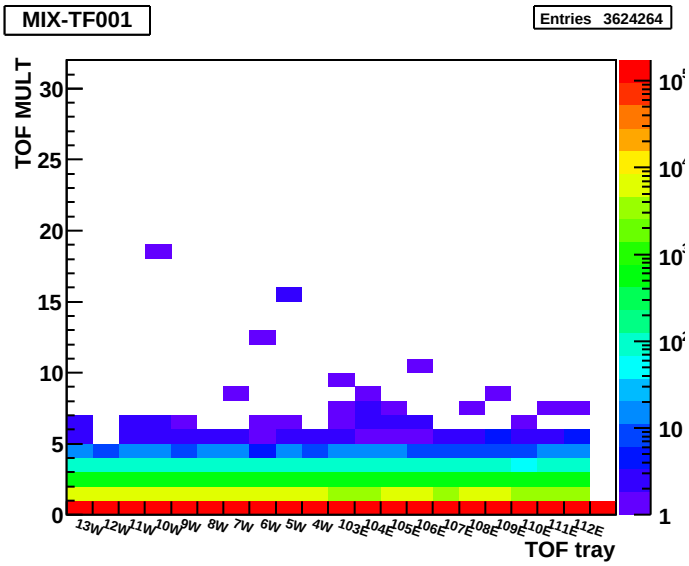

Endcap EMC L0 Input - High Tower
Entries 7766280
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

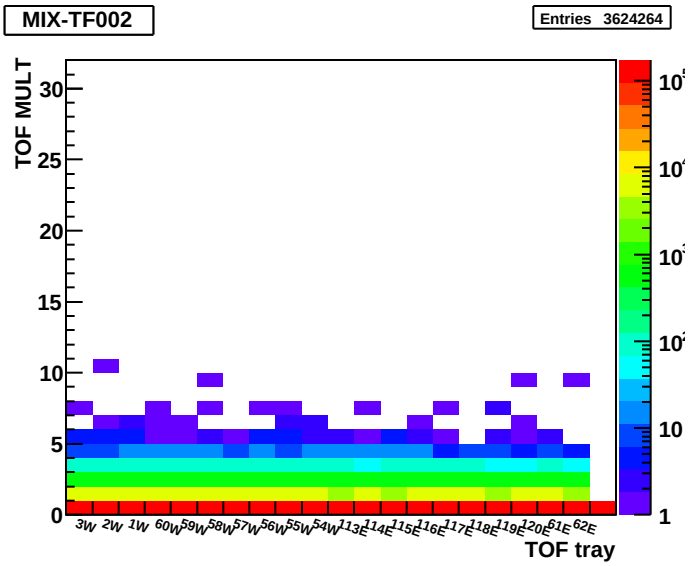

Endcap EMC L0 Input - Patch Sum
Entries 7766280
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

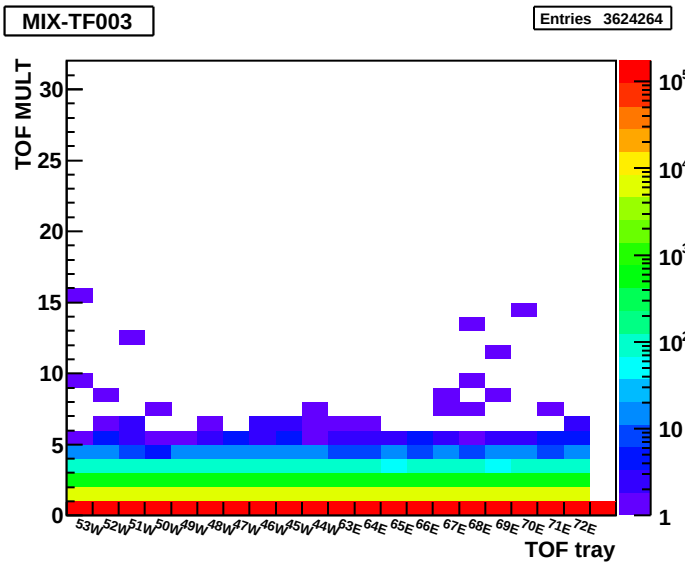

Barrel Jet Patches
Entries 1553256
JP ADC
JP ID

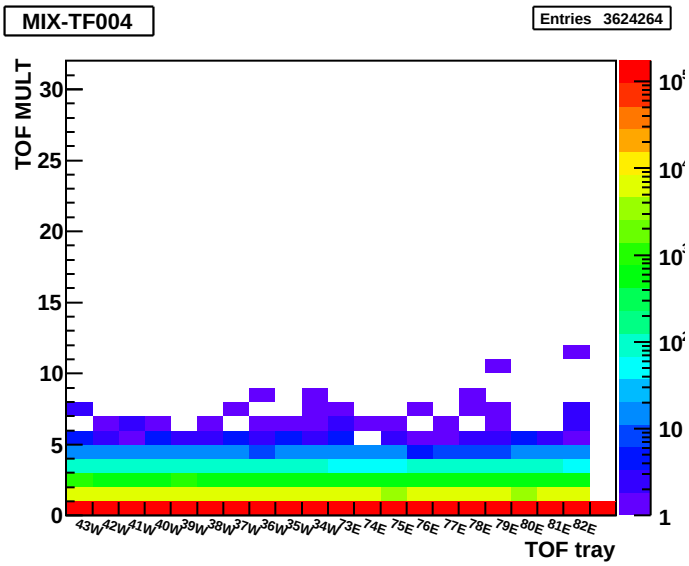

Endcap Jet Patches
Entries 517752
JP ADC
JP ID

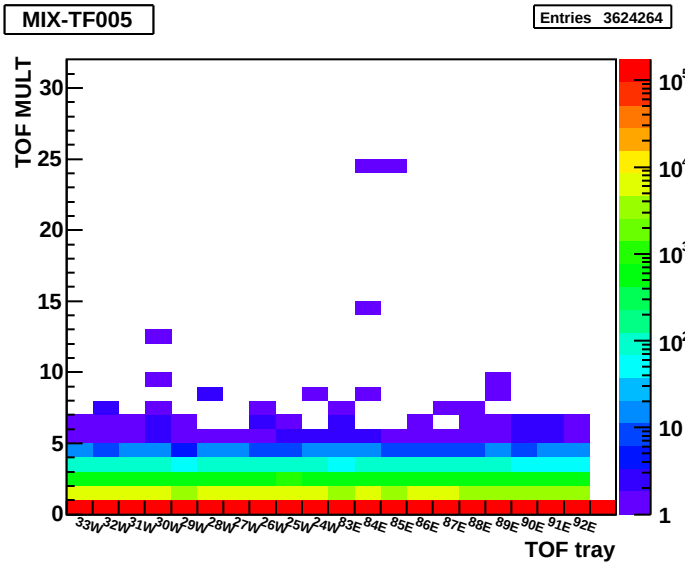

Hybrid Jet Patches
Entries 172584
JP ADC
JP ID

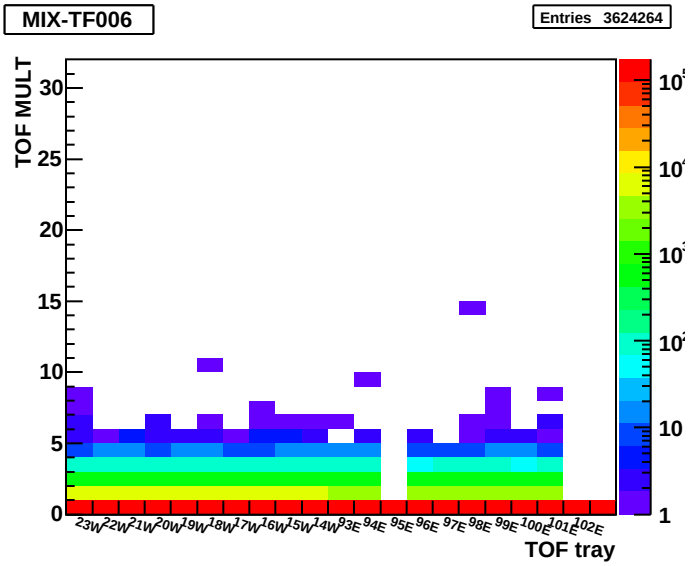

MIX-TF001
Entries 3624264
TOF MULT
TOF tray

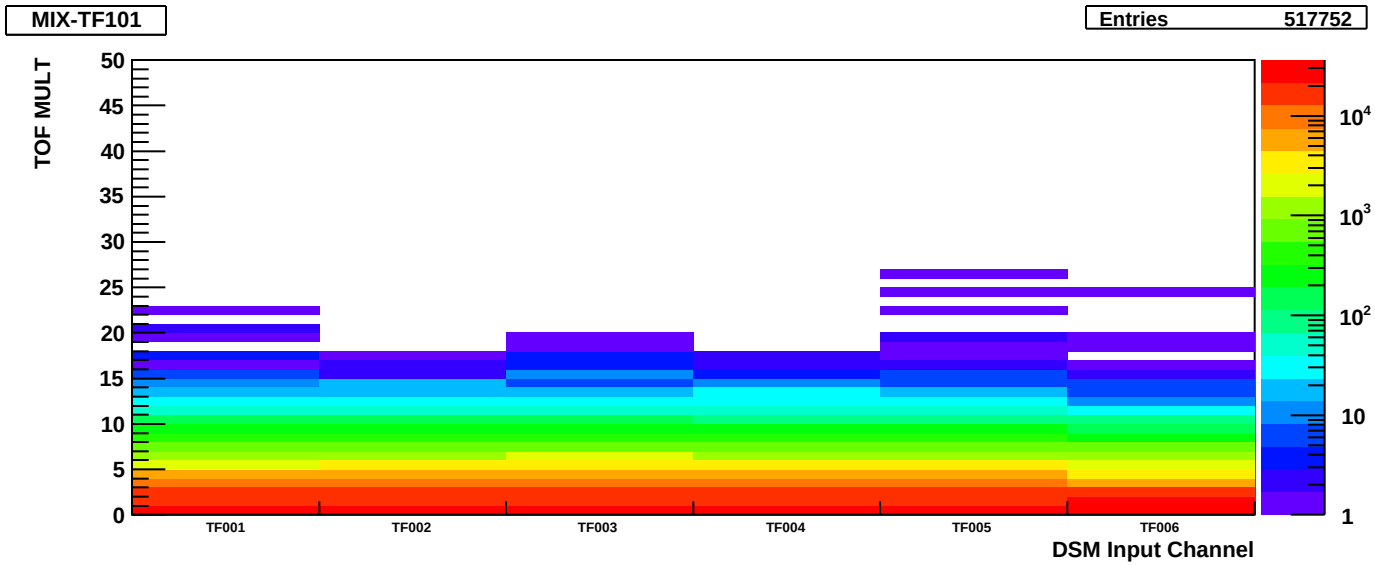

MIX-TF002
Entries 3624264
TOF MULT
TOF tray

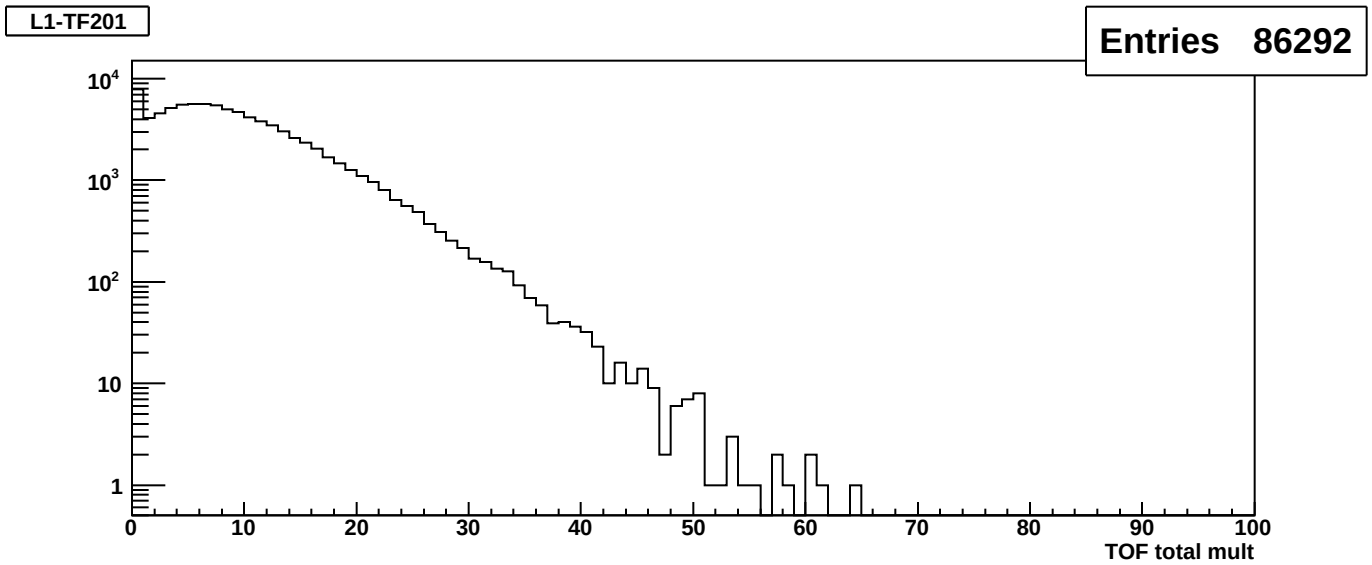

MIX-TF003
Entries 3624264
TOF MULT
TOF tray

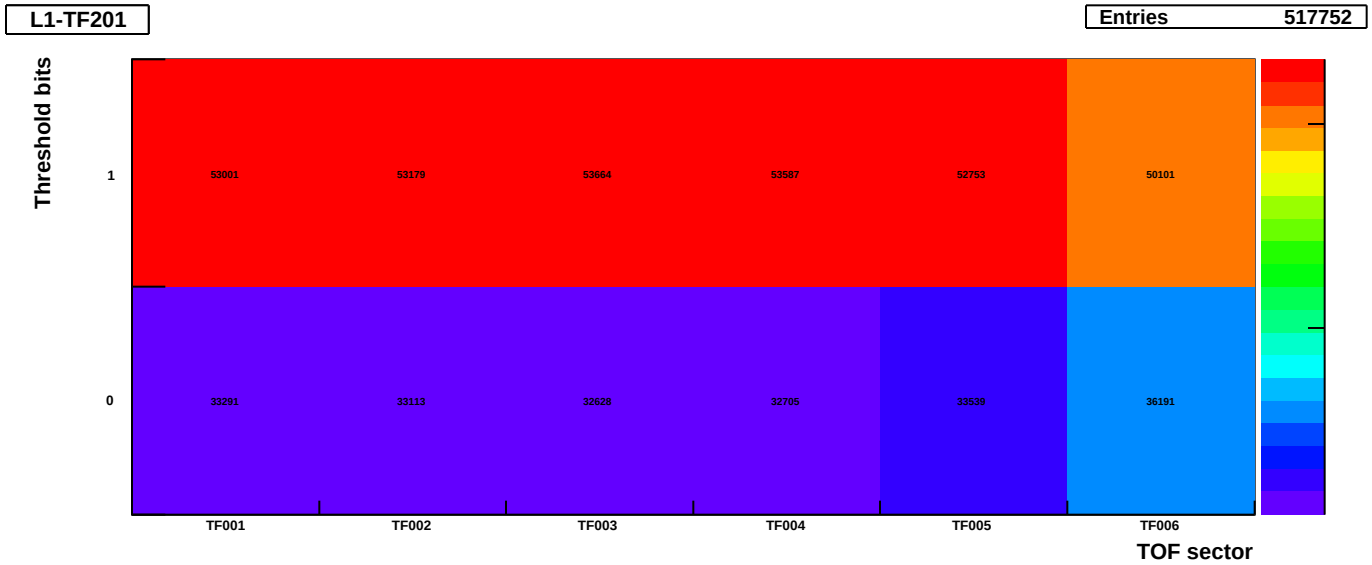

MIX-TF004
Entries 3624264
TOF MULT
TOF tray

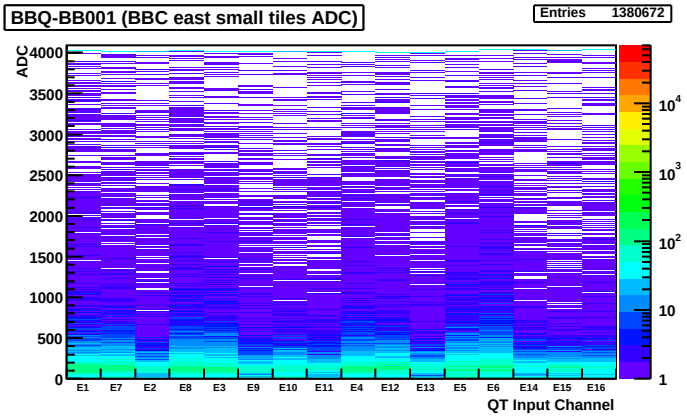

MIX-TF005
Entries 3624264
TOF MULT
TOF tray

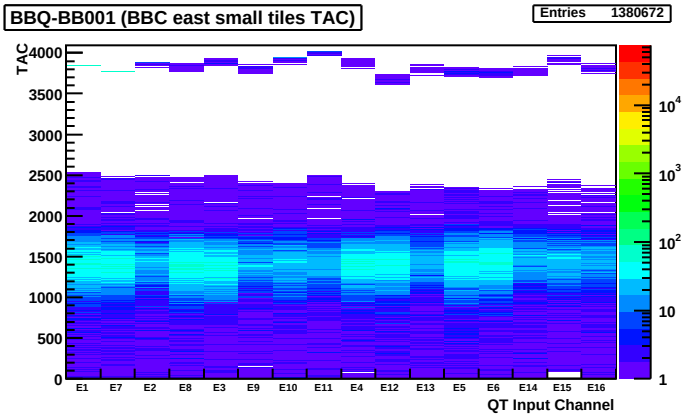

MIX-TF006
Entries 3624264
TOF MULT
TOF tray

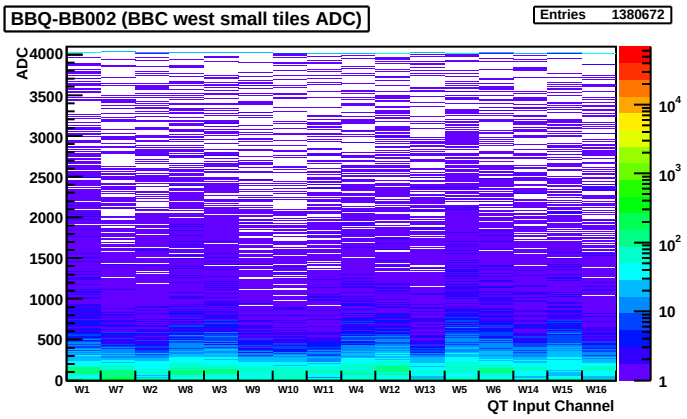

MIX-TF101
Entries 517752
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

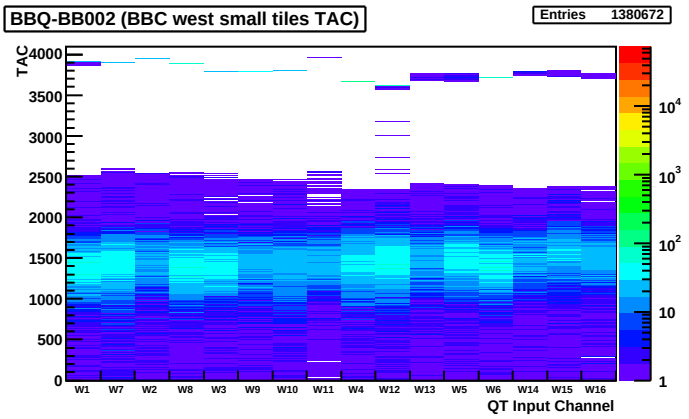

L1-TF201
Entries 86292
TOF total mult

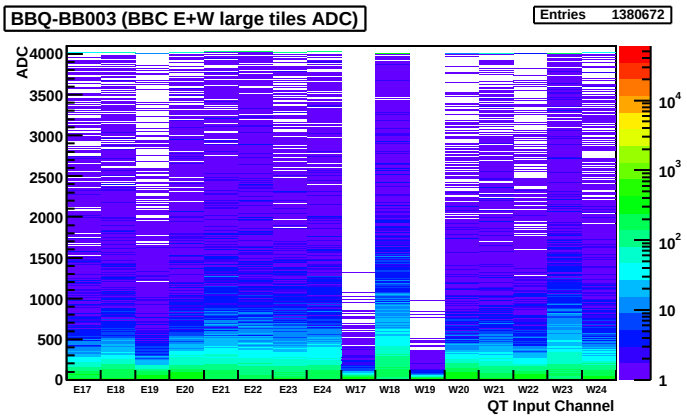

L1-TF201
Entries 517752
Threshold bits
53001
53179
53664
53587
52753
50101
33291
33113
32628
32705
33539
36191
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

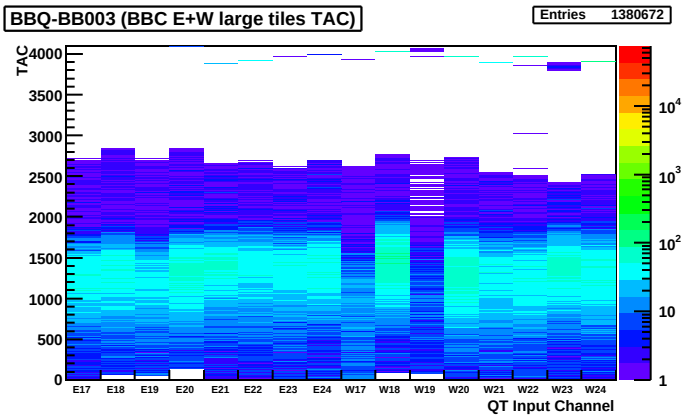

BBQ-BB001 (BBC east small tiles ADC)
Entries 1380672
ADC
QT Input Channel

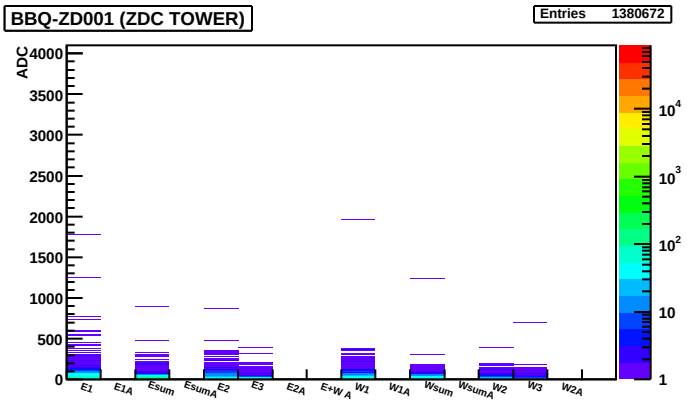

BBQ-BB001 (BBC east small tiles TAC)
Entries 1380672
TAC
QT Input Channel

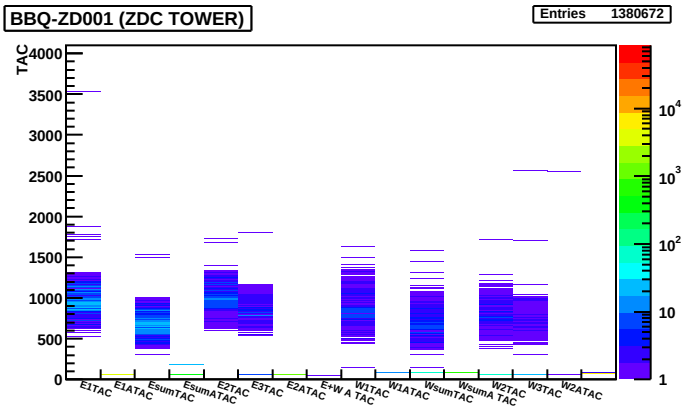

BBQ-BB002 (BBC west small tiles ADC)
Entries 1380672
ADC
QT Input Channel

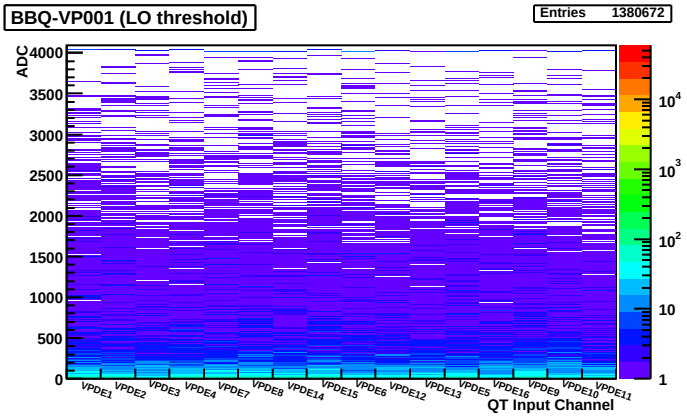

BBQ-BB002 (BBC west small tiles TAC)
Entries 1380672
TAC
QT Input Channel

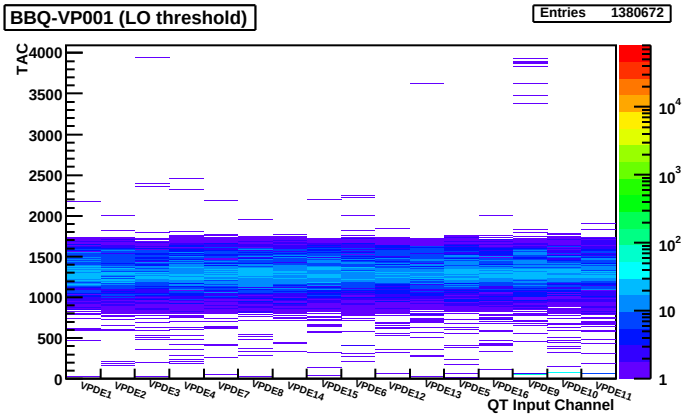

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1380672
ADC
QT Input Channel

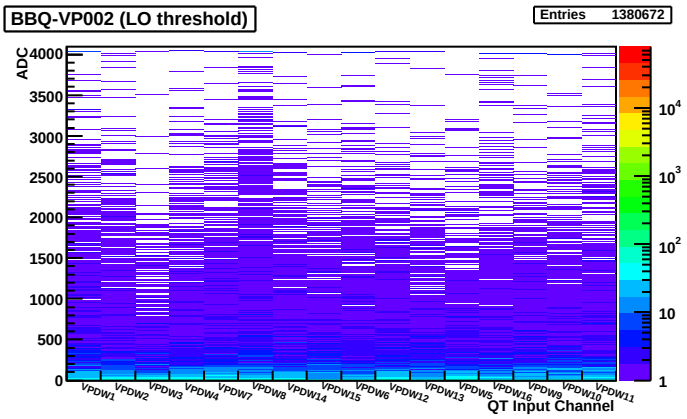

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1380672
TAC
QT Input Channel

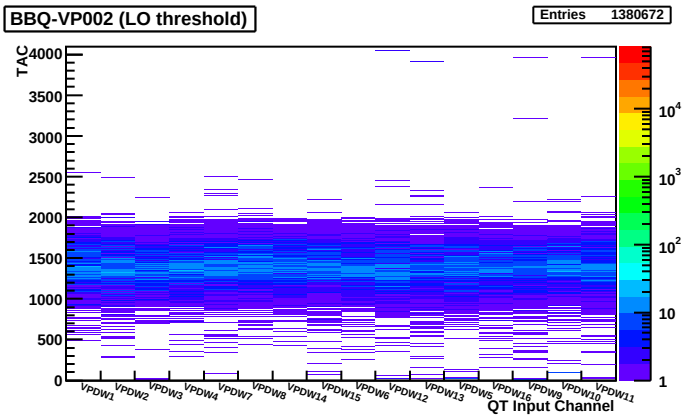

BBQ-ZD001 (ZDC TOWER)
Entries 1380672
ADC

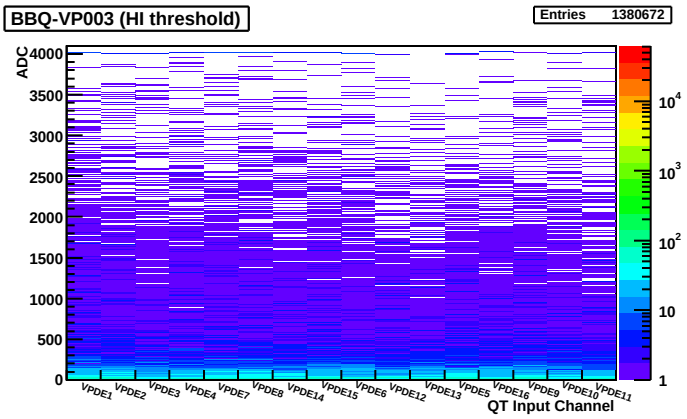

BBQ-ZD001 (ZDC TOWER)
Entries 1380672
TAC

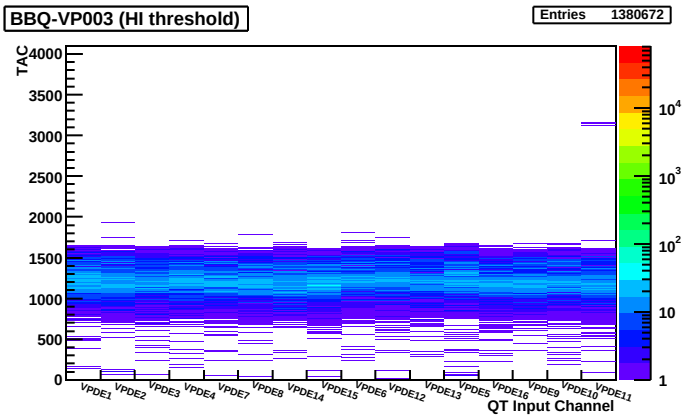

BBQ-VP001 (LO threshold)
Entries 1380672
ADC
QT Input Channel

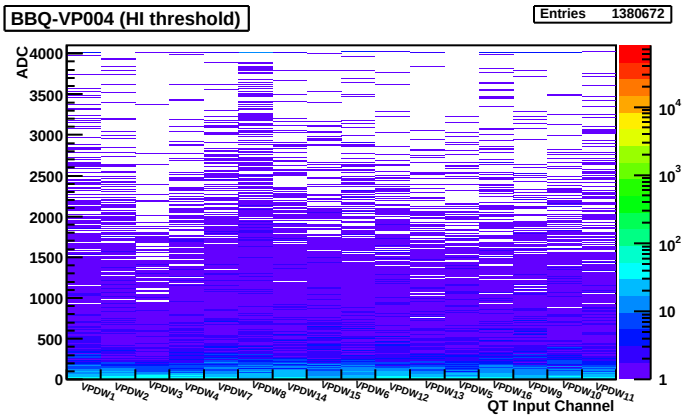

BBQ-VP001 (LO threshold)
Entries 1380672
TAC
QT Input Channel

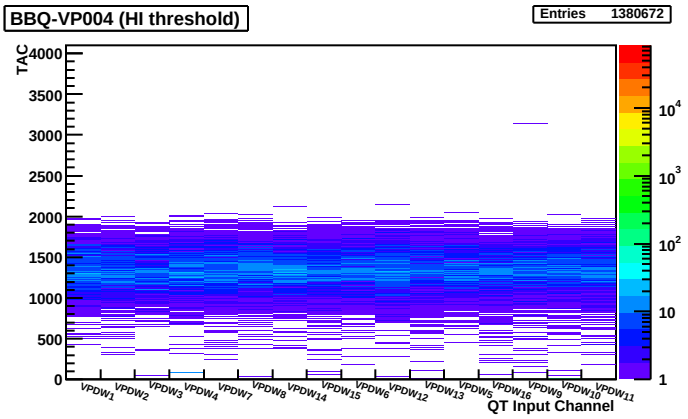

BBQ-VP002 (LO threshold)
Entries 1380672
ADC
QT Input Channel

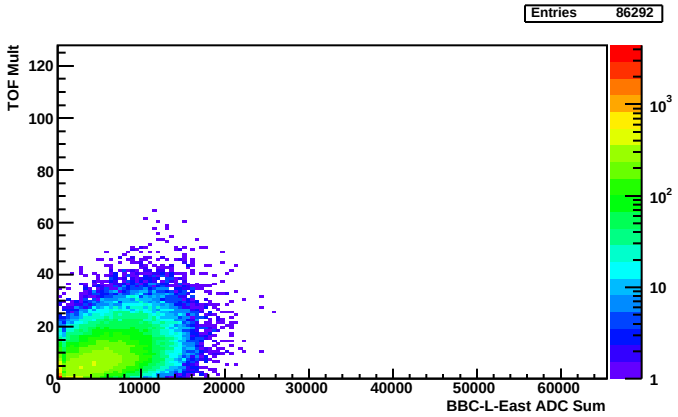

BBQ-VP002 (LO threshold)
Entries 1380672
TAC
QT Input Channel

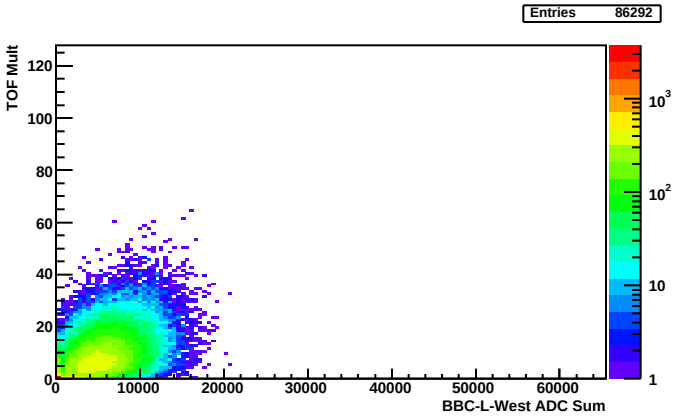

BBQ-VP003 (HI threshold)
Entries 1380672
ADC
QT Input Channel

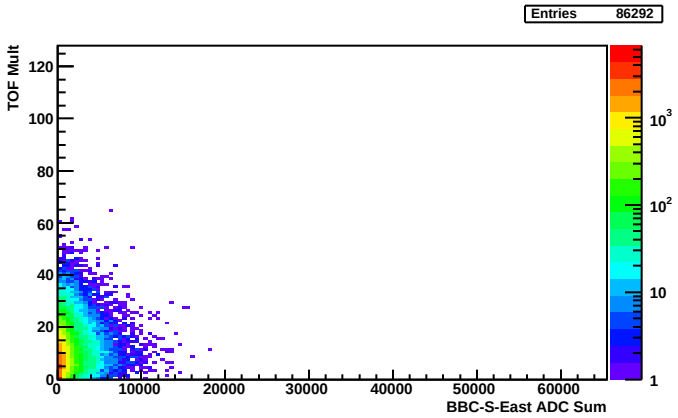

BBQ-VP003 (HI threshold)
Entries 1380672
TAC
QT Input Channel

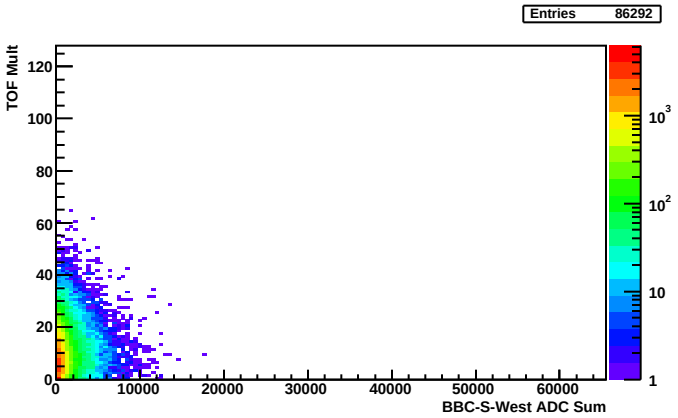

BBQ-VP004 (HI threshold)
Entries 1380672
ADC
QT Input Channel

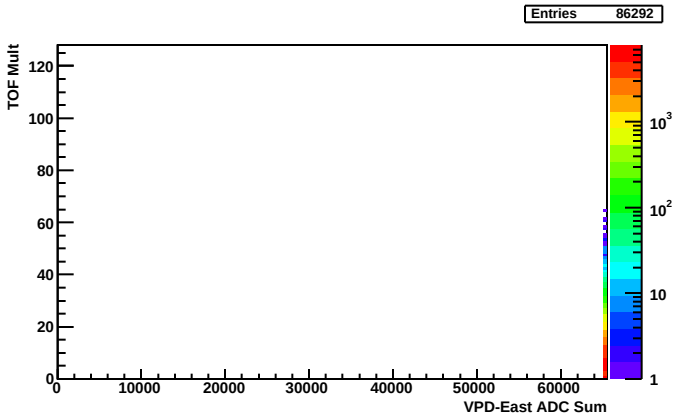

BBQ-VP004 (HI threshold)
Entries 1380672
TAC
QT Input Channel

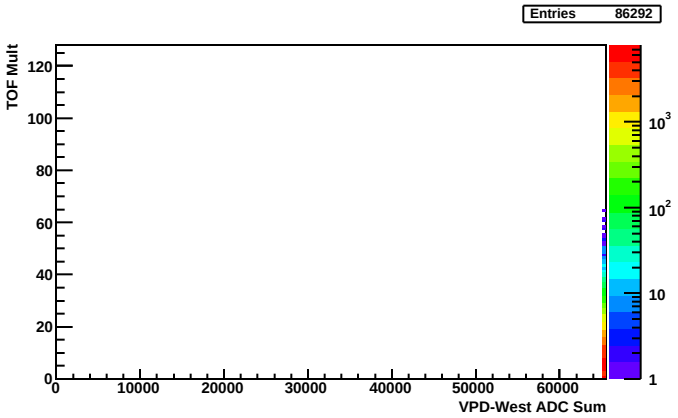

Entries 86292
TOF Mult
BBC-L-East ADC Sum

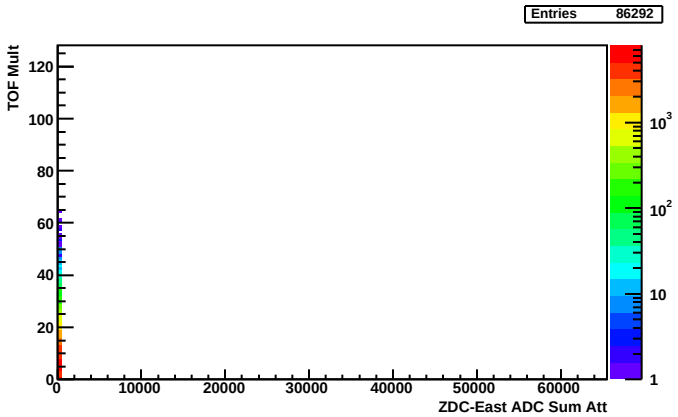

Entries 86292
TOF Mult
BBC-L-West ADC Sum

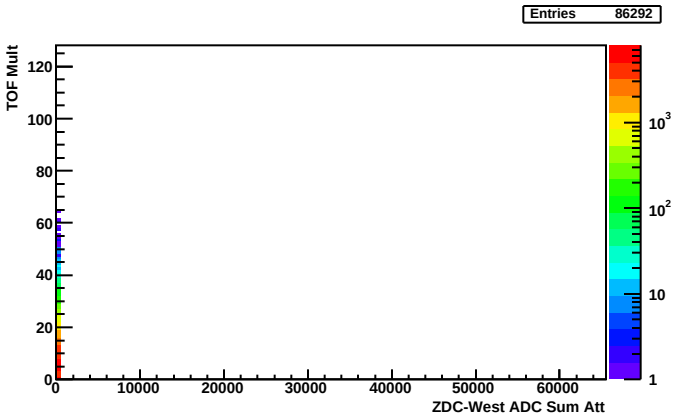

Entries 86292
TOF Mult
BBC-S-East ADC Sum

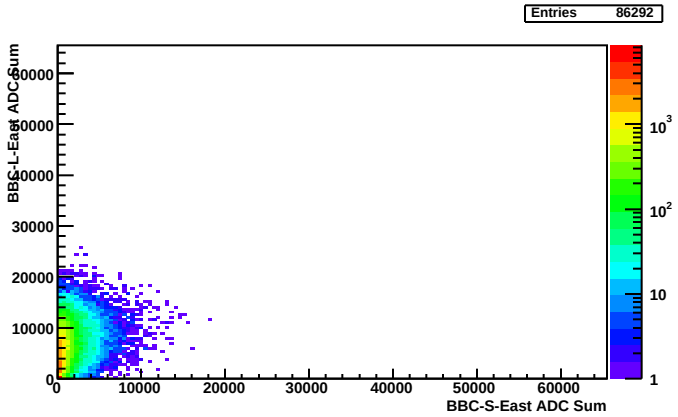

Entries 86292
TOF Mult
BBC-S-West ADC Sum

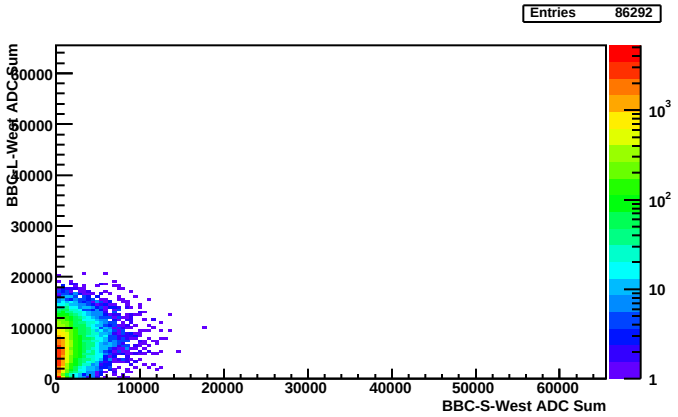

Entries 86292
TOF Mult
VPD-East ADC Sum

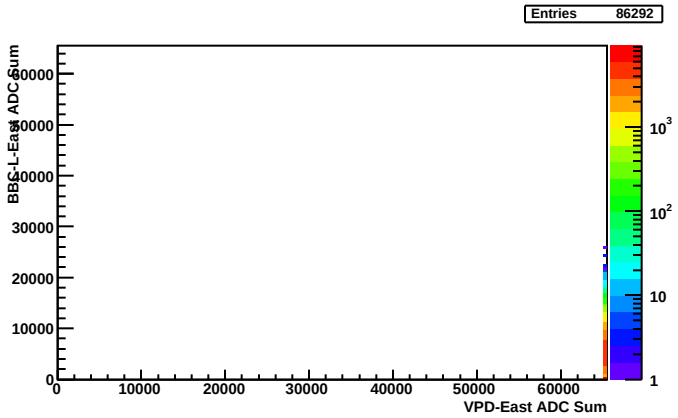

Entries 86292
TOF Mult
VPD-West ADC Sum

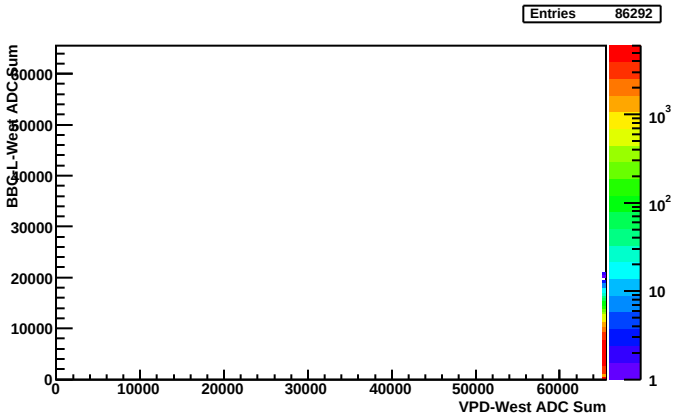

Entries 86292
TOF Mult
ZDC-East ADC Sum Att

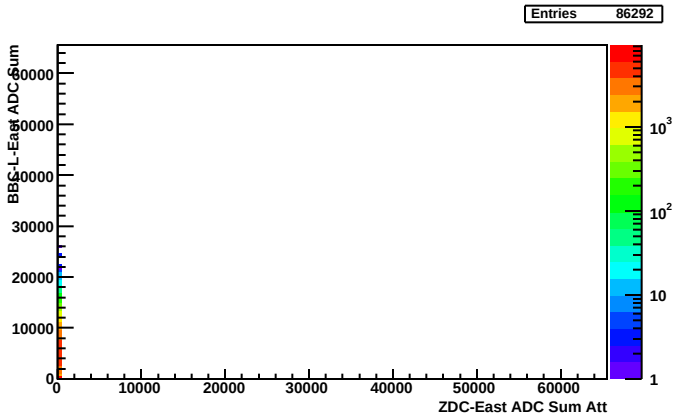

Entries 86292
TOF Mult
ZDC-West ADC Sum Att


Entries 86292
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 86292
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 86292
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 86292
BBC-L-West ADC Sum
VPD-West ADC Sum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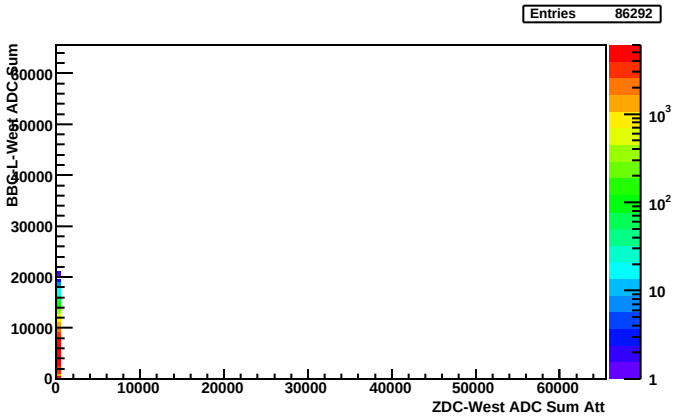
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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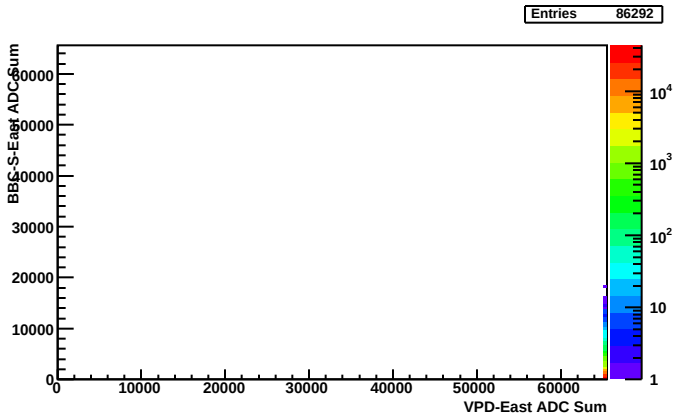
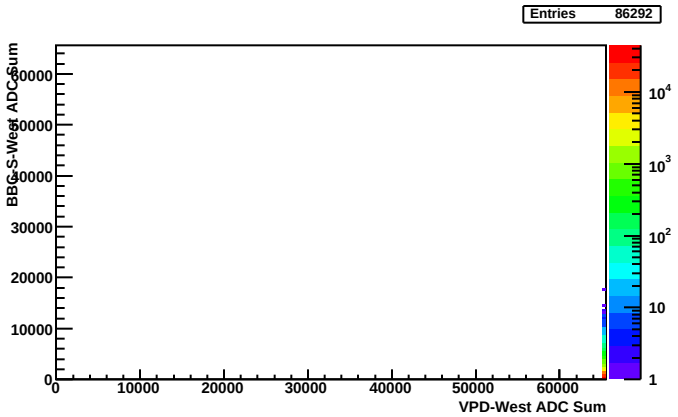
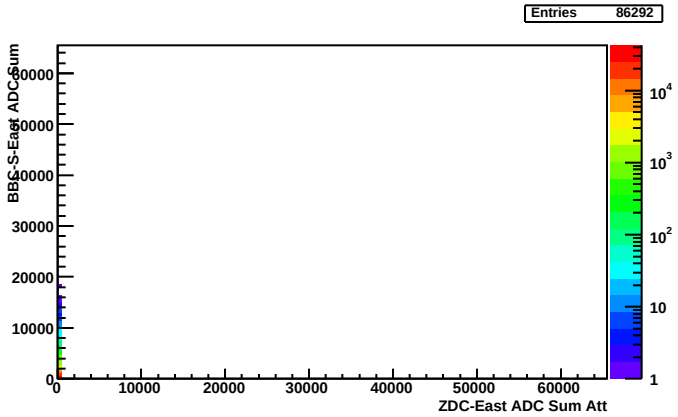
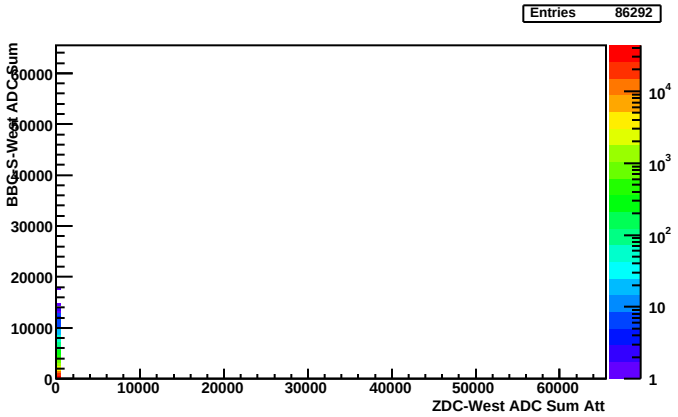
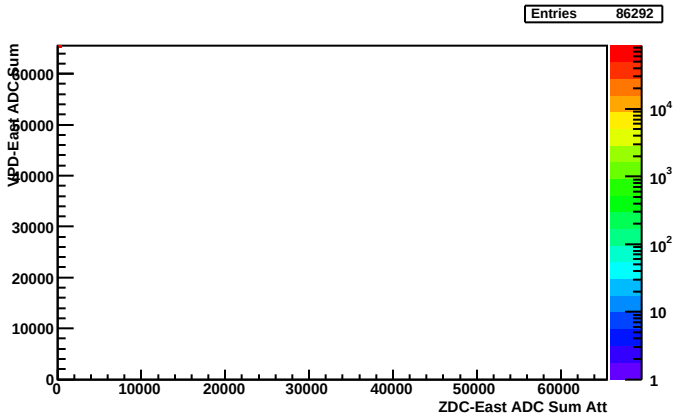
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

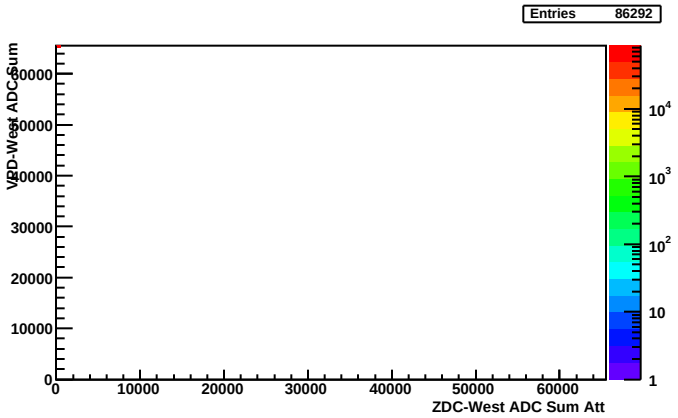

Entries 86292
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 86292
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 86292
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 86292
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 86292
VPD-East ADC Sum
ZDC-East ADC Sum Att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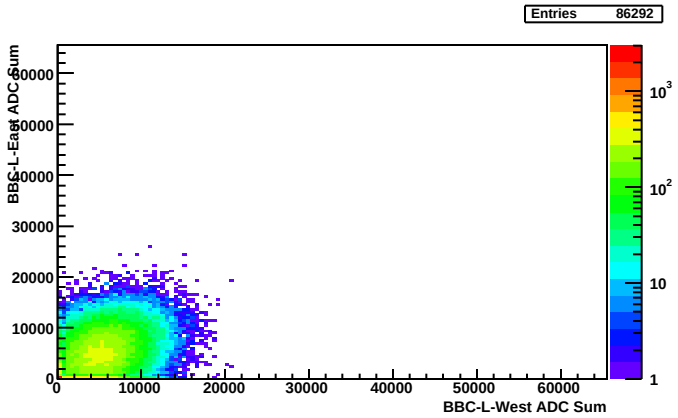
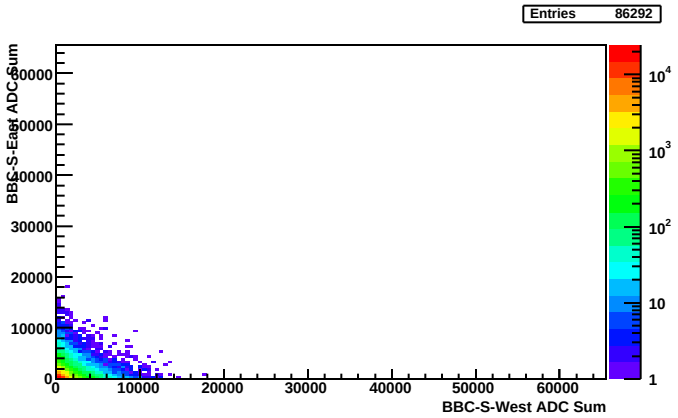
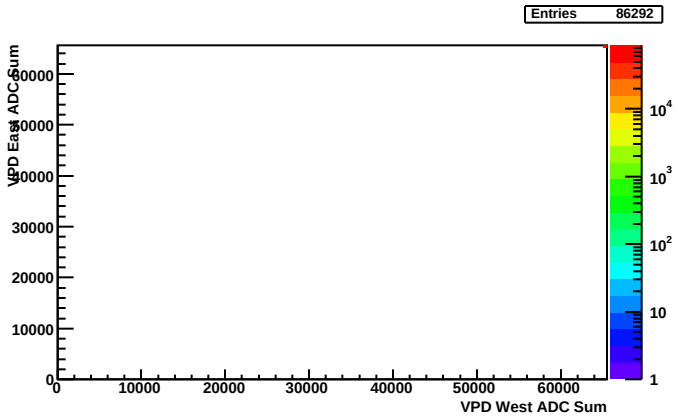
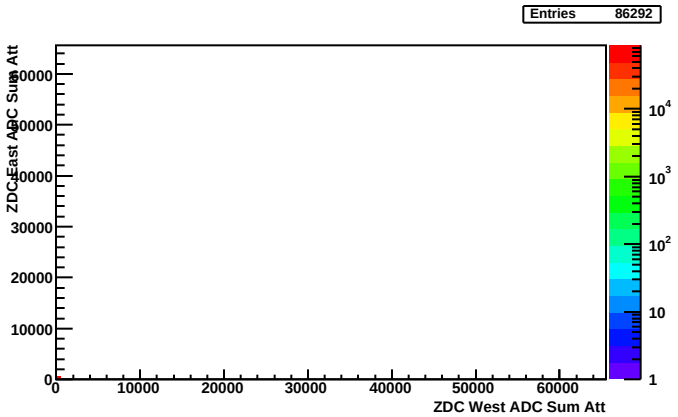
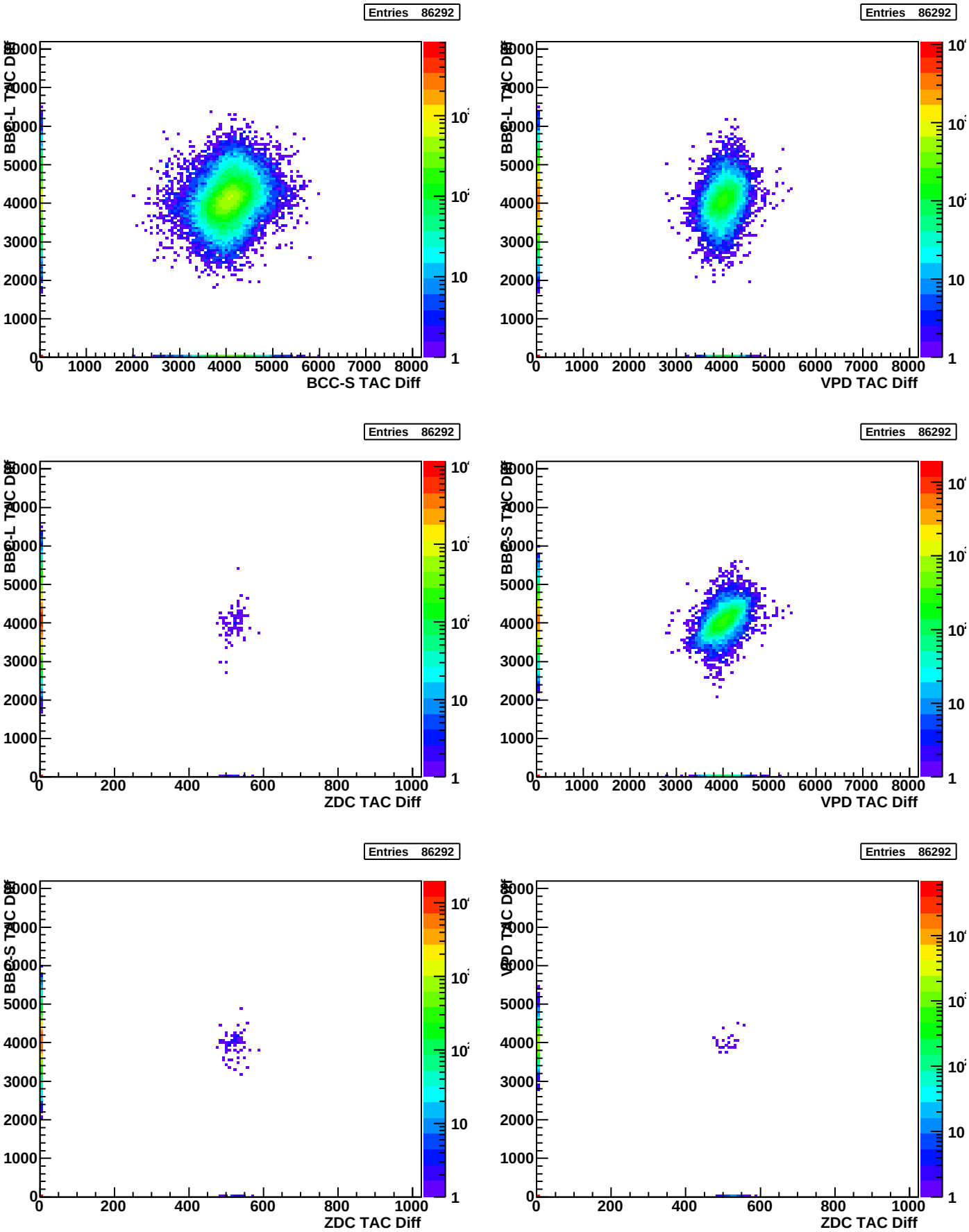
VPD-West ADC Sum
ZDC-West ADC Sum Att

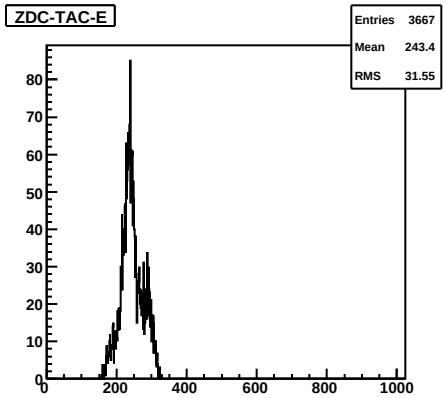

Entries 86292
BBC-L-East ADC Sum
BBC-L-West ADC Sum

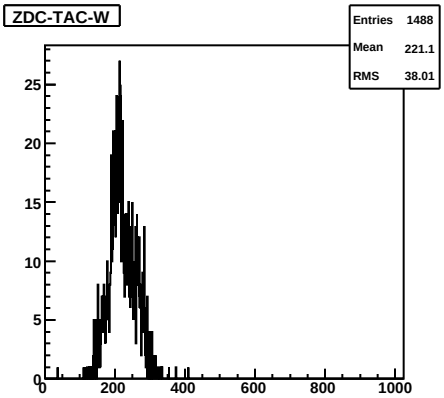

Entries 86292
BBC-S-East ADC Sum
BBC-S-West ADC Sum

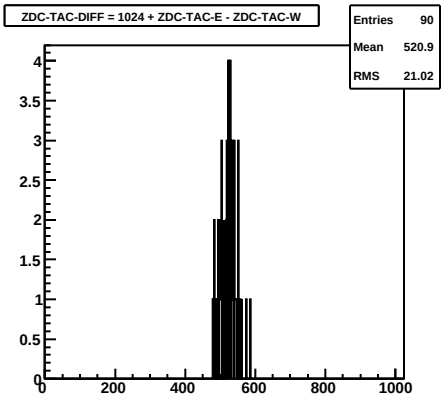

Entries 86292
VPD East ADC Sum
VPD West ADC Sum

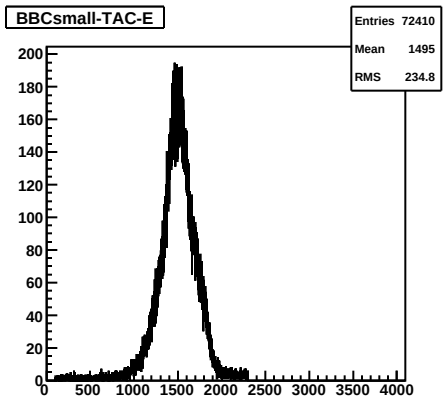

Entries 86292
ZDC East ADC Sum Att
ZDC West ADC Sum Att

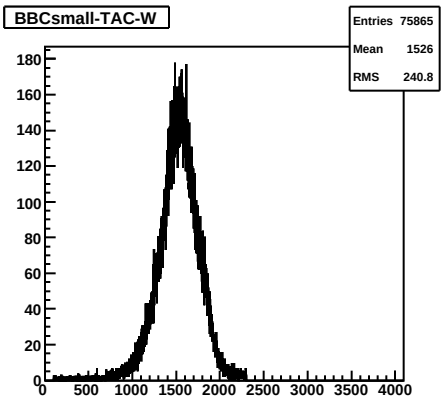

Entries 86292
BBC-L TAC Diff
BCC-S TAC Diff
Entries 86292
BBC-L TAC Diff
VPD TAC Diff
Entries 86292
BBC-L TAC Diff
ZDC TAC Diff
Entries 86292
BBC-S TAC Diff
VPD TAC Diff
Entries 86292
BBC-S TAC Diff
ZDC TAC Diff
Entries 86292
VPD TAC Diff
ZDC TAC Diff

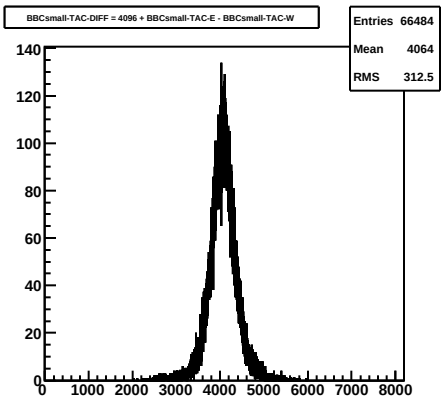

ZDC-TAC-E
Entries 3667
Mean 243.4
RMS 31.55

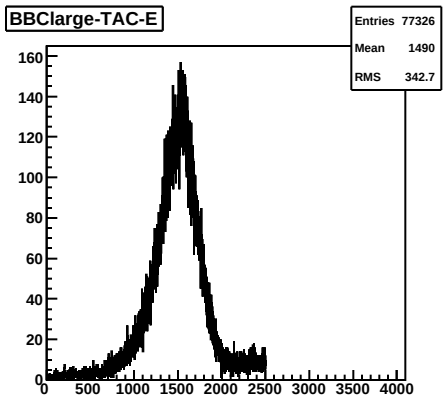

ZDC-TAC-W
Entries 1488
Mean 221.1
RMS 38.01

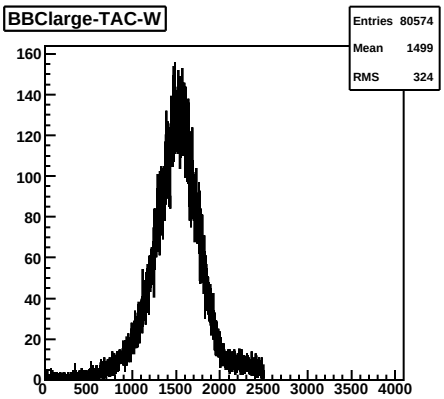

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 90
Mean 520.9
RMS 21.02

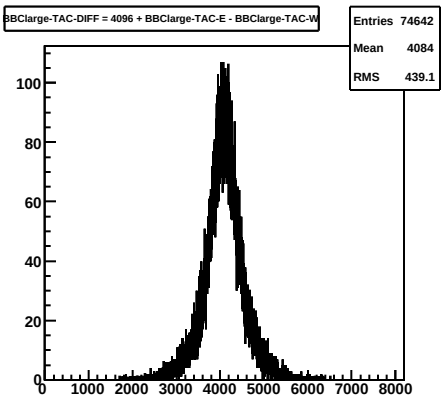

BBCsmall-TAC-E
Entries 72410
Mean 1495
RMS 234.8

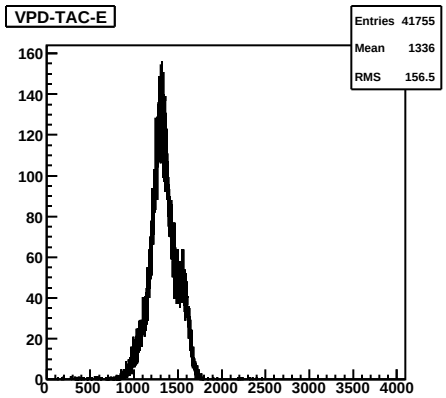

BBCsmall-TAC-W
Entries 75865
Mean 1526
RMS 240.8

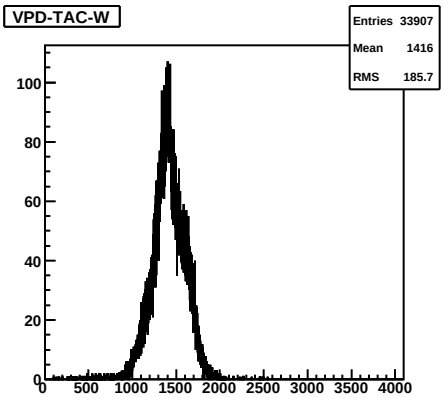

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 66484
Mean 4064
RMS 312.5

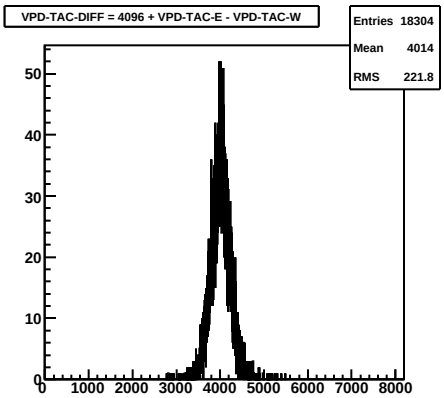

BBClarge-TAC-E
Entries 77326
Mean 1490
RMS 342.7

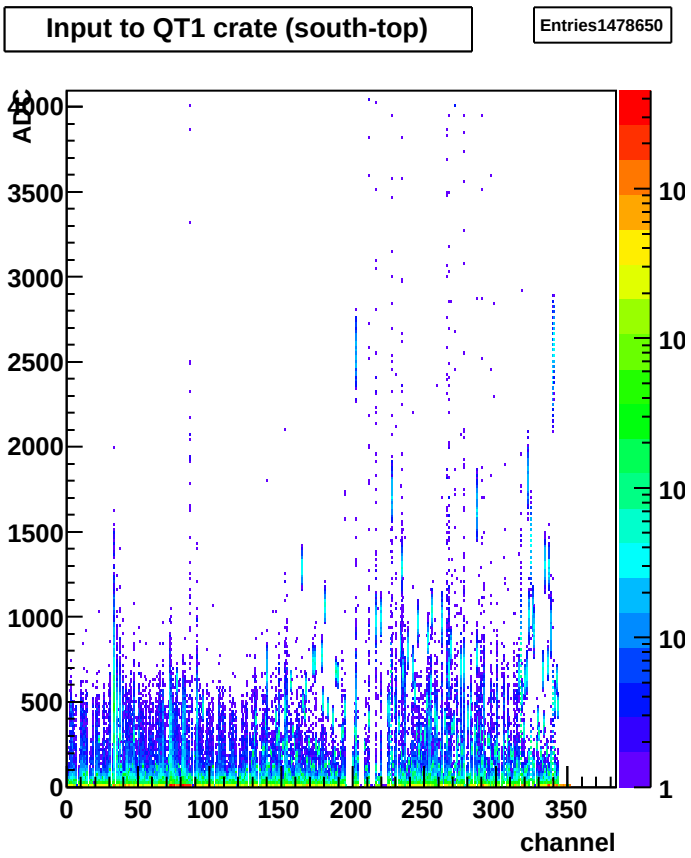

BBClarge-TAC-W
Entries 80574
Mean 1499
RMS 324

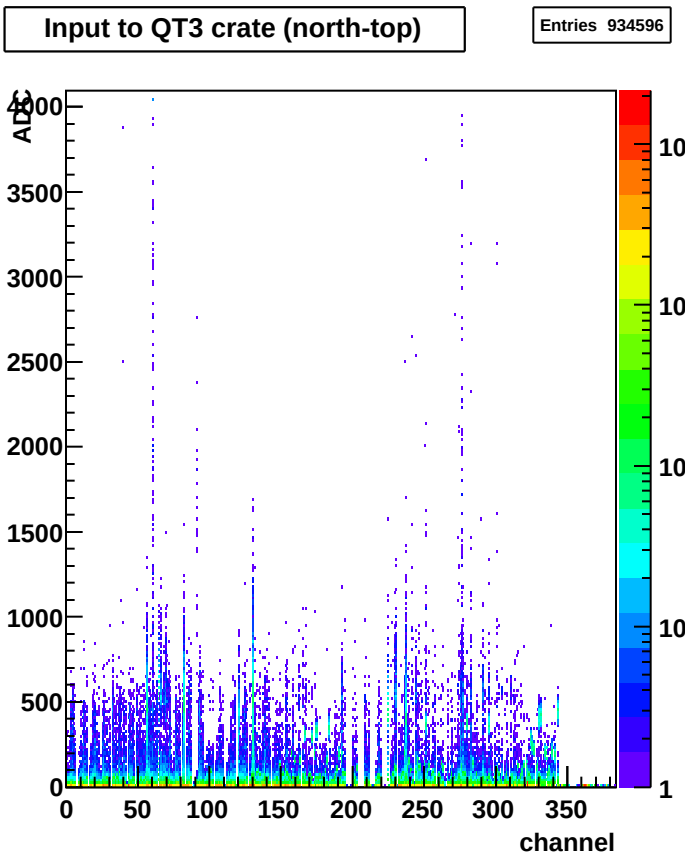

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 74642
Mean 4084
RMS 439.1

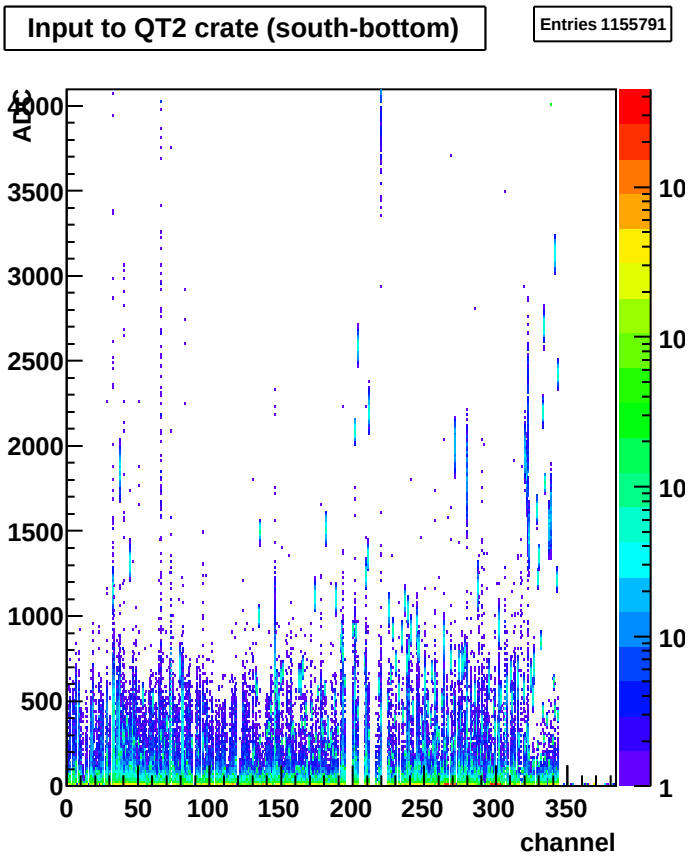

VPD-TAC-E
Entries 41755
Mean 1336
RMS 156.5

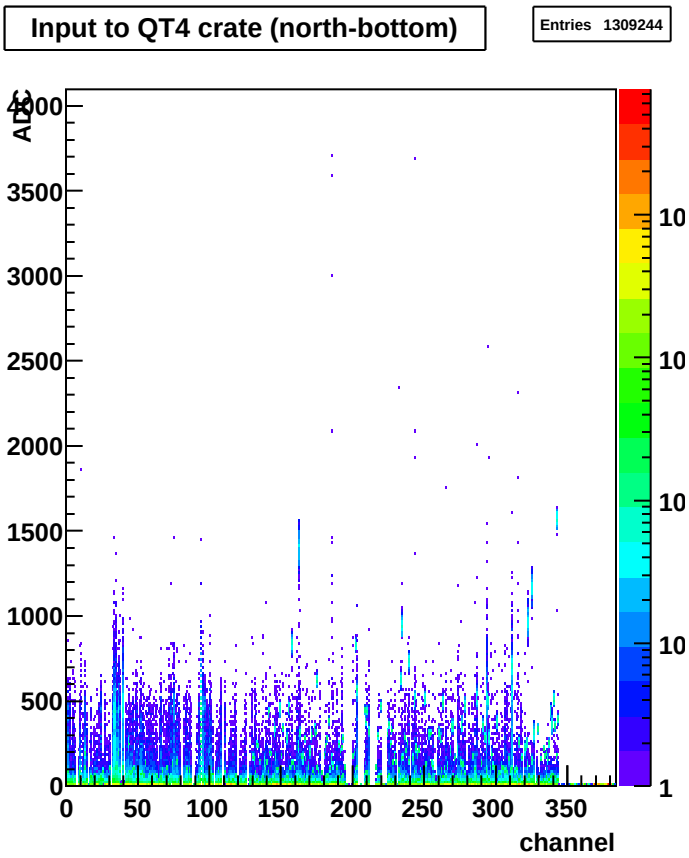

VPD-TAC-W
Entries 33907
Mean 1416
RMS 185.7

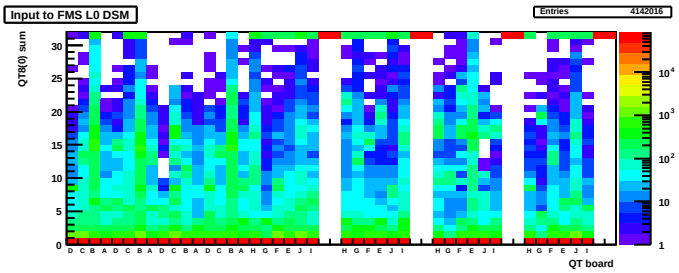

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 18304
Mean 4014
RMS 221.8

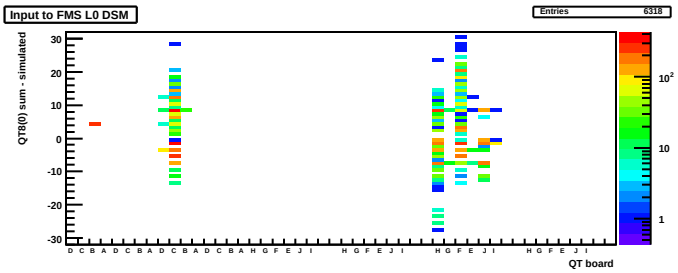

Input to QT1 crate (south-top)
Entries1478650
ADC
channel

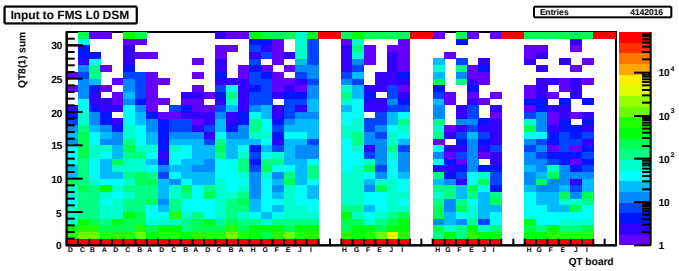

Input to QT3 crate (north-top)
Entries 934596
ADC
channel

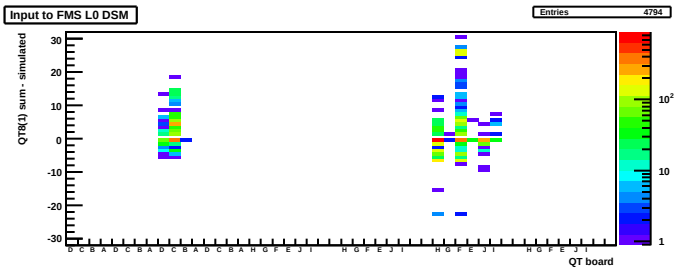

Input to QT2 crate (south-bottom)
Entries 1155791
ADC
channel

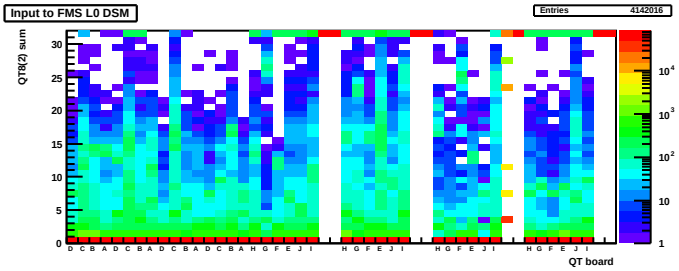

Input to QT4 crate (north-bottom)
Entries 1309244
ADC
channel

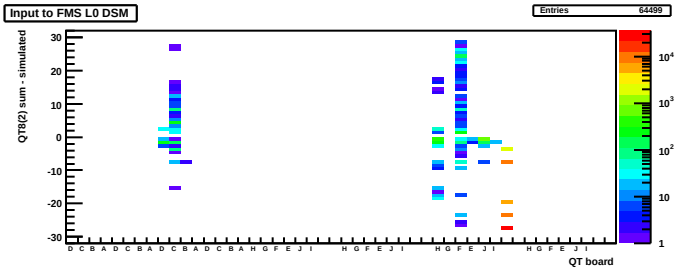

Input to FMS L0 DSM
Entries 4142016
QT8(0) sum
QT board

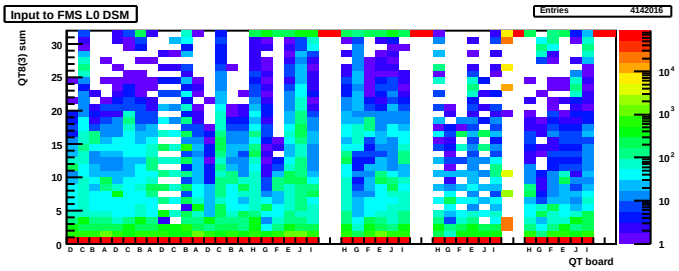

Input to FMS L0 DSM
Entries 6318
QT8(0) sum - simulated
QT board

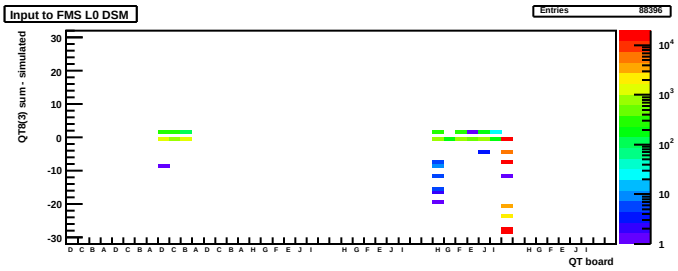

Input to FMS L0 DSM
Entries 4142016
QT8(1) sum
QT board

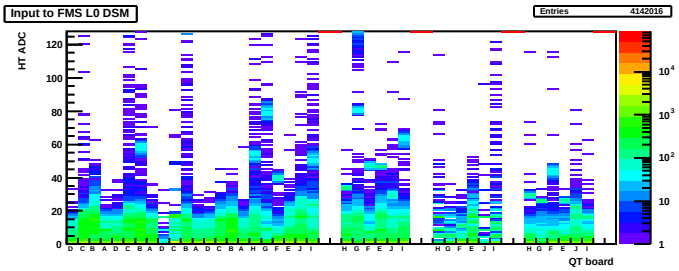

Input to FMS L0 DSM
Entries 4794
QT8(1) sum - simulated
QT board

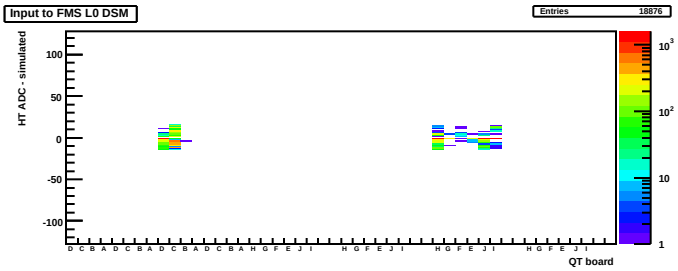

Input to FMS L0 DSM
Entries 4142016
QT8(2) sum
QT board

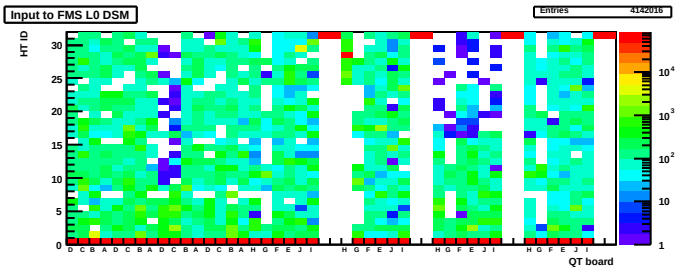

Input to FMS L0 DSM
Entries 64499
QT8(2) sum - simulated
QT board

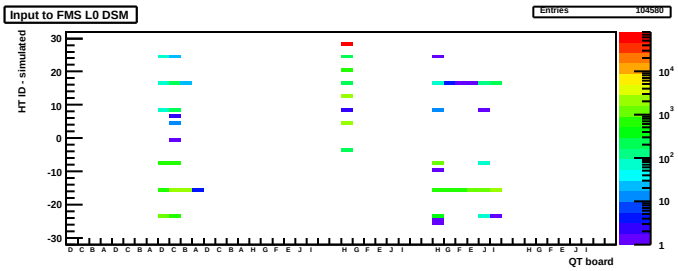

Input to FMS L0 DSM
Entries 4142016
QT8(3) sum
QT board

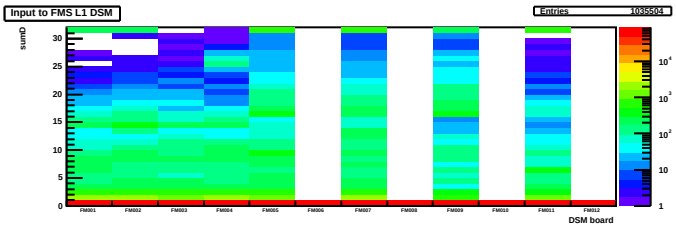

Input to FMS L0 DSM
Entries 88396
QT8(3) sum - simulated
QT board

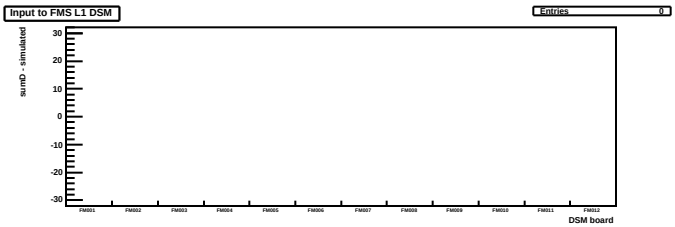

Input to FMS L0 DSM
Entries 4142016
HT ADC
QT board

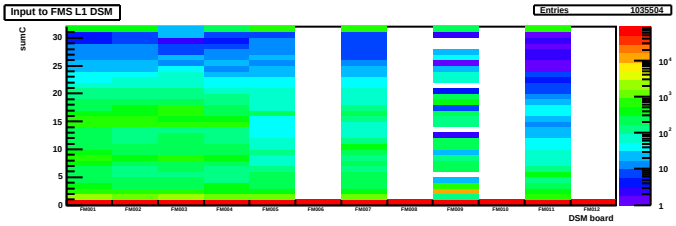

Input to FMS L0 DSM
Entries 18876
HT ADC - simulated
QT board

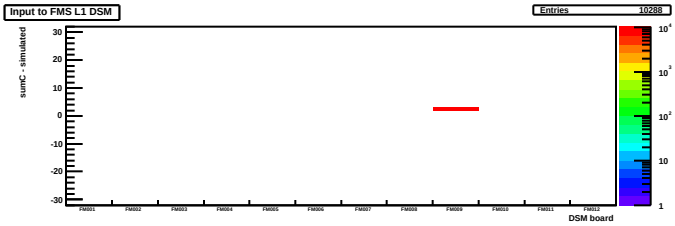

Input to FMS L0 DSM
Entries 4142016
HT ID
QT board

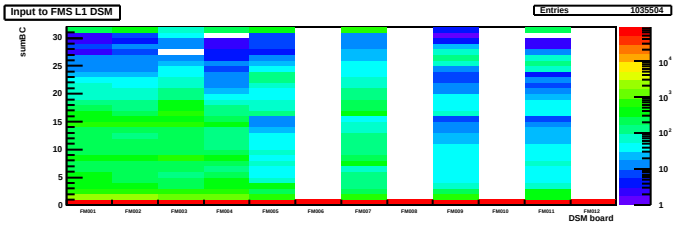

Input to FMS L0 DSM
Entries 104580
HT ID - simulated
QT board

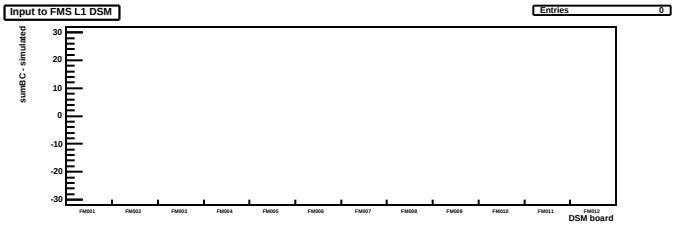

Input to FMS L1 DSM
Entries 1035504
sumD
DSM board

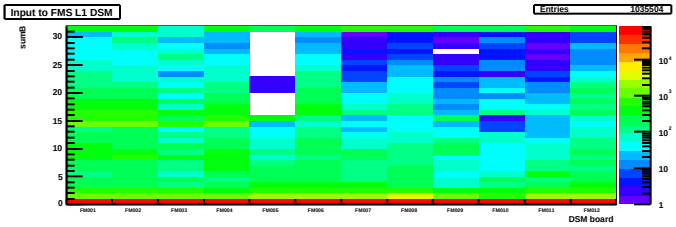

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

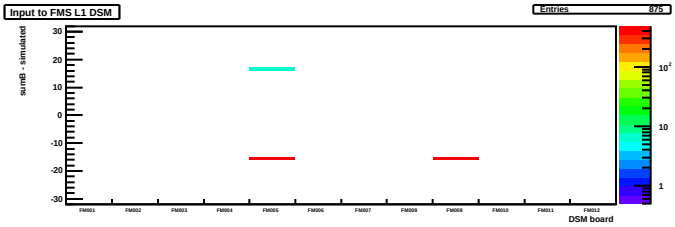

Input to FMS L1 DSM
Entries 1035504
sumC
DSM board

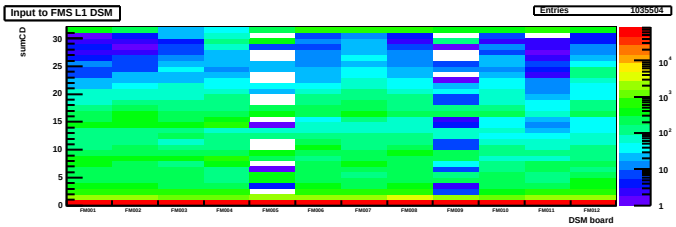

Input to FMS L1 DSM
Entries 10288
sumC - simulated
DSM board

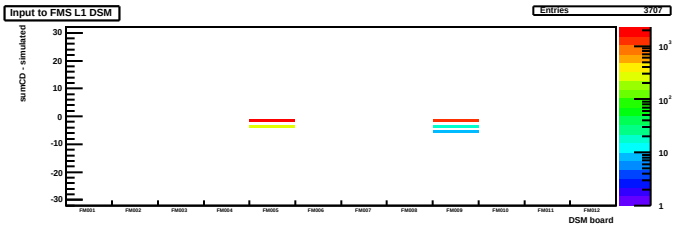

Input to FMS L1 DSM
Entries 1035504
sumBC
DSM board

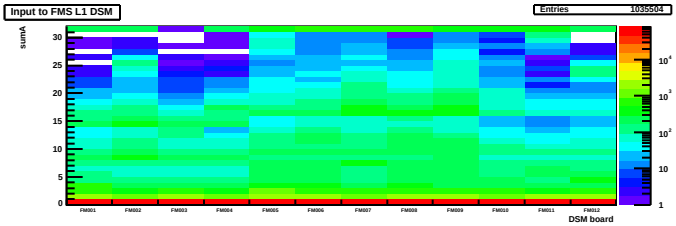

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

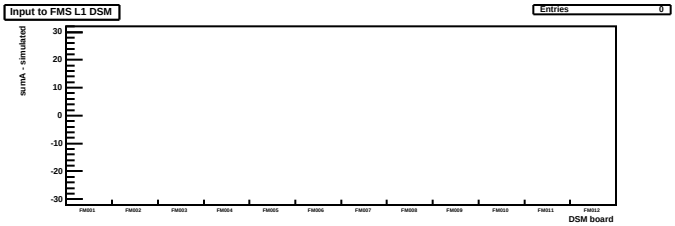

Input to FMS L1 DSM
Entries 1035504
sumB
DSM board

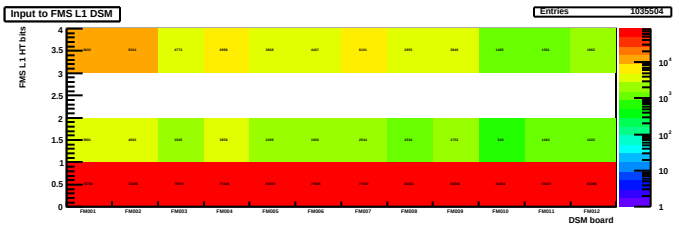

Input to FMS L1 DSM
Entries 875
sumB - simulated
DSM board

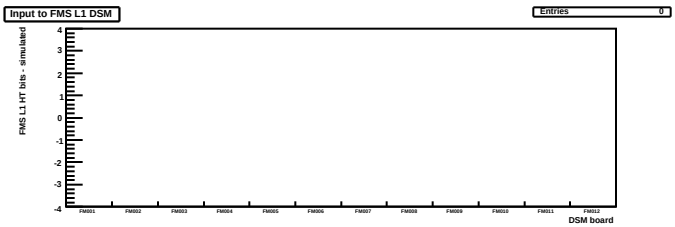

Input to FMS L1 DSM
Entries 1035504
sumCD
DSM board

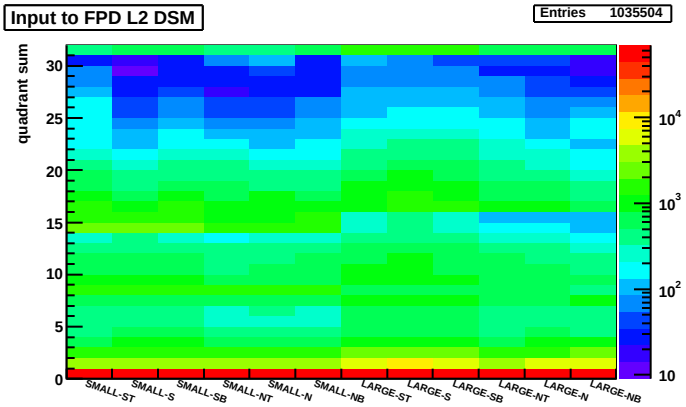

Input to FMS L1 DSM
Entries 3707
sumCD - simulated
DSM board

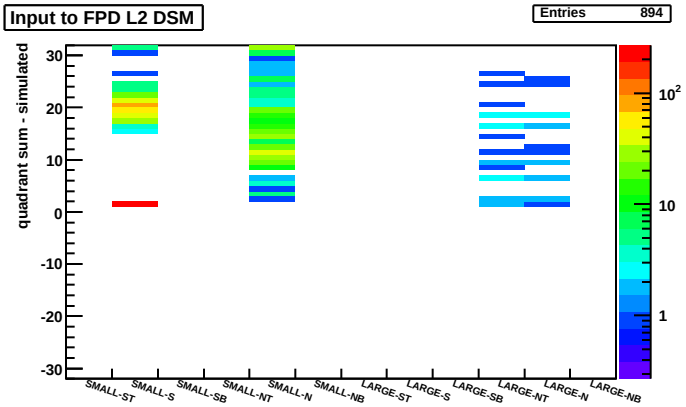

Input to FMS L1 DSM
Entries 1035504
sumA
DSM board

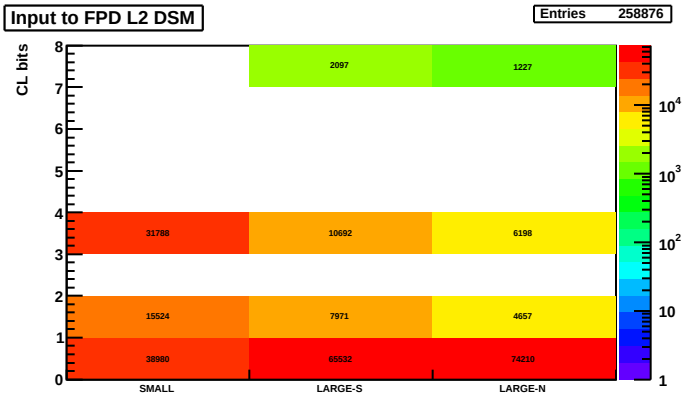

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

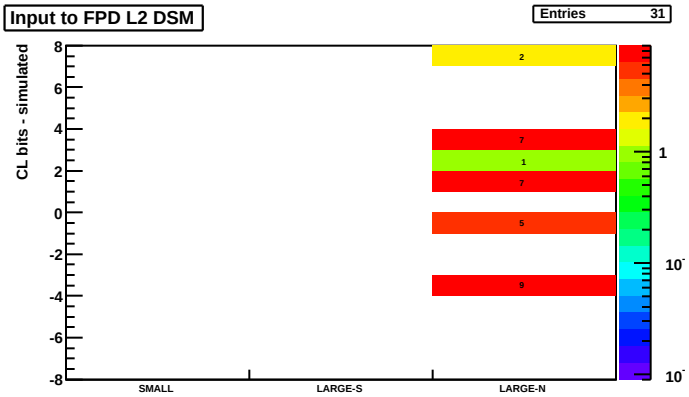

Input to FMS L1 DSM
Entries 1035504
FMS L1 HT bits
DSM board

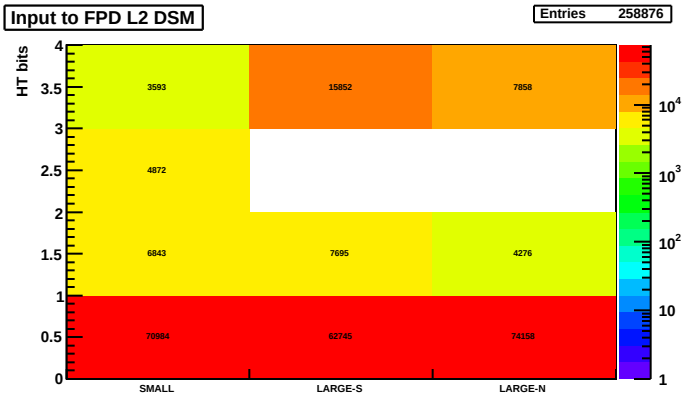

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

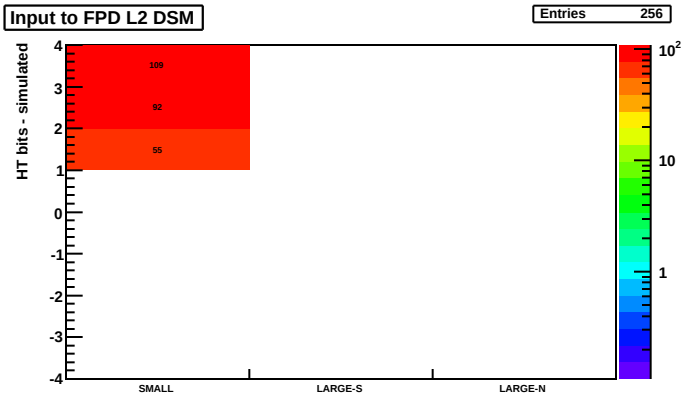

Input to FPD L2 DSM
Entries 1035504
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

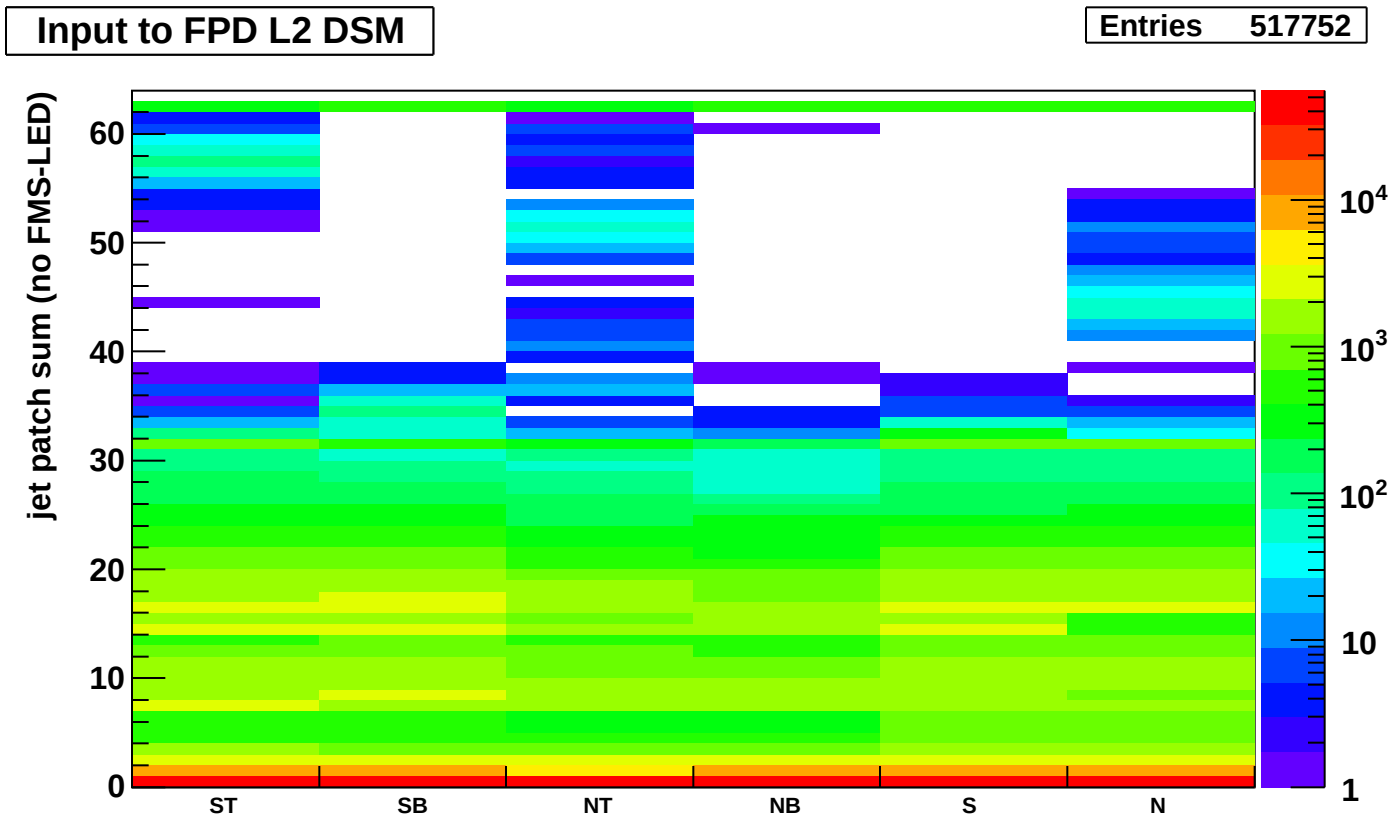

Input to FPD L2 DSM
Entries 894
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

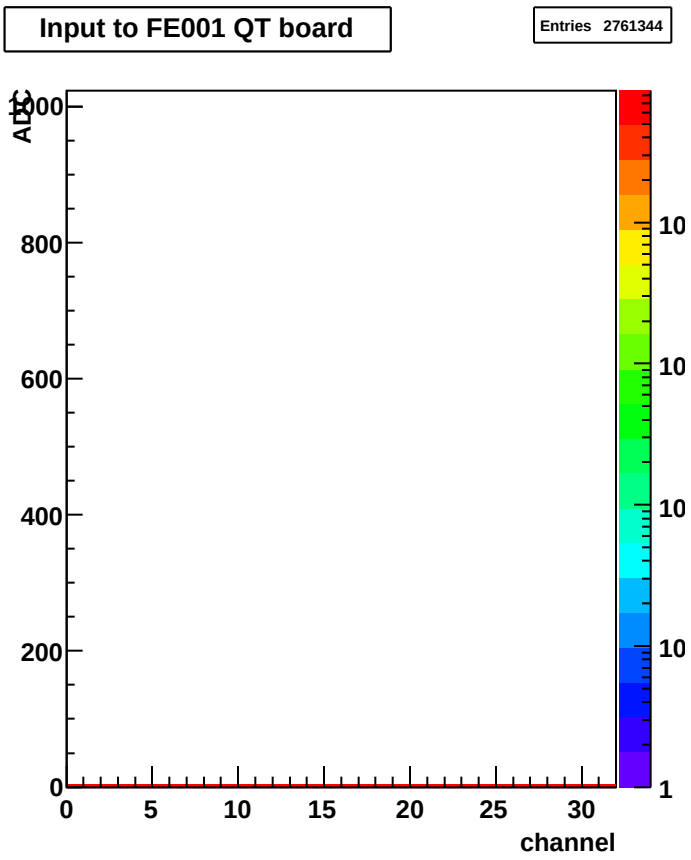

Input to FPD L2 DSM
Entries 258876
CL bits
2097
1227
31788
10692
6198
15524
7971
4657
38980
65532
74210
SMALL
LARGE-S
LARGE-N

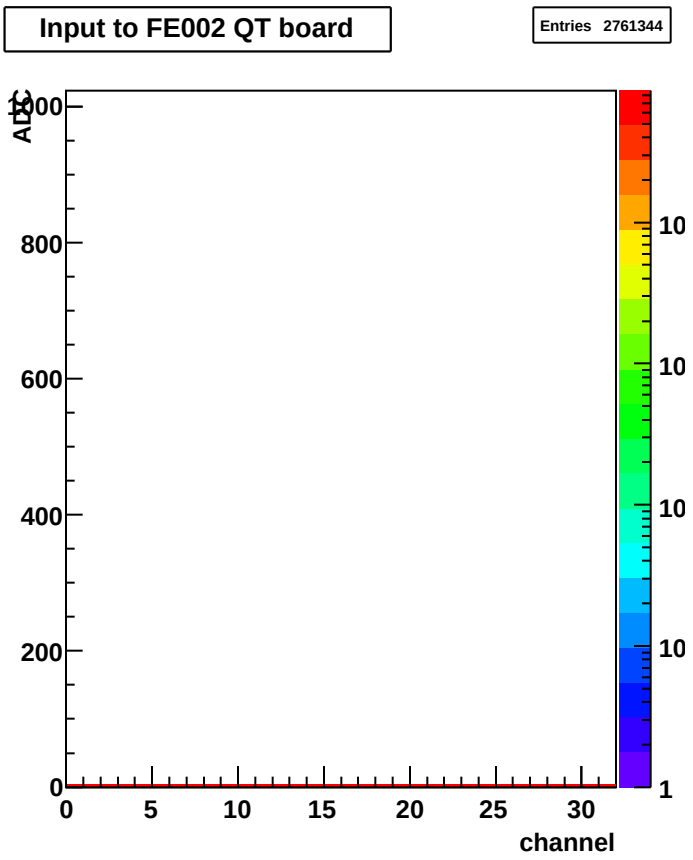

Input to FPD L2 DSM
Entries 31
CL bits - simulated
2
7
1
7
5
9
SMALL
LARGE-S
LARGE-N

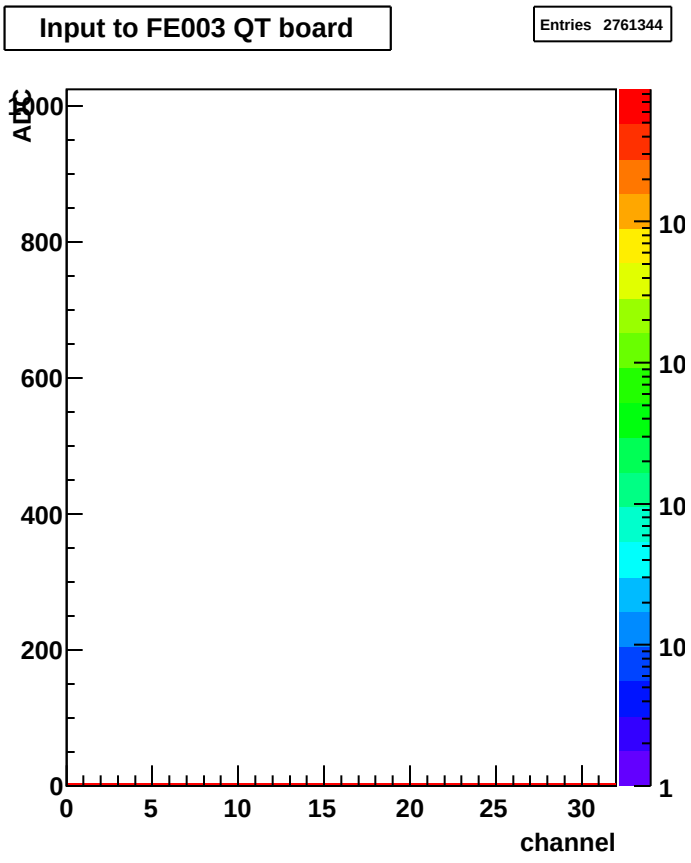

Input to FPD L2 DSM
Entries 258876
HT bits
3593
15852
7858
4872
6843
7695
4276
70984
62745
74158
SMALL
LARGE-S
LARGE-N

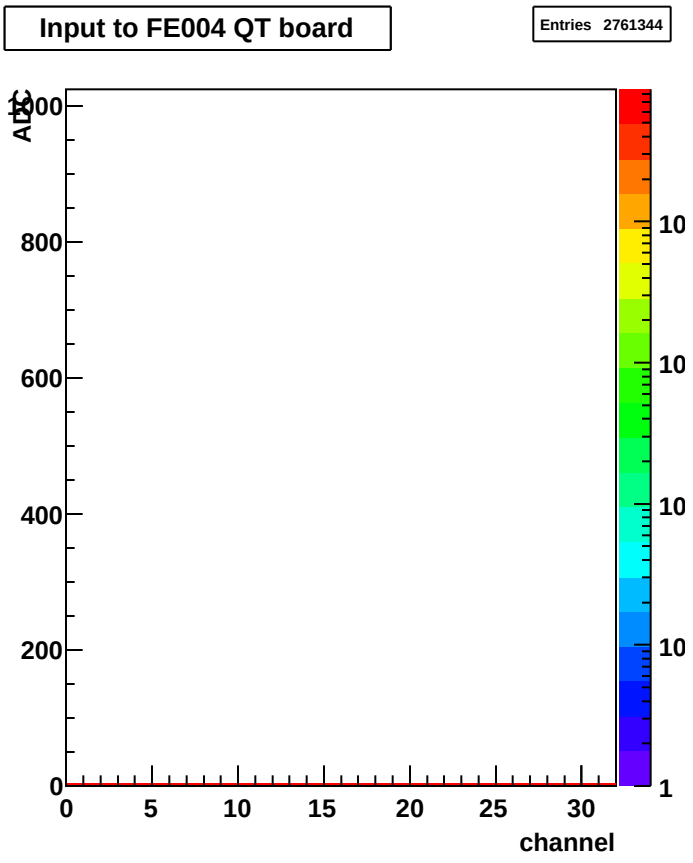

Input to FPD L2 DSM
Entries 256
HT bits - simulated
109
92
55
SMALL
LARGE-S
LARGE-N

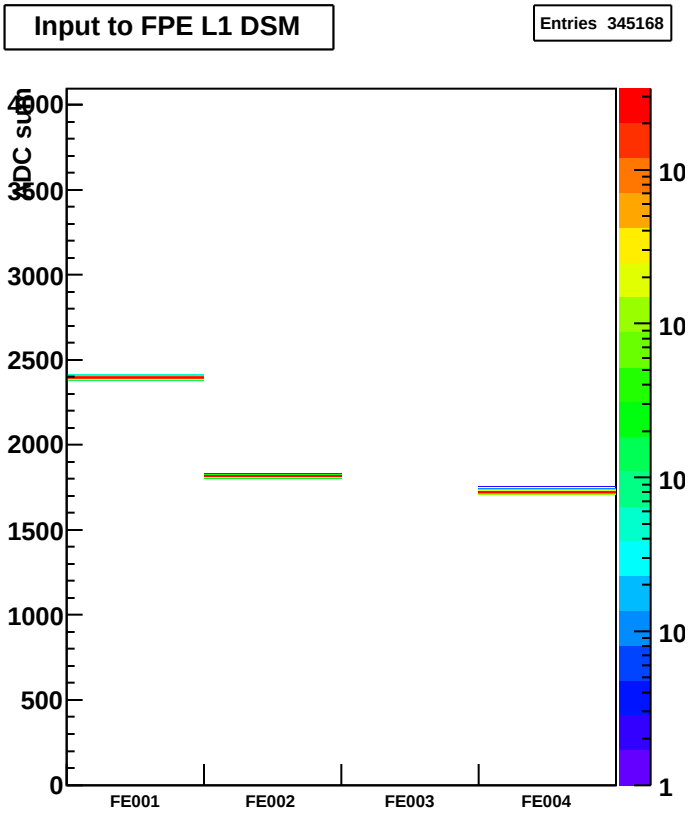

Input to FPD L2 DSM
Entries 517752
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

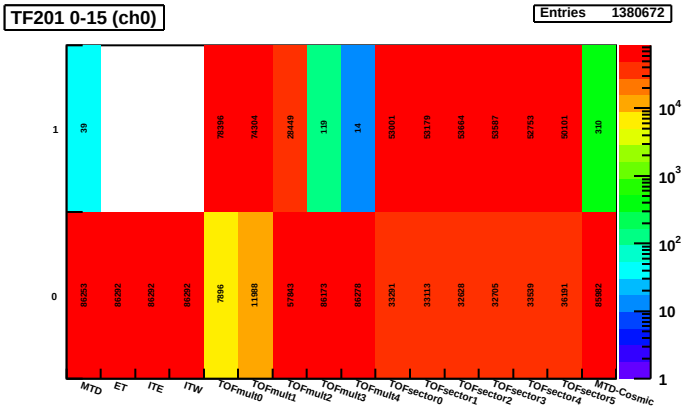

Input to FE001 QT board
Entries 2761344
ADC
channel

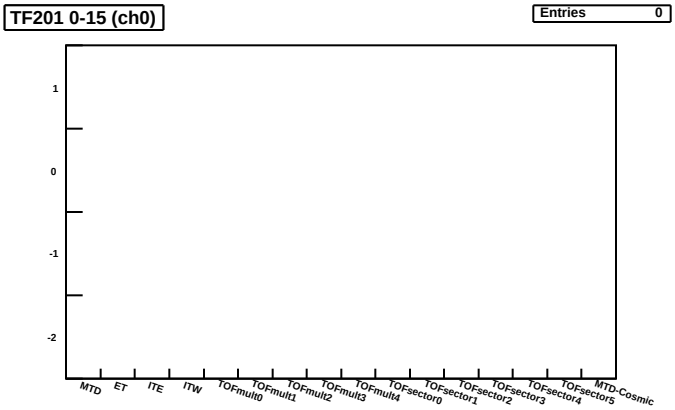

Input to FE002 QT board
Entries 2761344
ADC
channel

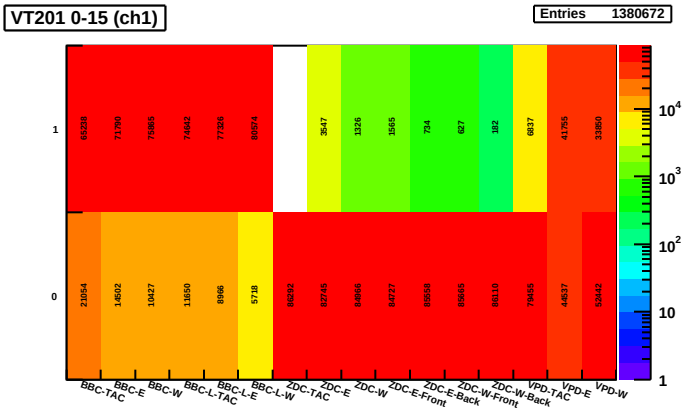

Input to FE003 QT board
Entries 2761344
ADC
channel

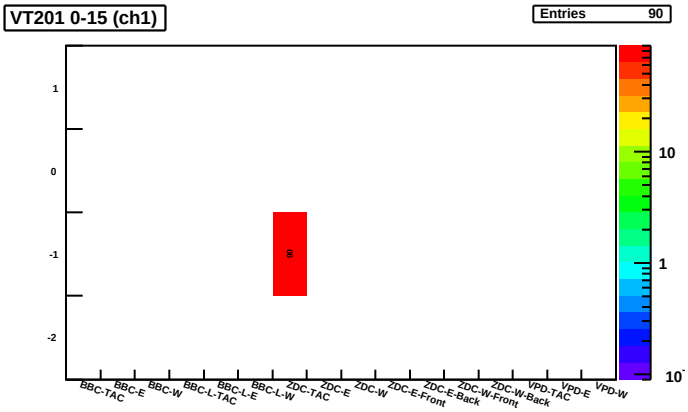

Input to FE004 QT board
Entries 2761344
ADC
channel

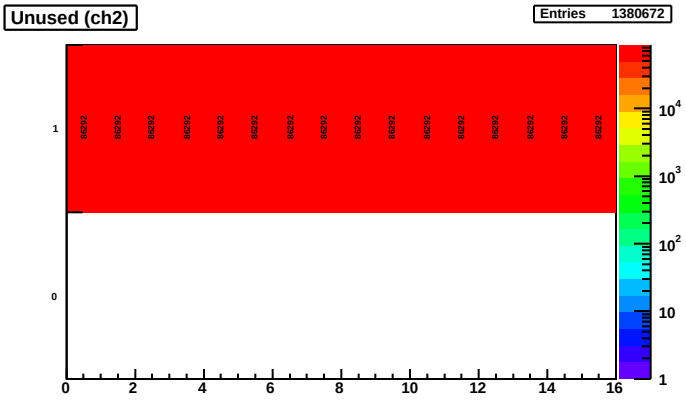

Input to FPE L1 DSM
Entries 345168
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

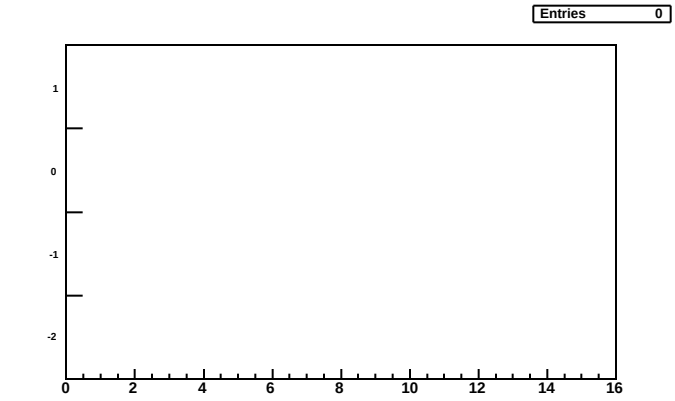

TF201 0-15 (ch0)
Entries 1380672
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

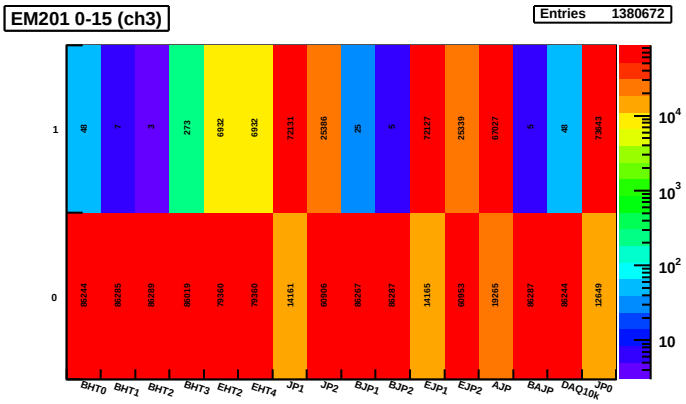

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1380672
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 90
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1380672


Entries 0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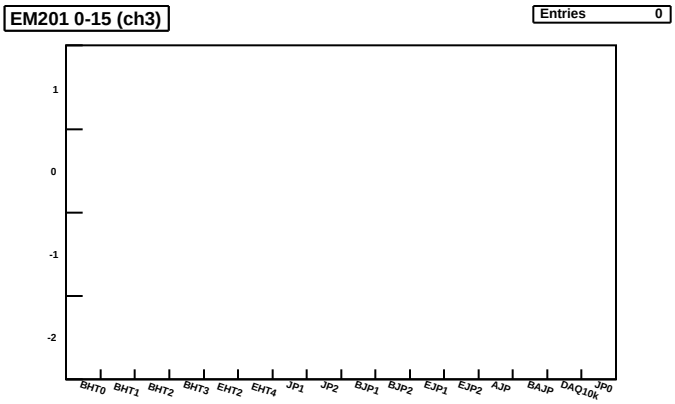
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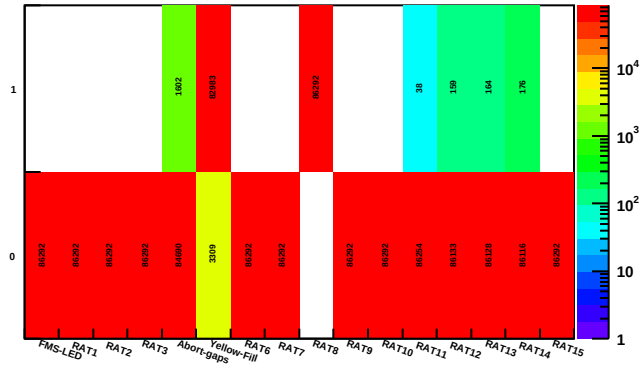
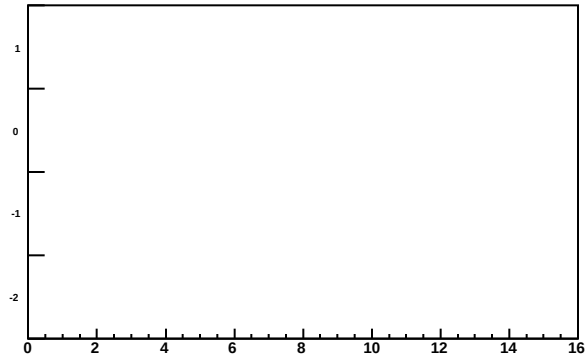
RAT board (ch4)





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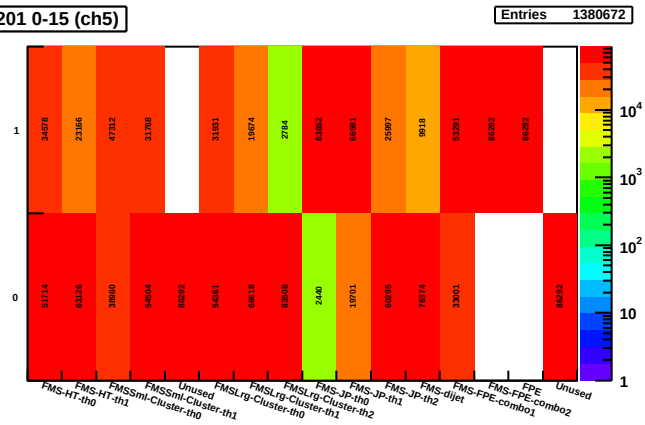

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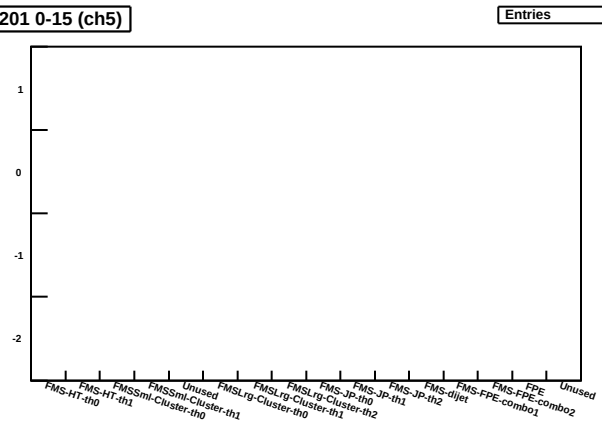

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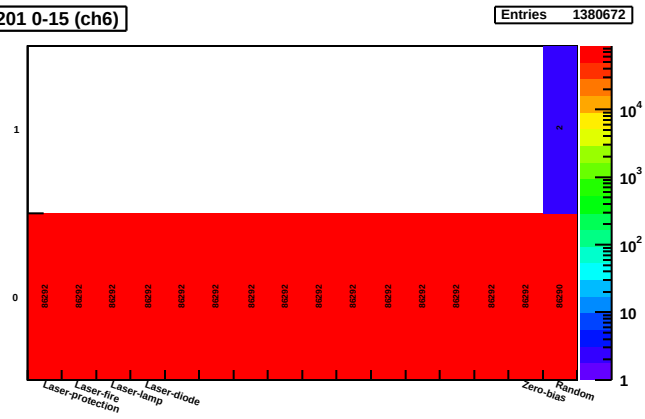

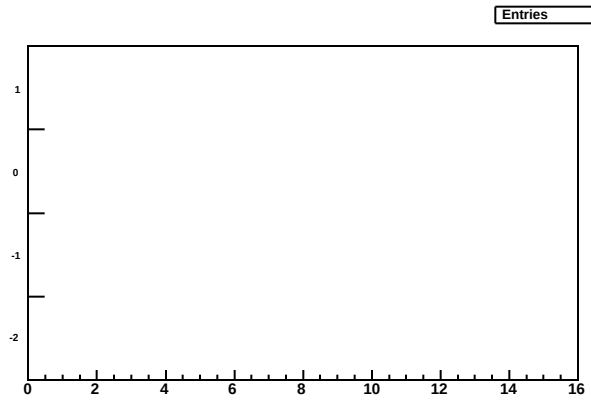
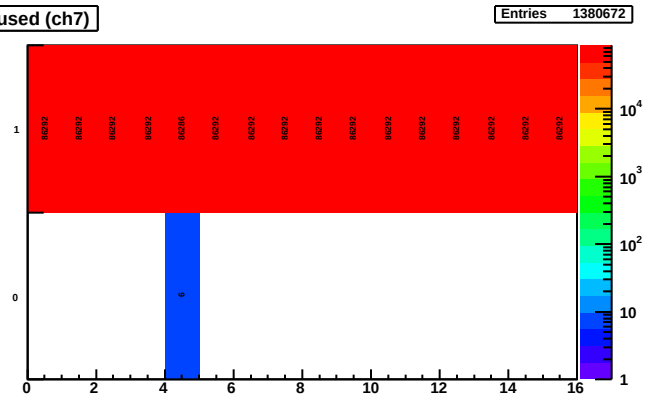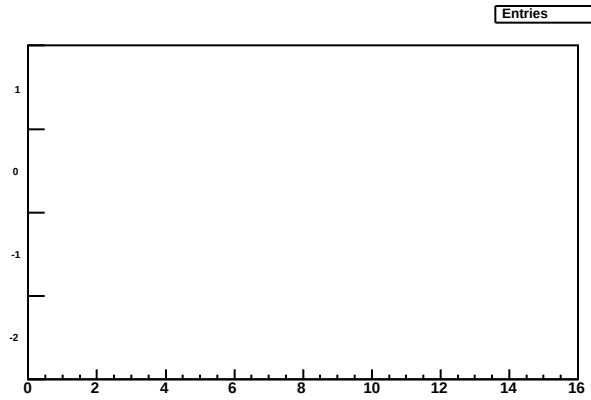
Unused (ch7)