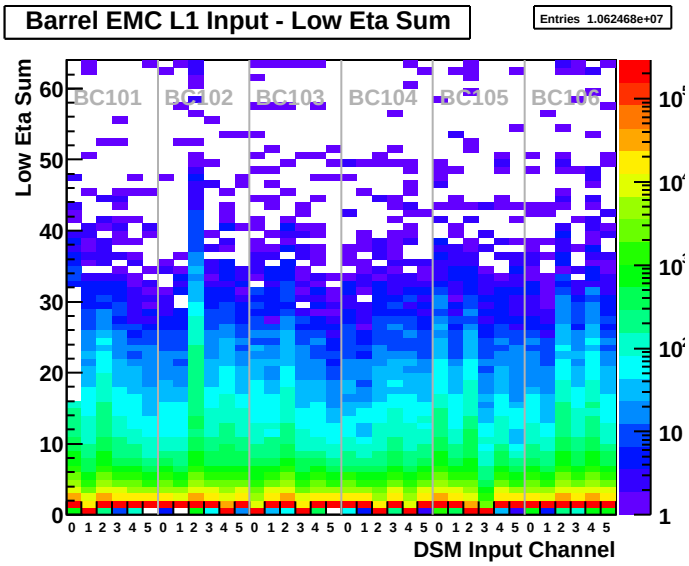
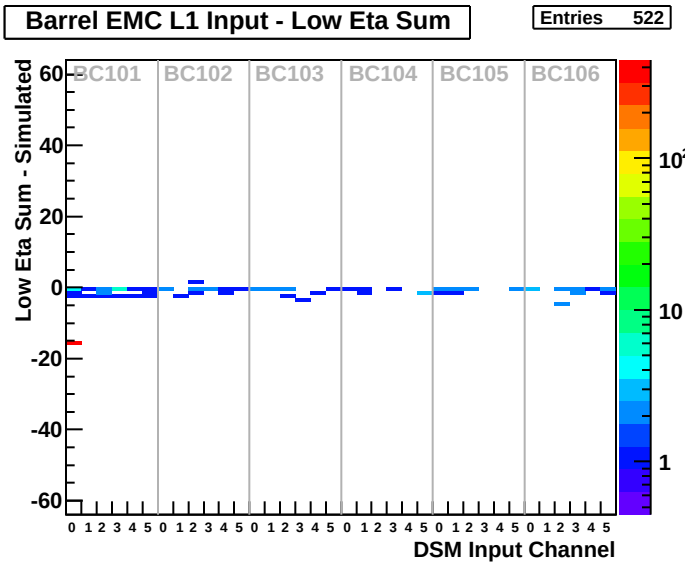
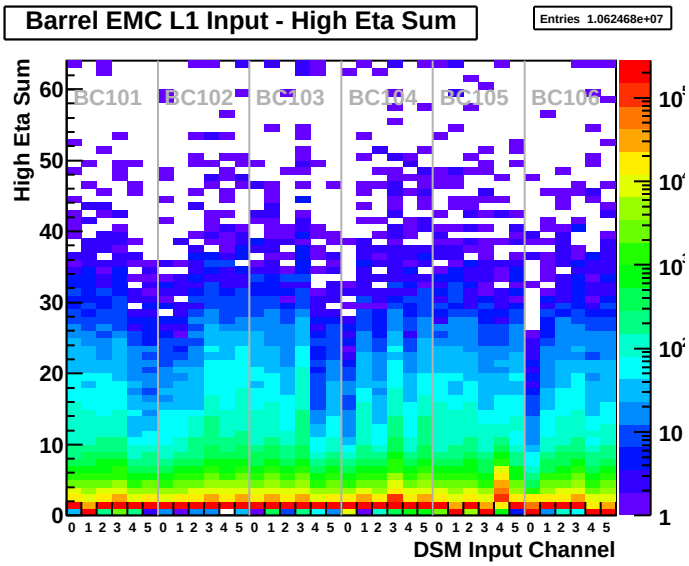
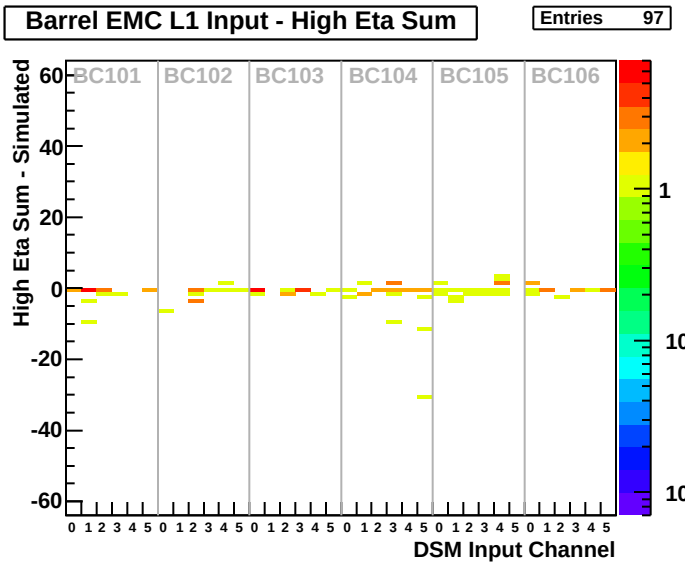
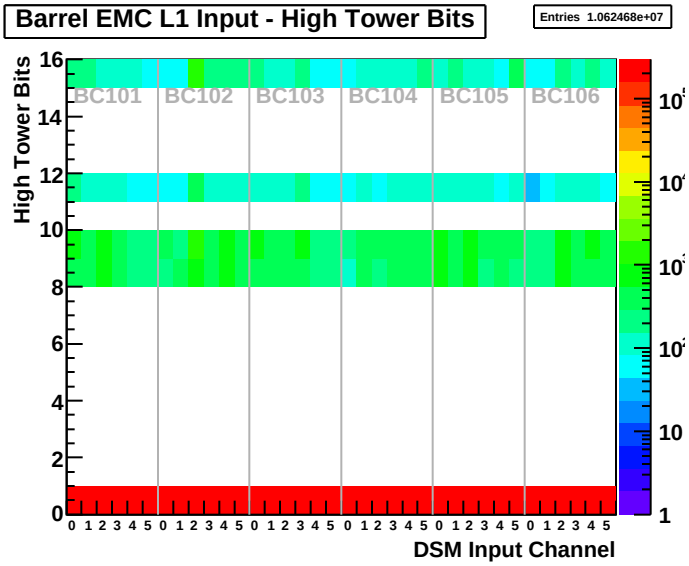
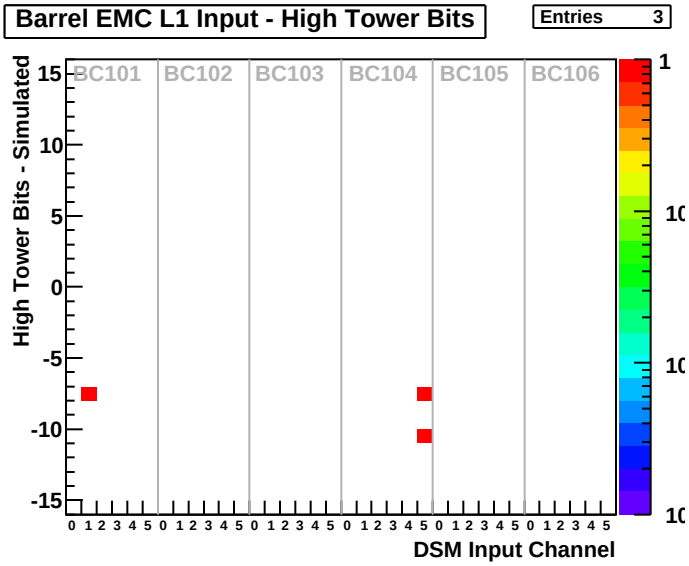

Barrel EMC L1 Input - Low Eta Sum
Entries 1.062468e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

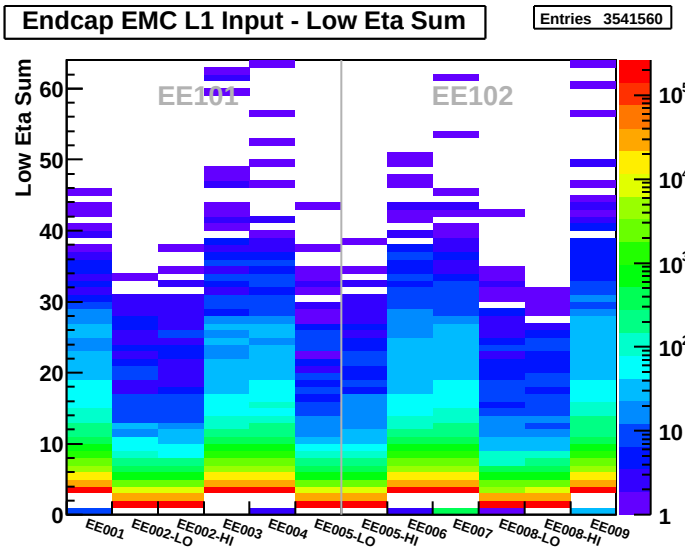

Barrel EMC L1 Input - Low Eta Sum
Entries 522
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

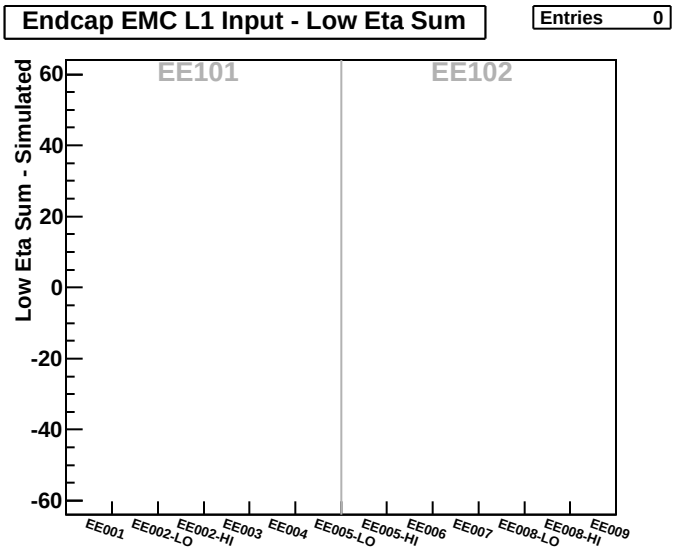

Barrel EMC L1 Input - High Eta Sum
Entries 1.062468e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

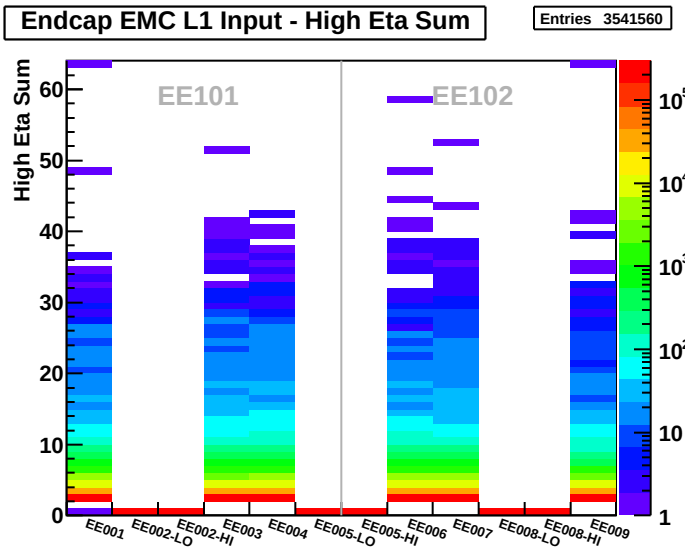

Barrel EMC L1 Input - High Eta Sum
Entries 97
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

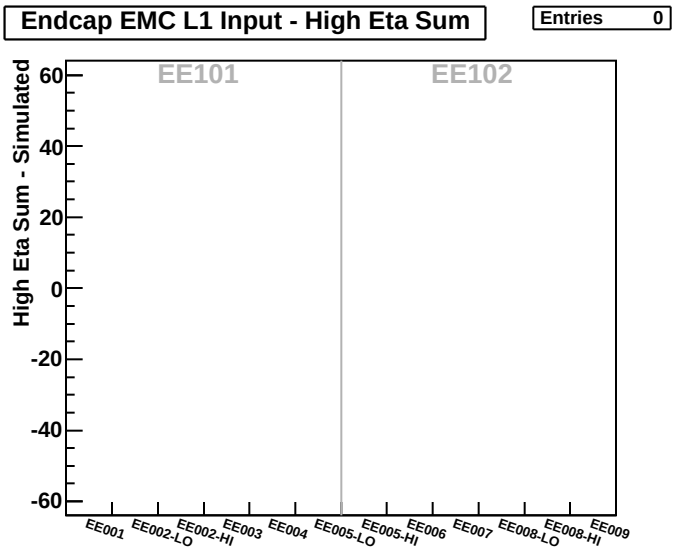

Barrel EMC L1 Input - High Tower Bits
Entries 1.062468e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

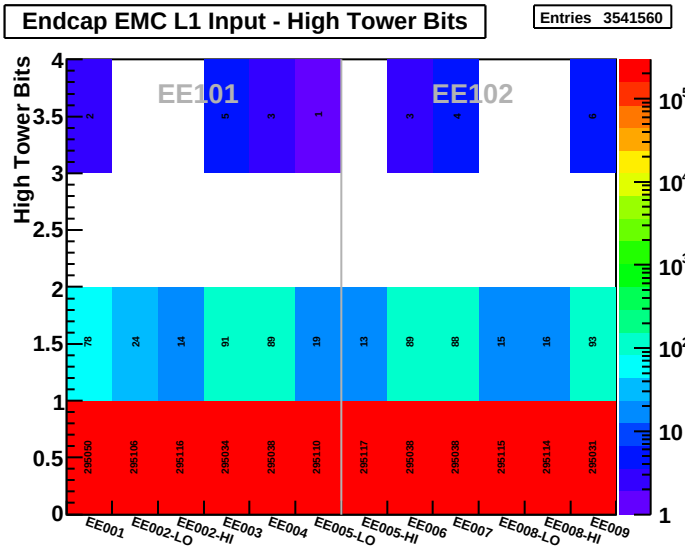

Barrel EMC L1 Input - High Tower Bits
Entries 3
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

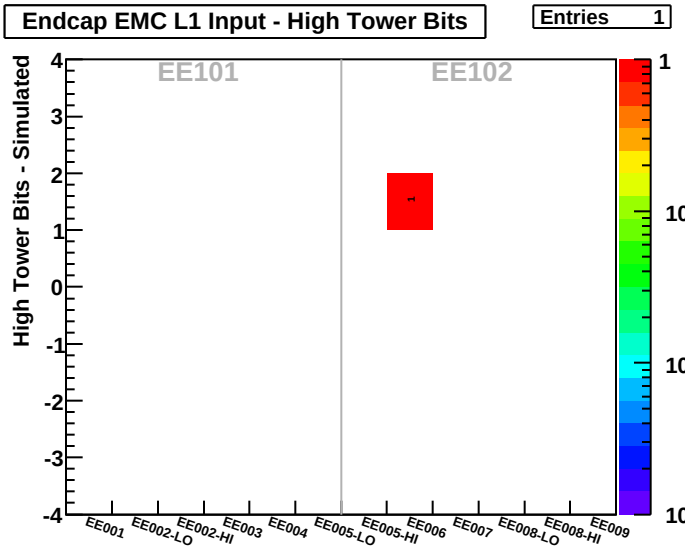

Endcap EMC L1 Input - Low Eta Sum
Entries 3541560
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

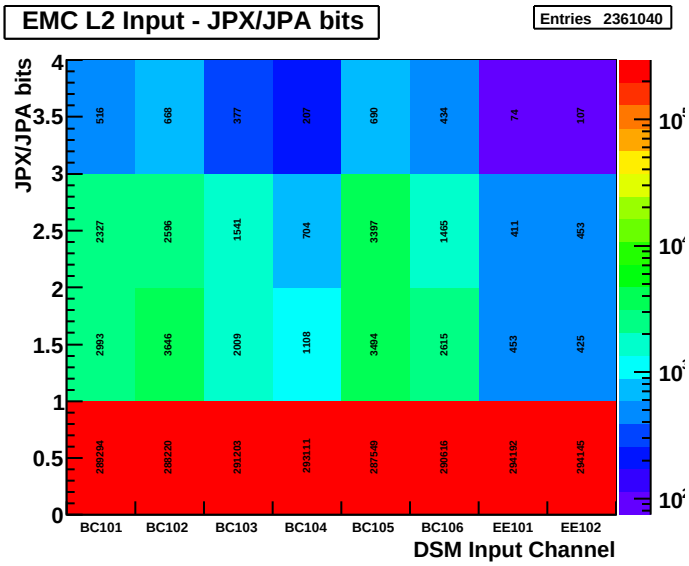

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

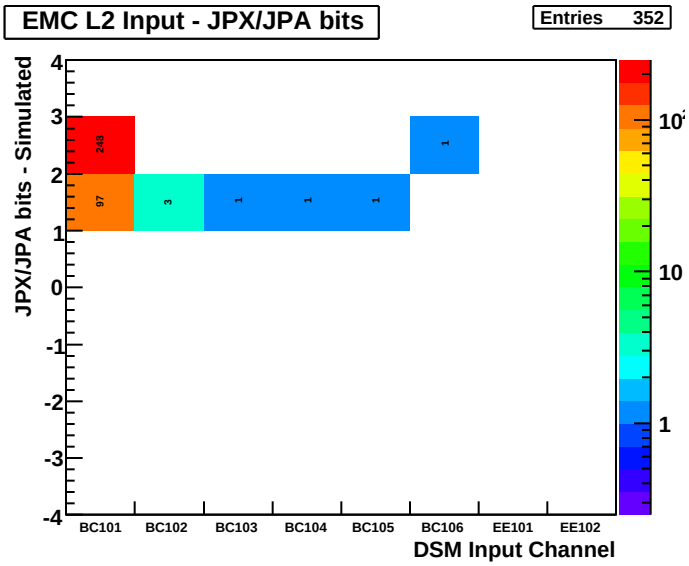

Endcap EMC L1 Input - High Eta Sum
Entries 3541560
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

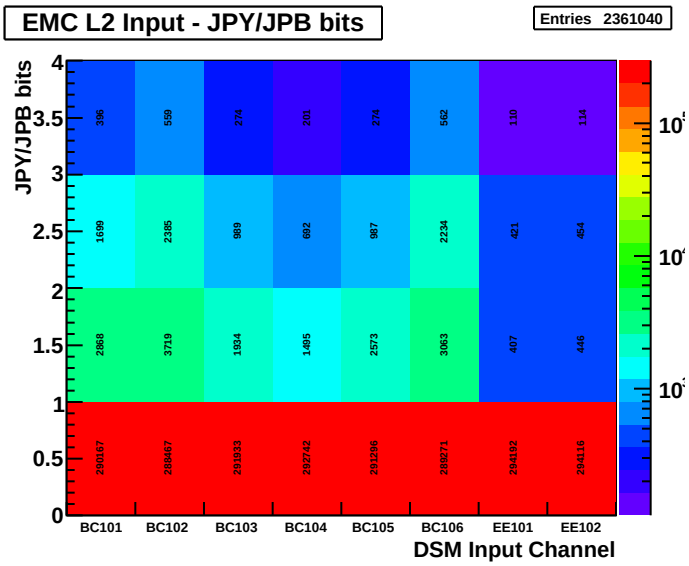

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

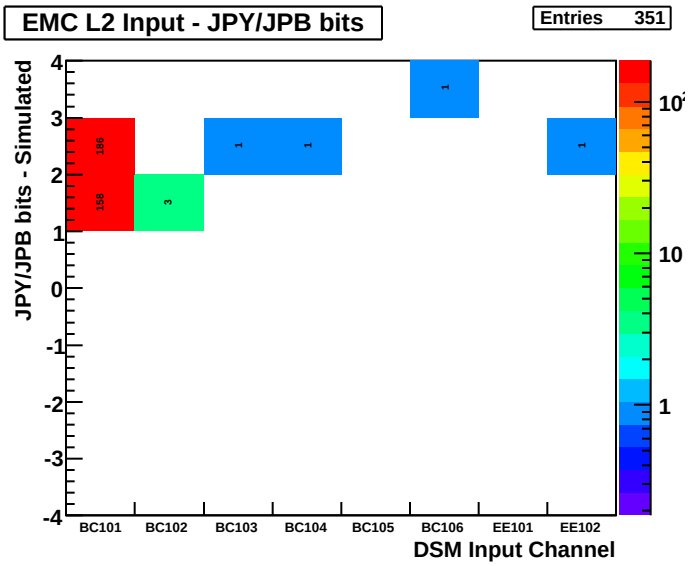

Endcap EMC L1 Input - High Tower Bits
Entries 3541560
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

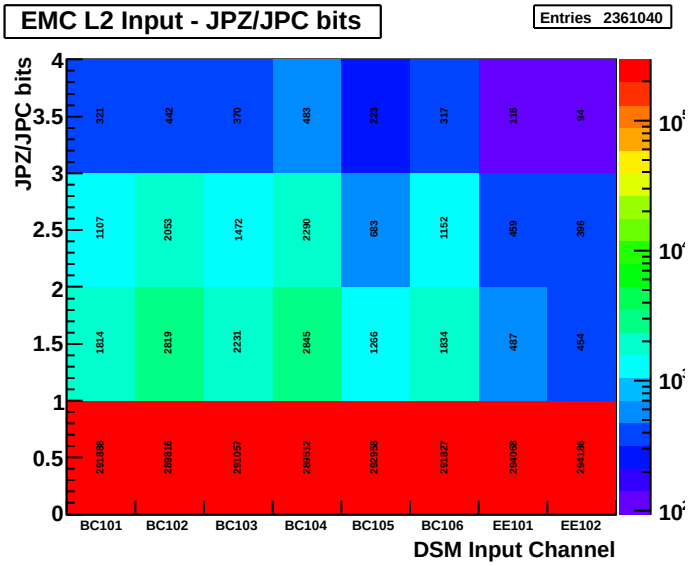

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

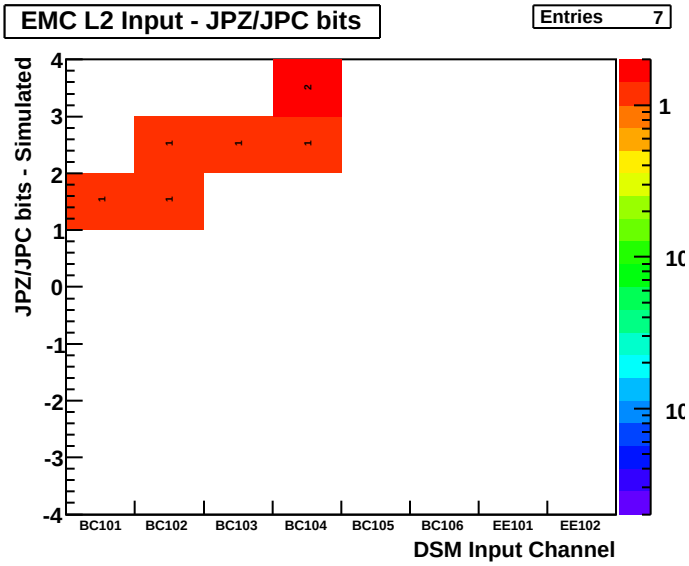

EMC L2 Input - JPX/JPA bits
Entries 2361040
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

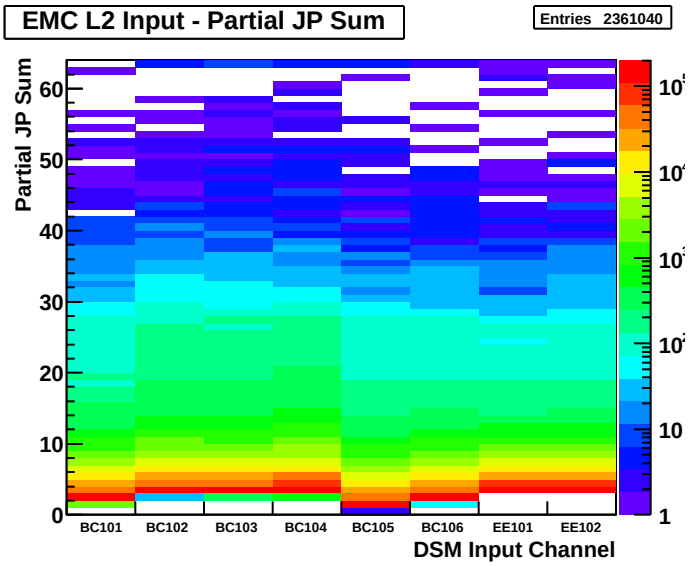

EMC L2 Input - JPX/JPA bits
Entries 352
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

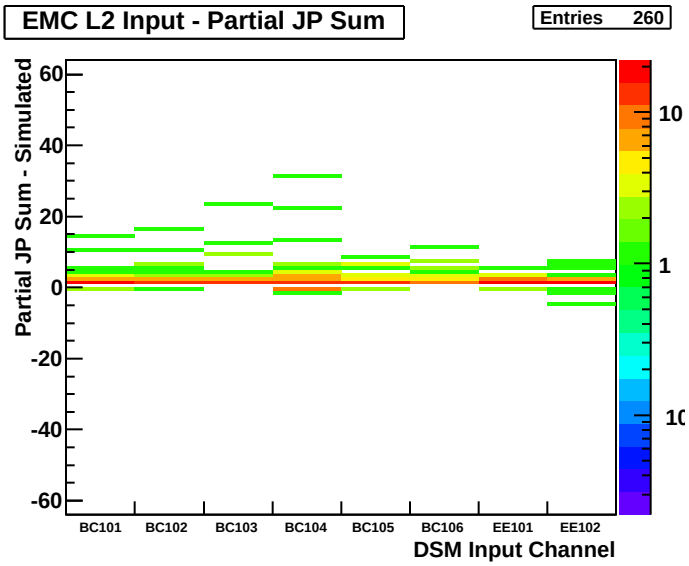

EMC L2 Input - JPY/JPB bits
Entries 2361040
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

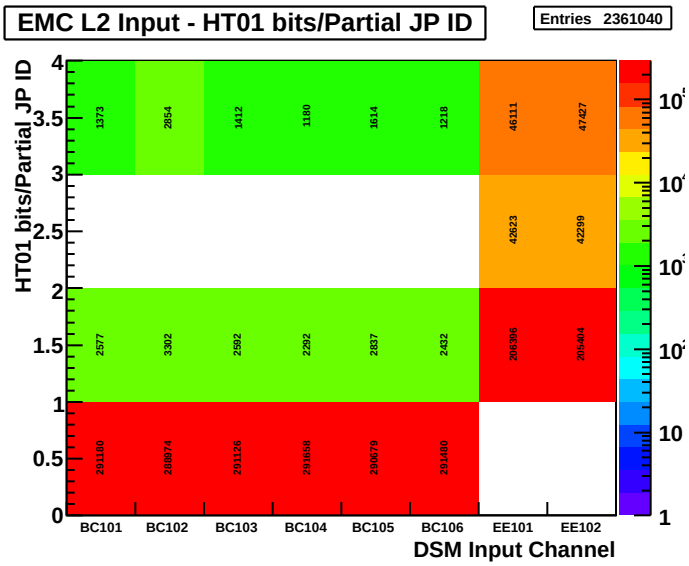

EMC L2 Input - JPY/JPB bits
Entries 351
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

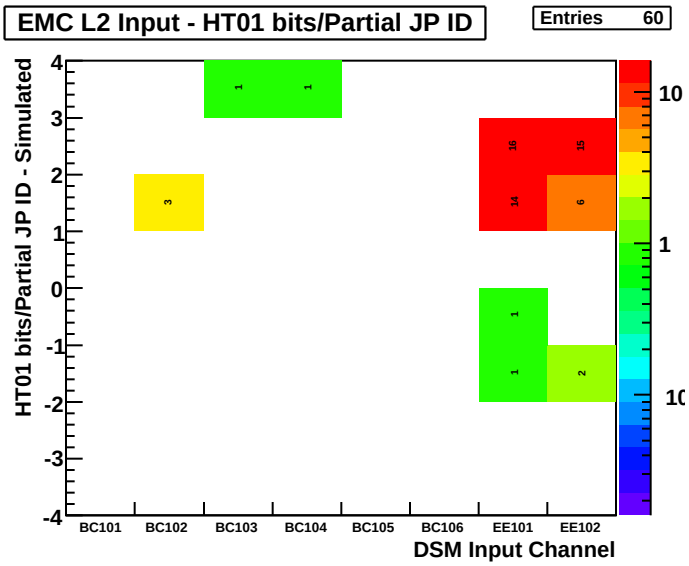

EMC L2 Input - JPZ/JPC bits
Entries 2361040
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

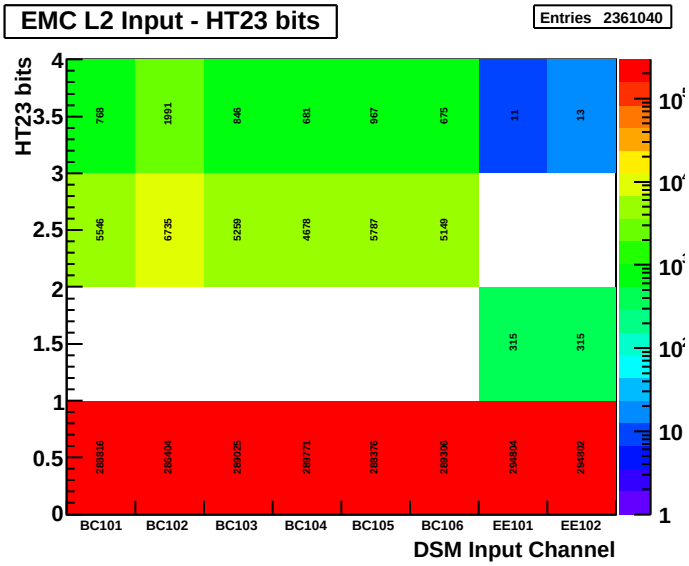

EMC L2 Input - JPZ/JPC bits
Entries 7
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

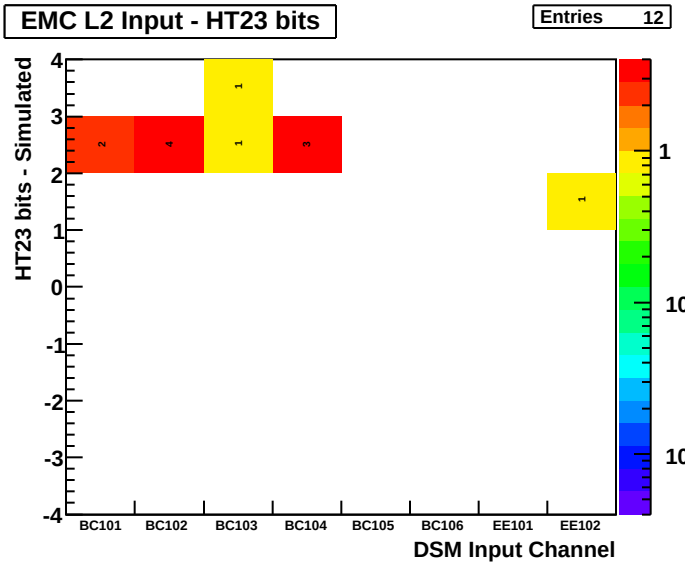

EMC L2 Input - Partial JP Sum
Entries 2361040
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

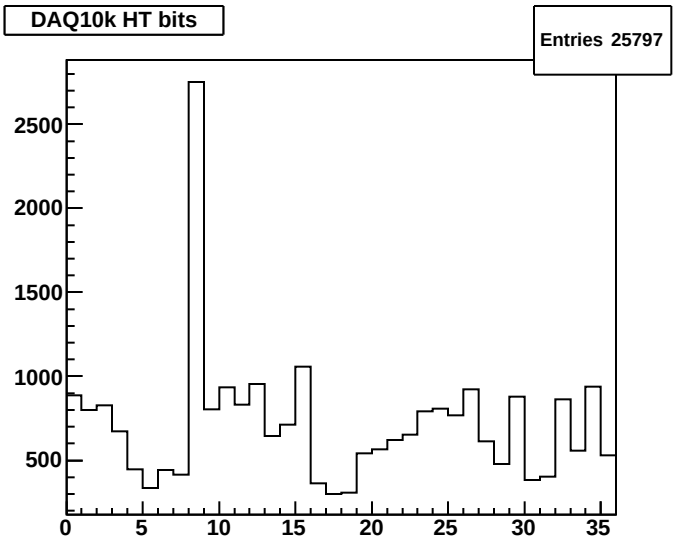

EMC L2 Input - Partial JP Sum
Entries 260
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

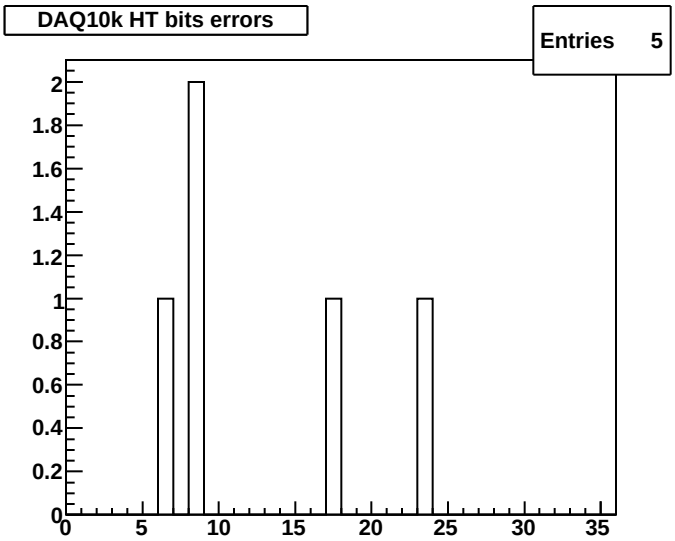

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2361040
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

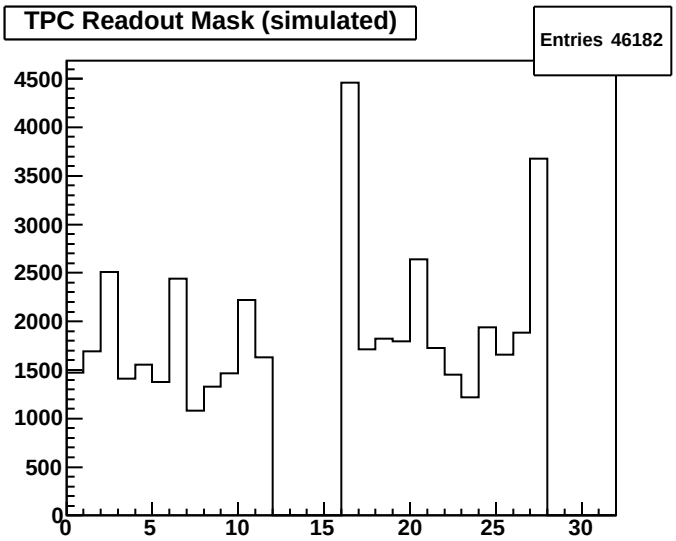

EMC L2 Input - HT01 bits/Partial JP ID
Entries 60
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

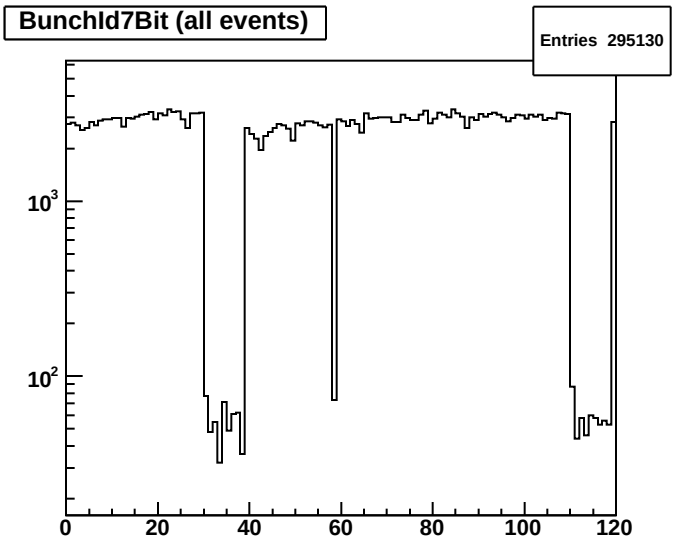

EMC L2 Input - HT23 bits
Entries 2361040
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

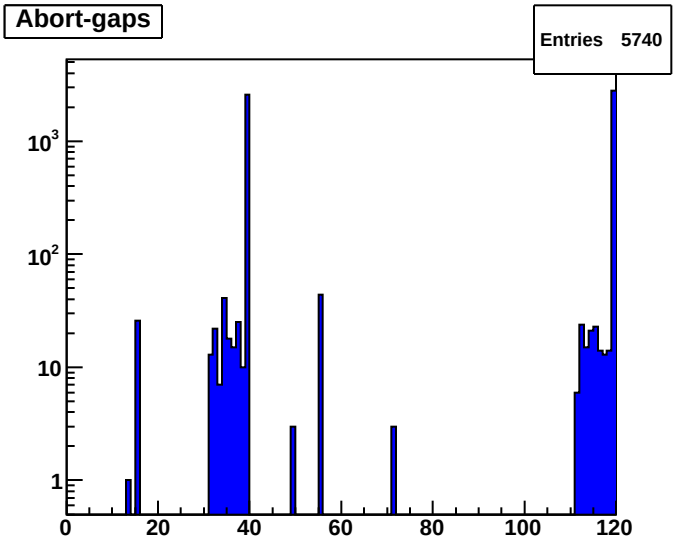

EMC L2 Input - HT23 bits
Entries 12
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

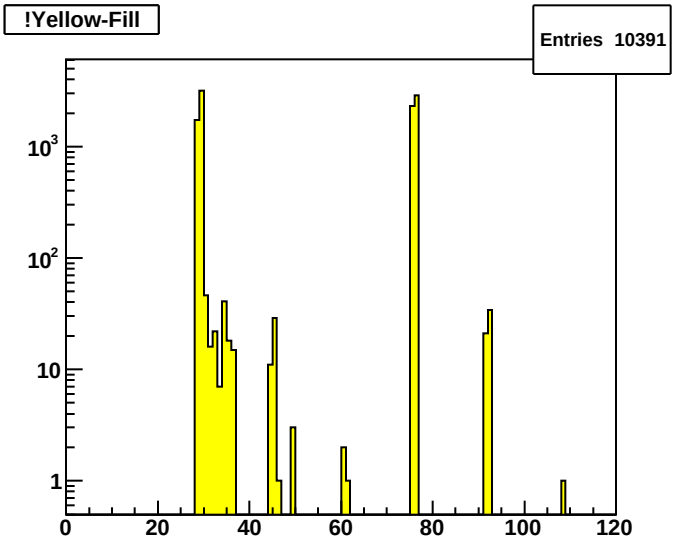

DAQ10k HT bits
Entries 25797

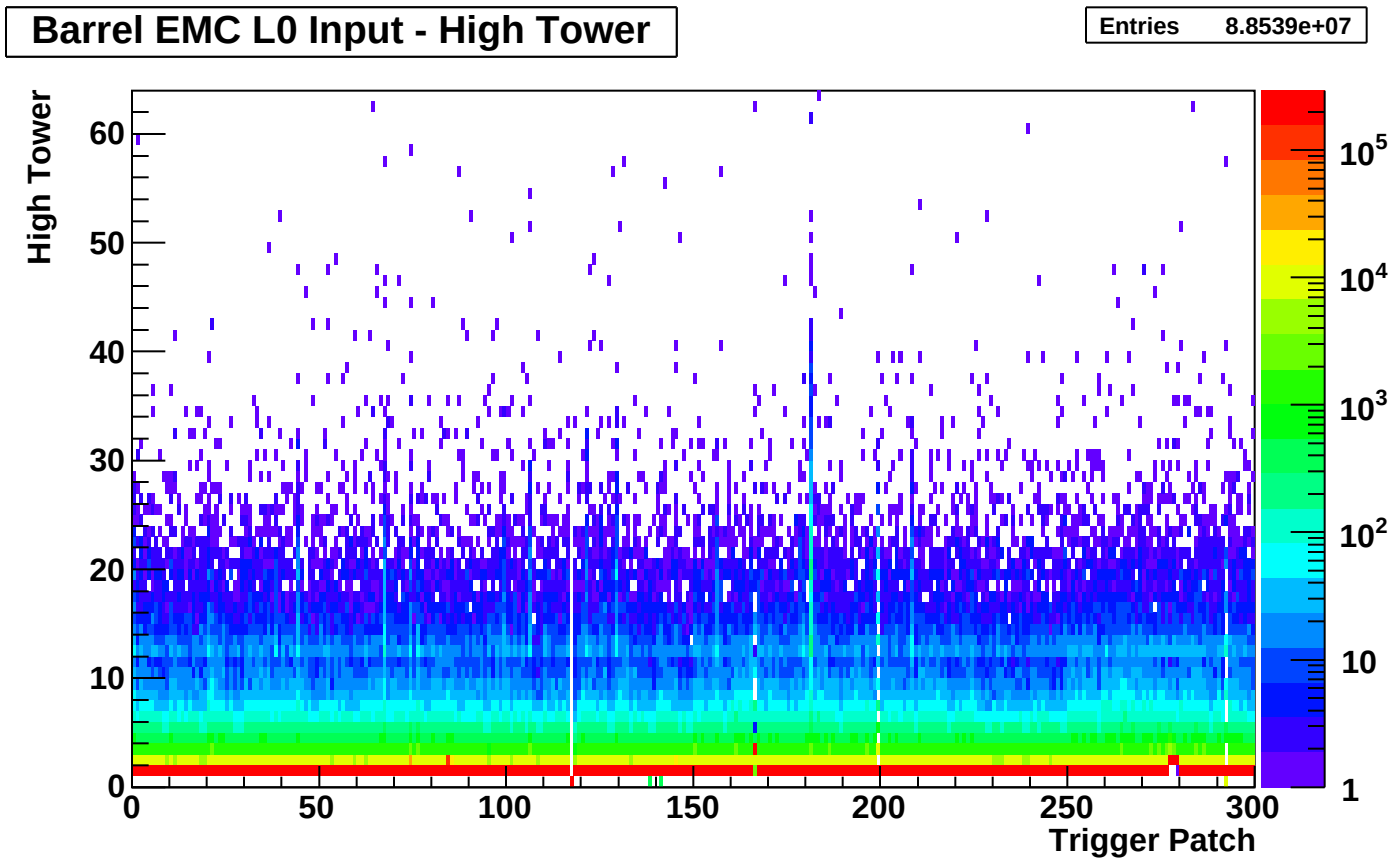

DAQ10k HT bits errors
Entries 5

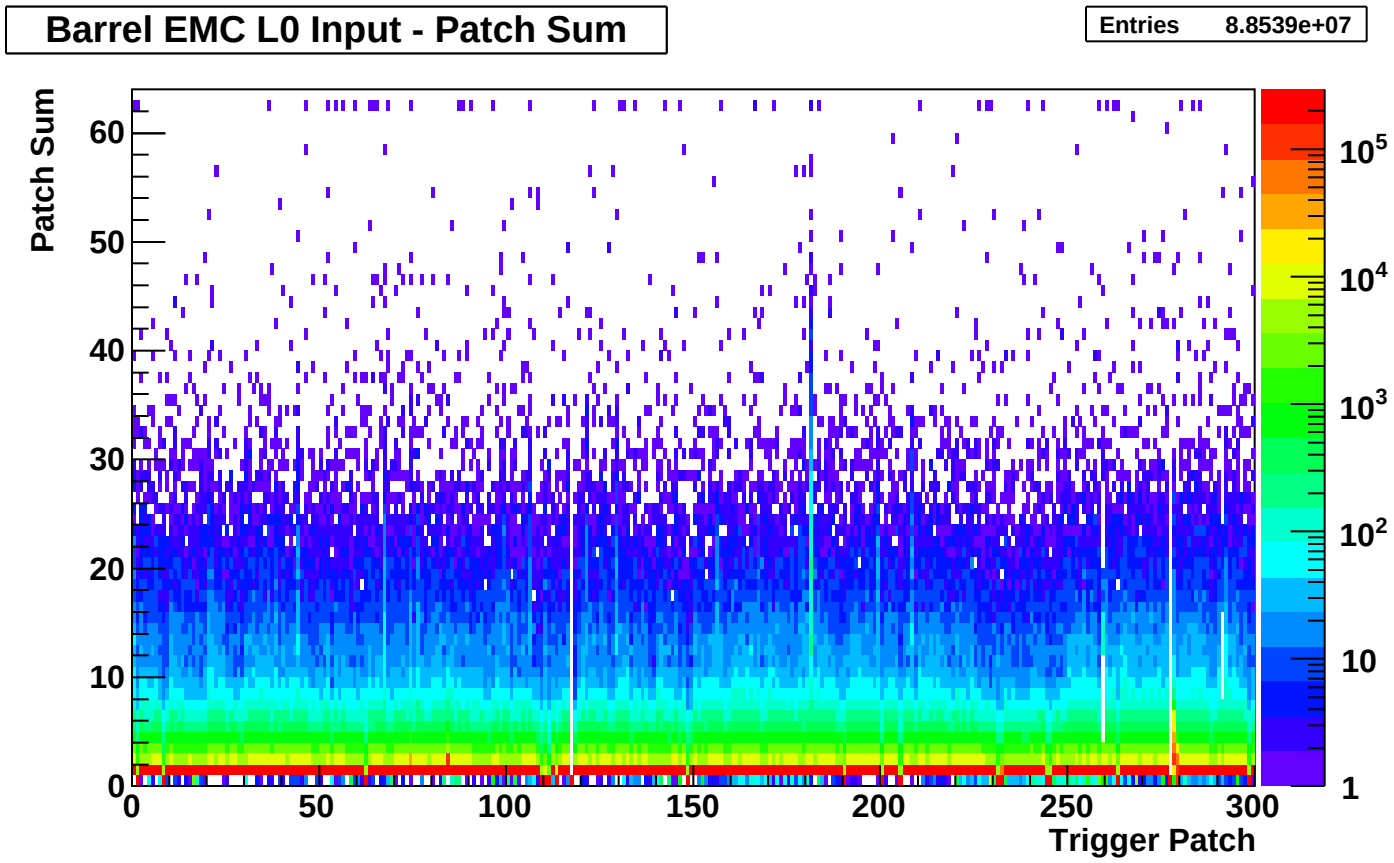

TPC Readout Mask (simulated)
Entries 46182

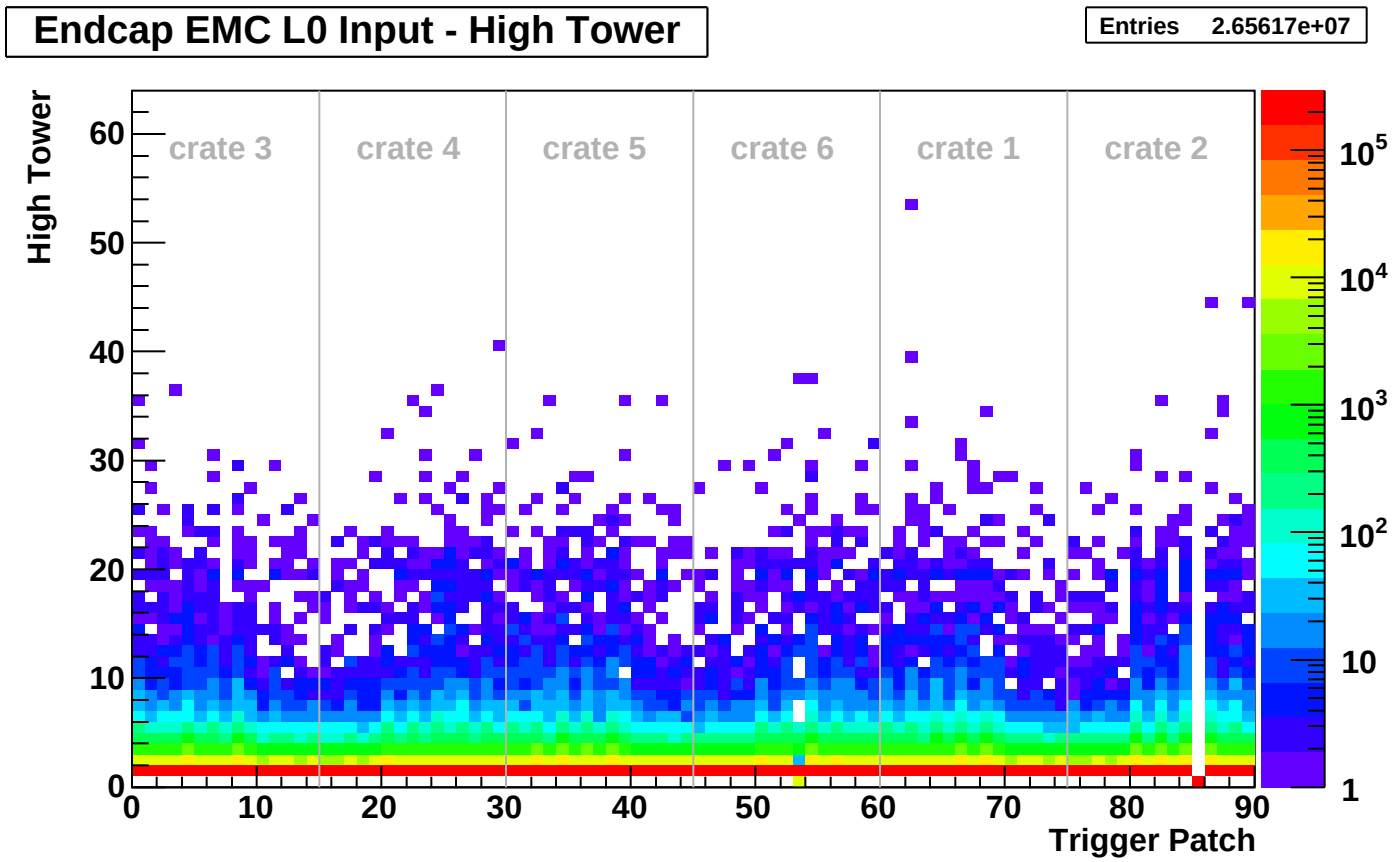

BunchId7Bit (all events)
Entries 295130

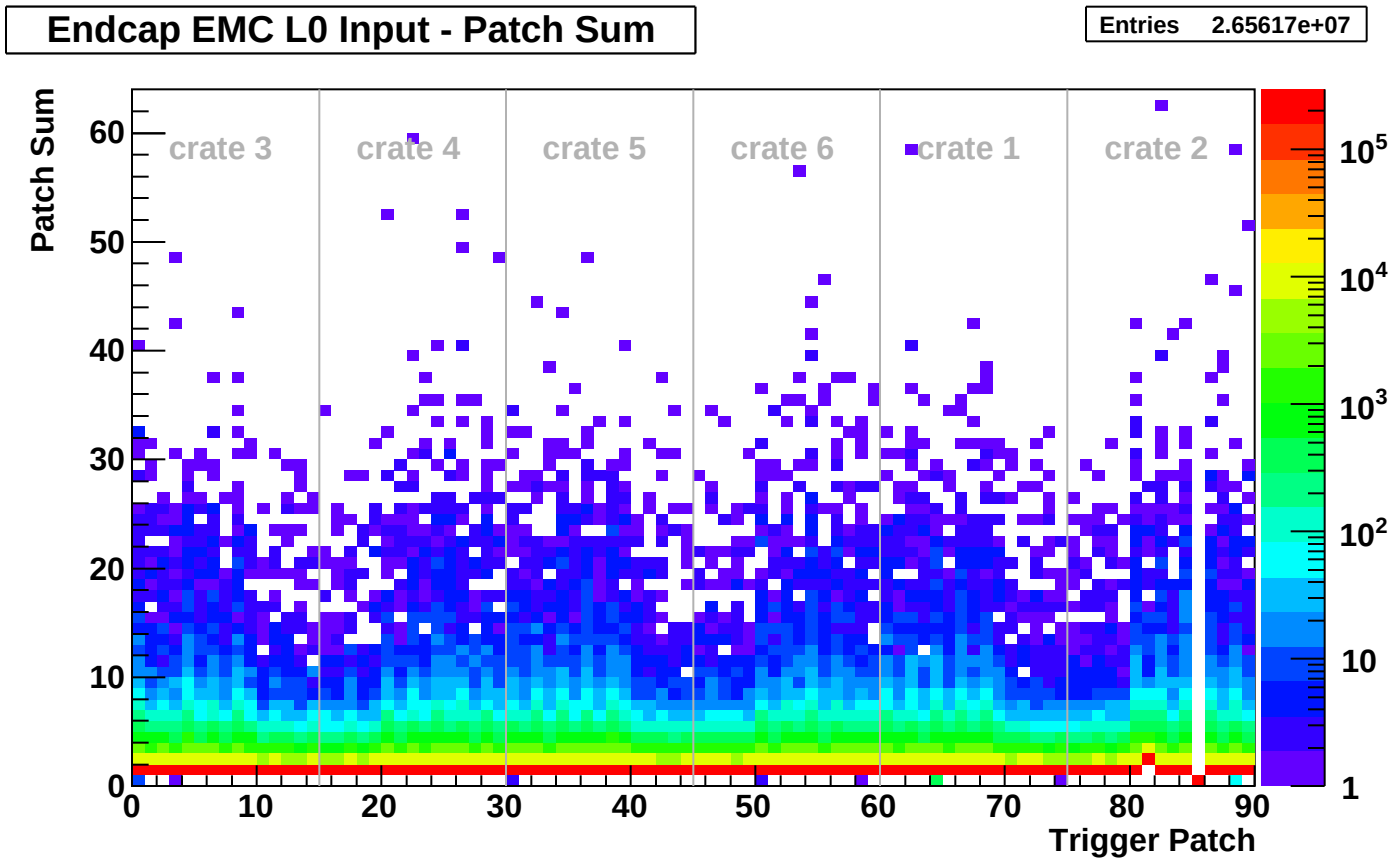

Abort-gaps
Entries 5740

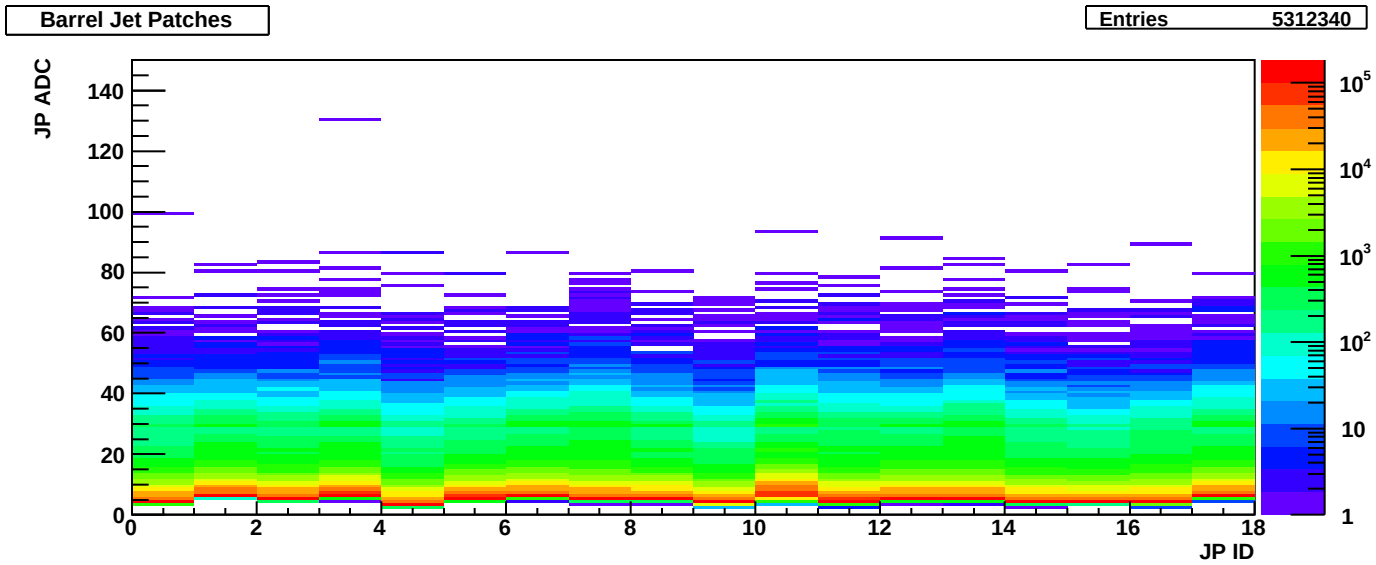

!Yellow-Fill
Entries 10391

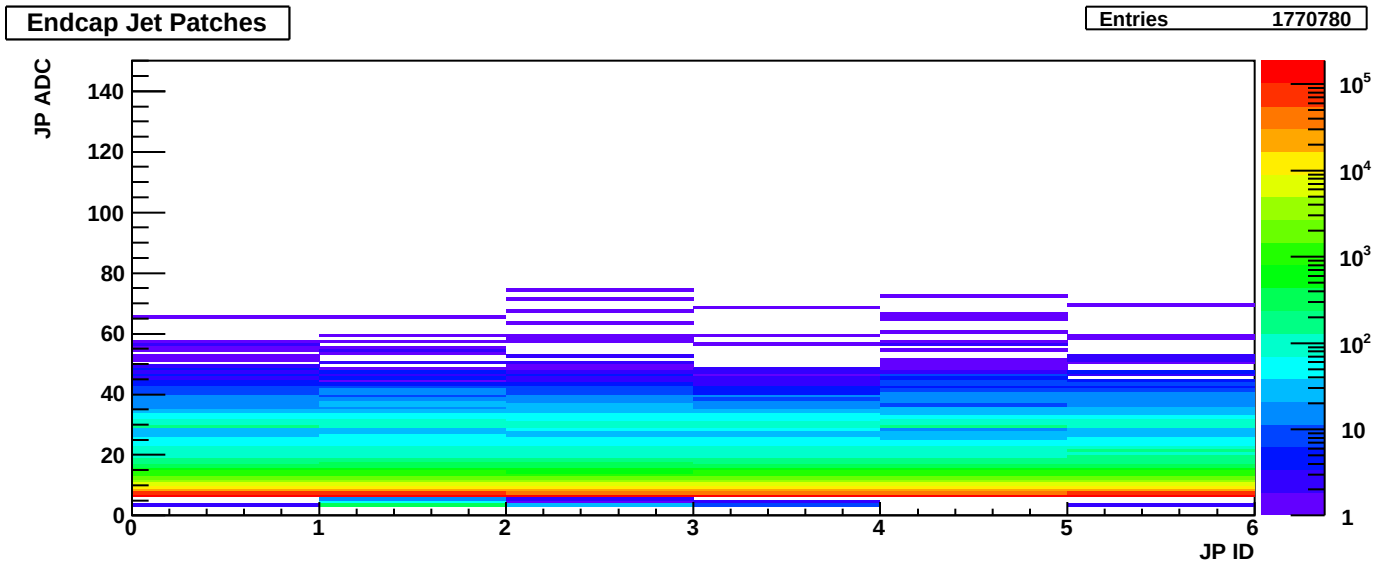

Barrel EMC L0 Input - High Tower
Entries 8.8539e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

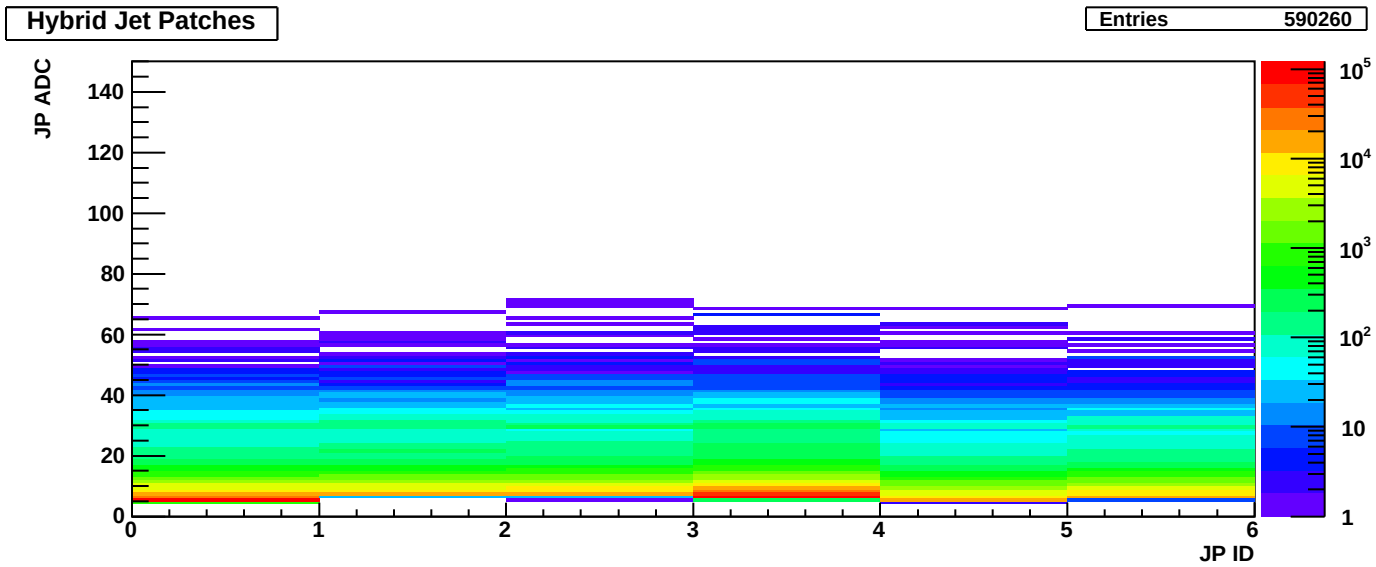

Barrel EMC L0 Input - Patch Sum
Entries 8.8539e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

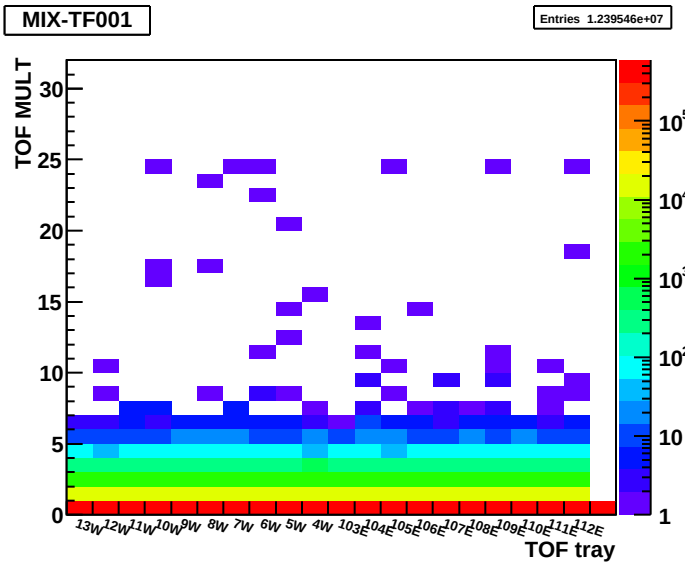

Endcap EMC L0 Input - High Tower
Entries 2.65617e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

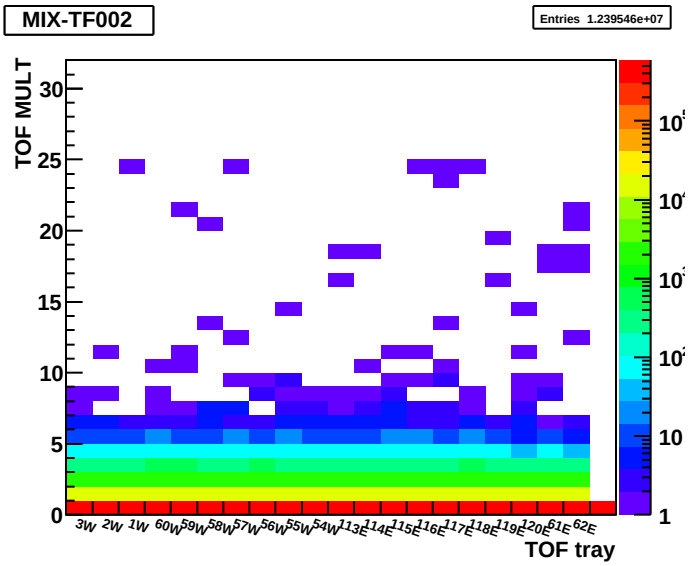

Endcap EMC L0 Input - Patch Sum
Entries 2.65617e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

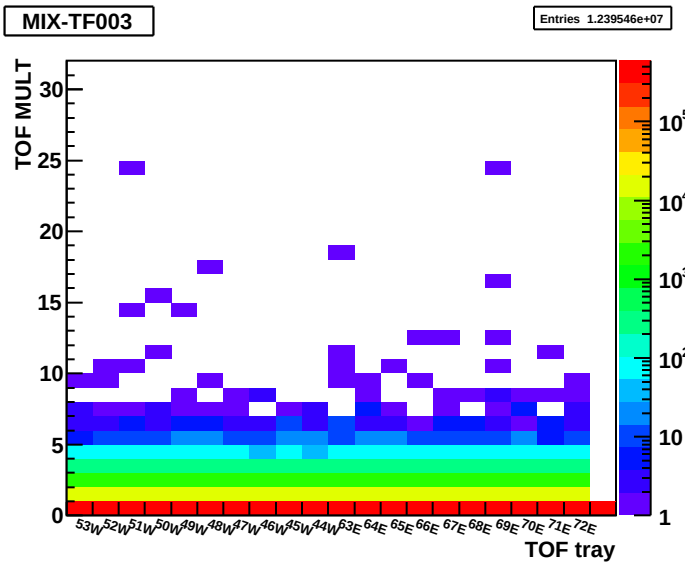

Barrel Jet Patches
Entries 5312340
JP ADC
JP ID

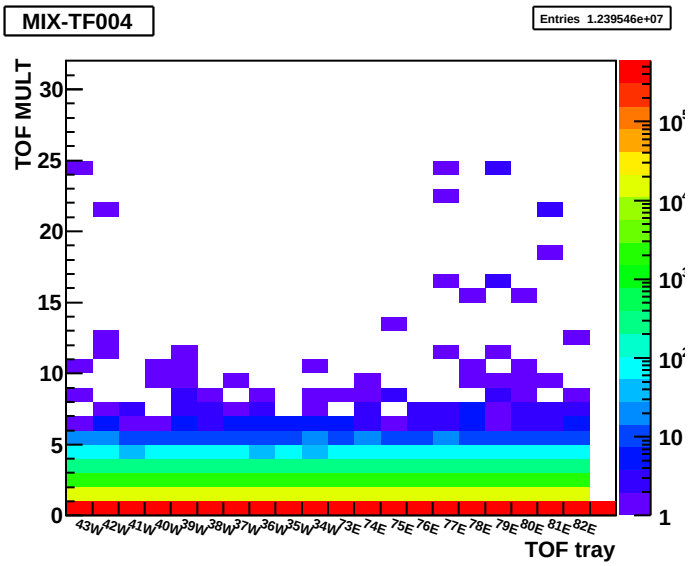

Endcap Jet Patches
Entries 1770780
JP ADC
JP ID

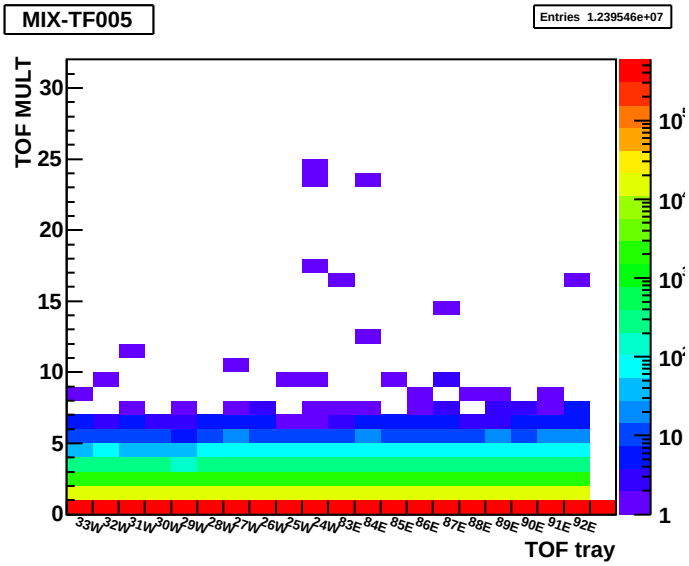

Hybrid Jet Patches
Entries 590260
JP ADC
JP ID

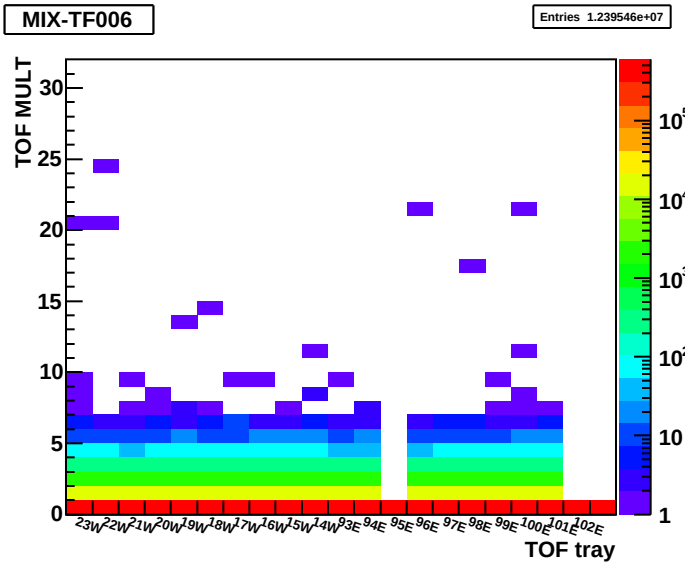

MIX-TF001
Entries 1.239546e+07
TOF MULT
TOF tray

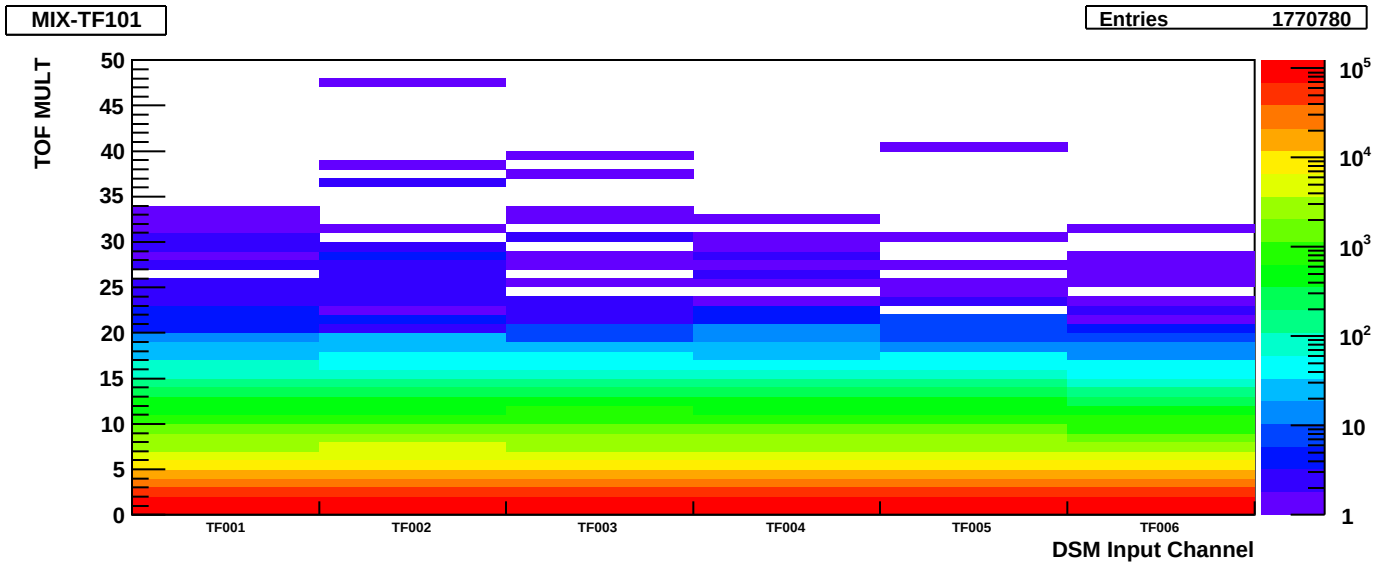

MIX-TF002
Entries 1.239546e+07
TOF MULT
TOF tray

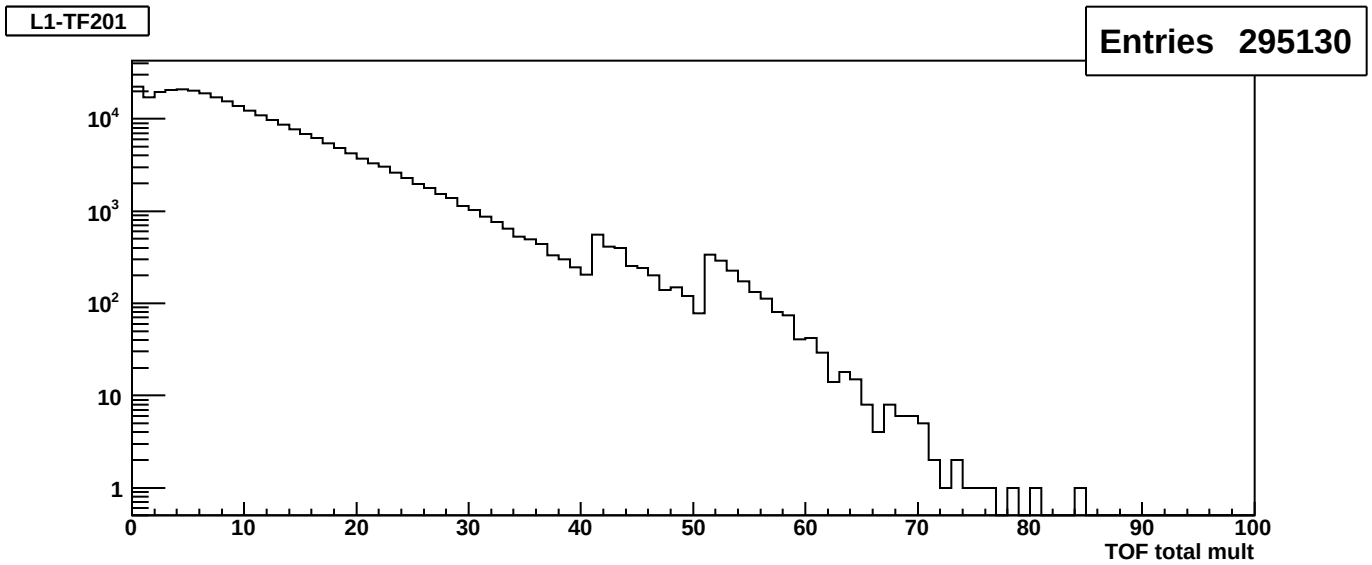

MIX-TF003
Entries 1.239546e+07
TOF MULT
TOF tray

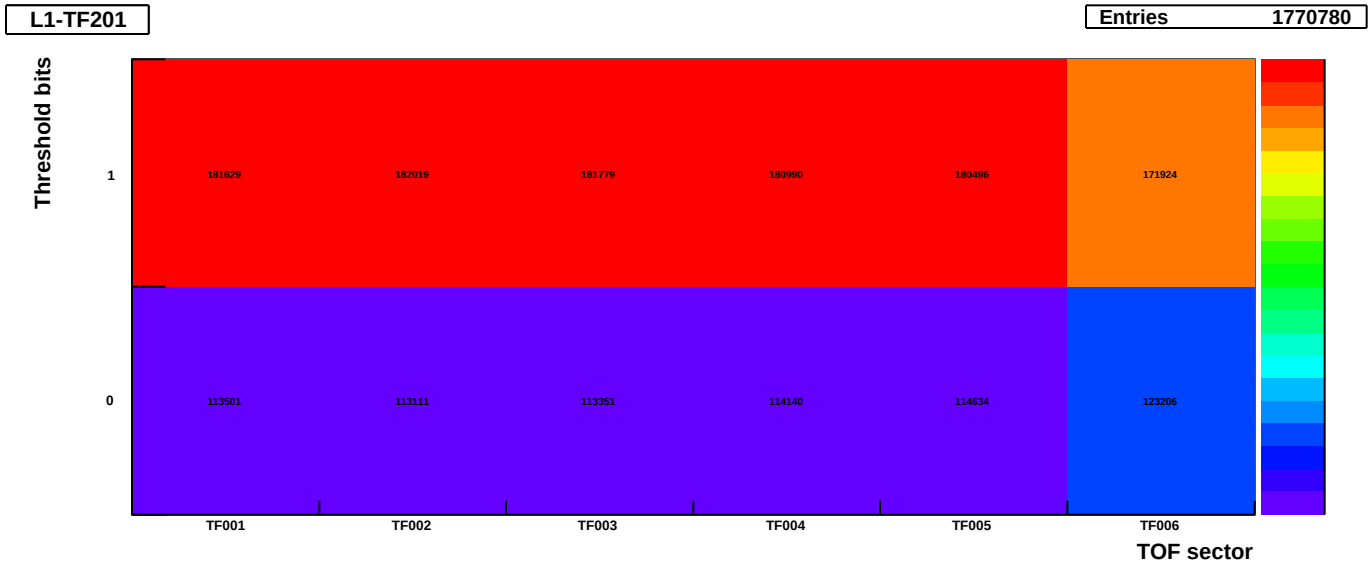

MIX-TF004
Entries 1.239546e+07
TOF MULT
TOF tray

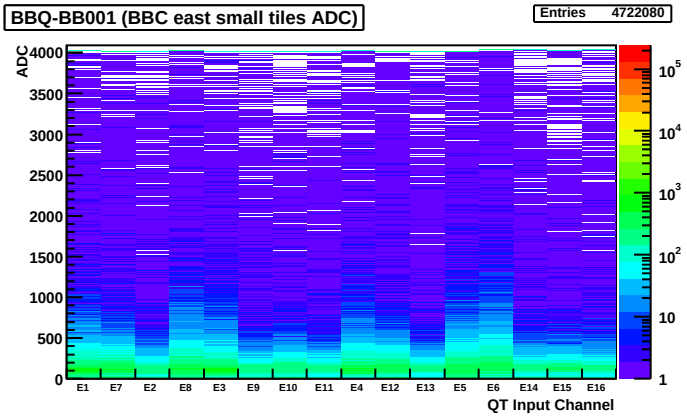

MIX-TF005
Entries 1.239546e+07
TOF MULT
TOF tray

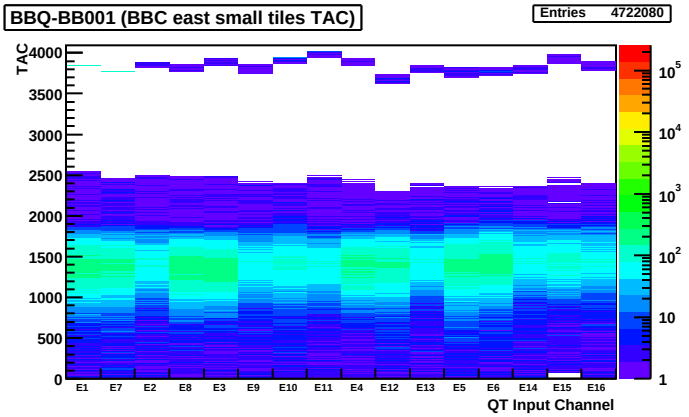

MIX-TF006
Entries 1.239546e+07
TOF MULT
TOF tray

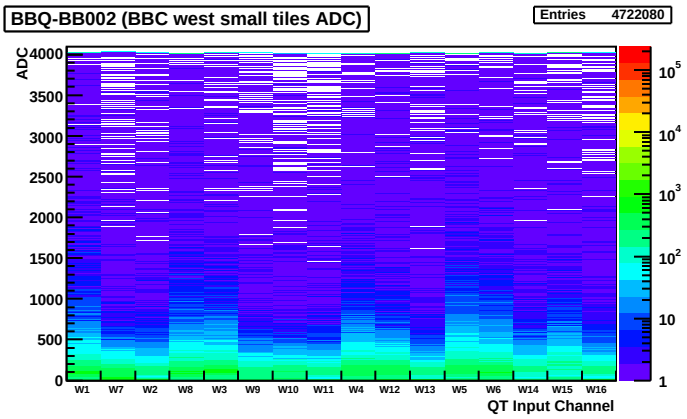

MIX-TF101
Entries 1770780
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

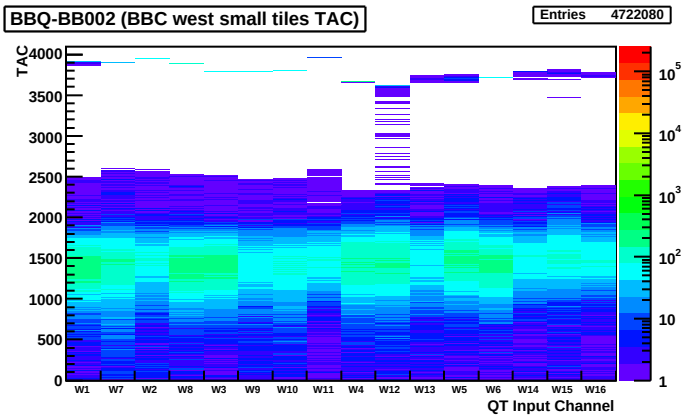

L1-TF201
Entries 295130
TOF total mult

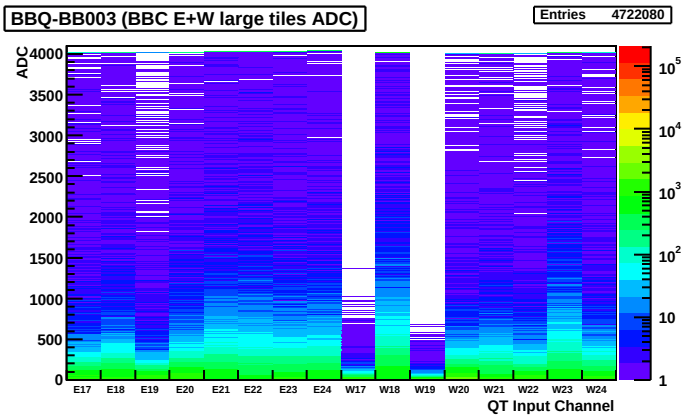

L1-TF201
Entries 1770780
Threshold bits
181629
182019
181779
180990
180496
171924
113501
113111
113351
114140
114634
123206
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

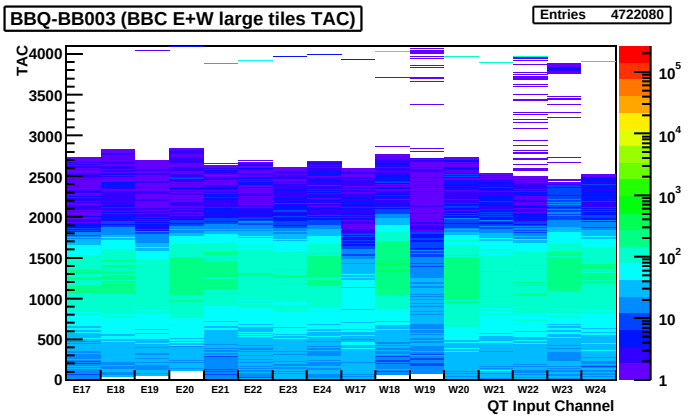

BBQ-BB001 (BBC east small tiles ADC)
Entries 4722080
ADC
QT Input Channel

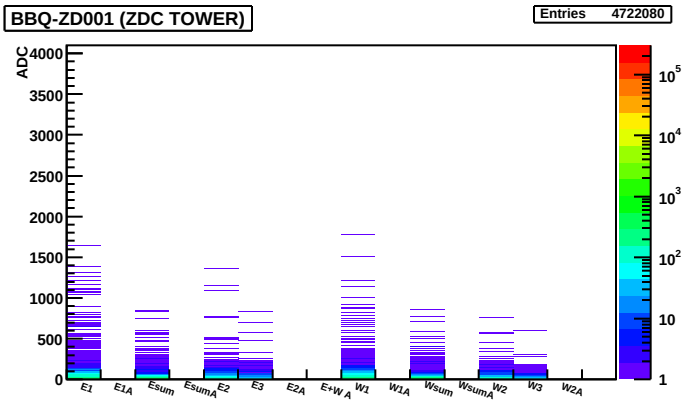

BBQ-BB001 (BBC east small tiles TAC)
Entries 4722080
TAC
QT Input Channel

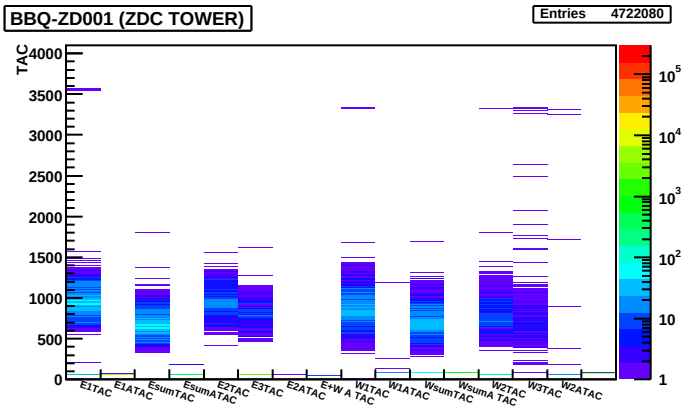

BBQ-BB002 (BBC west small tiles ADC)
Entries 4722080
ADC
QT Input Channel

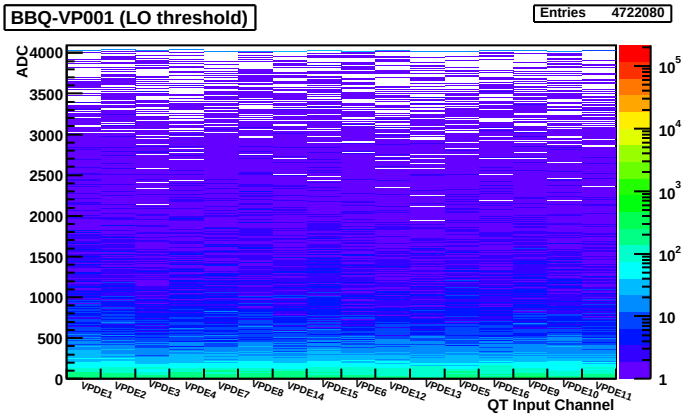

BBQ-BB002 (BBC west small tiles TAC)
Entries 4722080
TAC
QT Input Channel

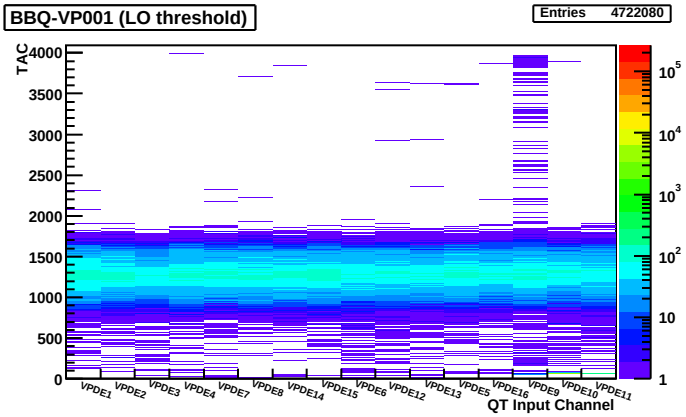

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4722080
ADC
QT Input Channel

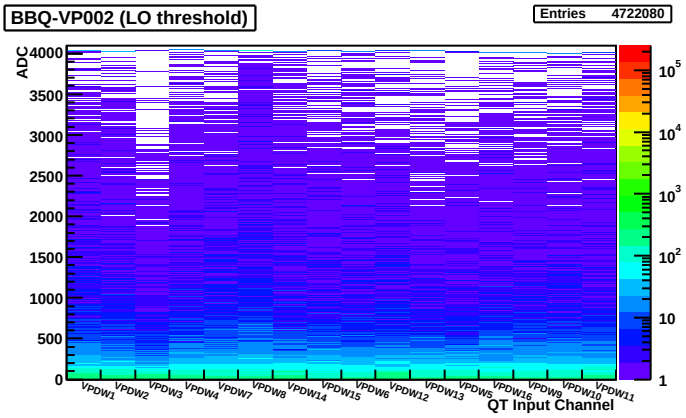

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4722080
TAC
QT Input Channel

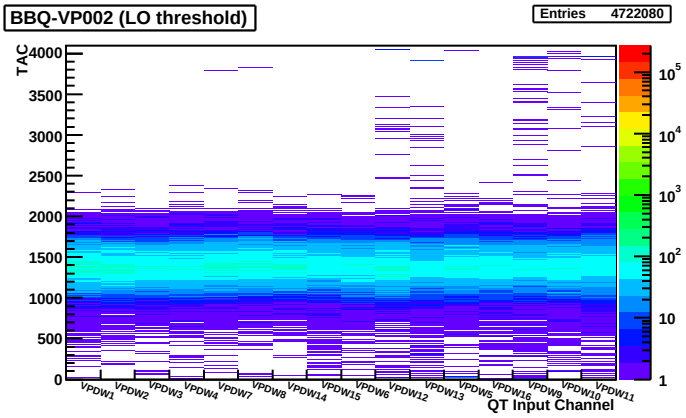

BBQ-ZD001 (ZDC TOWER)
Entries 4722080
ADC

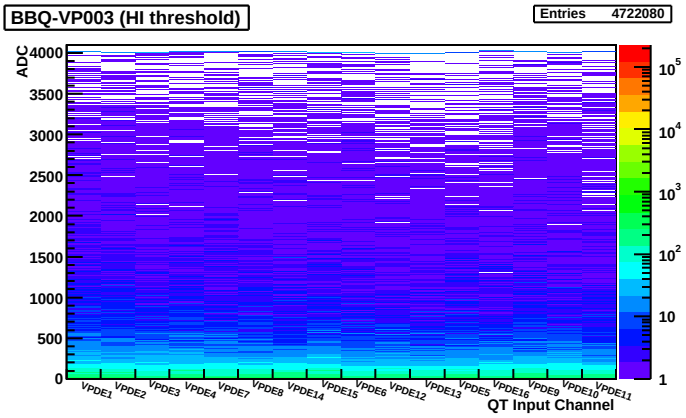

BBQ-ZD001 (ZDC TOWER)
Entries 4722080
TAC

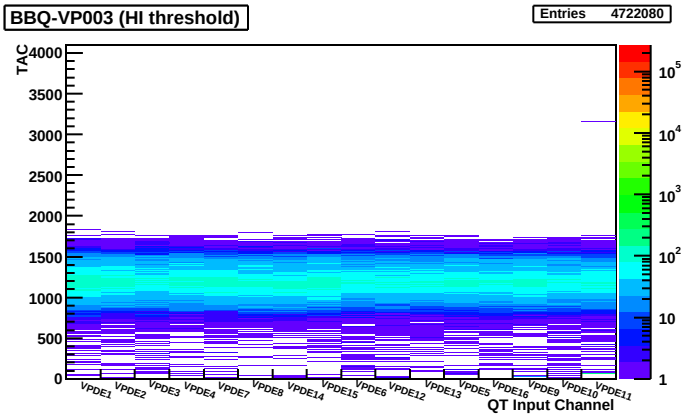

BBQ-VP001 (LO threshold)
Entries 4722080
ADC
QT Input Channel

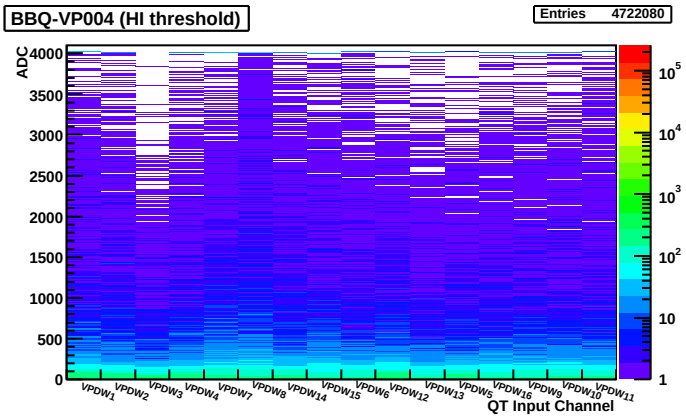

BBQ-VP001 (LO threshold)
Entries 4722080
TAC
QT Input Channel

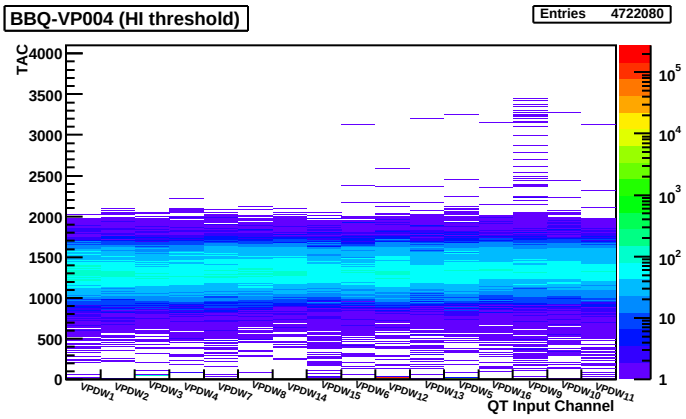

BBQ-VP002 (LO threshold)
Entries 4722080
ADC
QT Input Channel

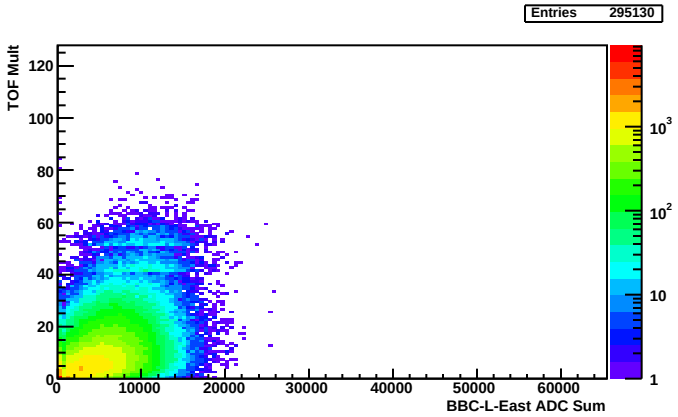

BBQ-VP002 (LO threshold)
Entries 4722080
TAC
QT Input Channel

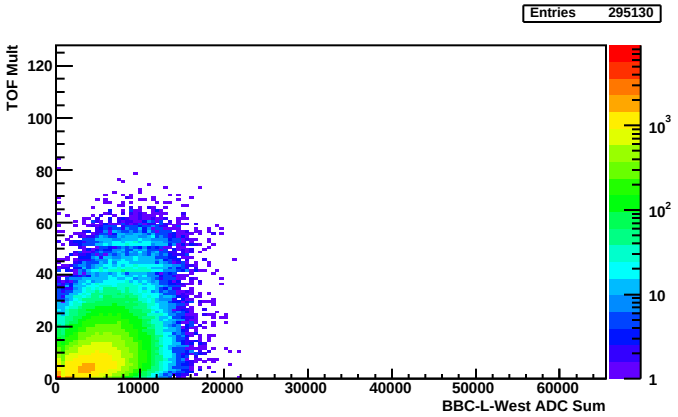

BBQ-VP003 (HI threshold)
Entries 4722080
ADC
QT Input Channel

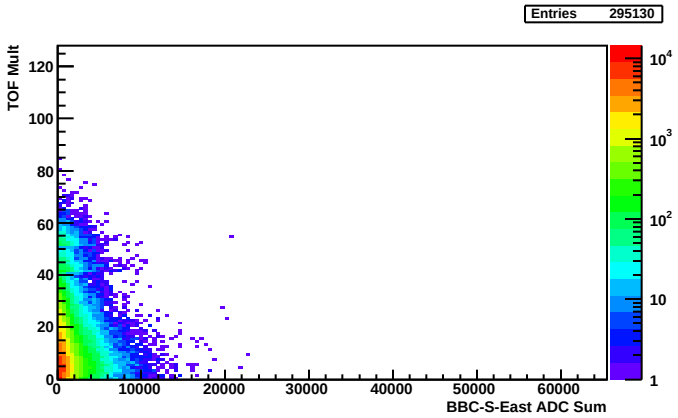

BBQ-VP003 (HI threshold)
Entries 4722080
TAC
QT Input Channel

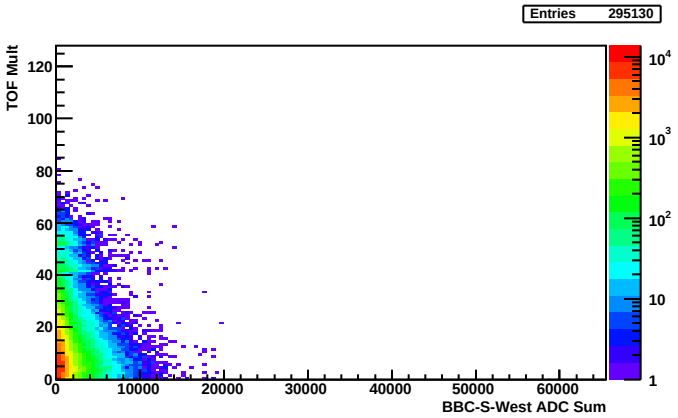

BBQ-VP004 (HI threshold)
Entries 4722080
ADC
QT Input Channel

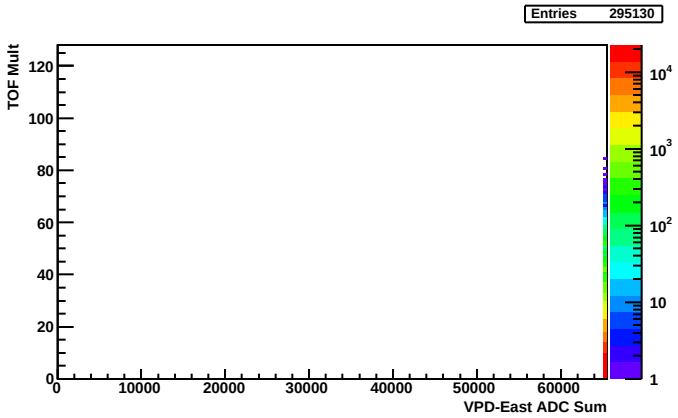

BBQ-VP004 (HI threshold)
Entries 4722080
TAC
QT Input Channel

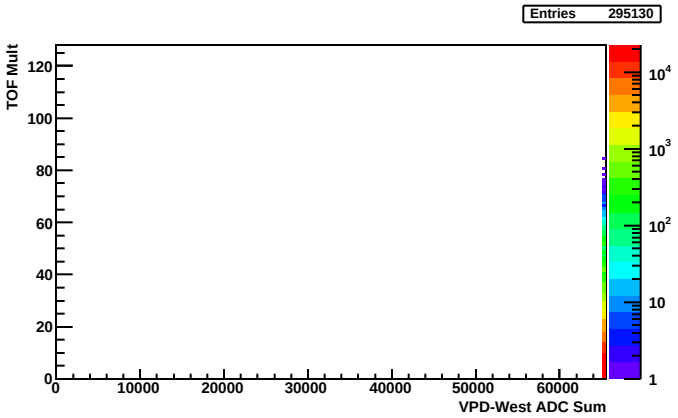

Entries 295130
TOF Mult
BBC-L-East ADC Sum

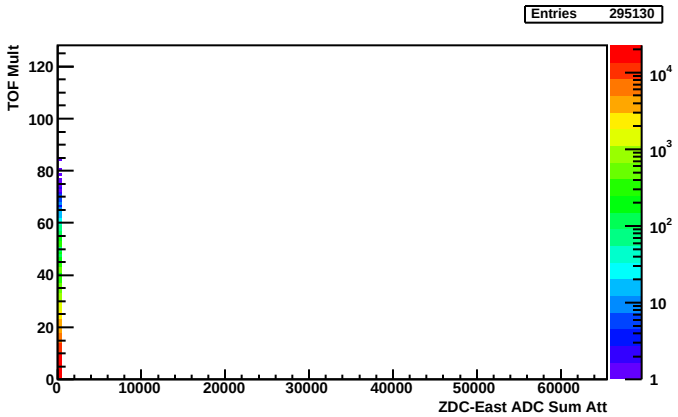

Entries 295130
TOF Mult
BBC-L-West ADC Sum

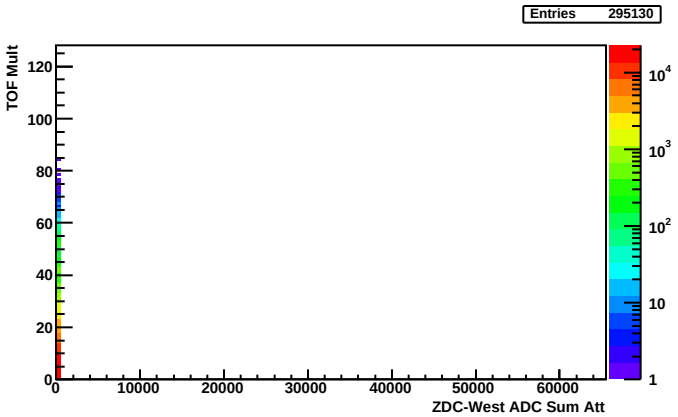

Entries 295130
TOF Mult
BBC-S-East ADC Sum

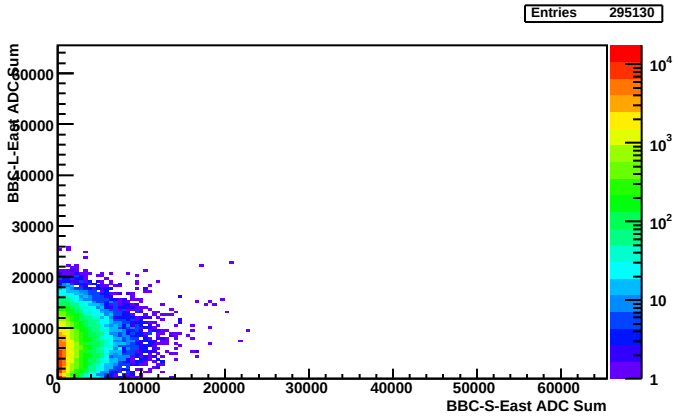

Entries 295130
TOF Mult
BBC-S-West ADC Sum

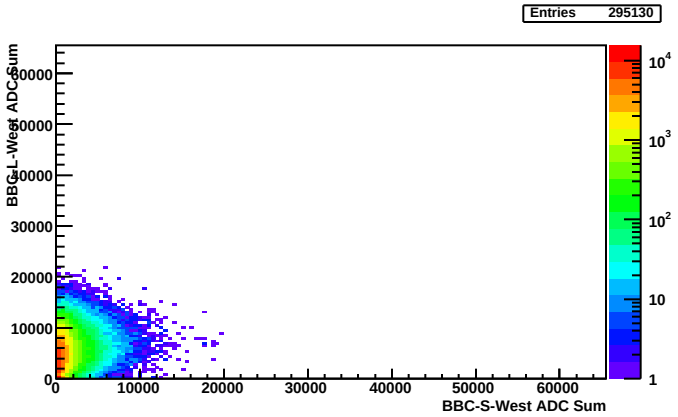

Entries 295130
TOF Mult
VPD-East ADC Sum

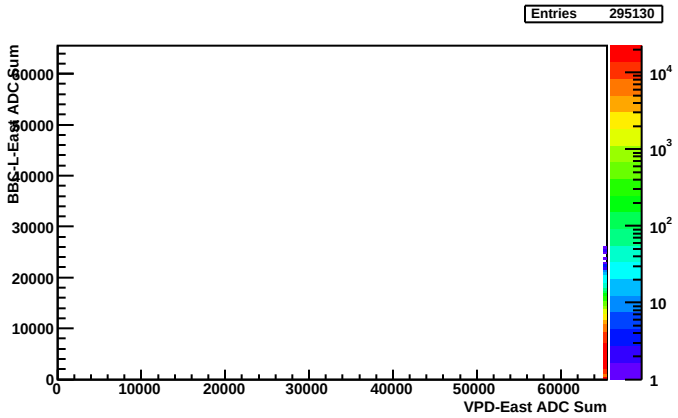

Entries 295130
TOF Mult
VPD-West ADC Sum

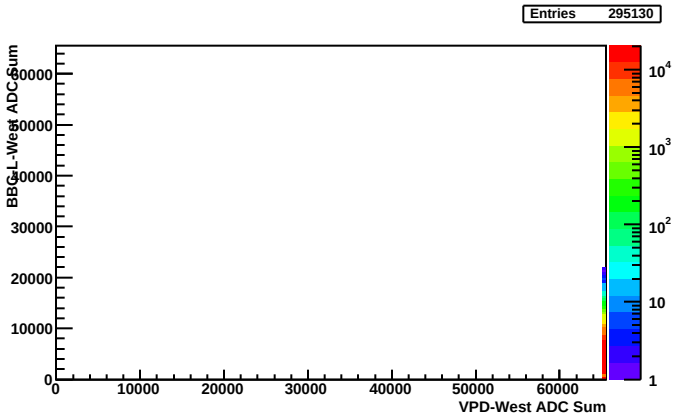

Entries 295130
TOF Mult
ZDC-East ADC Sum Att

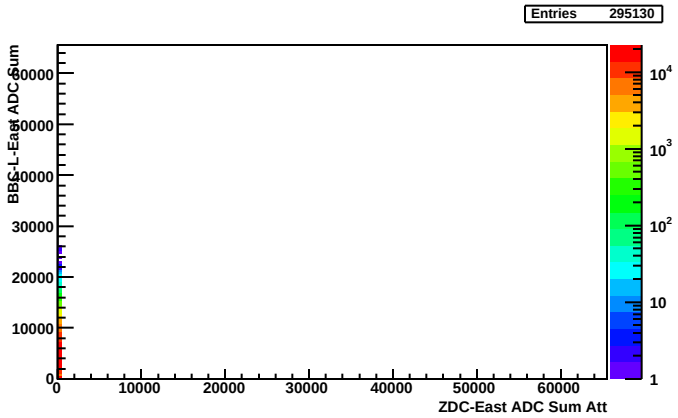

Entries 295130
TOF Mult
ZDC-West ADC Sum Att

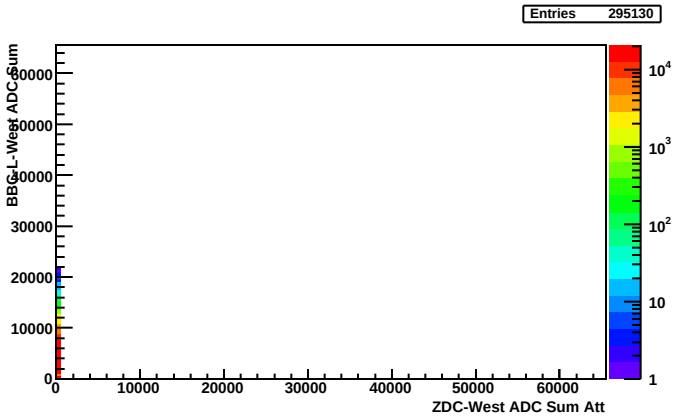

Entries 295130
BBC-L-East ADC Sum
BBC-S-East ADC Sum

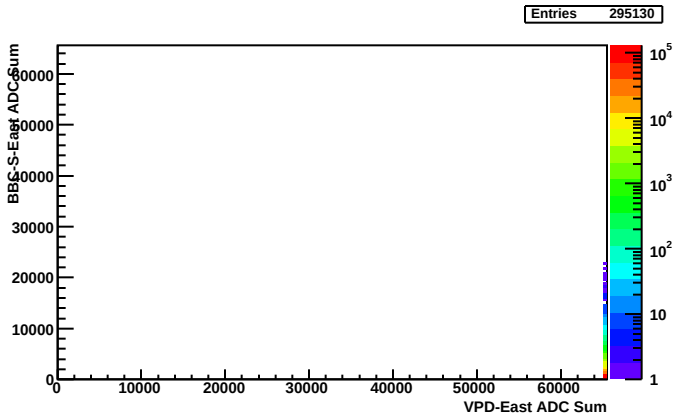

Entries 295130
BBC-L-West ADC Sum
BBC-S-West ADC Sum

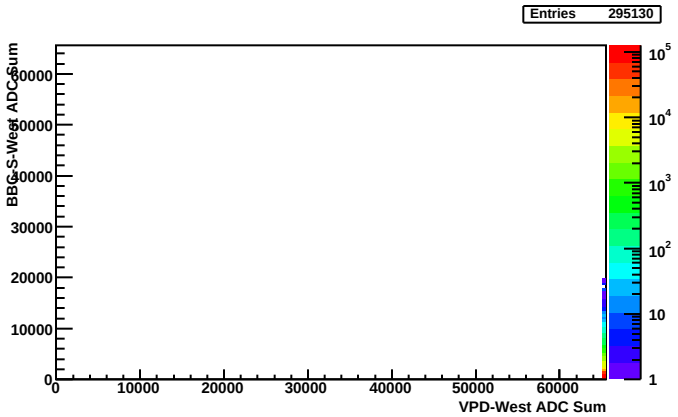

Entries 295130
BBC-L-East ADC Sum
VPD-East ADC Sum

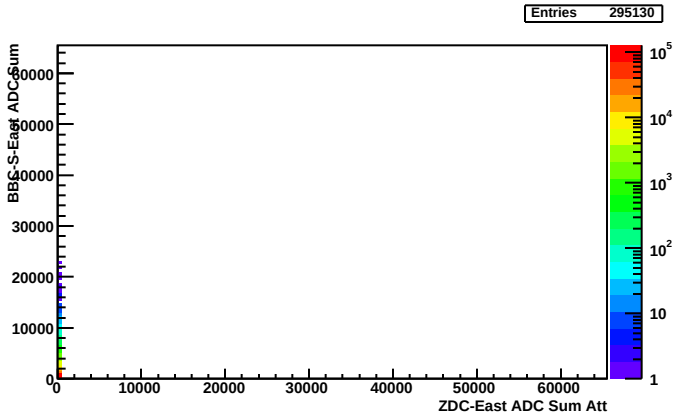

Entries 295130
BBC-L-West ADC Sum
VPD-West ADC Sum

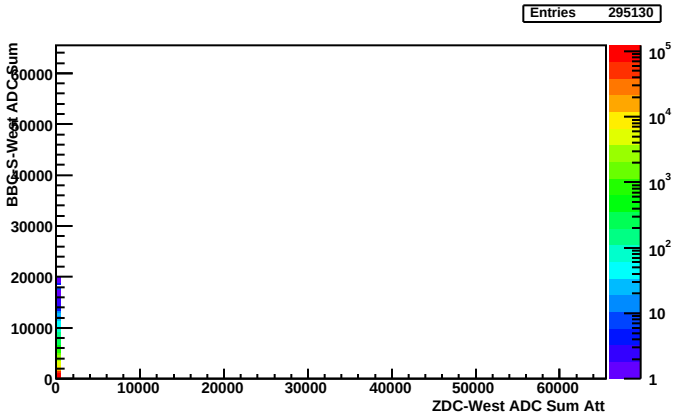

Entries 295130
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

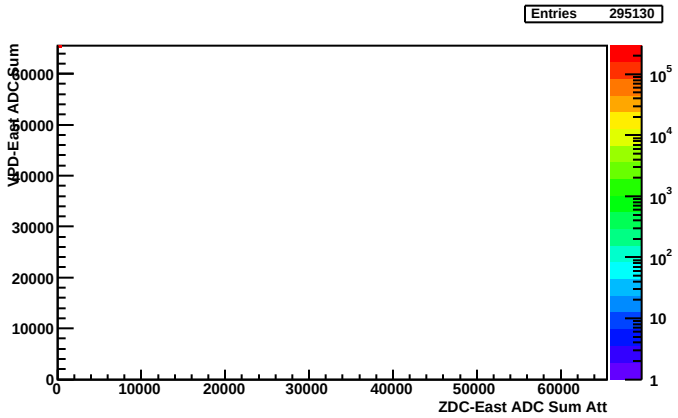

Entries 295130
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

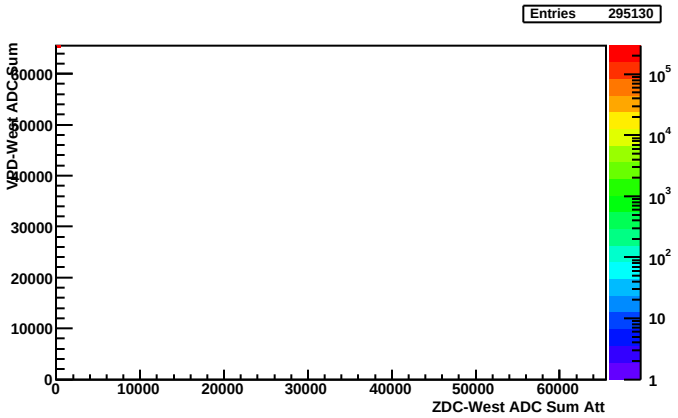

Entries 295130
BBC-S-East ADC Sum
VPD-East ADC Sum

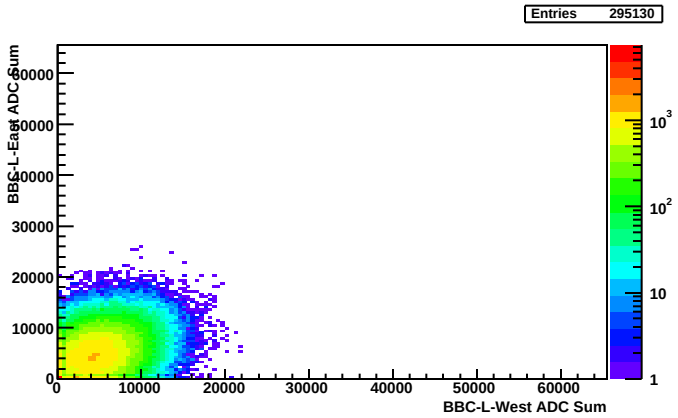

Entries 295130
BBC-S-West ADC Sum
VPD-West ADC Sum

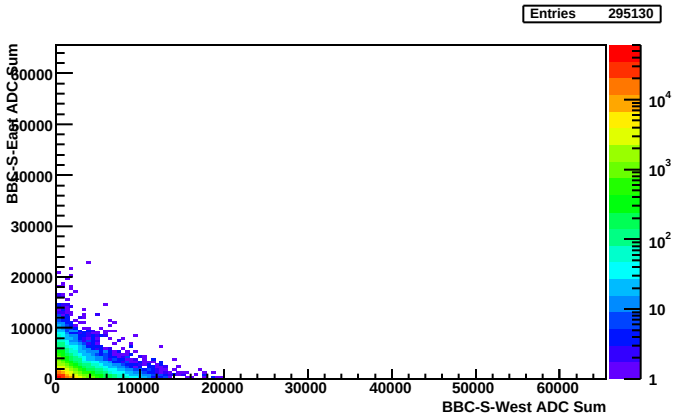

Entries 295130
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

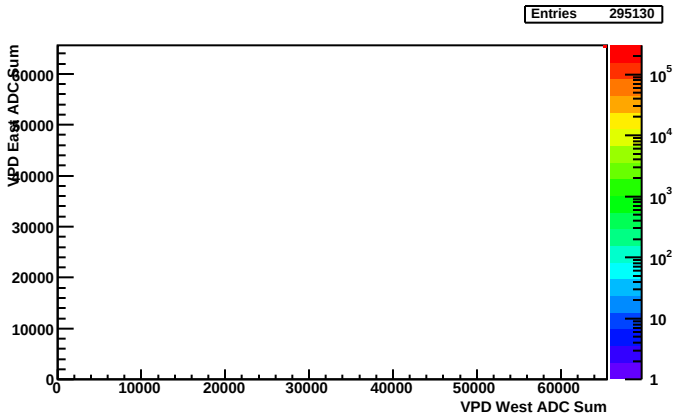

Entries 295130
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

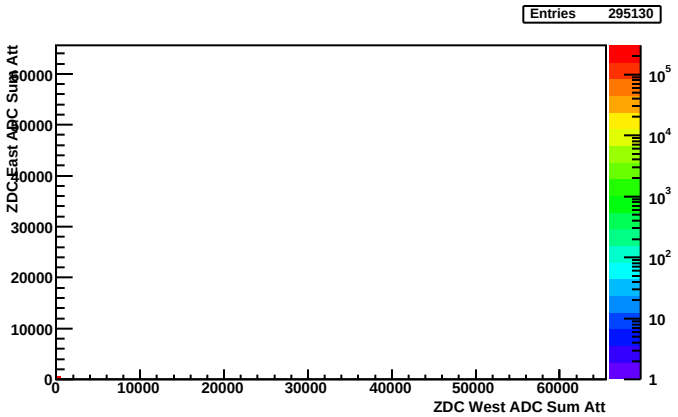

Entries 295130
VPD-East ADC Sum
ZDC-East ADC Sum Att

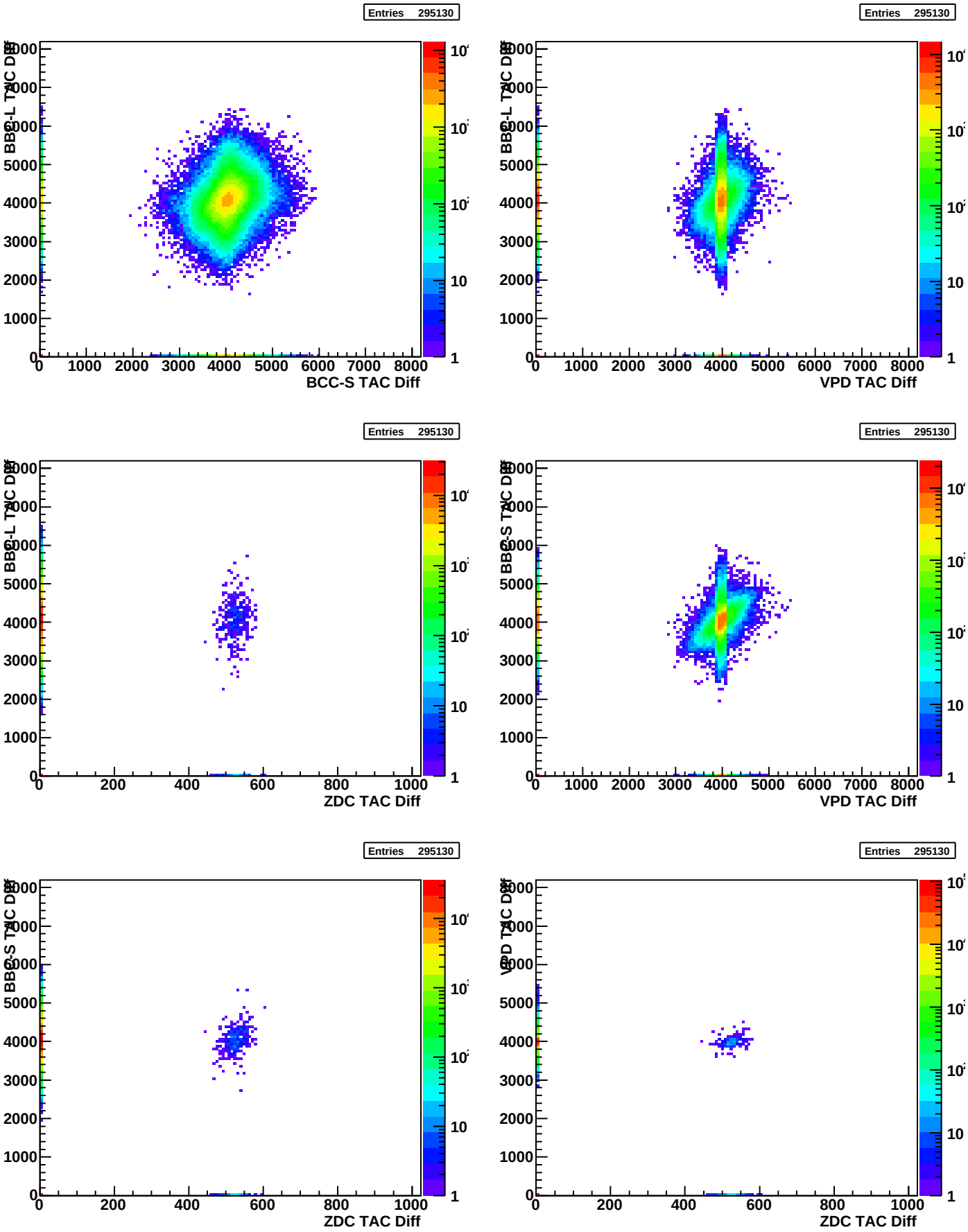

Entries 295130
VPD-West ADC Sum
ZDC-West ADC Sum Att

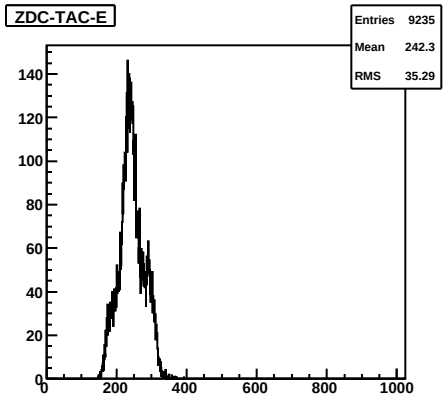

Entries 295130
BBC-L-East ADC Sum
BBC-L-West ADC Sum

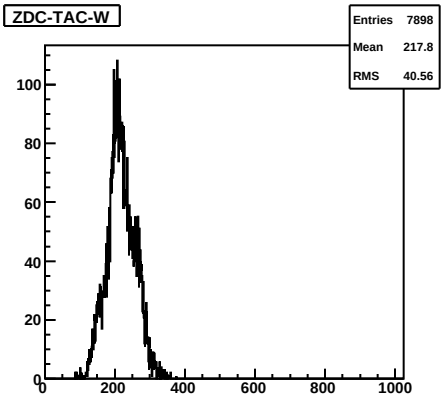

Entries 295130
BBC-S-East ADC Sum
BBC-S-West ADC Sum

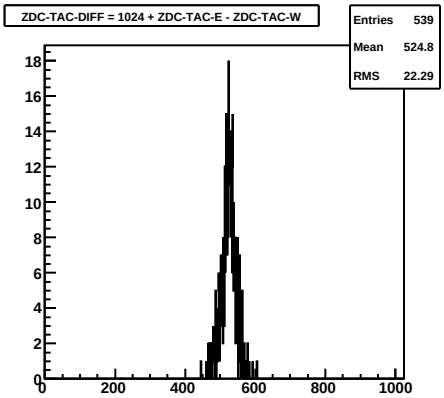

Entries 295130
VPD East ADC Sum
VPD West ADC Sum

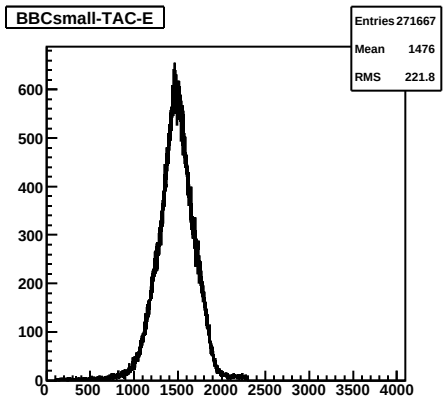

Entries 295130
ZDC East ADC Sum Att
ZDC West ADC Sum Att

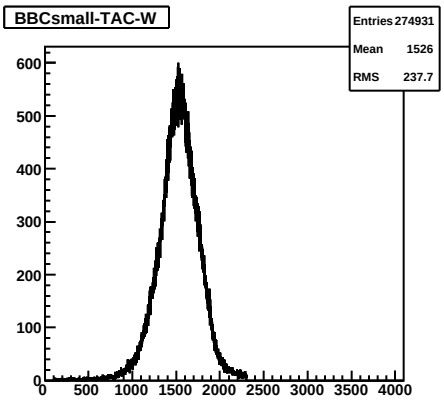

Entries 295130
BBC-L TAC Diff
BCC-S TAC Diff
Entries 295130
BBC-L TAC Diff
VPD TAC Diff
Entries 295130
BBC-L TAC Diff
ZDC TAC Diff
Entries 295130
BBC-S TAC Diff
VPD TAC Diff
Entries 295130
BBC-S TAC Diff
ZDC TAC Diff
Entries 295130
VPD TAC Diff
ZDC TAC Diff

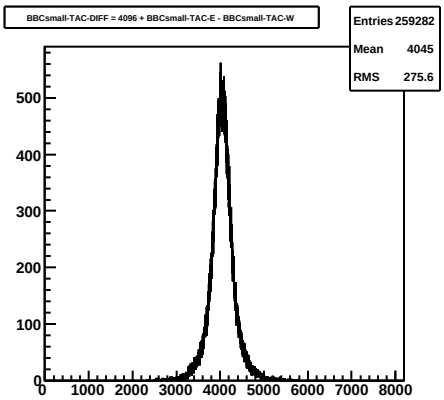

ZDC-TAC-E
Entries 9235
Mean 242.3
RMS 35.29

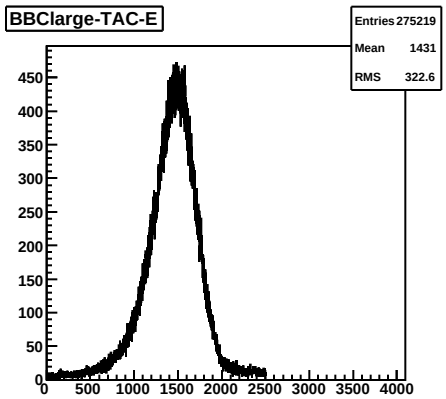

ZDC-TAC-W
Entries 7898
Mean 217.8
RMS 40.56

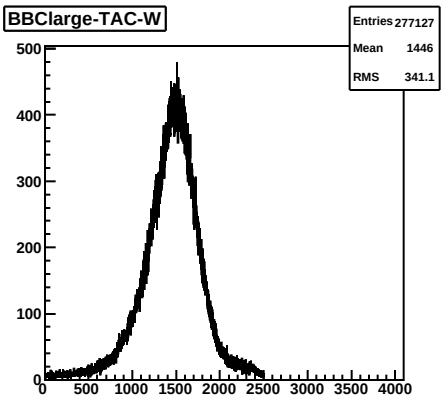

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 539
Mean 524.8
RMS 22.29

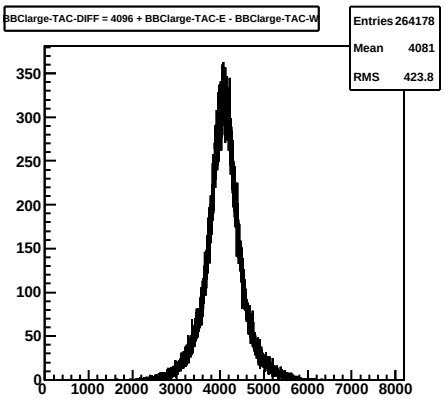

BBCsmall-TAC-E
Entries 271667
Mean 1476
RMS 221.8

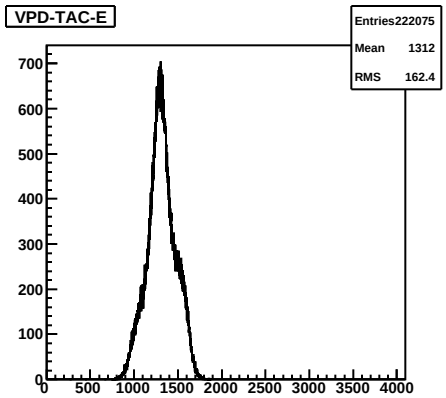

BBCsmall-TAC-W
Entries 274931
Mean 1526
RMS 237.7

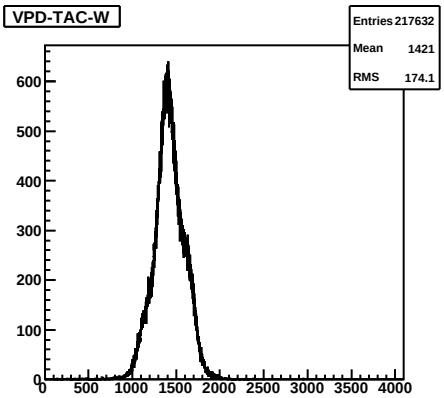

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 259282
Mean 4045
RMS 275.6

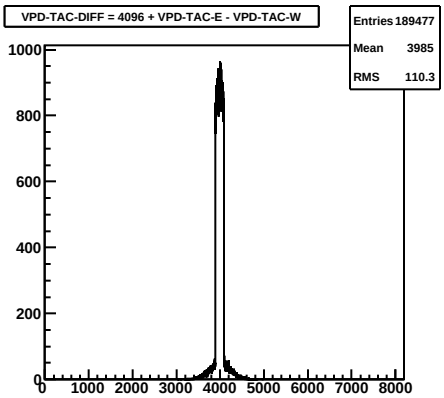

BBClarge-TAC-E
Entries 275219
Mean 1431
RMS 322.6

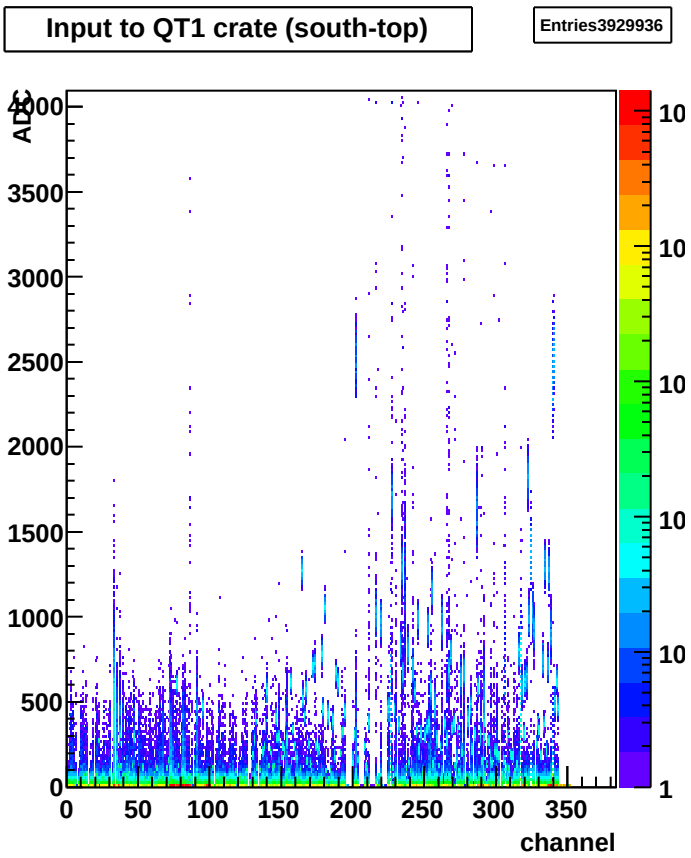

BBClarge-TAC-W
Entries 277127
Mean 1446
RMS 341.1

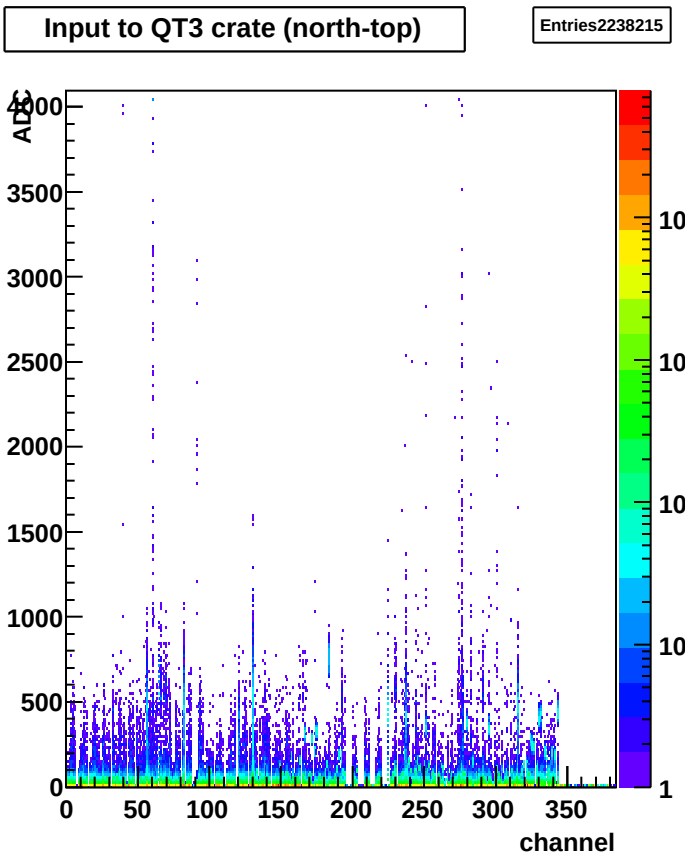

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 264178
Mean 4081
RMS 423.8

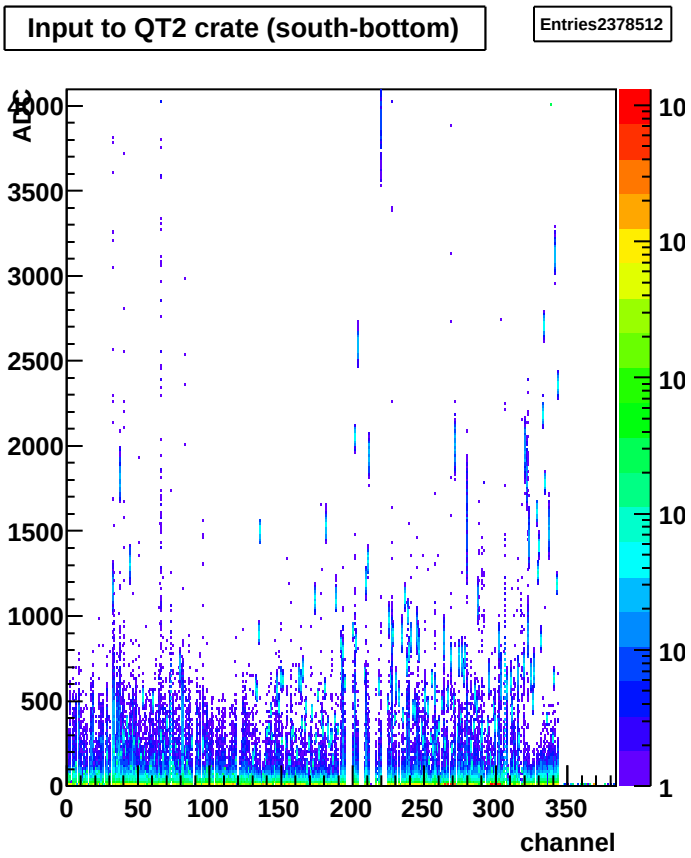

VPD-TAC-E
Entries 222075
Mean 1312
RMS 162.4

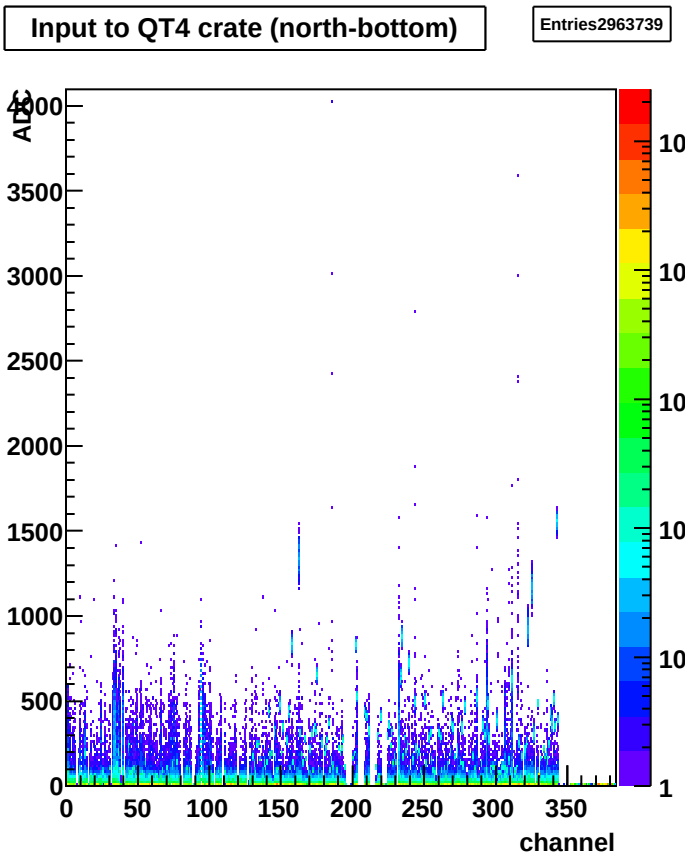

VPD-TAC-W
Entries 217632
Mean 1421
RMS 174.1

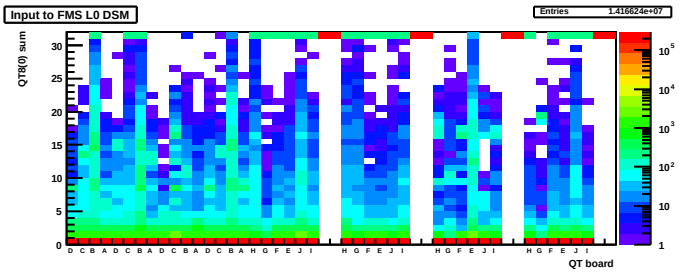

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 189477
Mean 3985
RMS 110.3

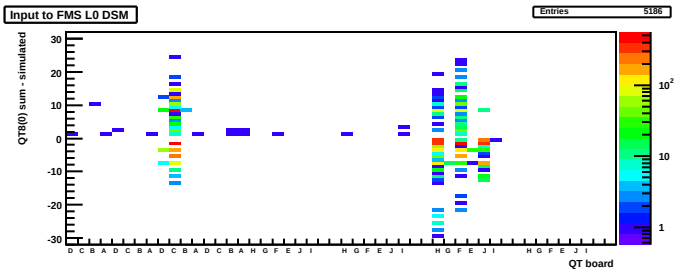

Input to QT1 crate (south-top)
Entries3929936
ADC
channel

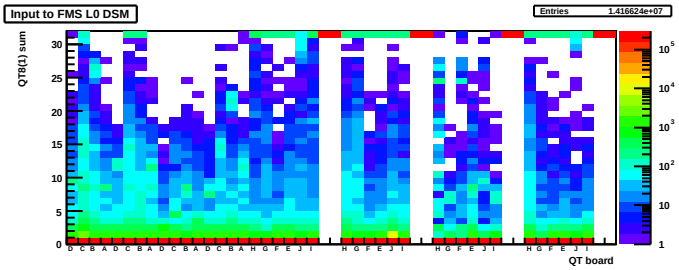

Input to QT3 crate (north-top)
Entries2238215
ADC
channel

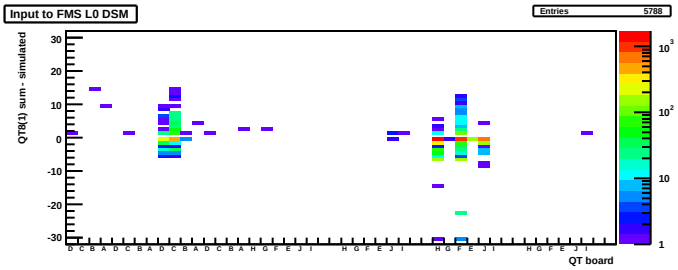

Input to QT2 crate (south-bottom)
Entries2378512
ADC
channel

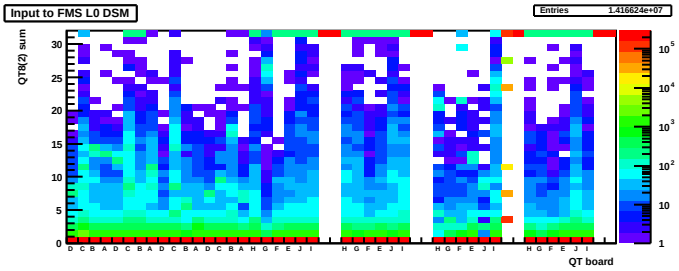

Input to QT4 crate (north-bottom)
Entries2963739
ADC
channel

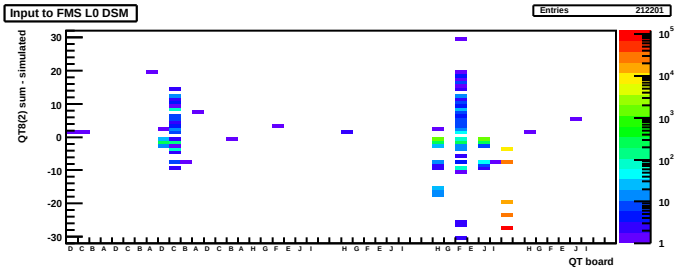

Input to FMS L0 DSM
Entries 1.416624e+07
QT8(0) sum
QT board

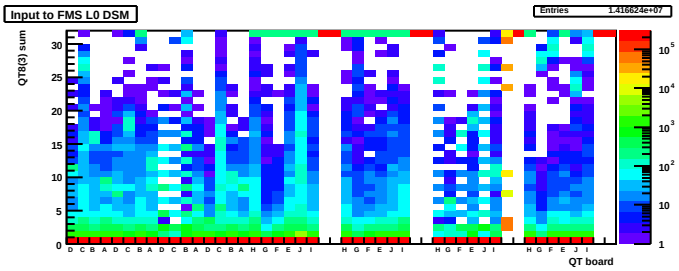

Input to FMS L0 DSM
Entries 5186
QT8(0) sum - simulated
QT board

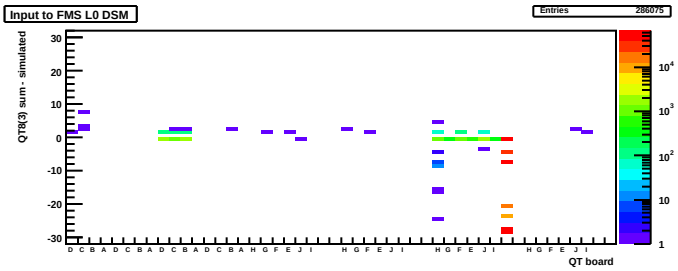

Input to FMS L0 DSM
Entries 1.416624e+07
QT8(1) sum
QT board

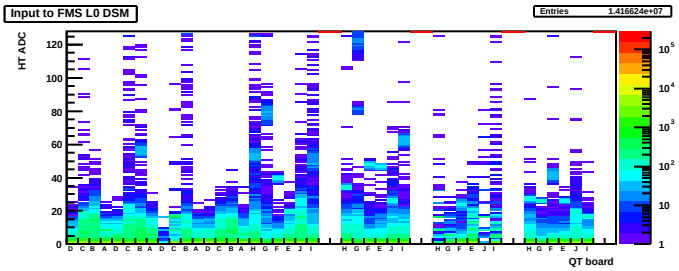

Input to FMS L0 DSM
Entries 5788
QT8(1) sum - simulated
QT board

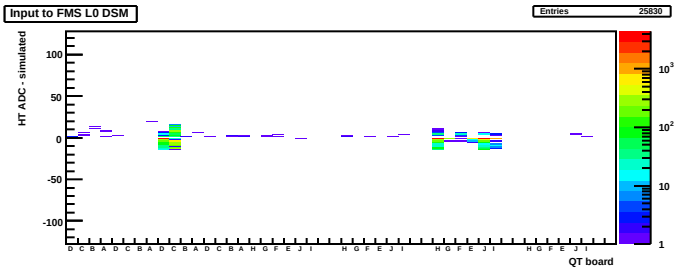

Input to FMS L0 DSM
Entries 1.416624e+07
QT8(2) sum
QT board

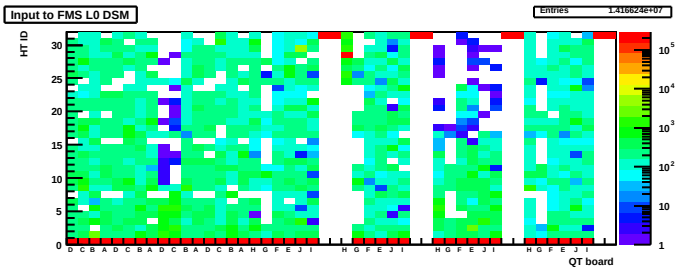

Input to FMS L0 DSM
Entries 212201
QT8(2) sum - simulated
QT board

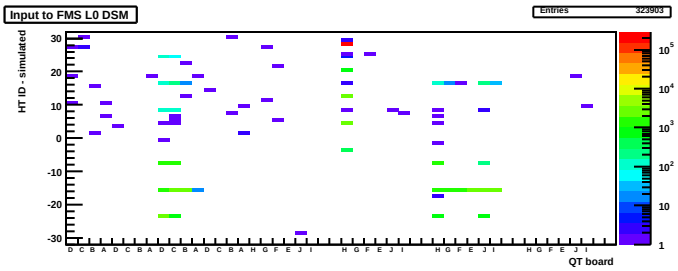

Input to FMS L0 DSM
Entries 1.416624e+07
QT8(3) sum
QT board

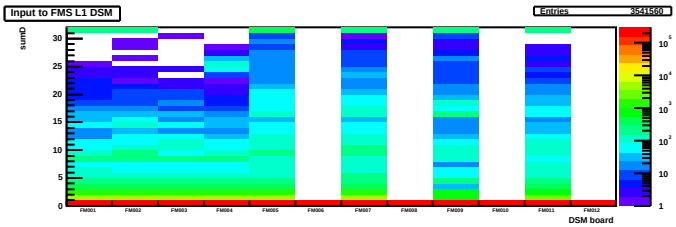

Input to FMS L0 DSM
Entries 286075
QT8(3) sum - simulated
QT board

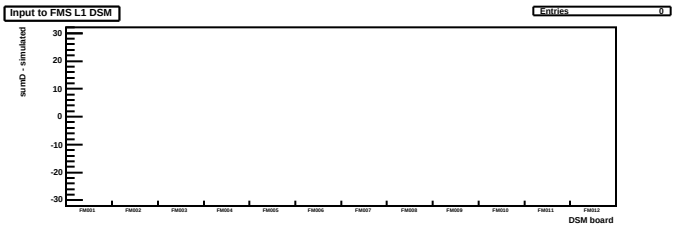

Input to FMS L0 DSM
Entries 1.416624e+07
HT ADC
QT board

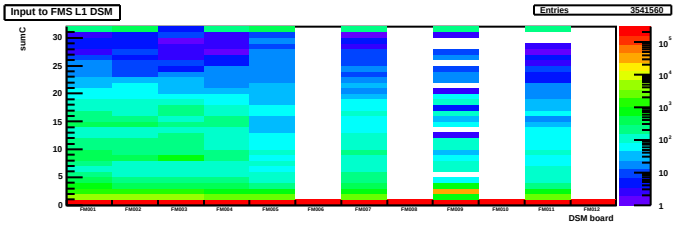

Input to FMS L0 DSM
Entries 25830
HT ADC - simulated
QT board

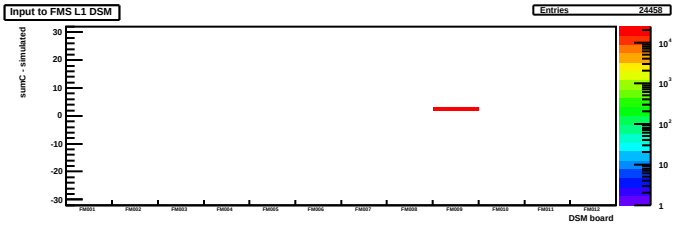

Input to FMS L0 DSM
Entries 1.416624e+07
HT ID
QT board

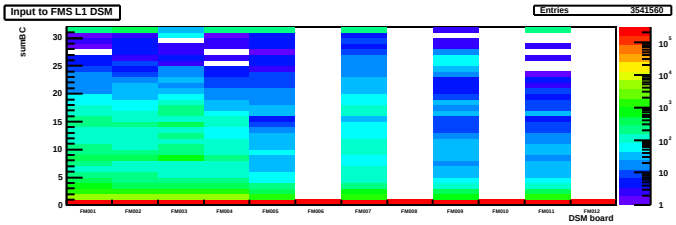

Input to FMS L0 DSM
Entries 323903
HT ID - simulated
QT board

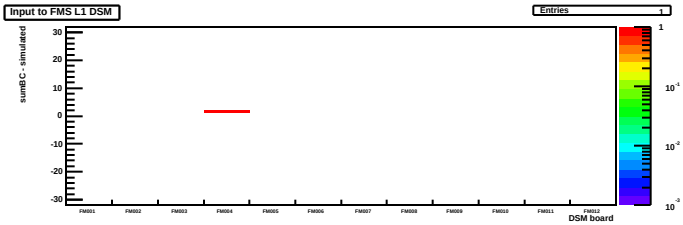

Input to FMS L1 DSM
Entries 3541560
sumD
DSM board

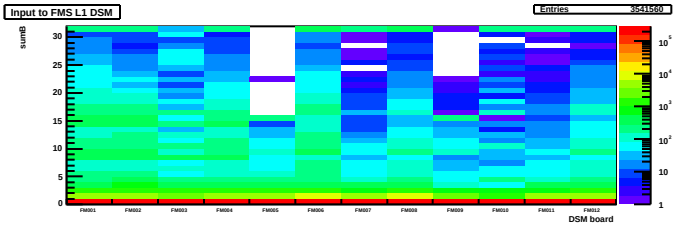

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

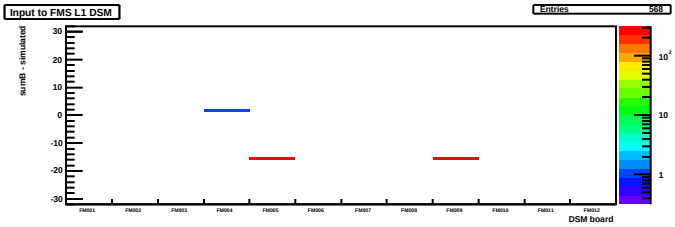

Input to FMS L1 DSM
Entries 3541560
sumC
DSM board

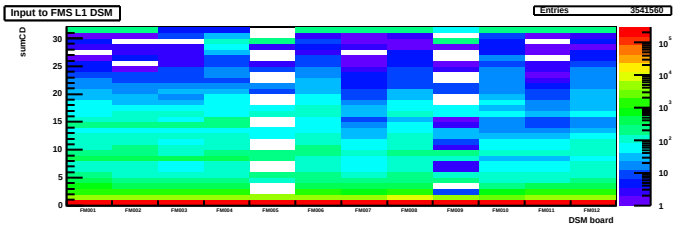

Input to FMS L1 DSM
Entries 24458
sumC - simulated
DSM board

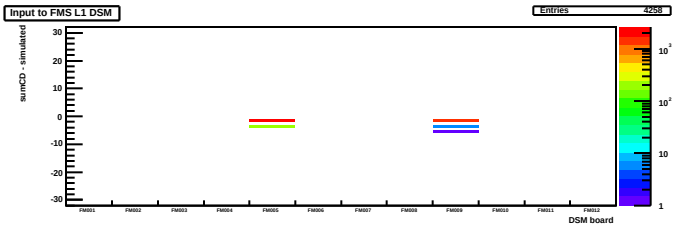

Input to FMS L1 DSM
Entries 3541560
sumBC
DSM board

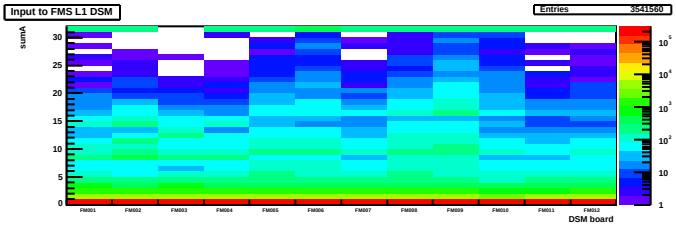

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

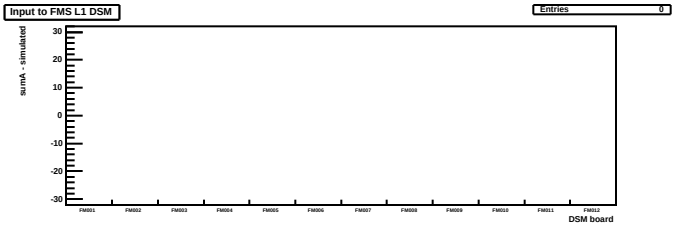

Input to FMS L1 DSM
Entries 3541560
sumB
DSM board

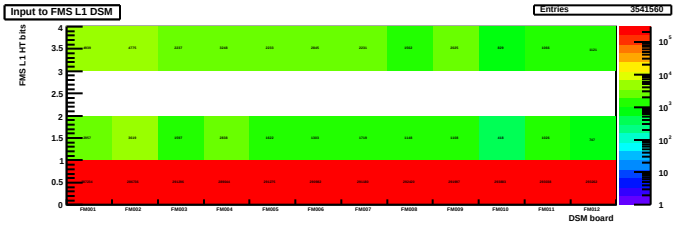

Input to FMS L1 DSM
Entries 568
sumB - simulated
DSM board

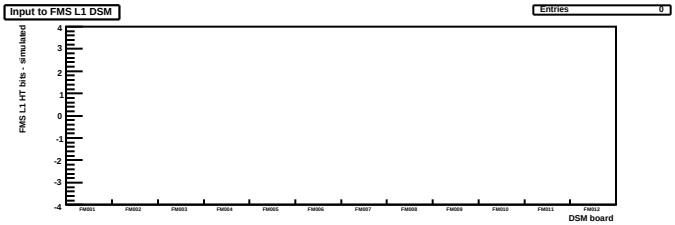

Input to FMS L1 DSM
Entries 3541560
sumCD
DSM board

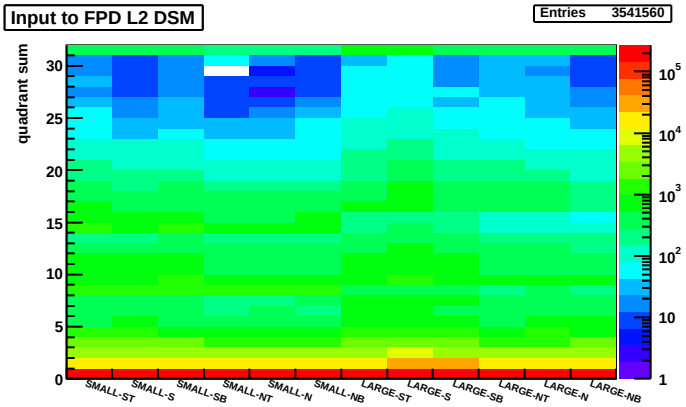

Input to FMS L1 DSM
Entries 4258
sumCD - simulated
DSM board

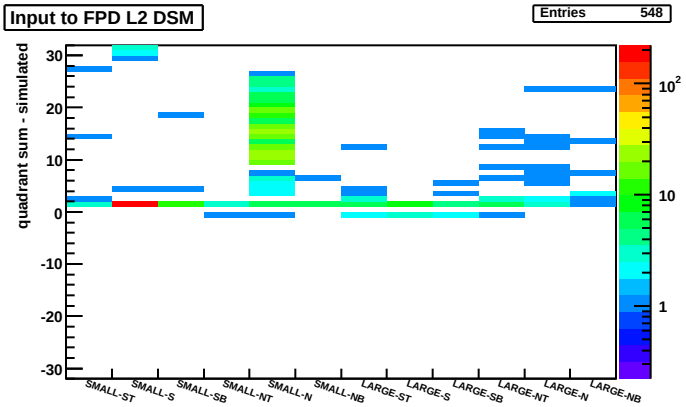

Input to FMS L1 DSM
Entries 3541560
sumA
DSM board

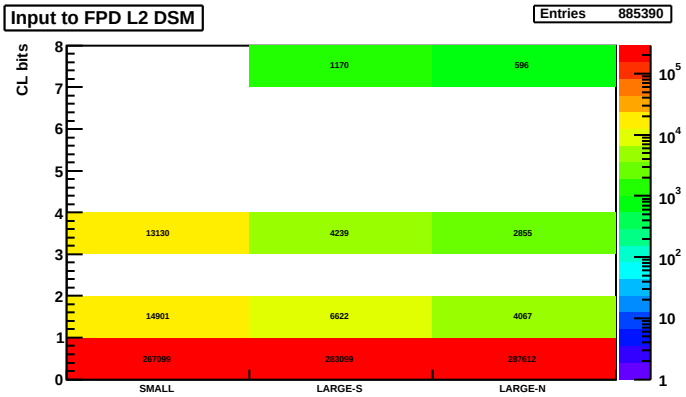

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

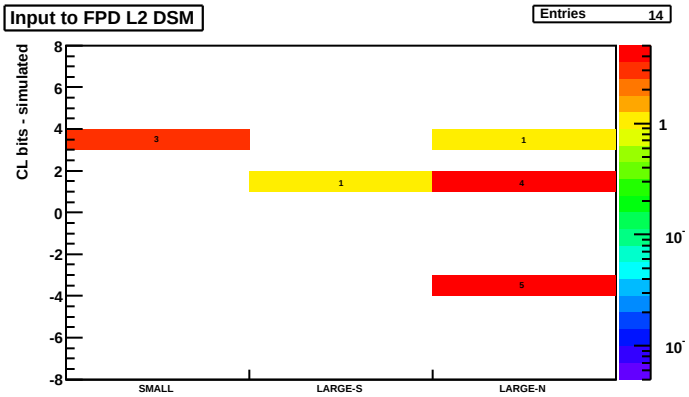

Input to FMS L1 DSM
Entries 3541560
FMS L1 HT bits
DSM board

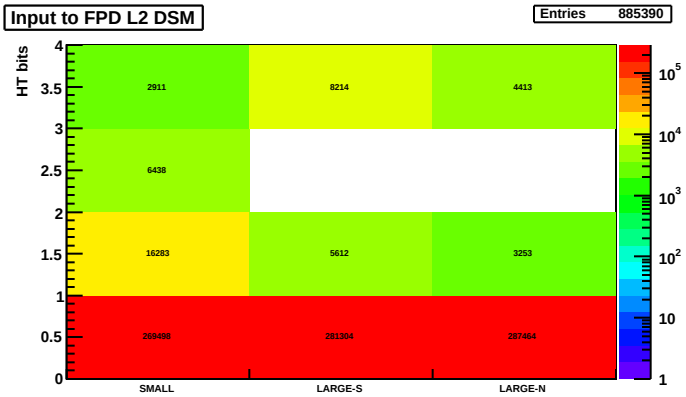

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

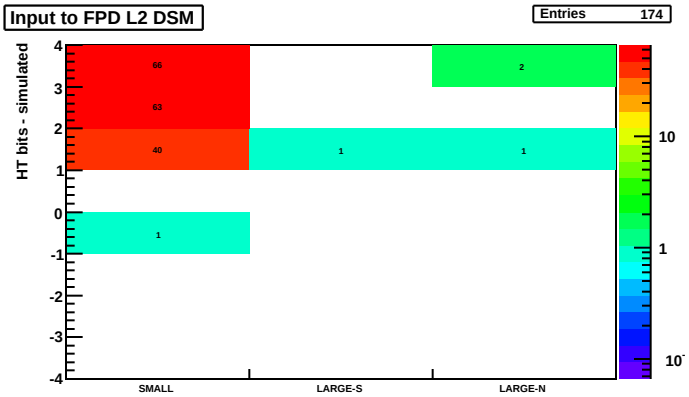

Input to FPD L2 DSM
Entries 3541560
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

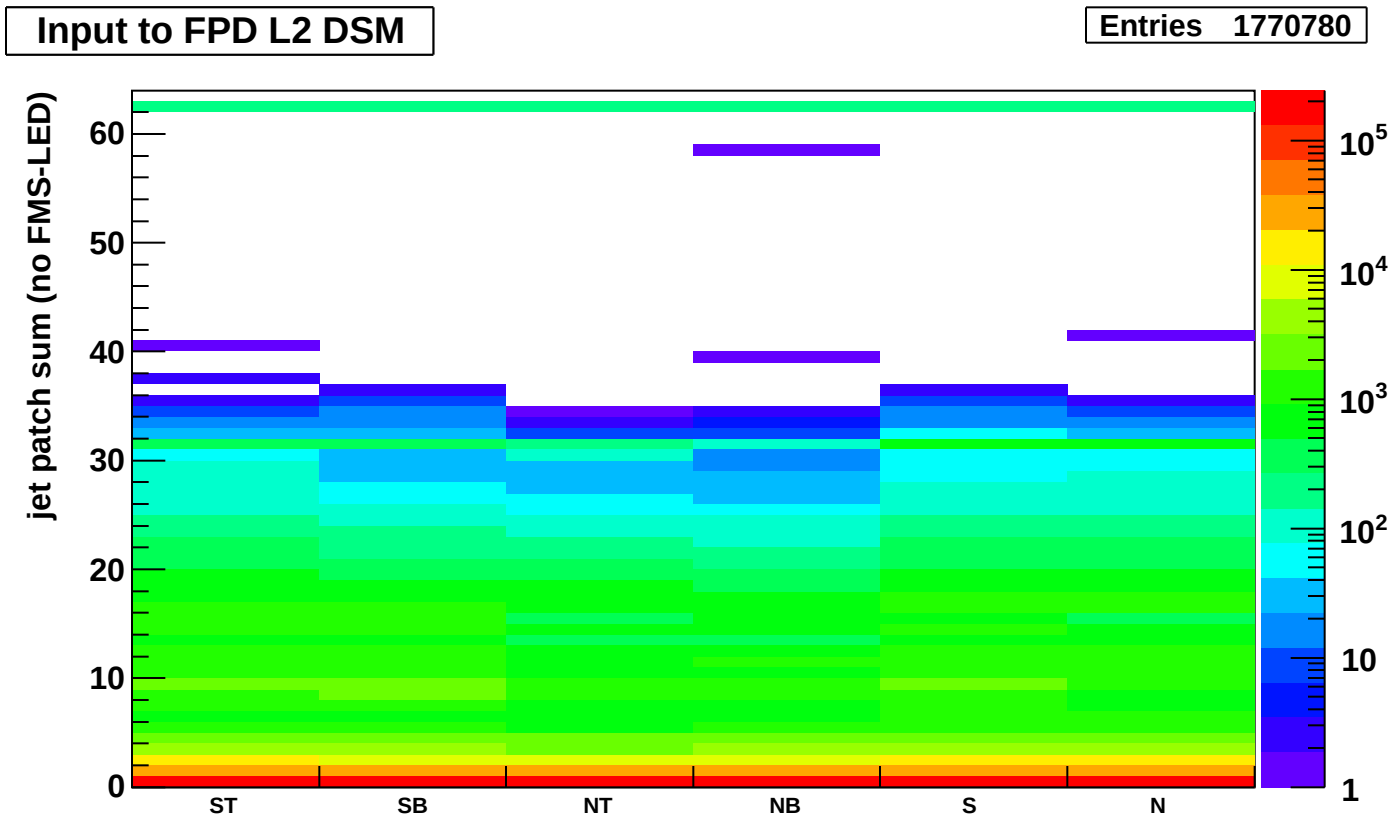

Input to FPD L2 DSM
Entries 548
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

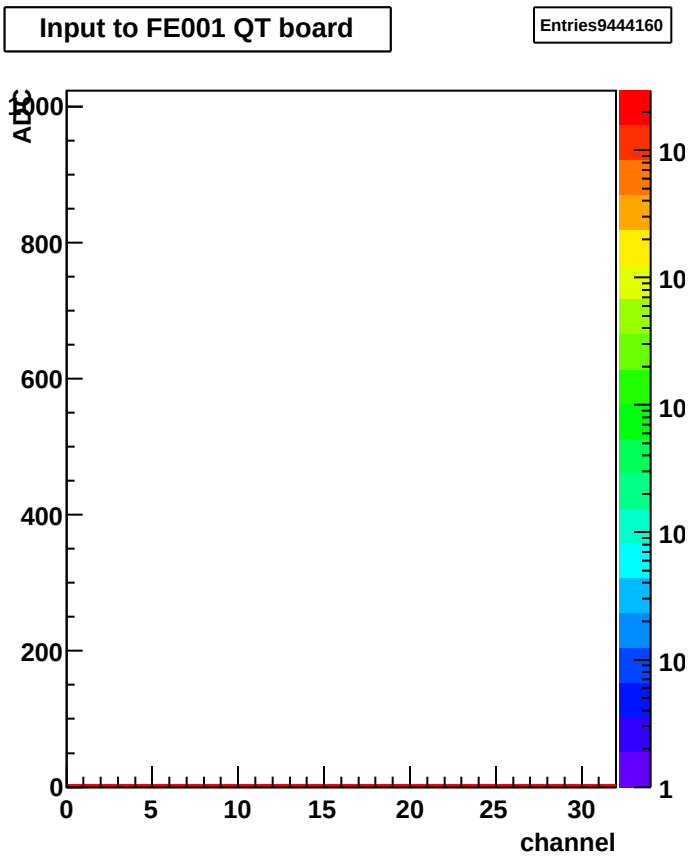

Input to FPD L2 DSM
Entries 885390
CL bits
1170
596
13130
4239
2855
14901
6622
4067
267099
283099
287612
SMALL
LARGE-S
LARGE-N

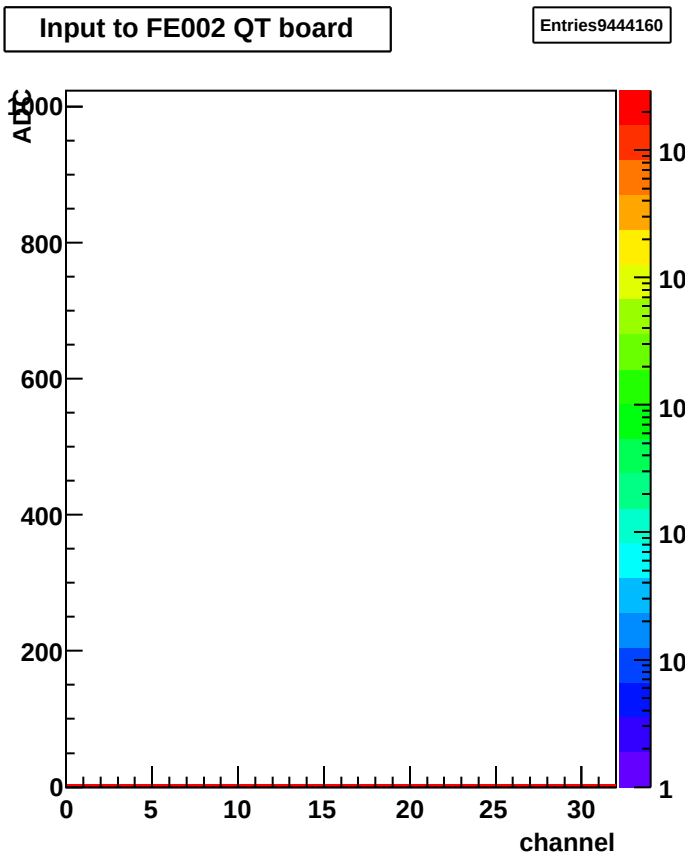

Input to FPD L2 DSM
Entries 14
CL bits - simulated
3
1
1
4
5
SMALL
LARGE-S
LARGE-N

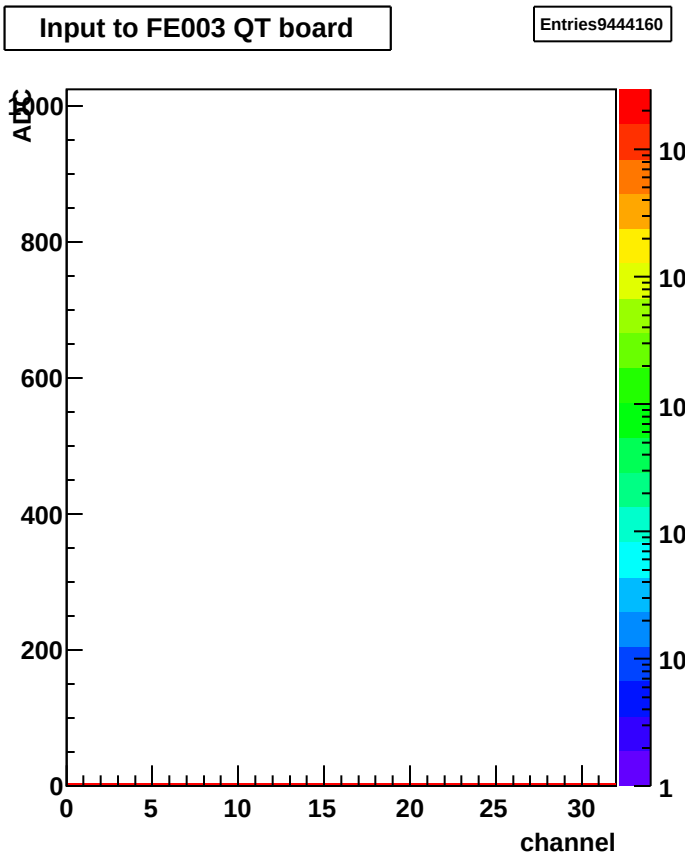

Input to FPD L2 DSM
Entries 885390
HT bits
2911
8214
4413
6438
16283
5612
3253
269498
281304
287464
SMALL
LARGE-S
LARGE-N

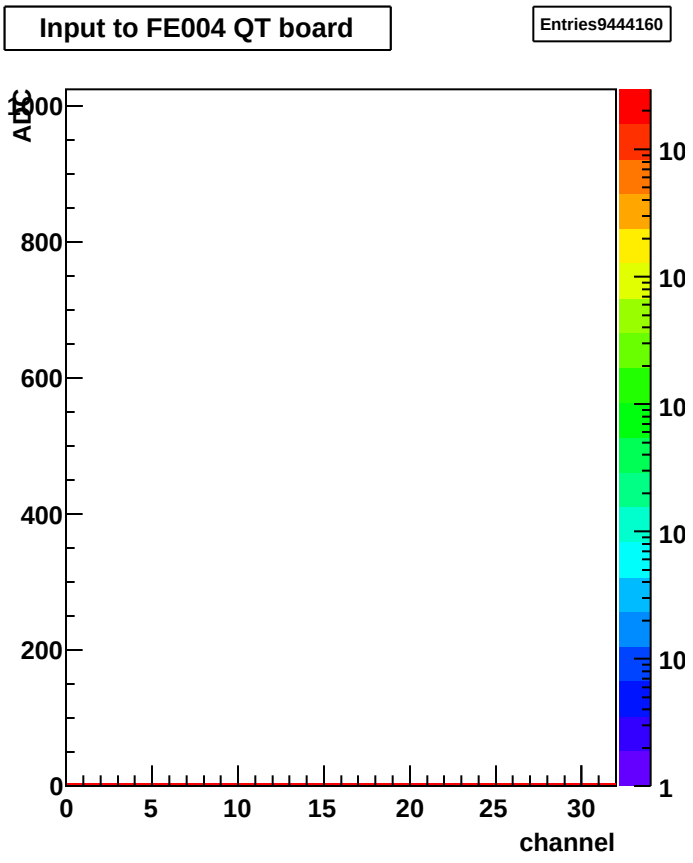

Input to FPD L2 DSM
Entries 174
HT bits - simulated
66
2
63
40
1
1
1
SMALL
LARGE-S
LARGE-N

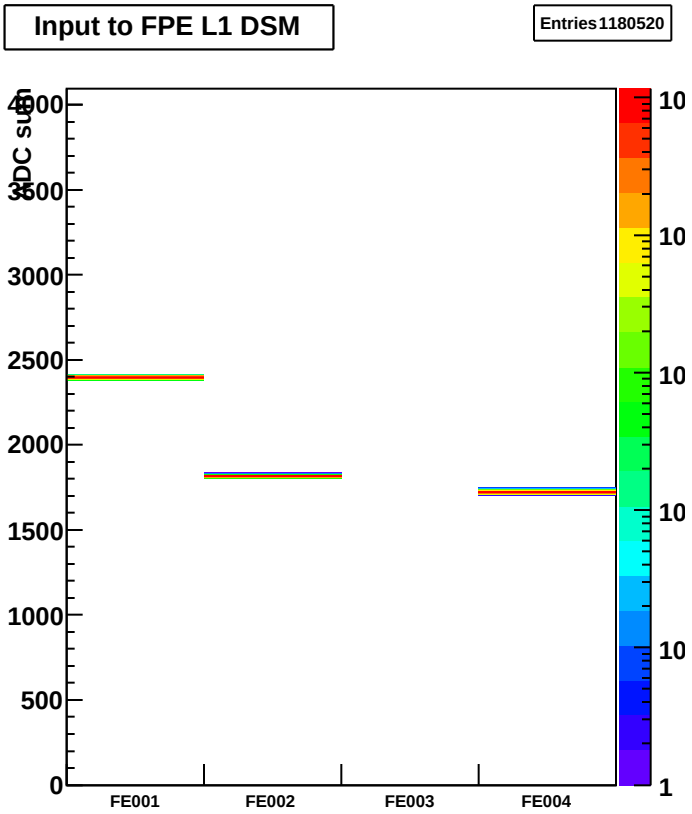

Input to FPD L2 DSM
Entries 1770780
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries9444160
ADC
channel

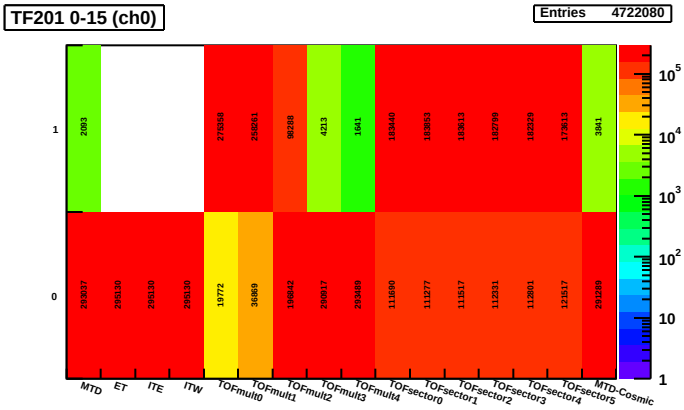

Input to FE002 QT board
Entries9444160
ADC
channel


Input to FE003 QT board
Entries9444160
ADC
channel

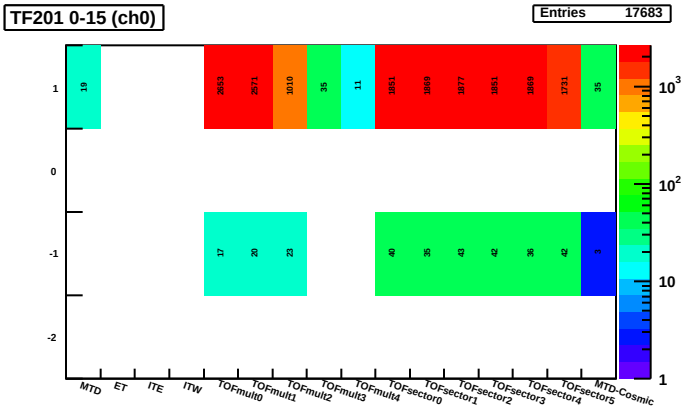

Input to FE004 QT board
Entries9444160
ADC
channel


Input to FPE L1 DSM
Entries 1180520
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

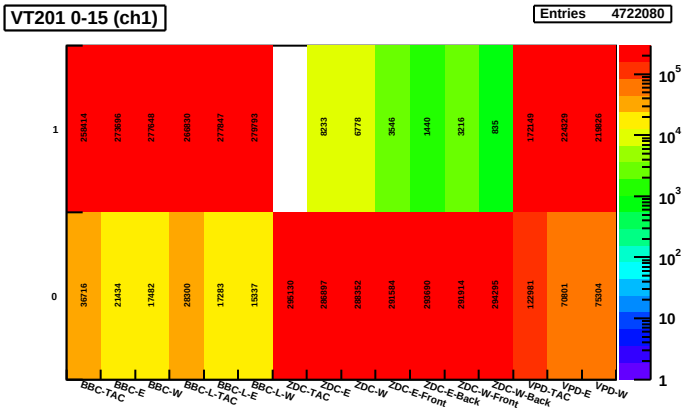
TF201 0-15 (ch0)



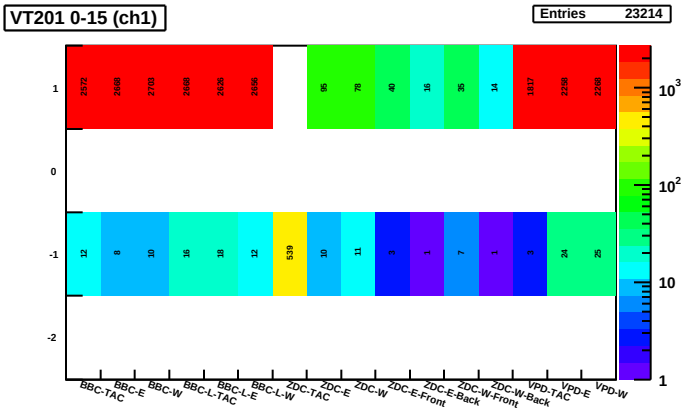
TF201 0-15 (ch0)



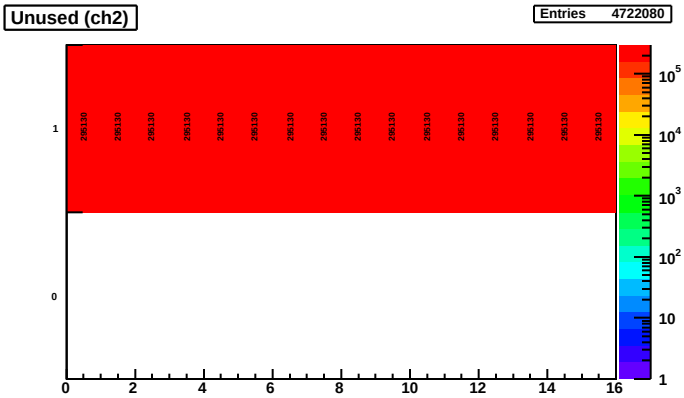
VT201 0-15 (ch1)

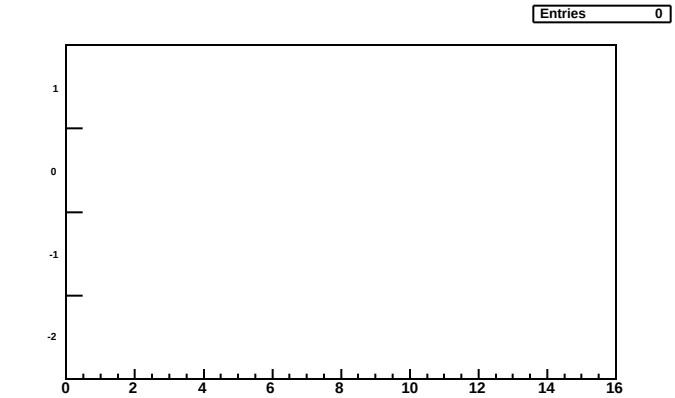


VT201 0-15 (ch1)

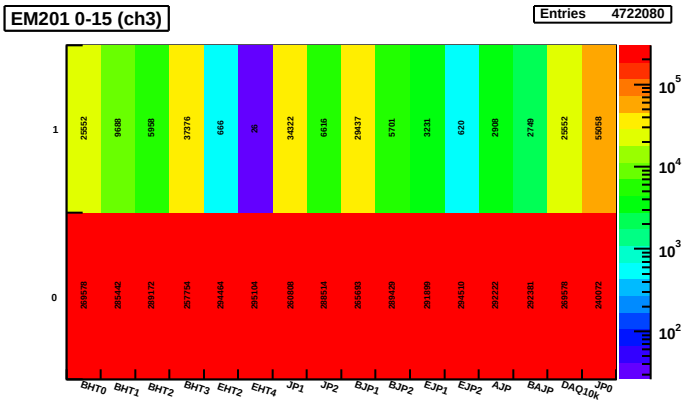


Unused (ch2)

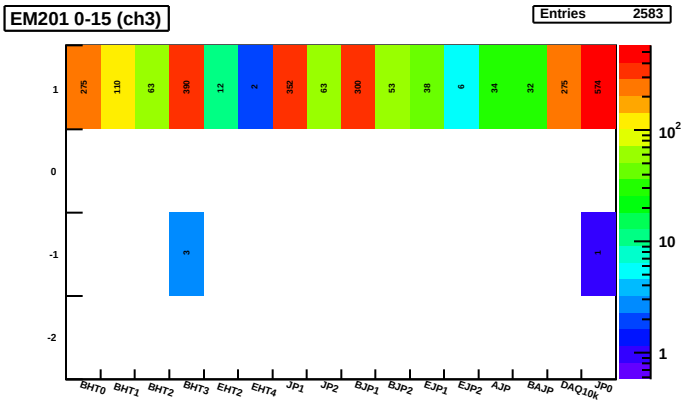




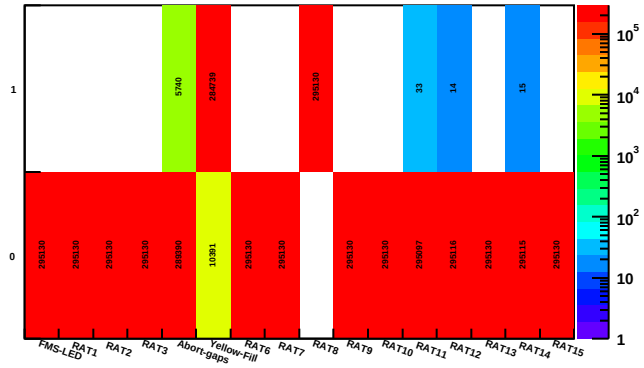
EM201 0-15 (ch3)

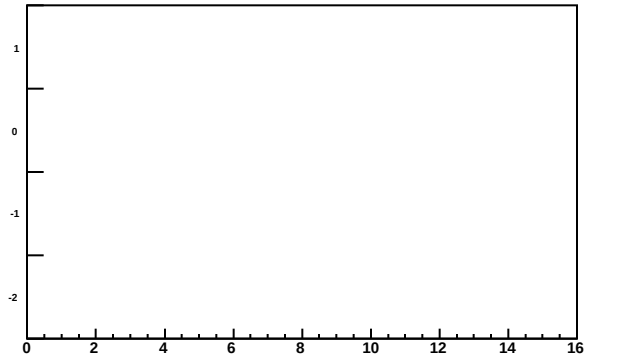


EM201 0-15 (ch3)

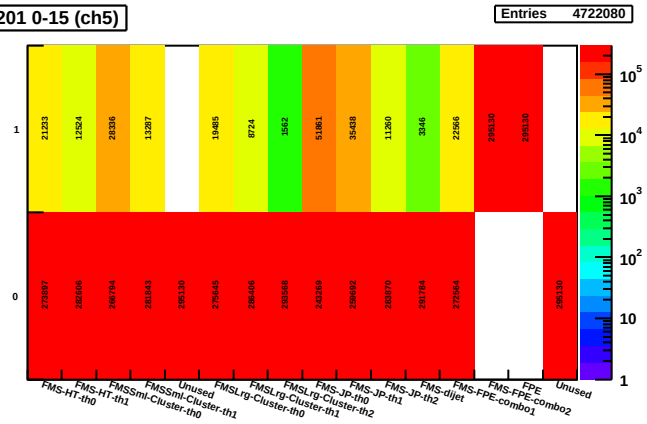


RAT board (ch4)





FP201 0-15 (ch5)



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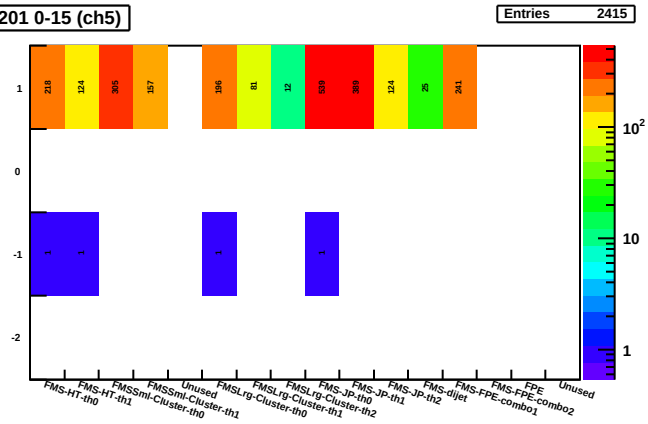
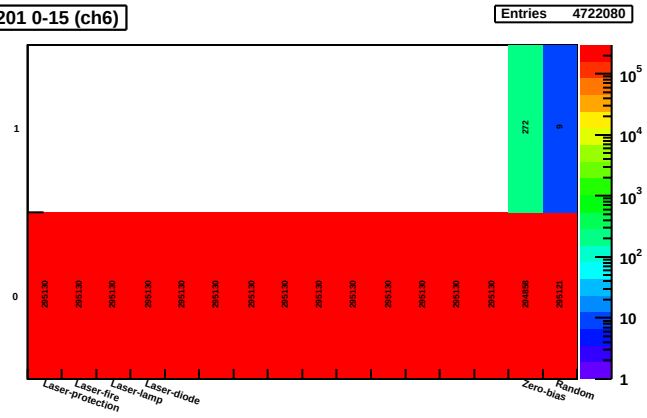
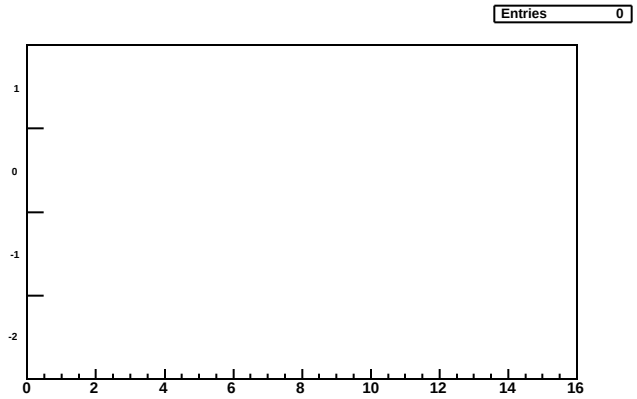
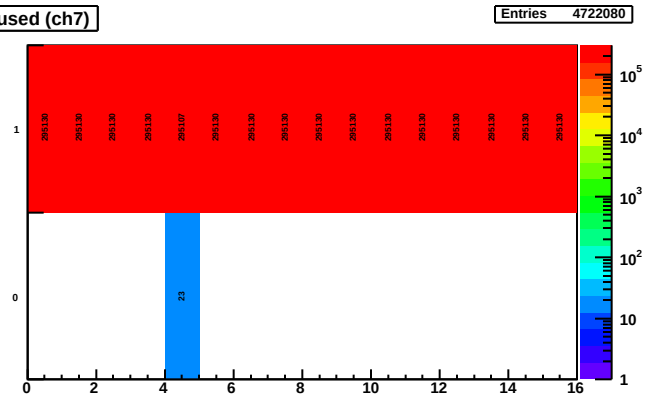
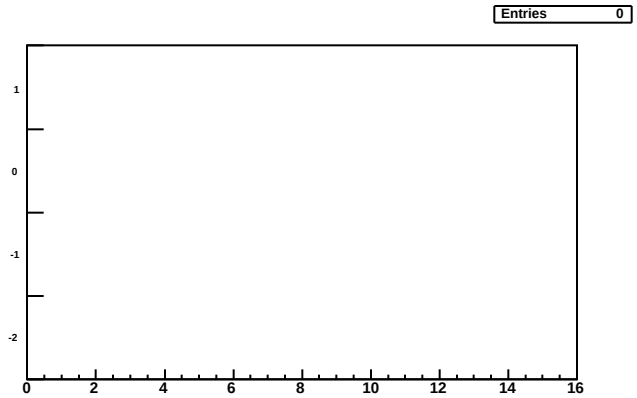


ST201 0-15 (ch6)





Unused (ch7)