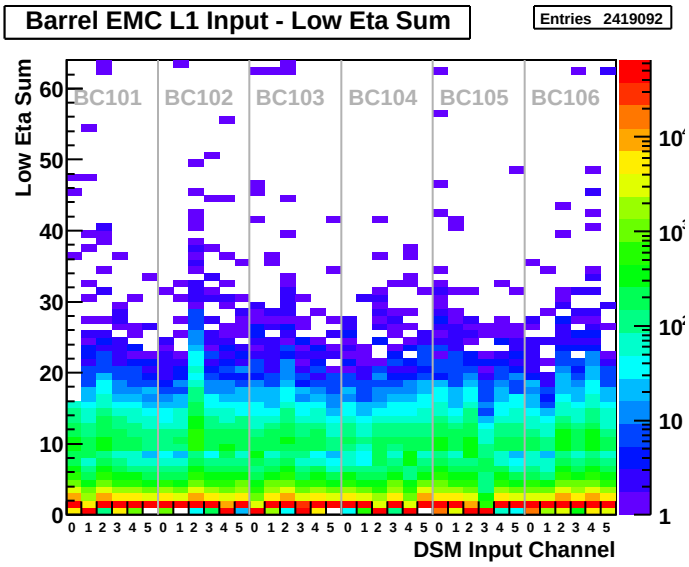
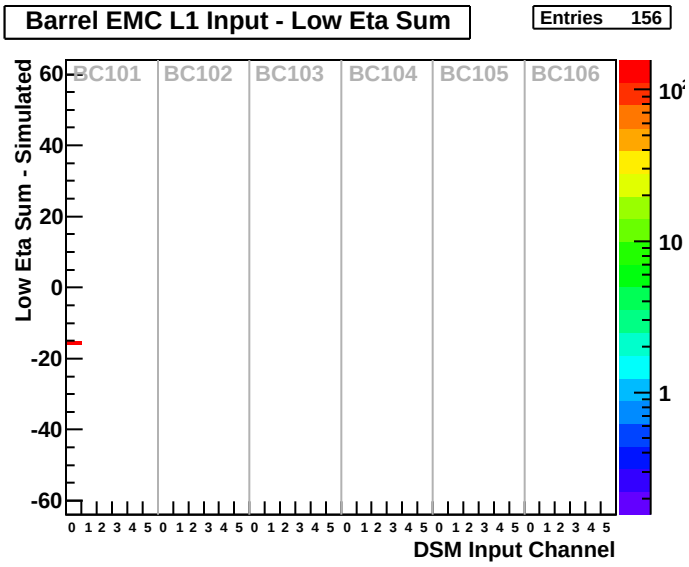
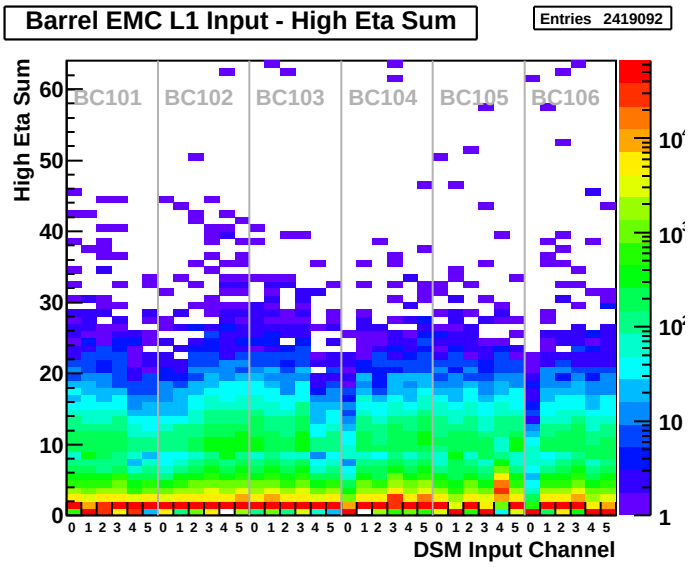
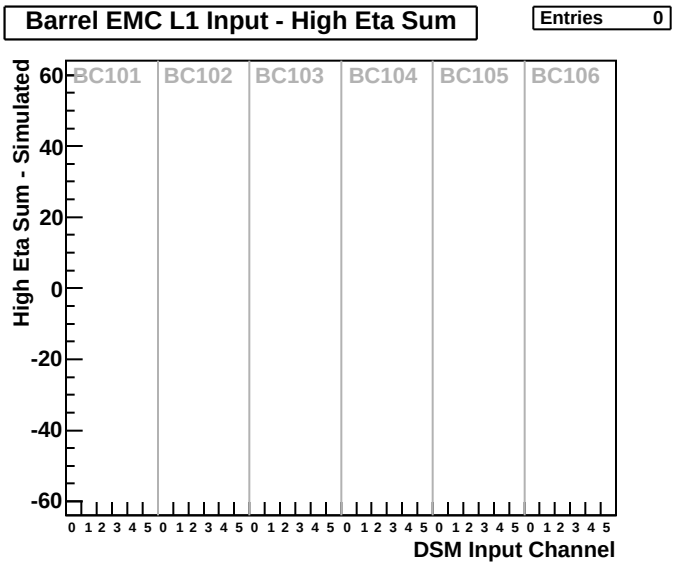
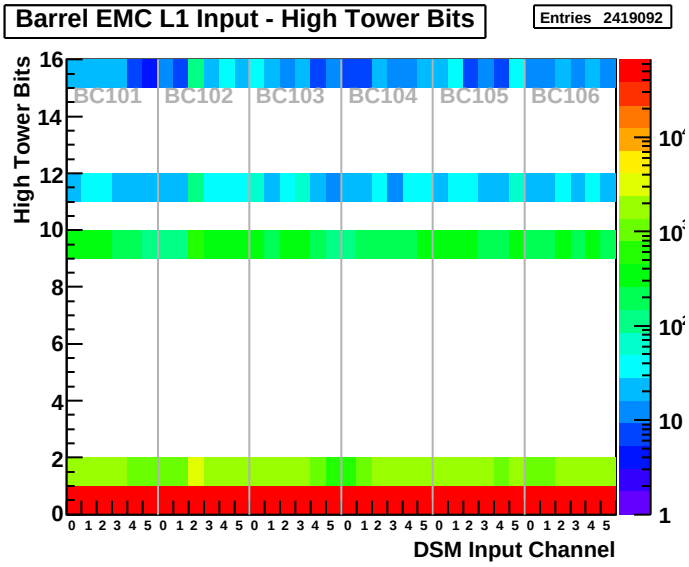
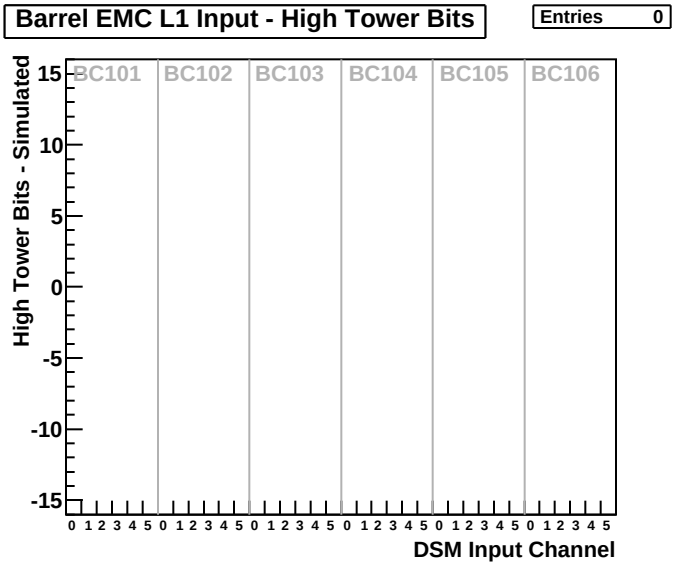

Barrel EMC L1 Input - Low Eta Sum
Entries 2419092
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

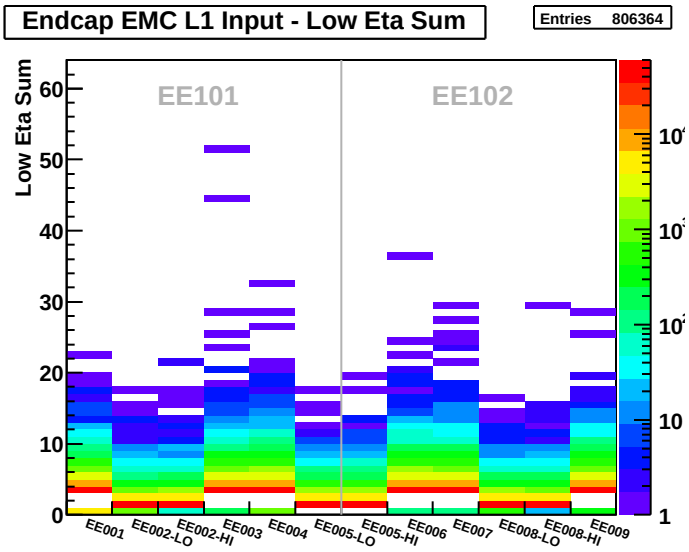

Barrel EMC L1 Input - Low Eta Sum
Entries 156
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

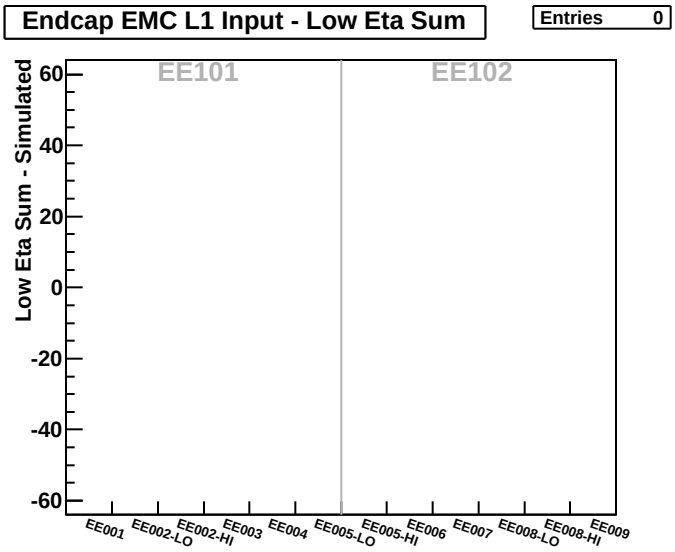

Barrel EMC L1 Input - High Eta Sum
Entries 2419092
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

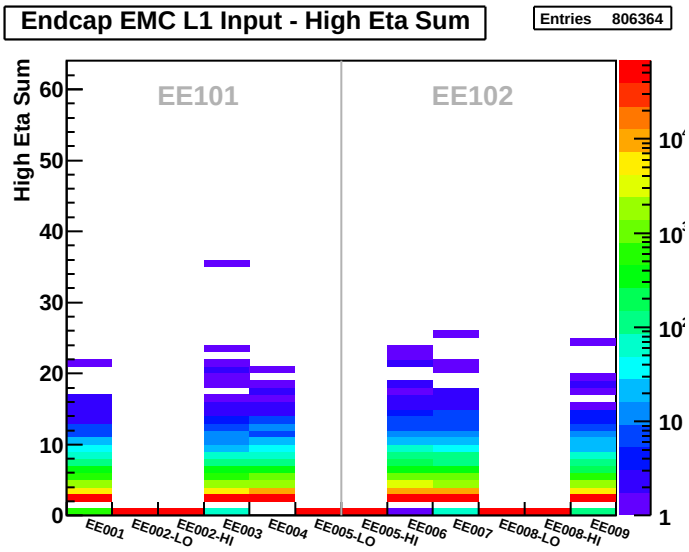

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

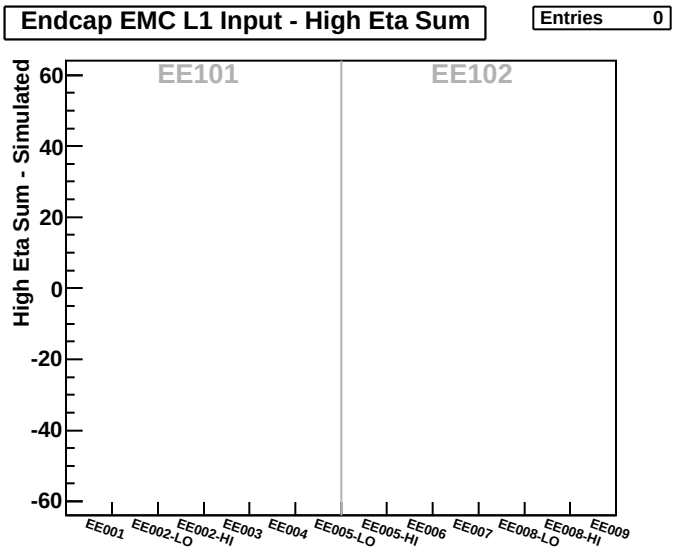

Barrel EMC L1 Input - High Tower Bits
Entries 2419092
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

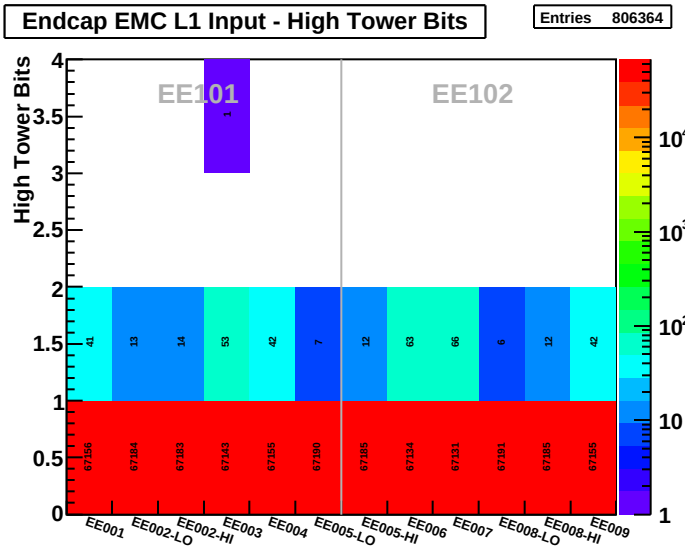

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

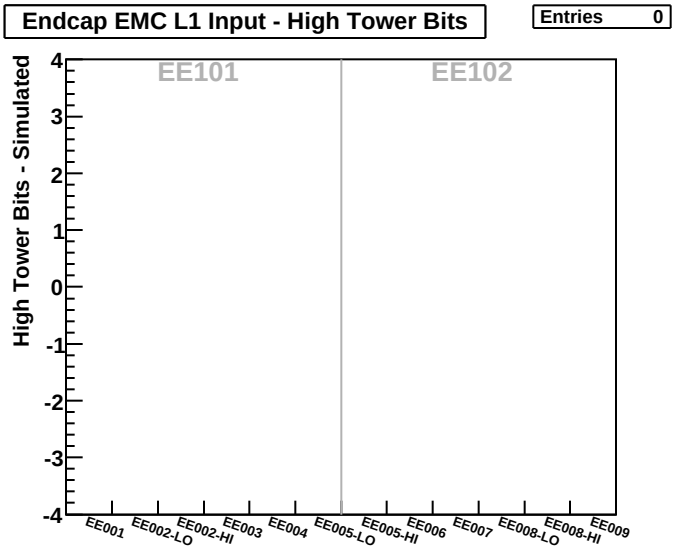

Endcap EMC L1 Input - Low Eta Sum
Entries 806364
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

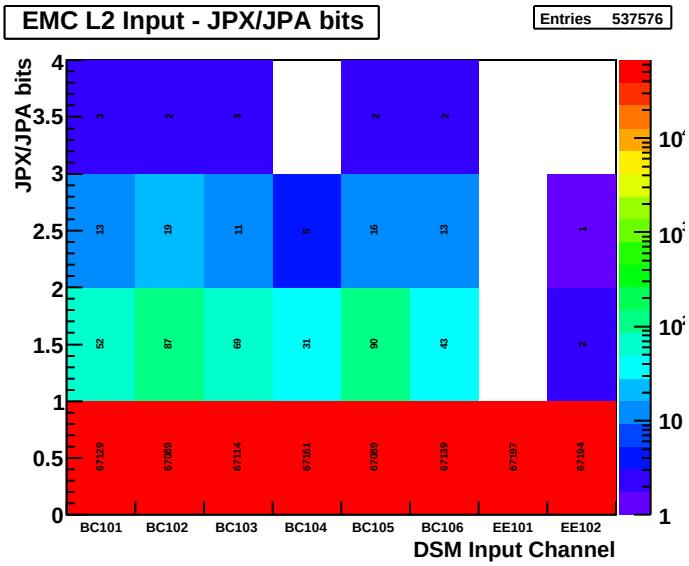

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

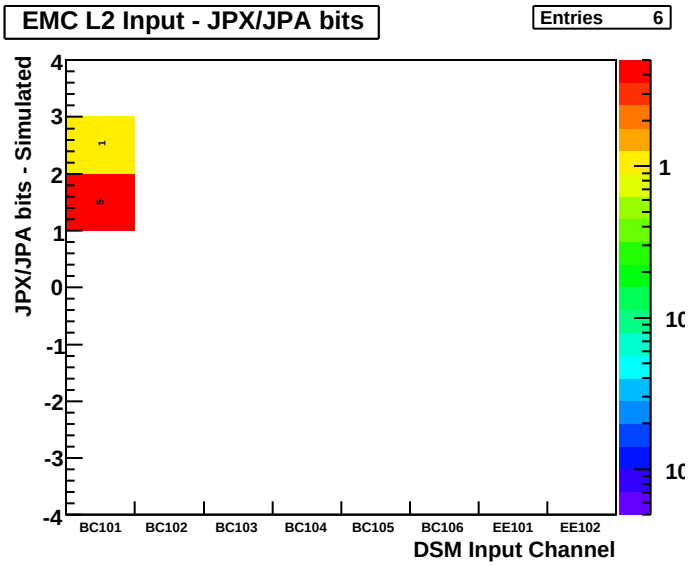

Endcap EMC L1 Input - High Eta Sum
Entries 806364
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

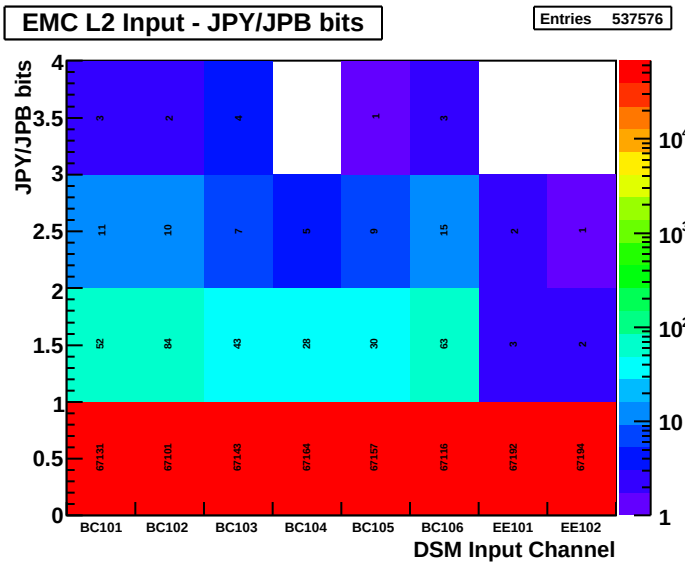

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

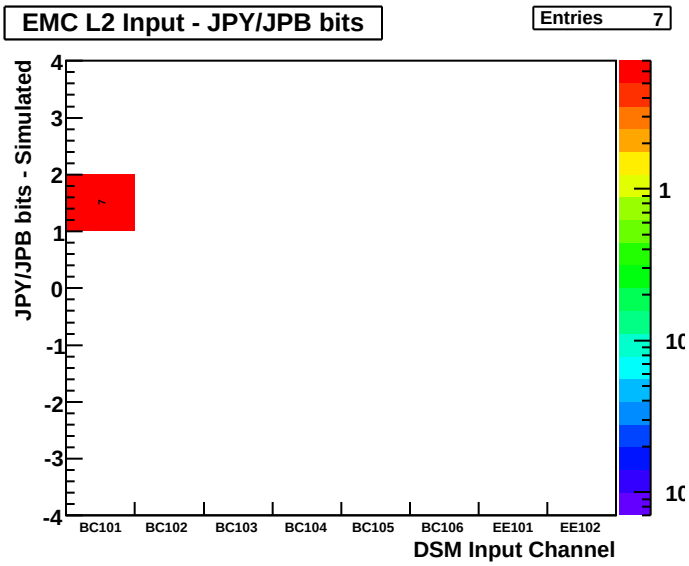

Endcap EMC L1 Input - High Tower Bits
Entries 806364
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

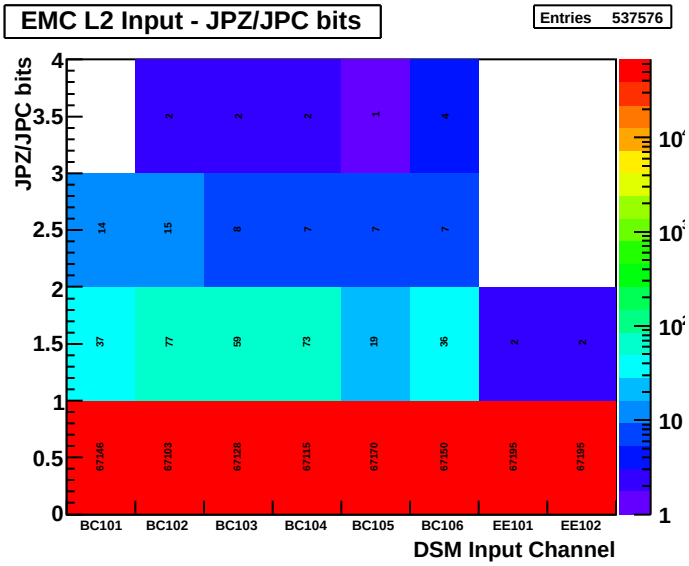

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

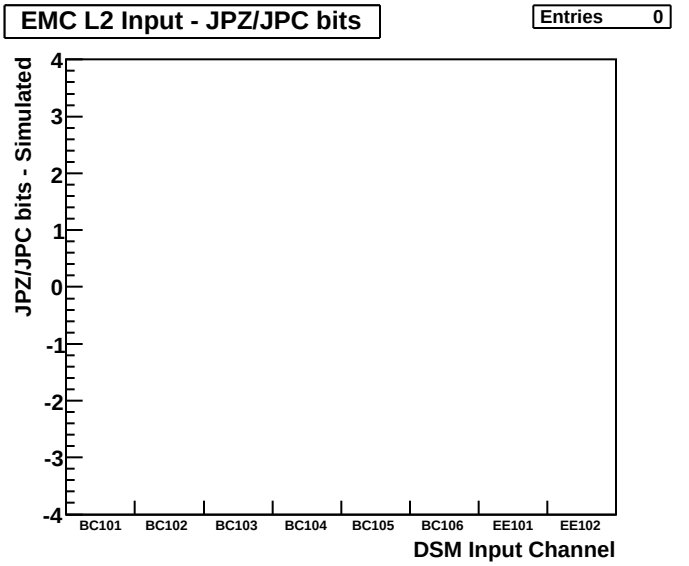

EMC L2 Input - JPX/JPA bits
Entries 537576
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

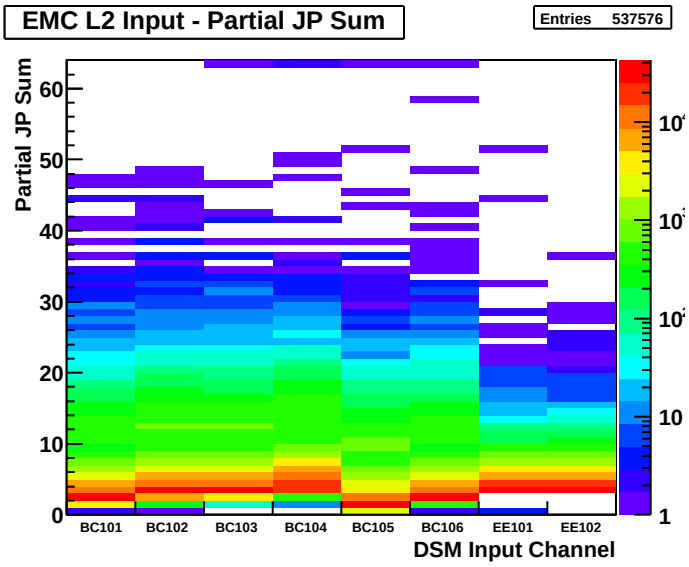

EMC L2 Input - JPX/JPA bits
Entries 6
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

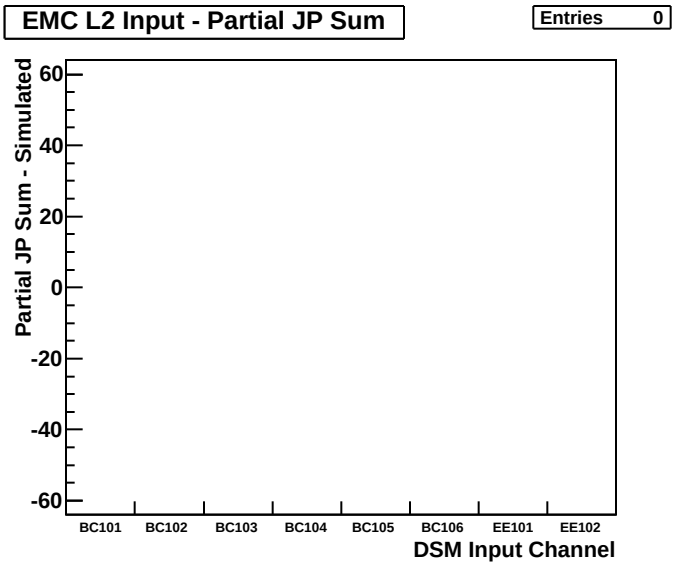

EMC L2 Input - JPY/JPB bits
Entries 537576
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

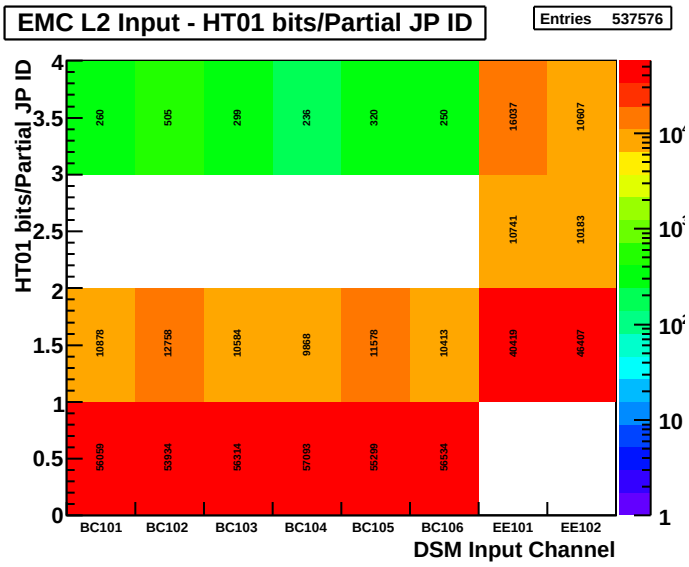

EMC L2 Input - JPY/JPB bits
Entries 7
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

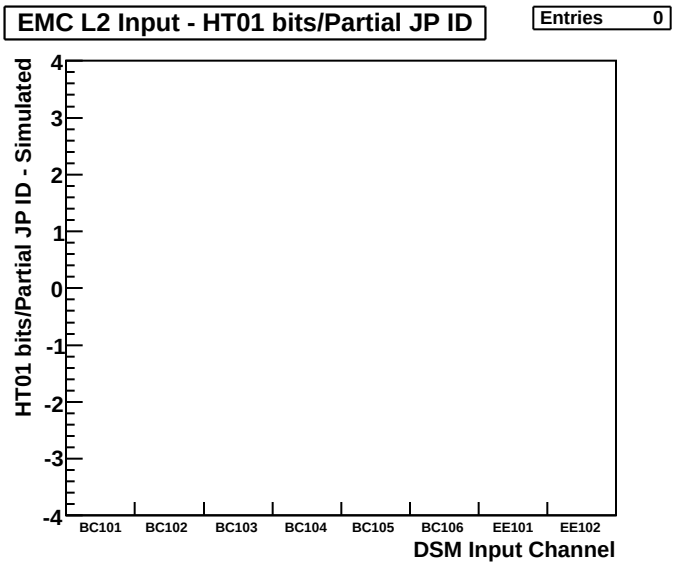

EMC L2 Input - JPZ/JPC bits
Entries 537576
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

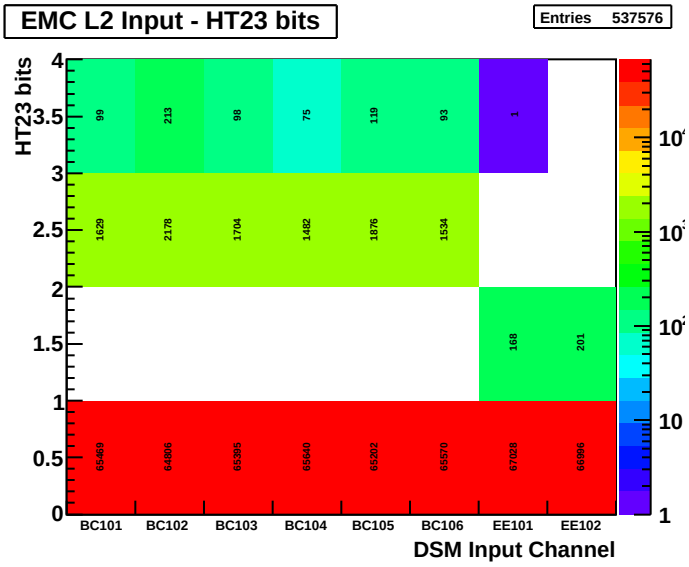

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

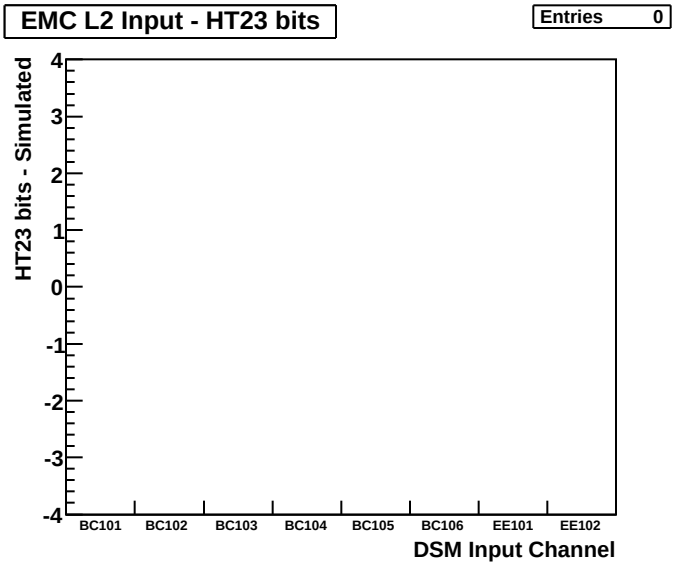

EMC L2 Input - Partial JP Sum
Entries 537576
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

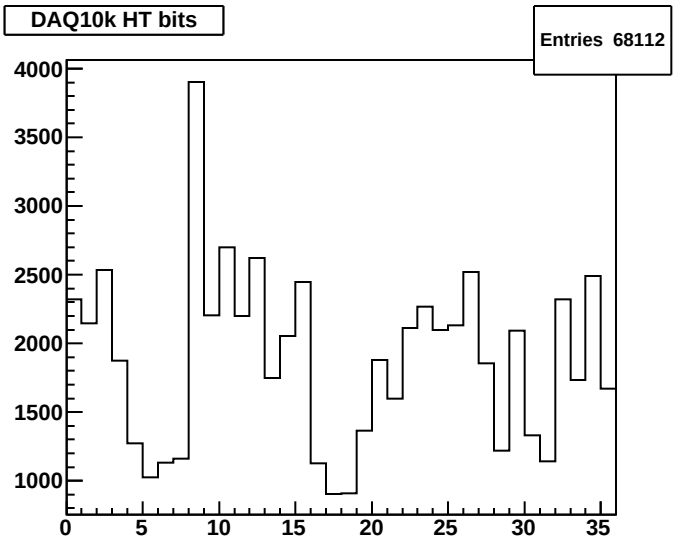

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

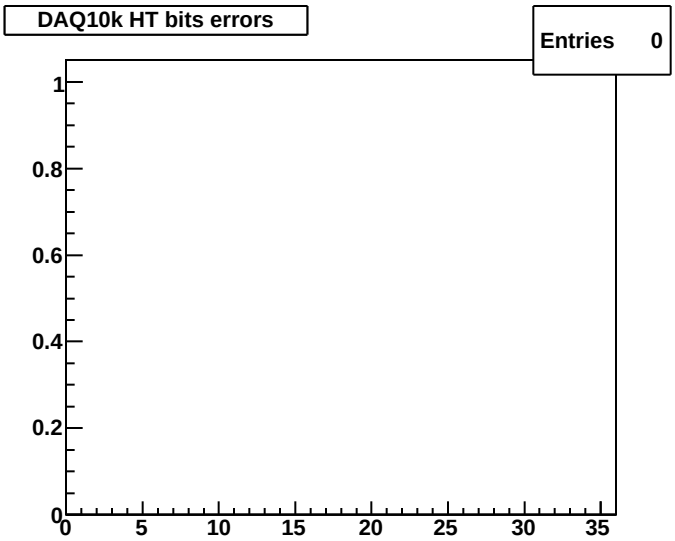

EMC L2 Input - HT01 bits/Partial JP ID
Entries 537576
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

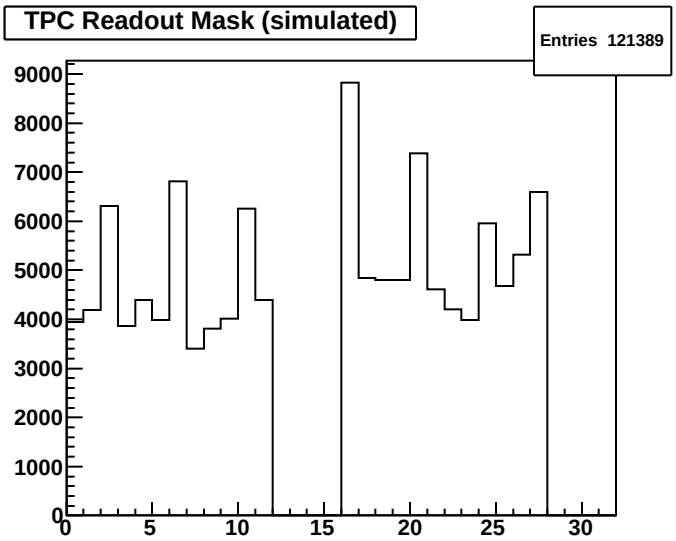

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

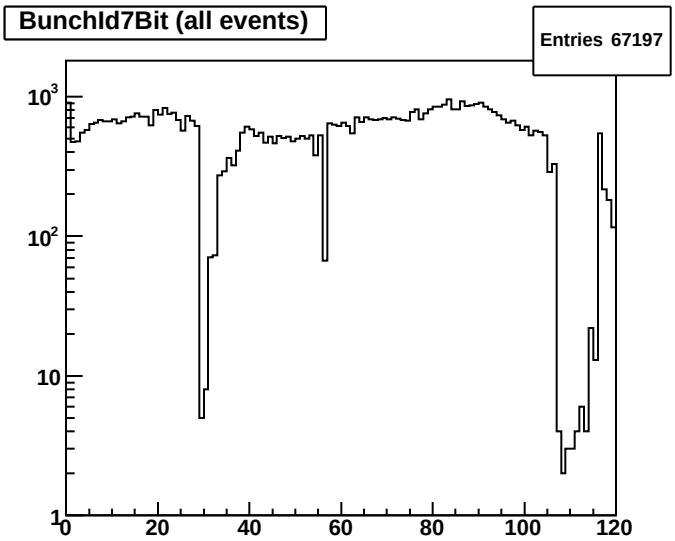

EMC L2 Input - HT23 bits
Entries 537576
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

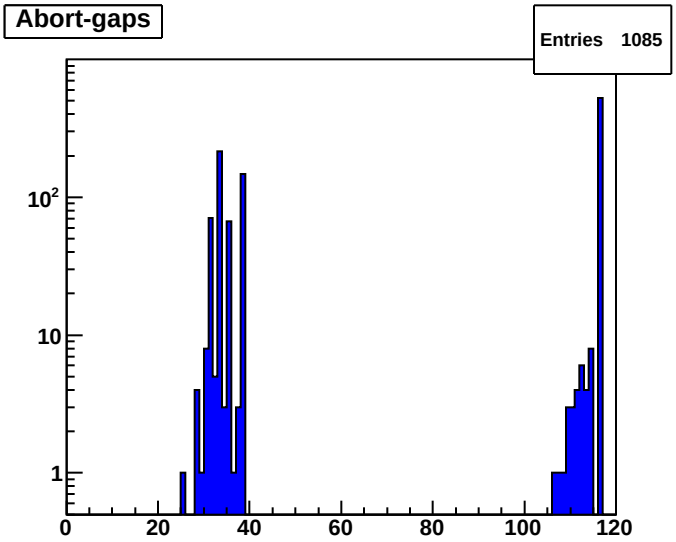

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

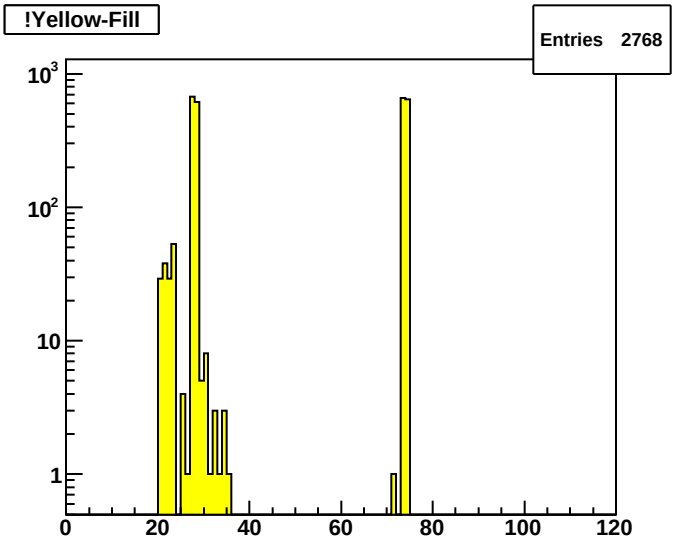

DAQ10k HT bits
Entries 68112

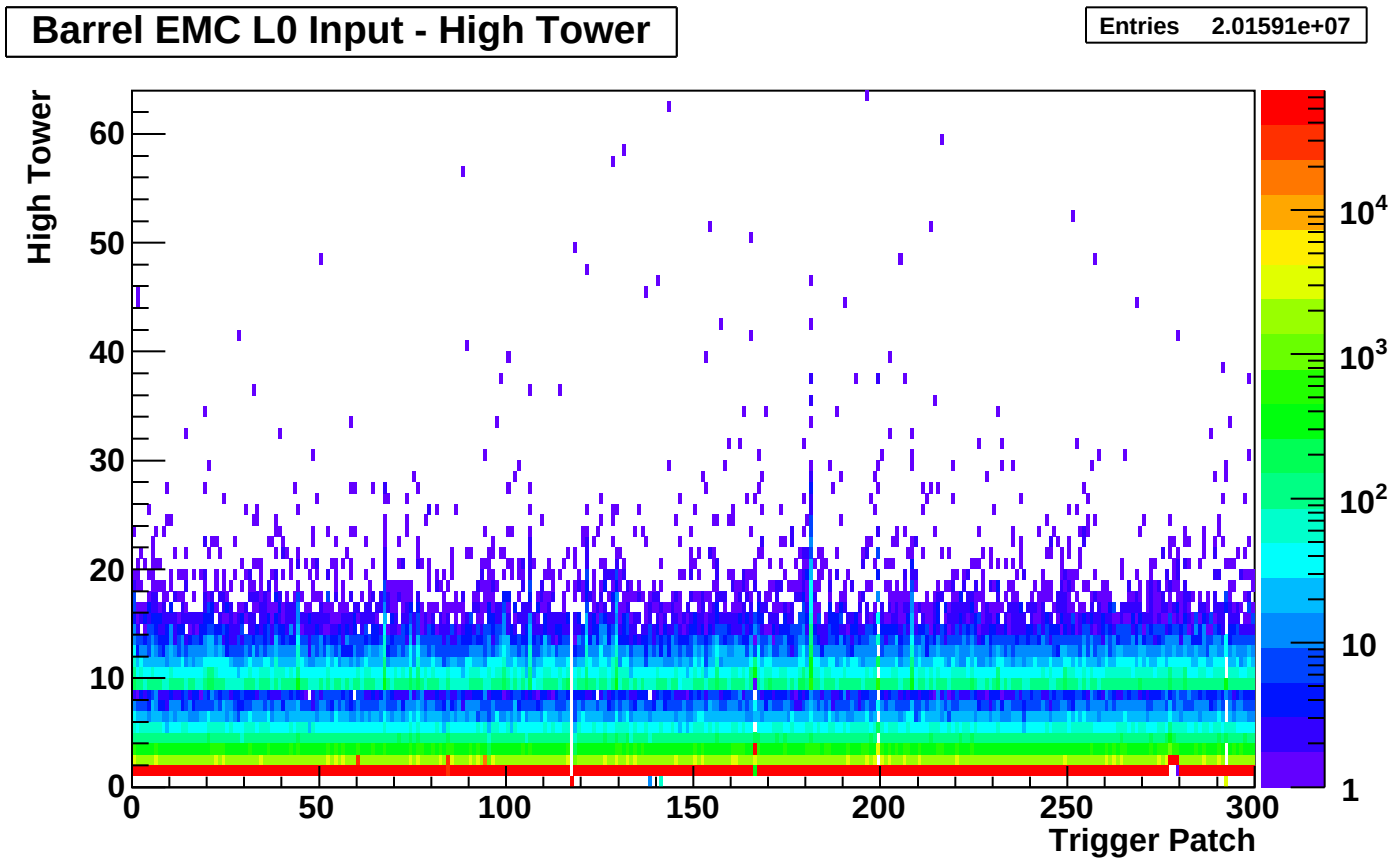

DAQ10k HT bits errors
Entries 0

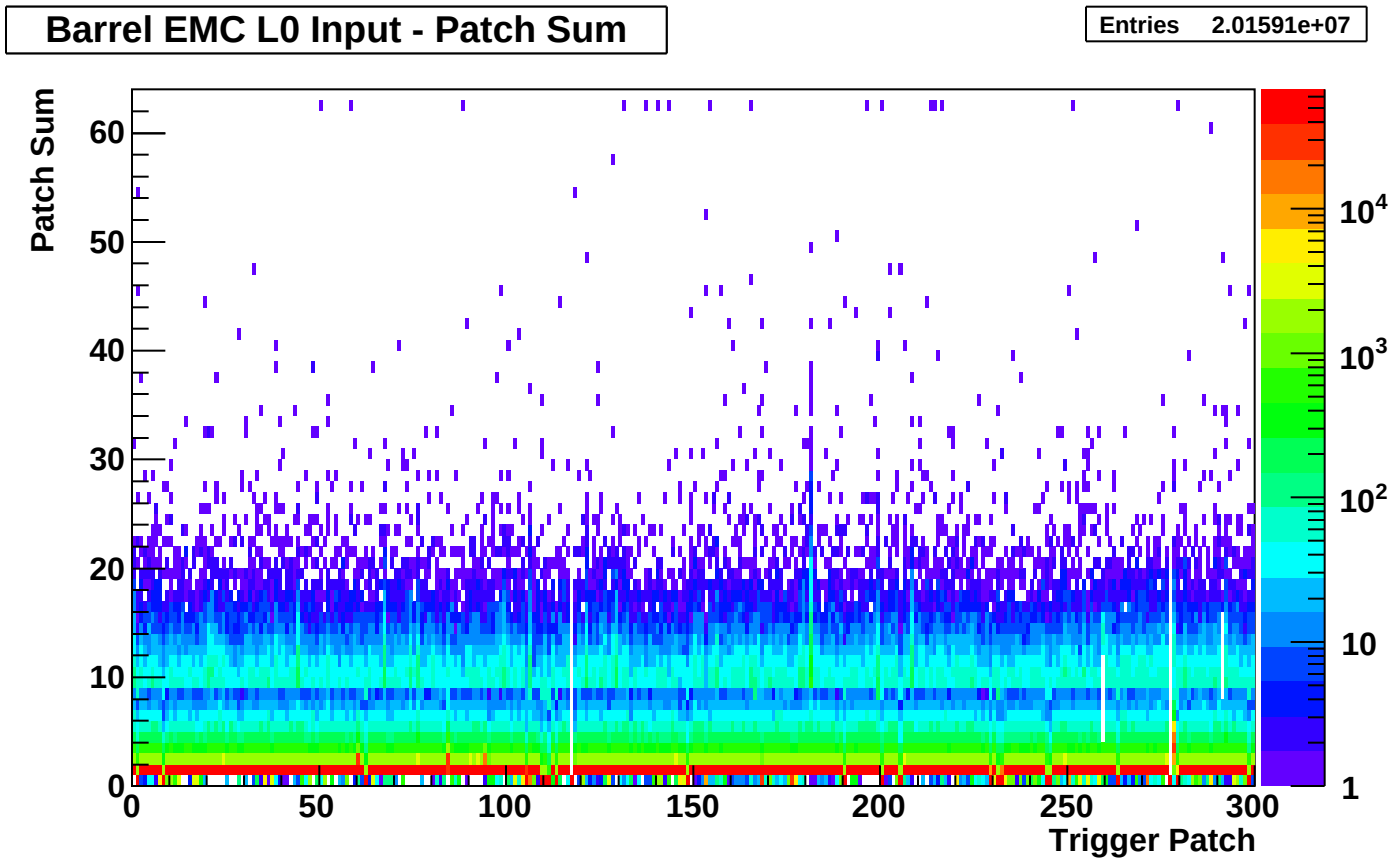

TPC Readout Mask (simulated)
Entries 121389

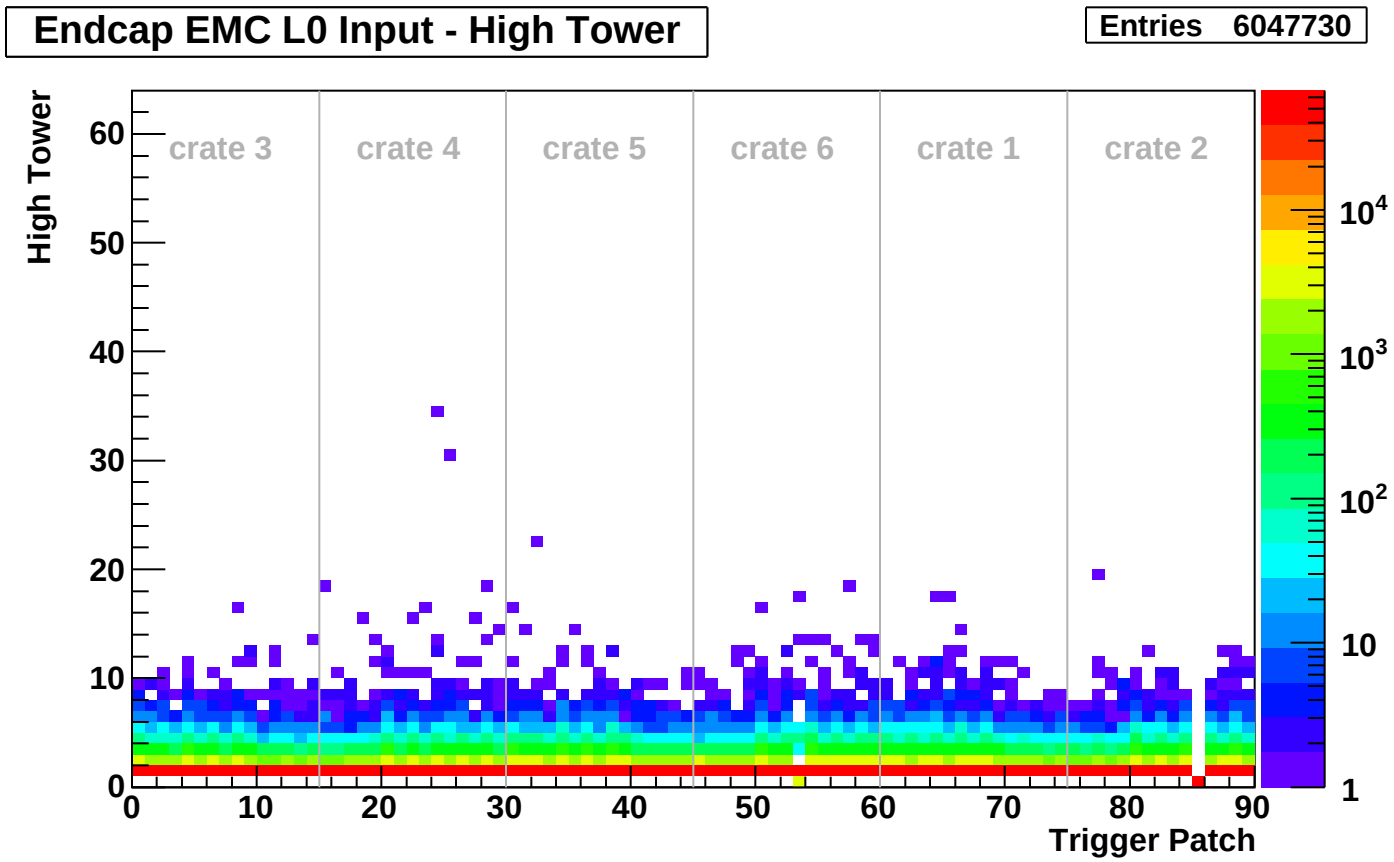

BunchId7Bit (all events)
Entries 67197

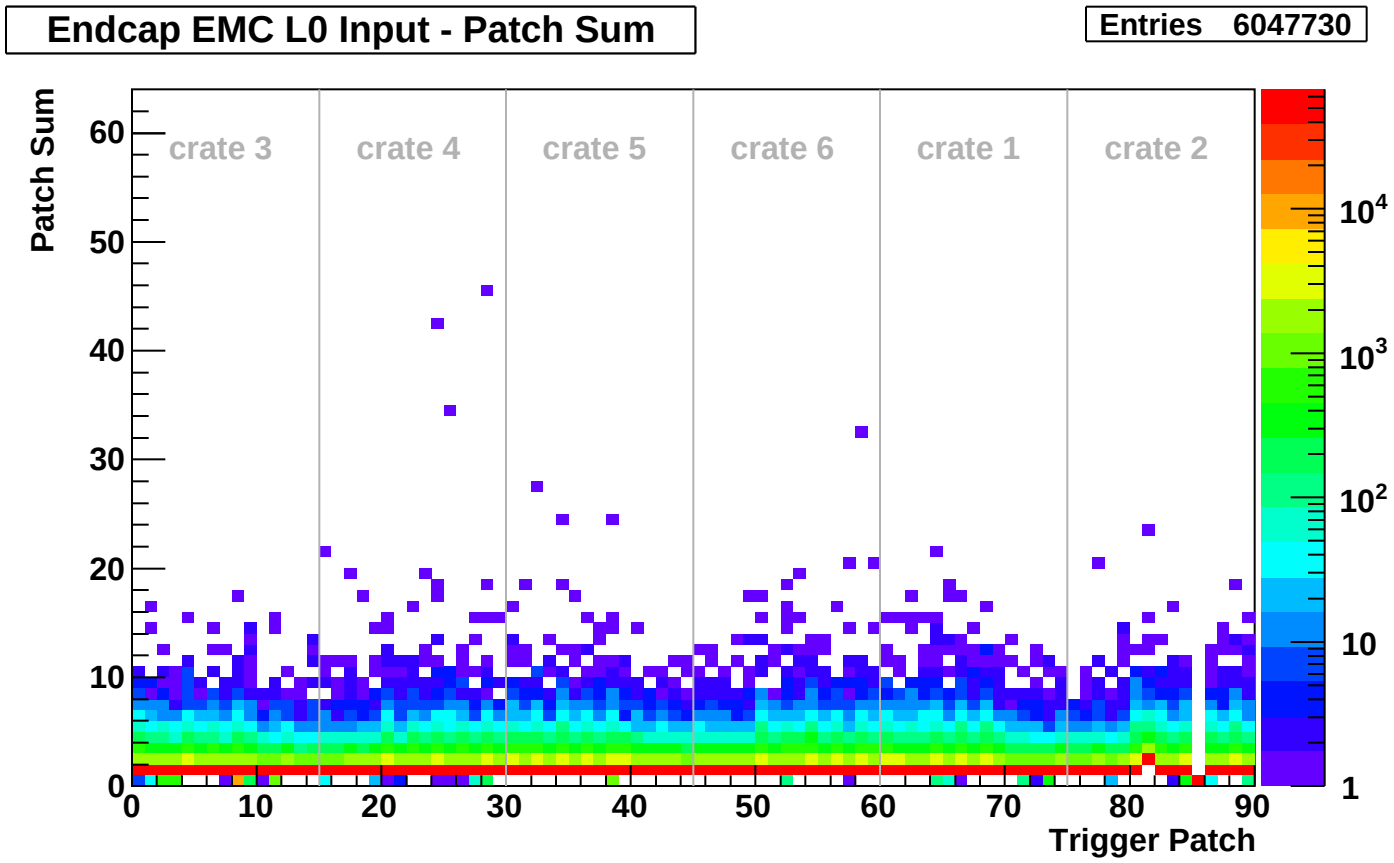

Abort-gaps
Entries 1085

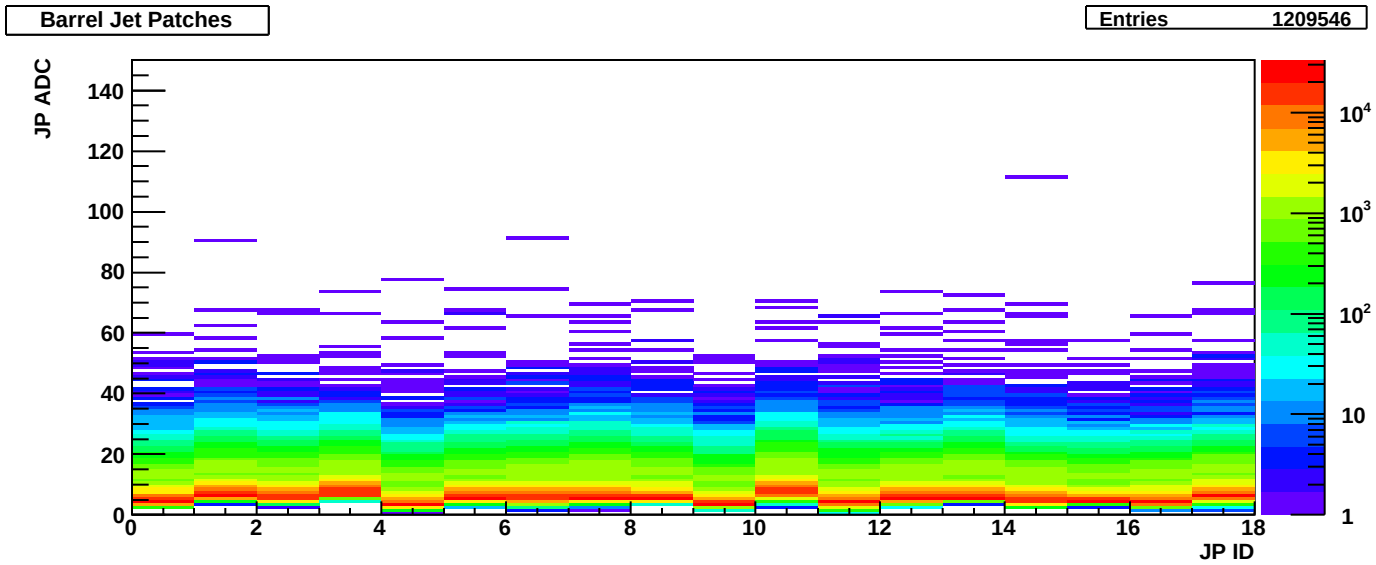

!Yellow-Fill
Entries 2768

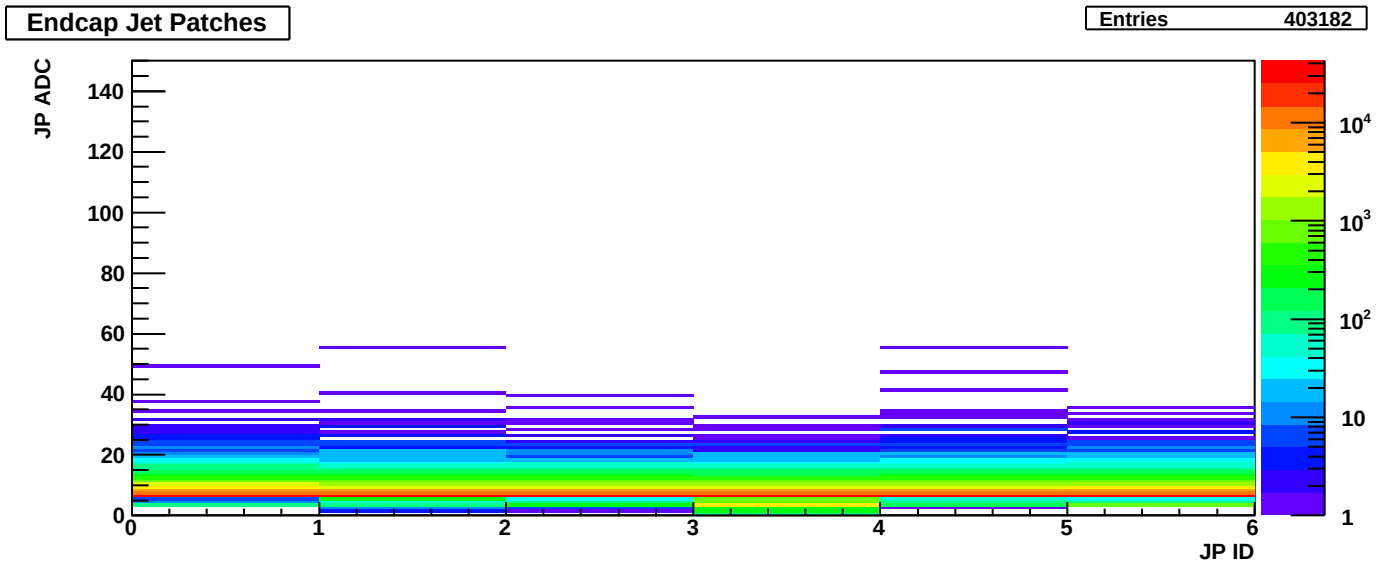

Barrel EMC L0 Input - High Tower
Entries 2.01591e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

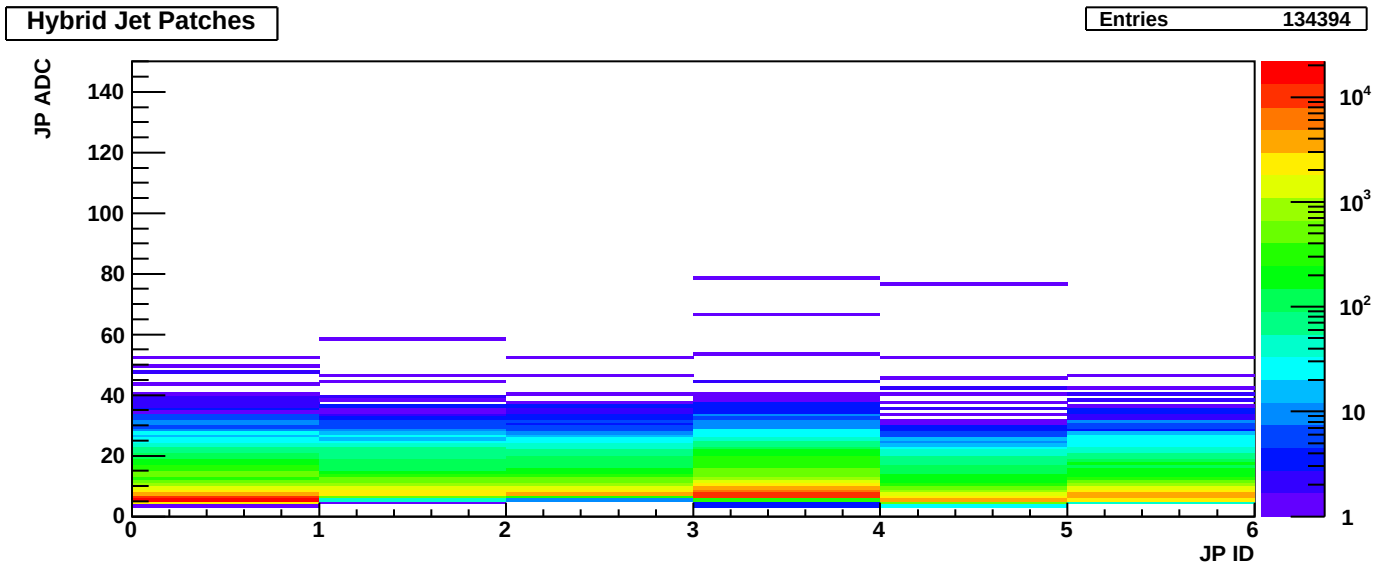

Barrel EMC L0 Input - Patch Sum
Entries 2.01591e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

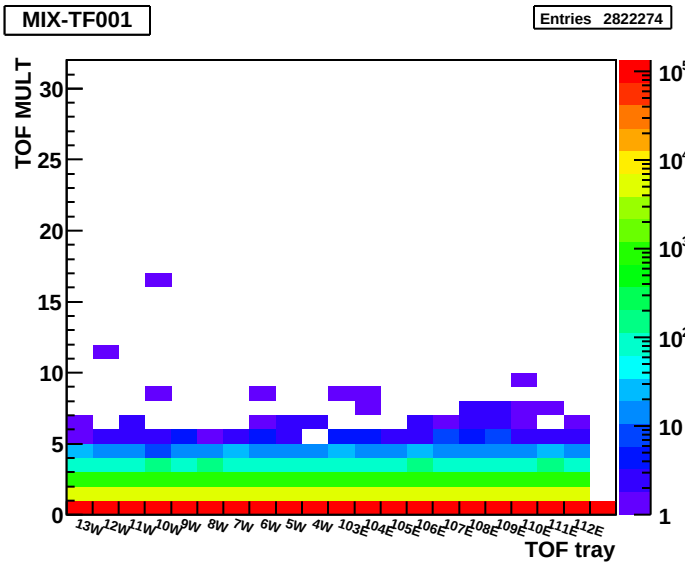

Endcap EMC L0 Input - High Tower
Entries 6047730
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

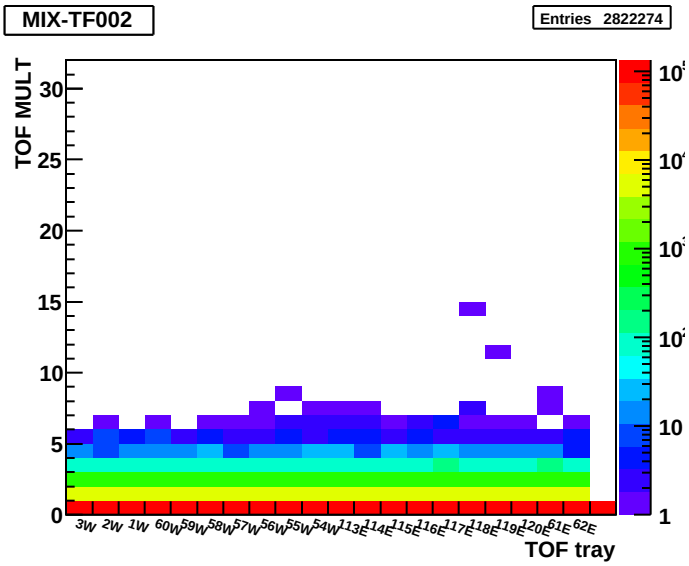

Endcap EMC L0 Input - Patch Sum
Entries 6047730
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

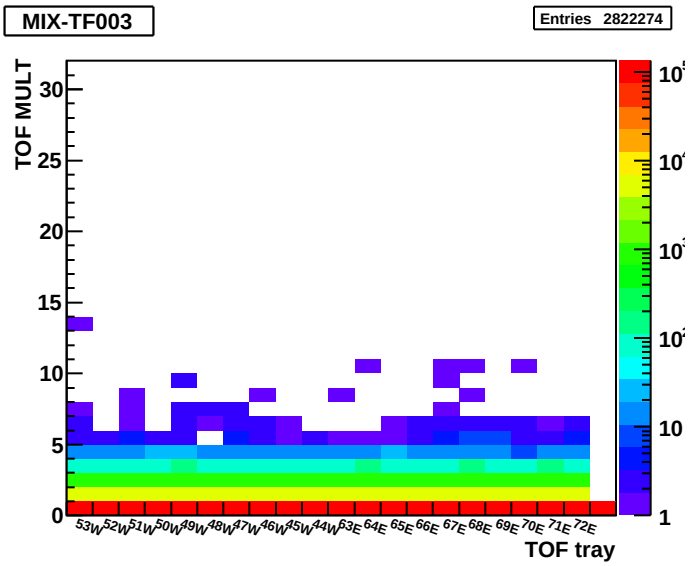

Barrel Jet Patches
Entries 1209546
JP ADC
JP ID

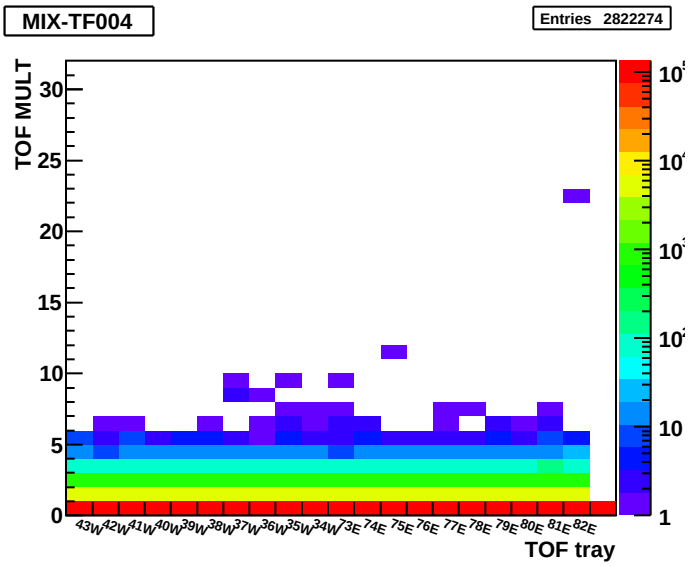

Endcap Jet Patches
Entries 403182
JP ADC
JP ID

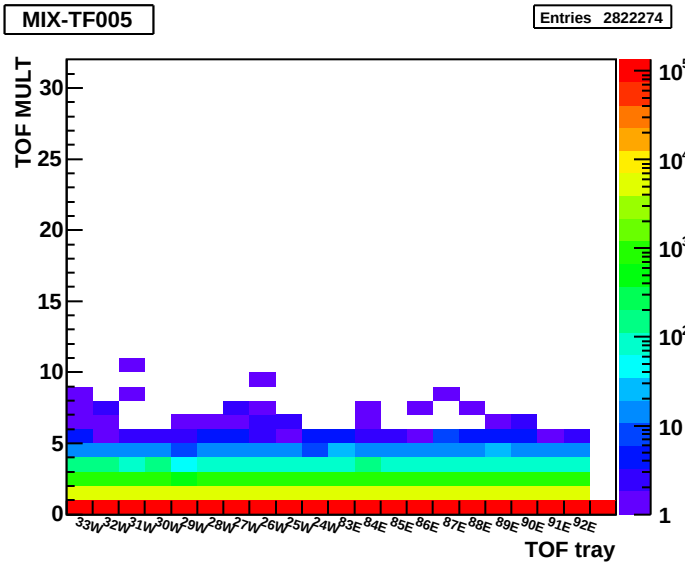

Hybrid Jet Patches
Entries 134394
JP ADC
JP ID

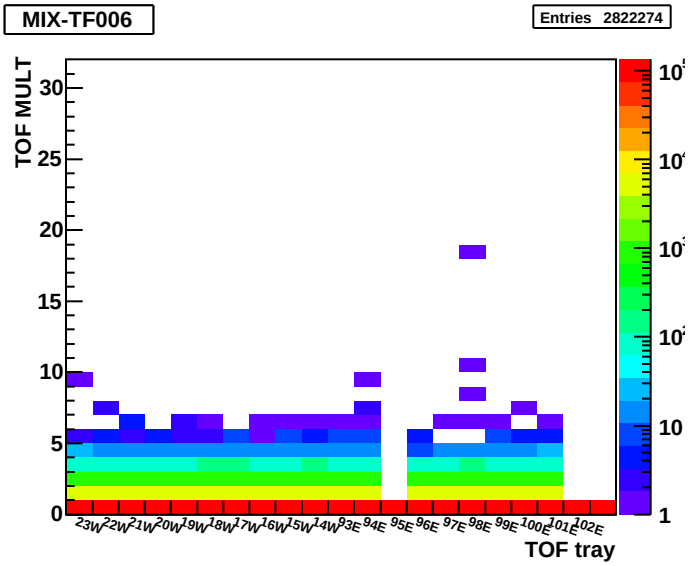

MIX-TF001
Entries 2822274
TOF MULT
TOF tray

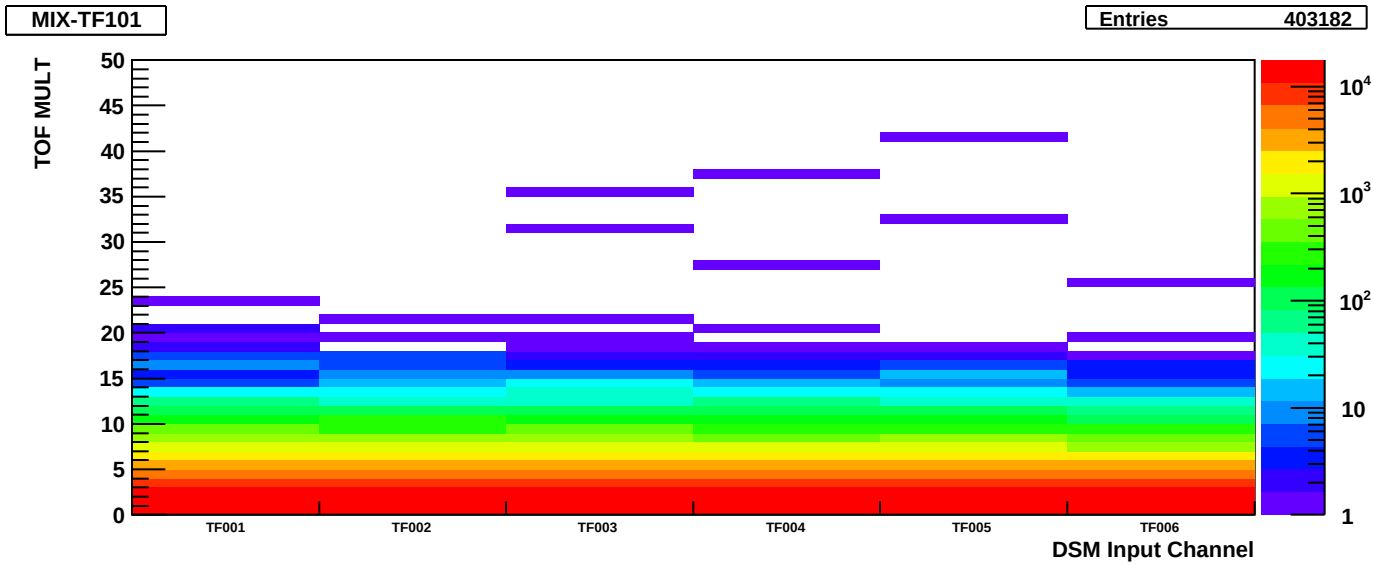

MIX-TF002
Entries 2822274
TOF MULT
TOF tray

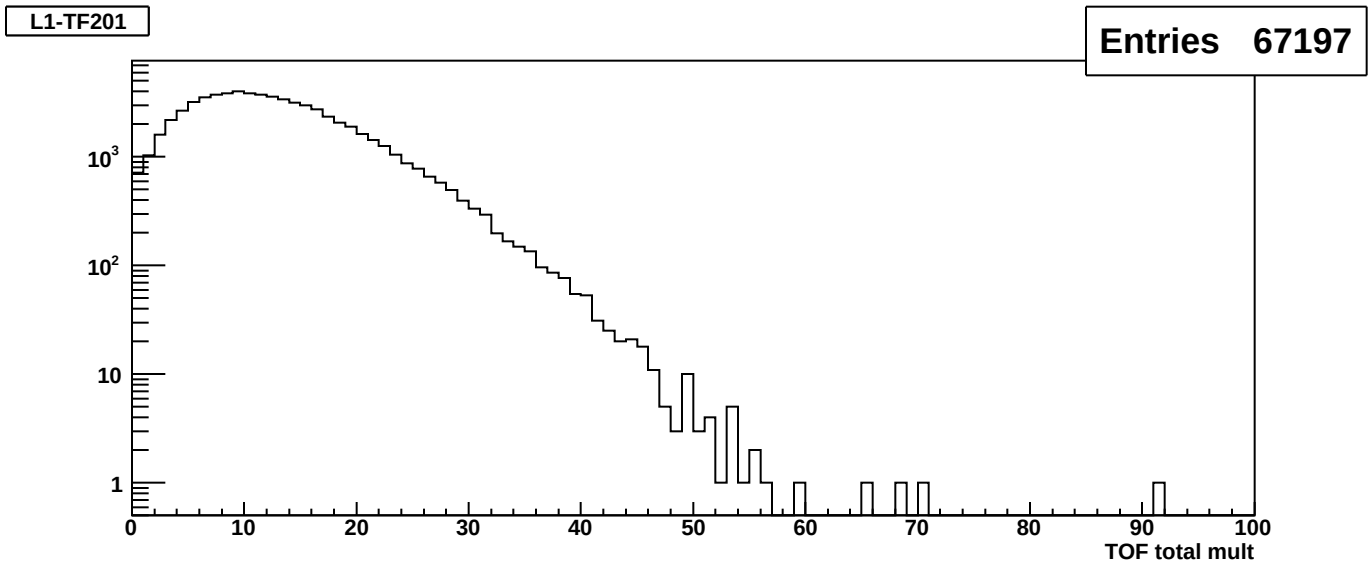

MIX-TF003
Entries 2822274
TOF MULT
TOF tray

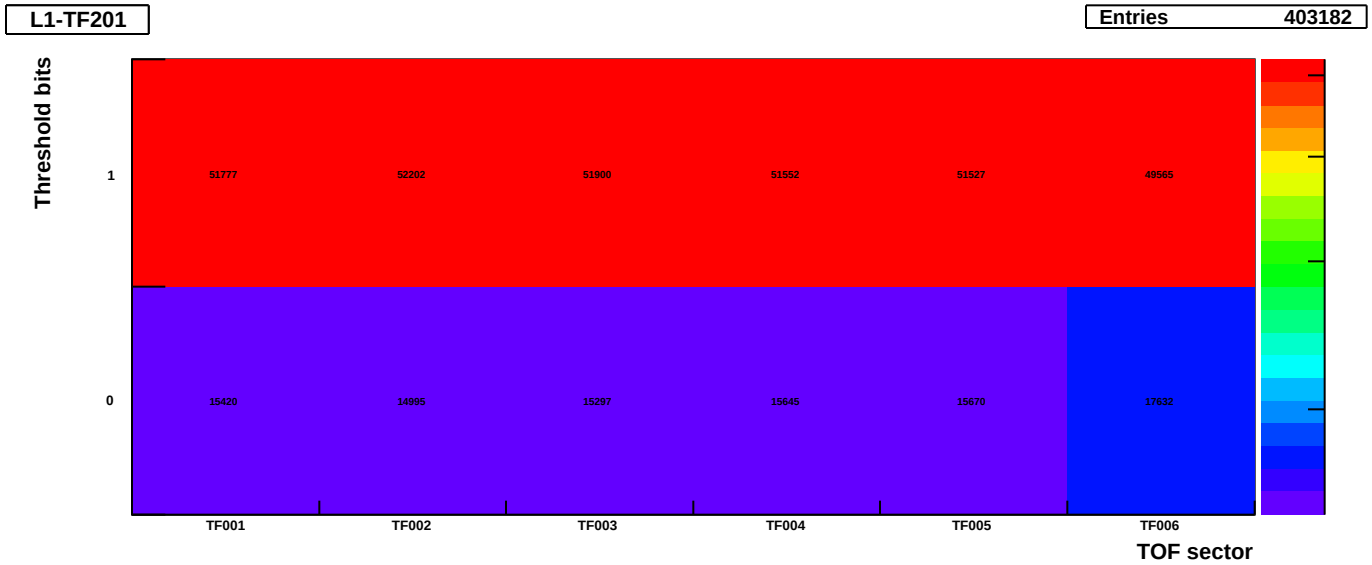

MIX-TF004
Entries 2822274
TOF MULT
TOF tray

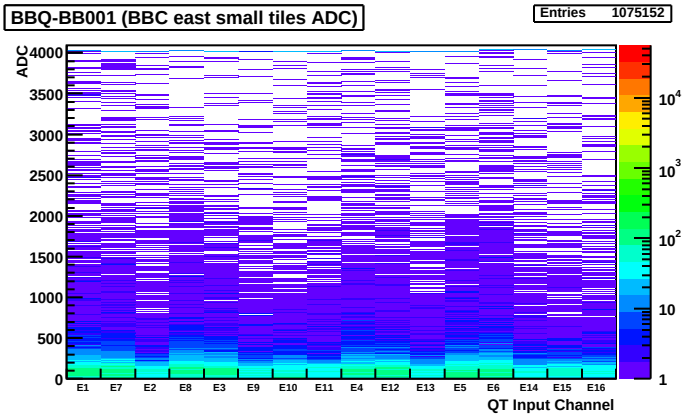

MIX-TF005
Entries 2822274
TOF MULT
TOF tray

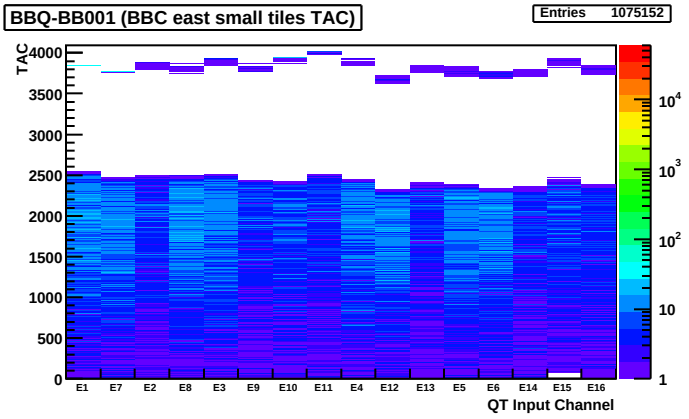

MIX-TF006
Entries 2822274
TOF MULT
TOF tray

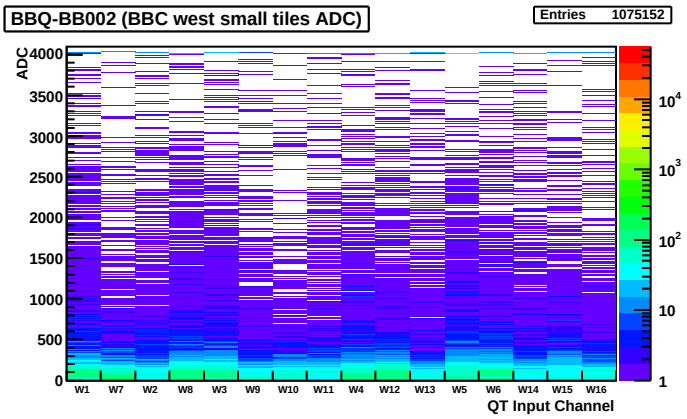

MIX-TF101
Entries 403182
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

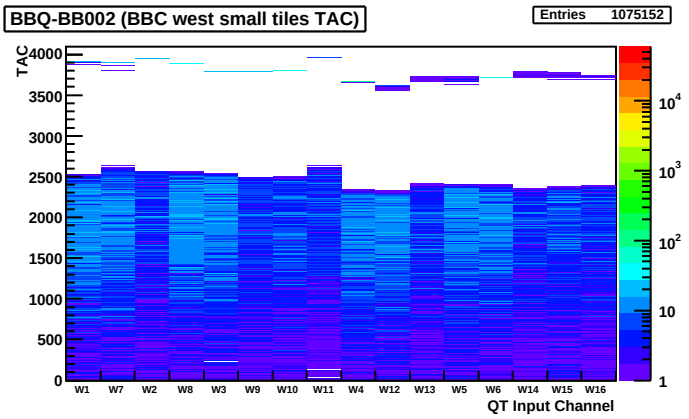

L1-TF201
Entries 67197
TOF total mult

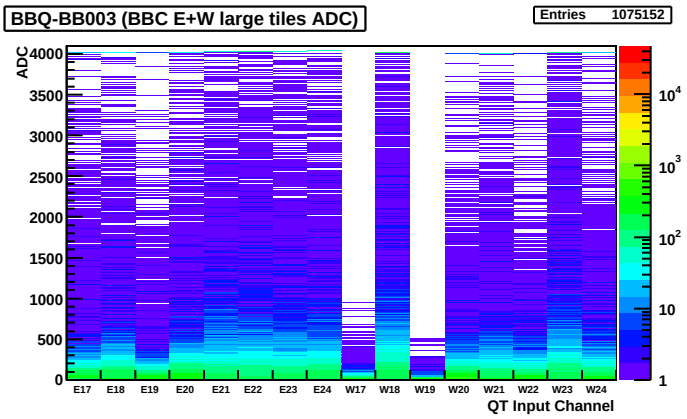

L1-TF201
Entries 403182
Threshold bits
51777
52202
51900
51552
51527
49565
15420
14995
15297
15645
15670
17632
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

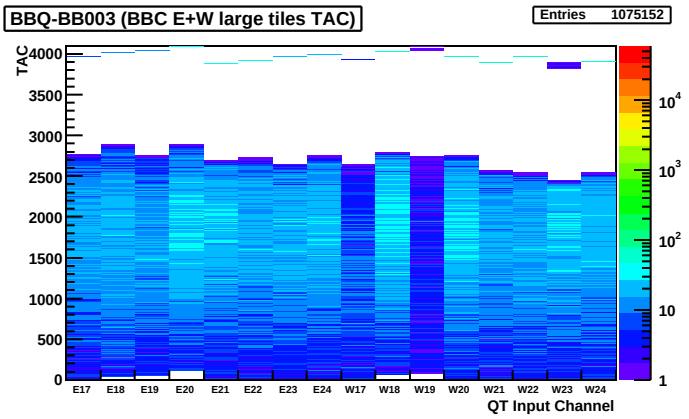

BBQ-BB001 (BBC east small tiles ADC)
Entries 1075152
ADC
QT Input Channel

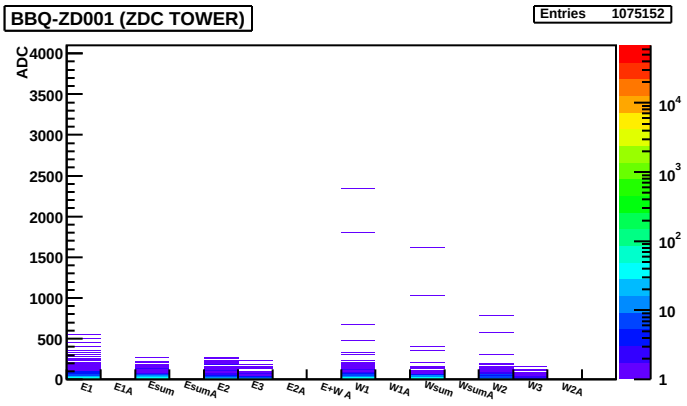

BBQ-BB001 (BBC east small tiles TAC)
Entries 1075152
TAC
QT Input Channel

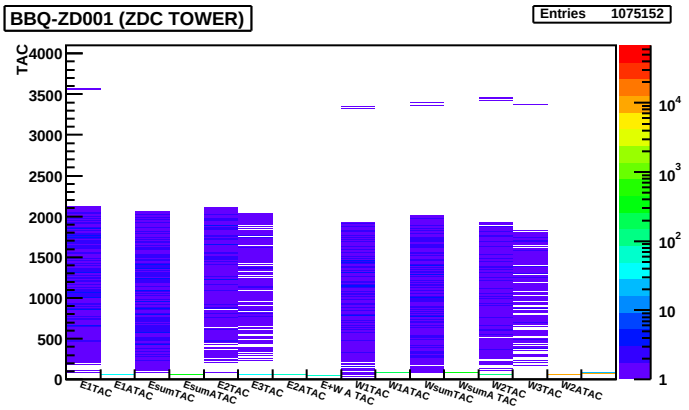

BBQ-BB002 (BBC west small tiles ADC)
Entries 1075152
ADC
QT Input Channel

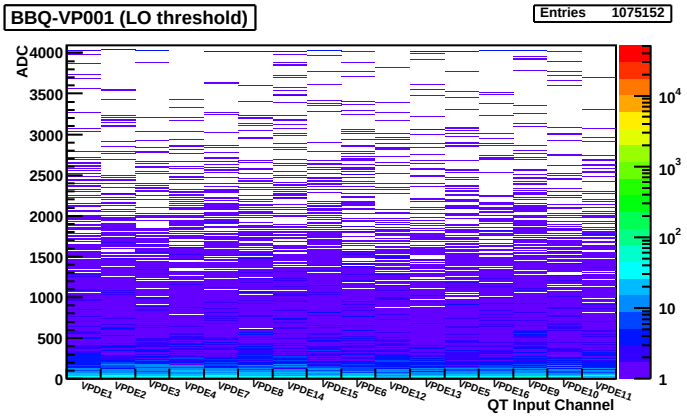

BBQ-BB002 (BBC west small tiles TAC)
Entries 1075152
TAC
QT Input Channel

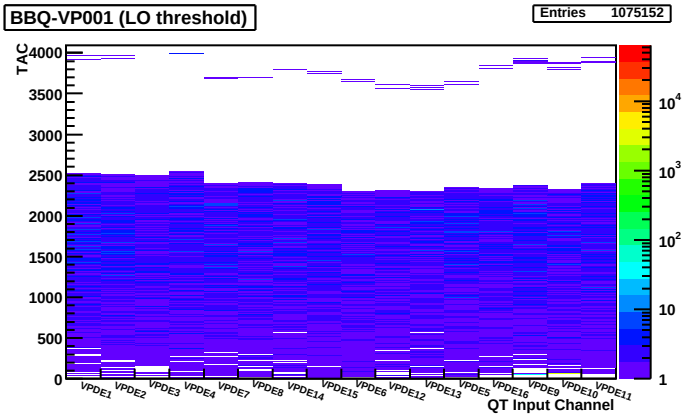

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1075152
ADC
QT Input Channel

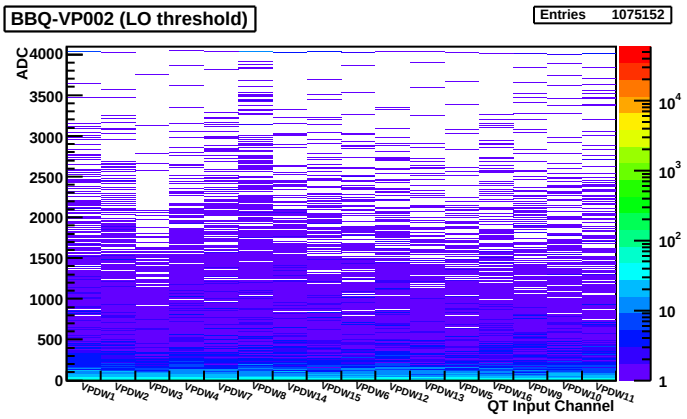

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1075152
TAC
QT Input Channel

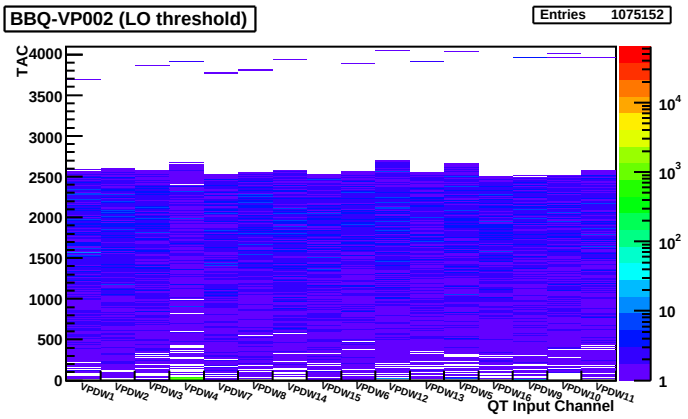

BBQ-ZD001 (ZDC TOWER)
Entries 1075152
ADC

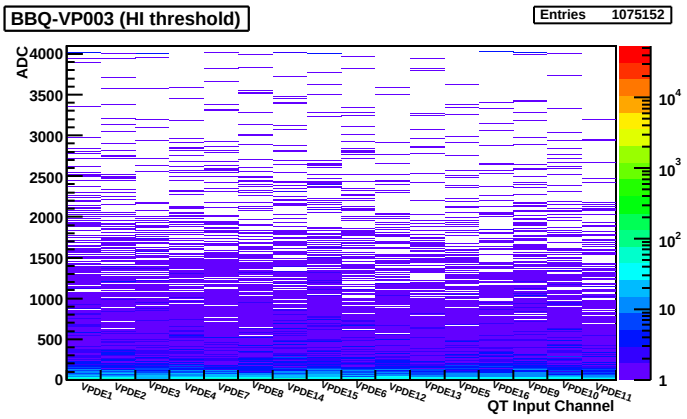

BBQ-ZD001 (ZDC TOWER)
Entries 1075152
TAC

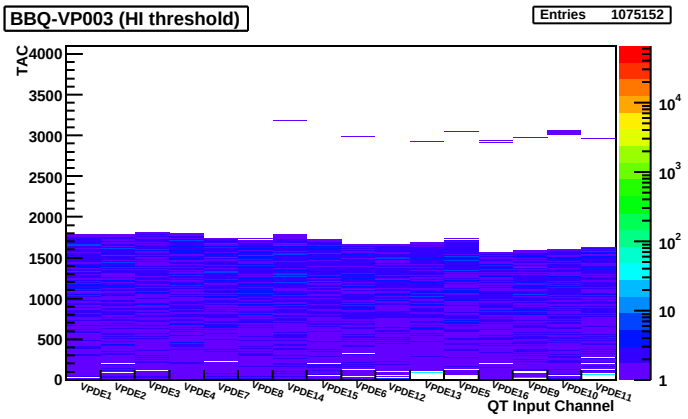

BBQ-VP001 (LO threshold)
Entries 1075152
ADC
QT Input Channel

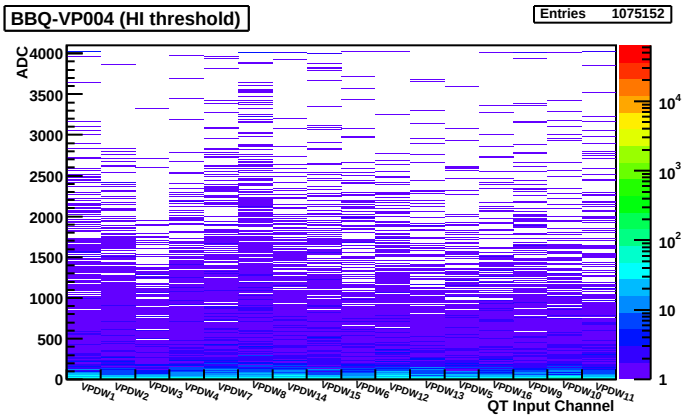

BBQ-VP001 (LO threshold)
Entries 1075152
TAC
QT Input Channel

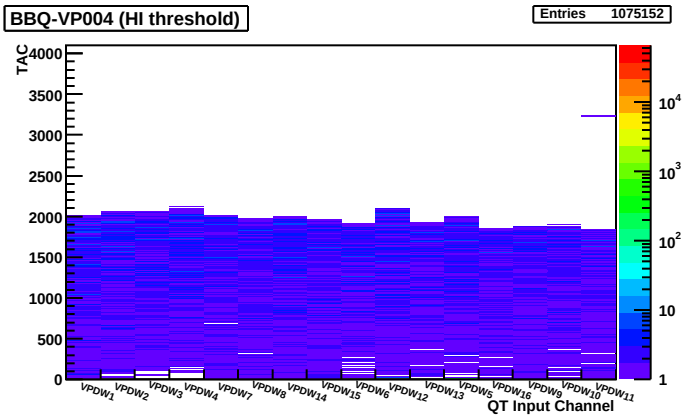

BBQ-VP002 (LO threshold)
Entries 1075152
ADC
QT Input Channel

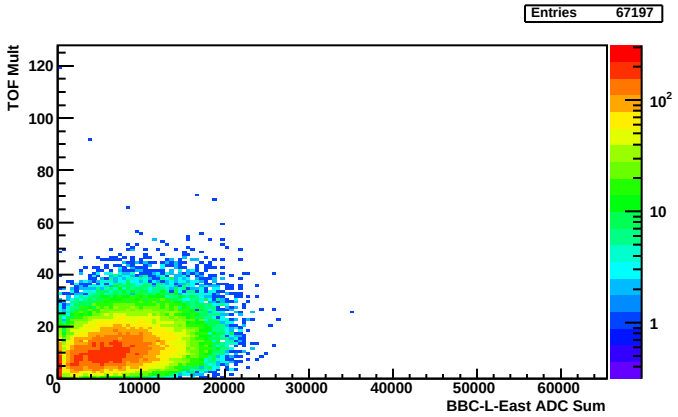

BBQ-VP002 (LO threshold)
Entries 1075152
TAC
QT Input Channel

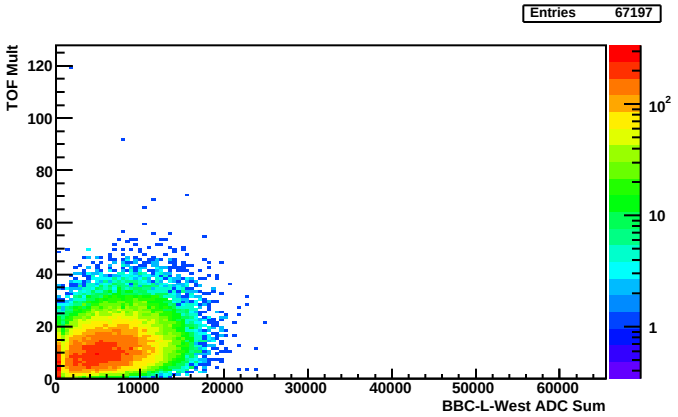

BBQ-VP003 (HI threshold)
Entries 1075152
ADC
QT Input Channel

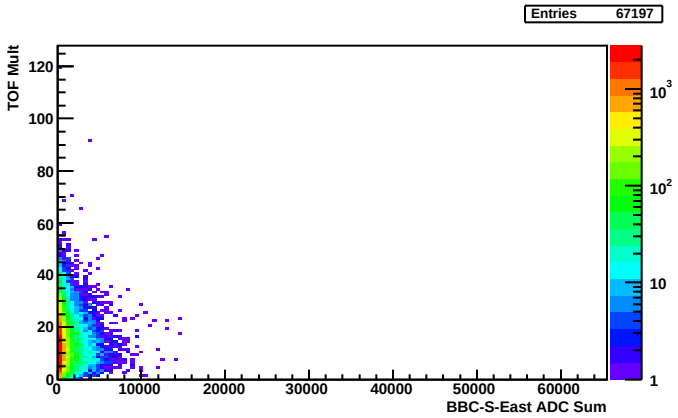

BBQ-VP003 (HI threshold)
Entries 1075152
TAC
QT Input Channel

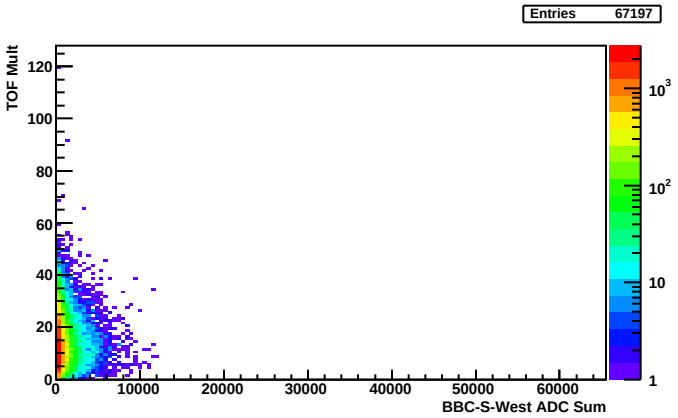

BBQ-VP004 (HI threshold)
Entries 1075152
ADC
QT Input Channel

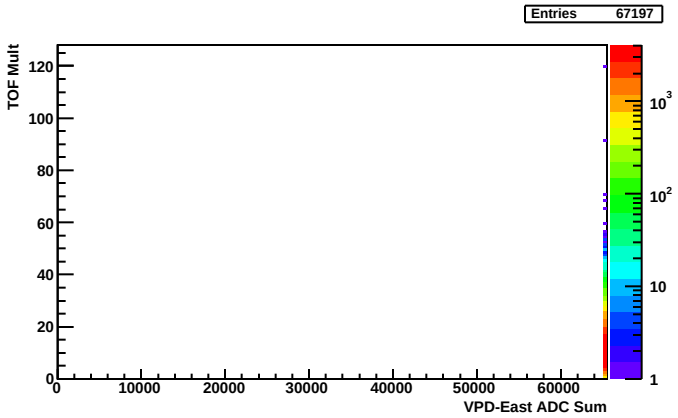

BBQ-VP004 (HI threshold)
Entries 1075152
TAC
QT Input Channel

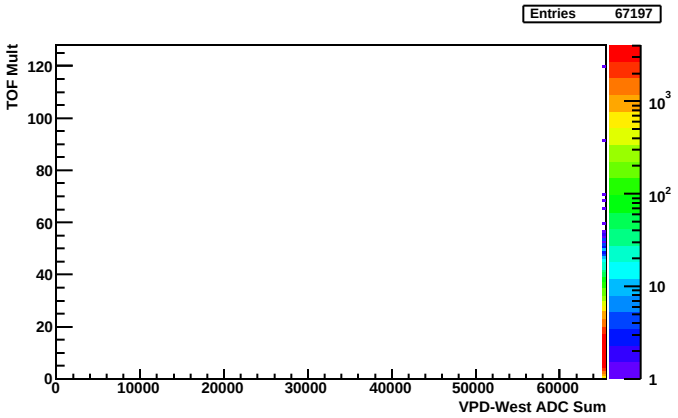

Entries 67197
TOF Mult
BBC-L-East ADC Sum

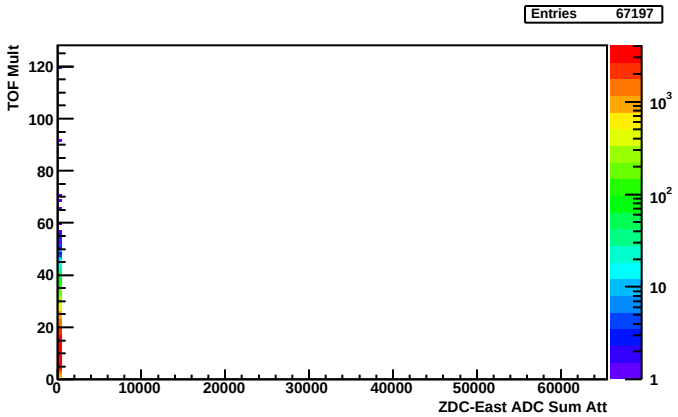

Entries 67197
TOF Mult
BBC-L-West ADC Sum

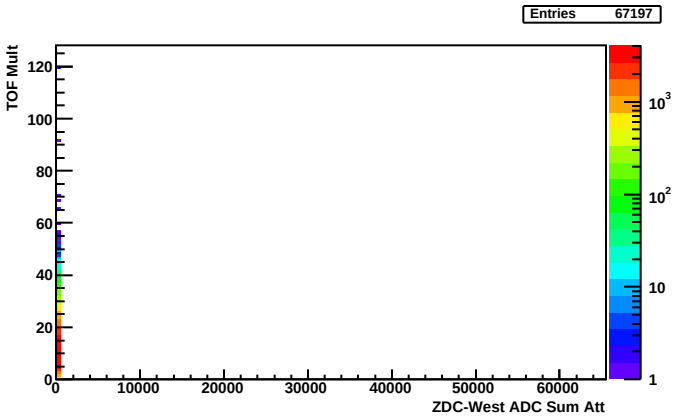

Entries 67197
TOF Mult
BBC-S-East ADC Sum

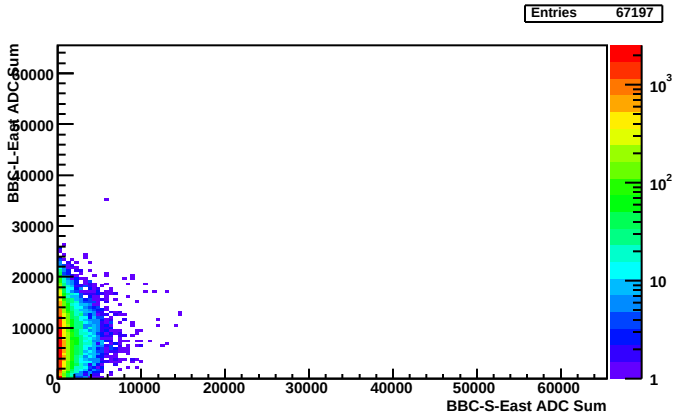

Entries 67197
TOF Mult
BBC-S-West ADC Sum

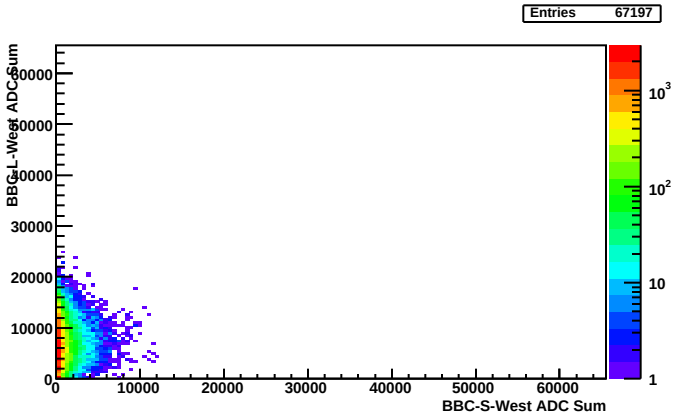

Entries 67197
TOF Mult
VPD-East ADC Sum

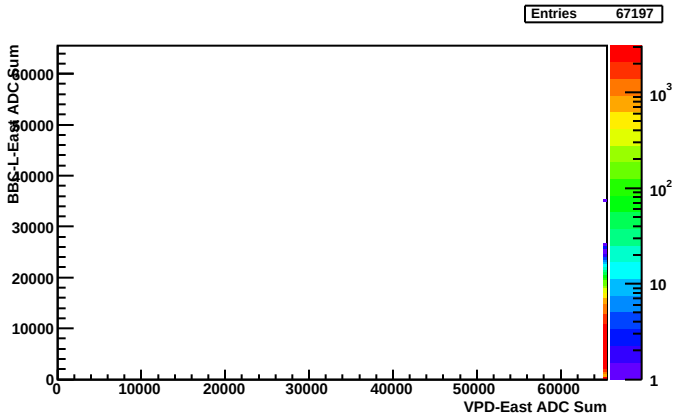

Entries 67197
TOF Mult
VPD-West ADC Sum

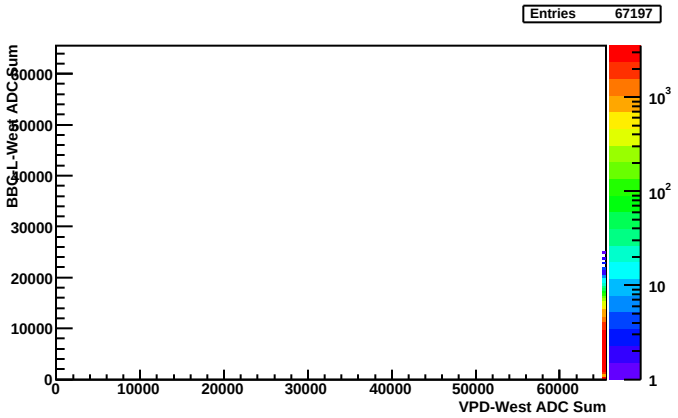

Entries 67197
TOF Mult
ZDC-East ADC Sum Att

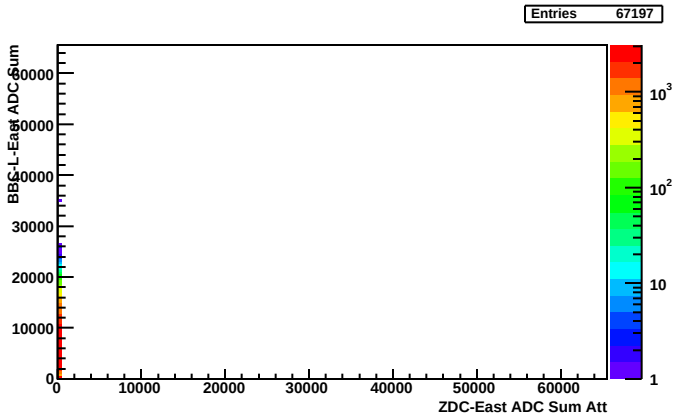

Entries 67197
TOF Mult
ZDC-West ADC Sum Att

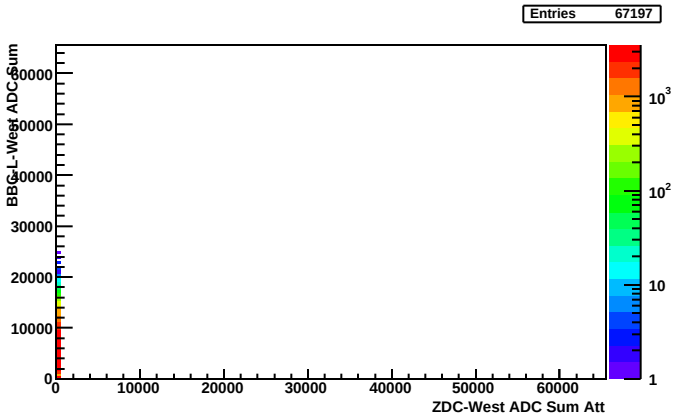

Entries 67197
BBC-L-East ADC Sum
BBC-S-East ADC Sum

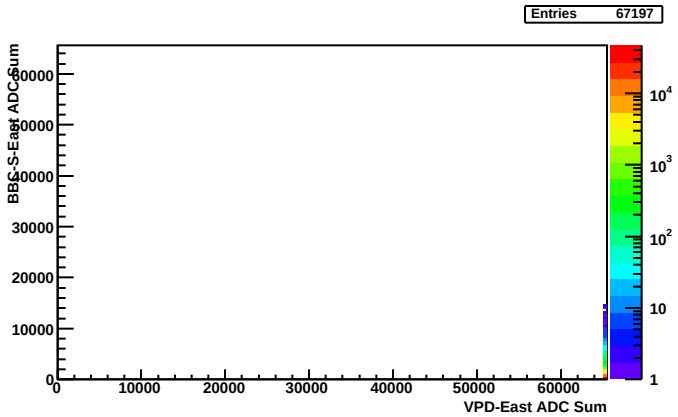

Entries 67197
BBC-L-West ADC Sum
BBC-S-West ADC Sum

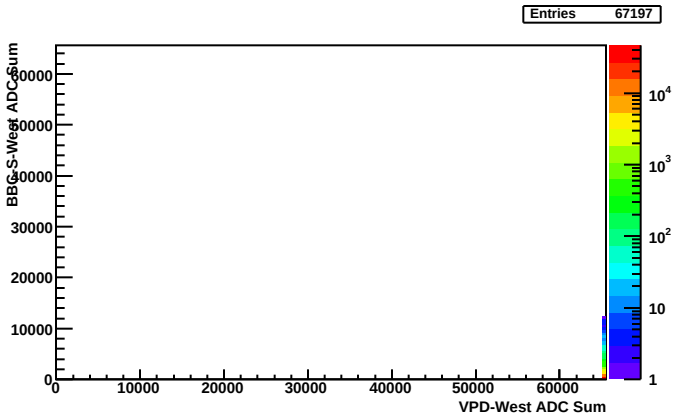

Entries 67197
BBC-L-East ADC Sum
VPD-East ADC Sum

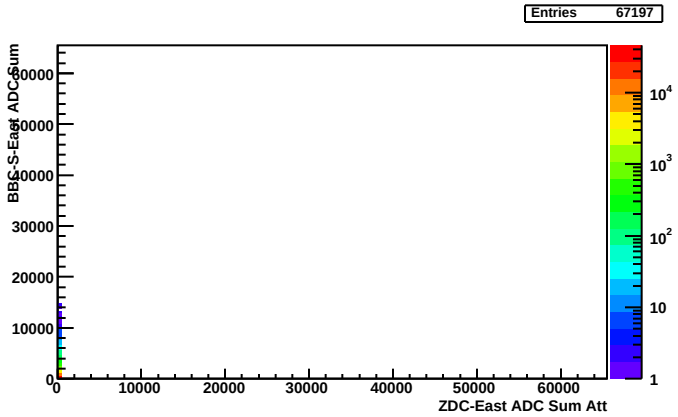

Entries 67197
BBC-L-West ADC Sum
VPD-West ADC Sum

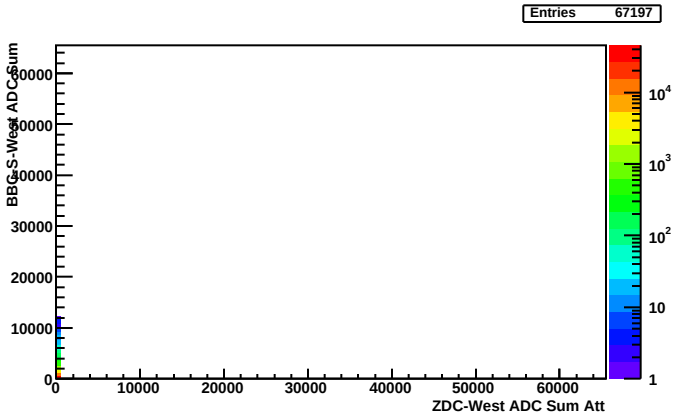

Entries 67197
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

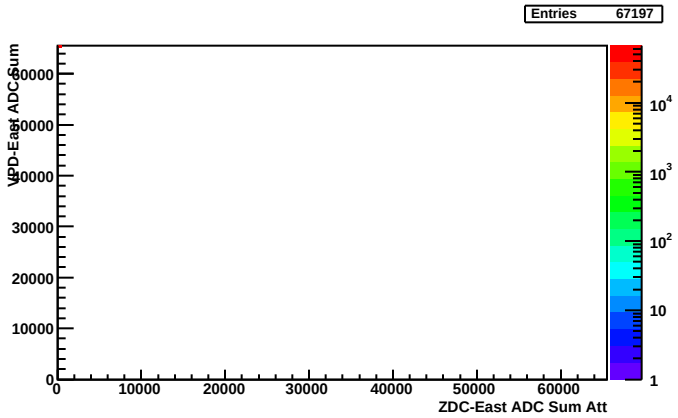

Entries 67197
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

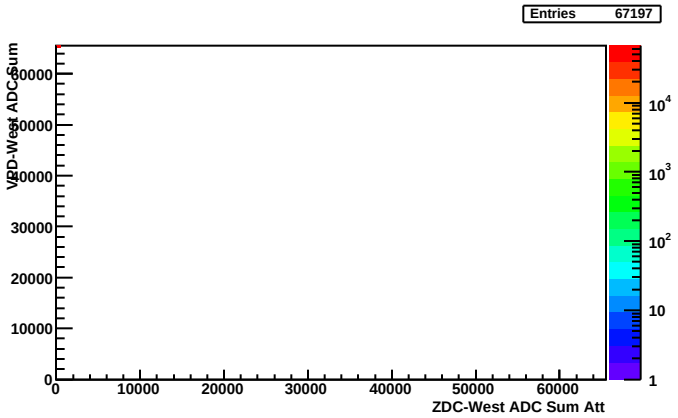

Entries 67197
BBC-S-East ADC Sum
VPD-East ADC Sum

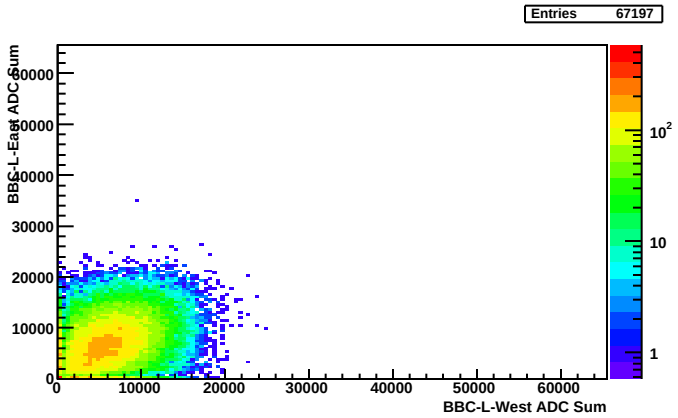

Entries 67197
BBC-S-West ADC Sum
VPD-West ADC Sum

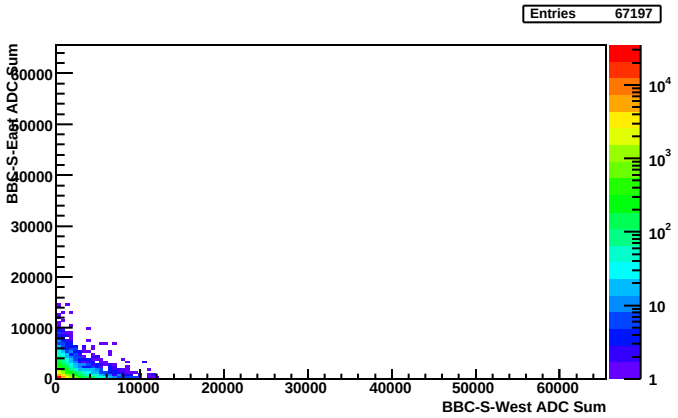

Entries 67197
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

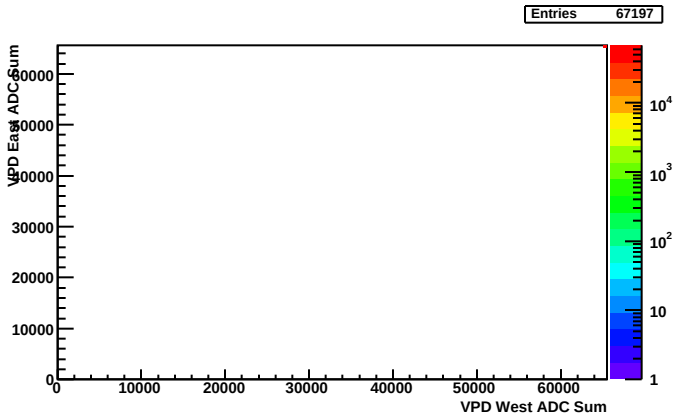

Entries 67197
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

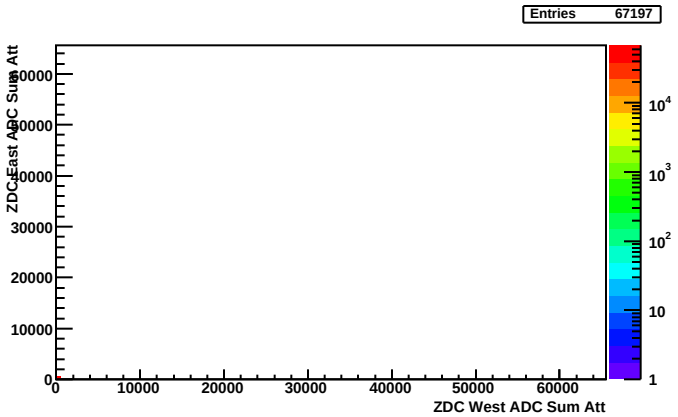

Entries 67197
VPD-East ADC Sum
ZDC-East ADC Sum Att

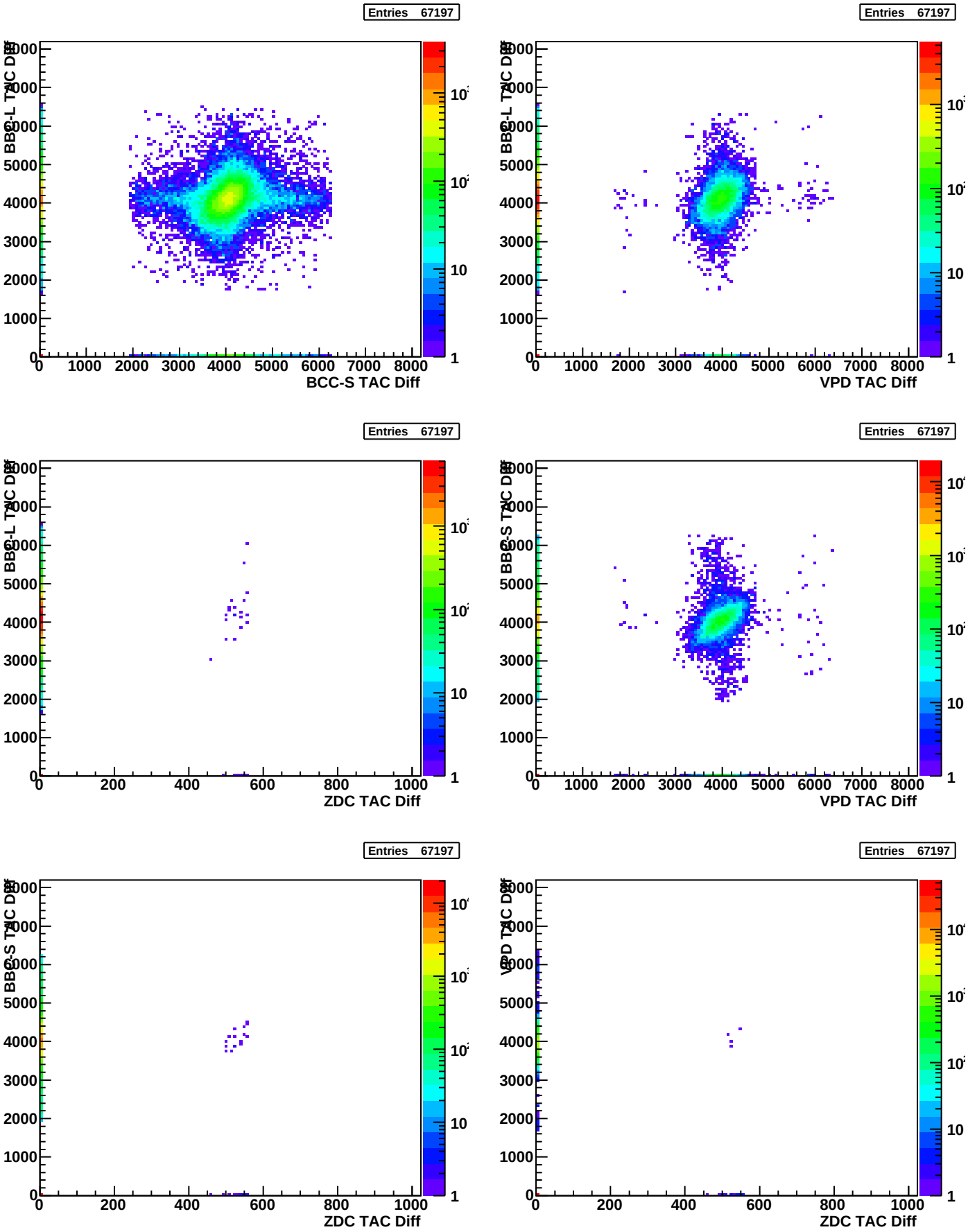

Entries 67197
VPD-West ADC Sum
ZDC-West ADC Sum Att

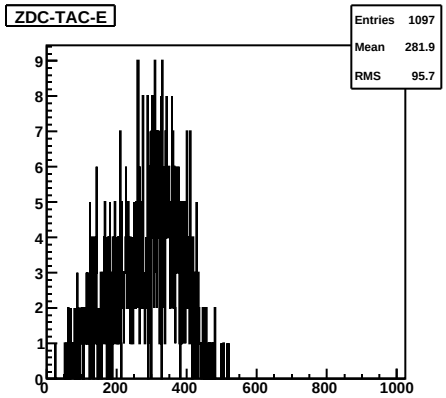

Entries 67197
BBC-L-East ADC Sum
BBC-L-West ADC Sum

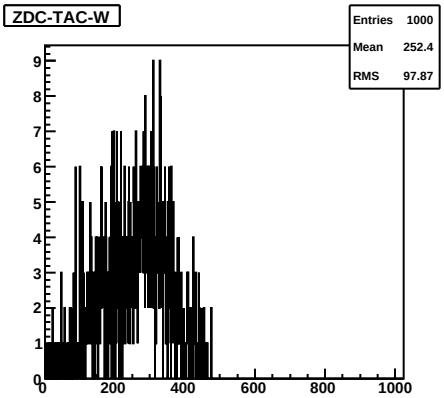

Entries 67197
BBC-S-East ADC Sum
BBC-S-West ADC Sum

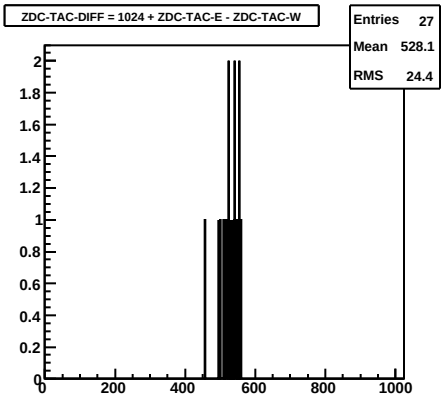

Entries 67197
VPD East ADC Sum
VPD West ADC Sum

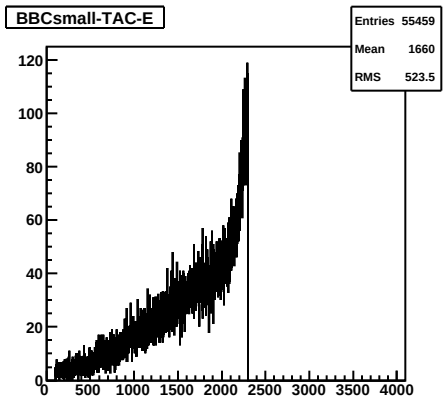

Entries 67197
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

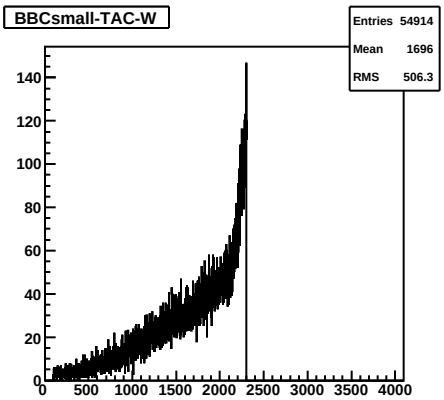

Entries 67197
BBC-L TAC Diff
BCC-S TAC Diff
Entries 67197
BBC-L TAC Diff
VPD TAC Diff
Entries 67197
BBC-L TAC Diff
ZDC TAC Diff
Entries 67197
BBC-S TAC Diff
VPD TAC Diff
Entries 67197
BBC-S TAC Diff
ZDC TAC Diff
Entries 67197
VPD TAC Diff
ZDC TAC Diff

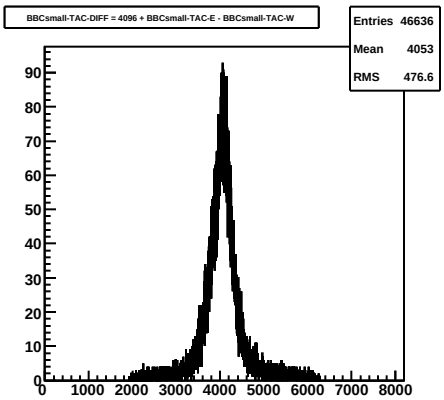

ZDC-TAC-E
Entries 1097
Mean 281.9
RMS 95.7

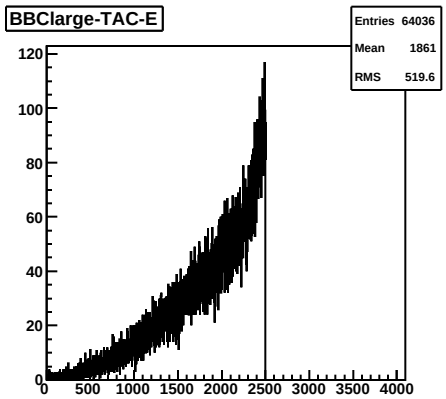

ZDC-TAC-W
Entries 1000
Mean 252.4
RMS 97.87

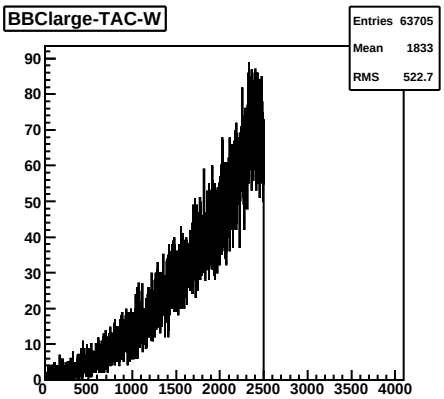

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 27
Mean 528.1
RMS 24.4

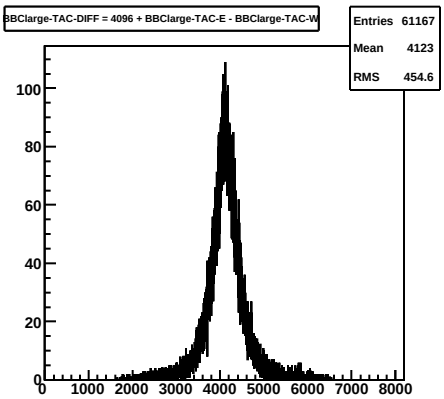

BBCsmall-TAC-E
Entries 55459
Mean 1660
RMS 523.5

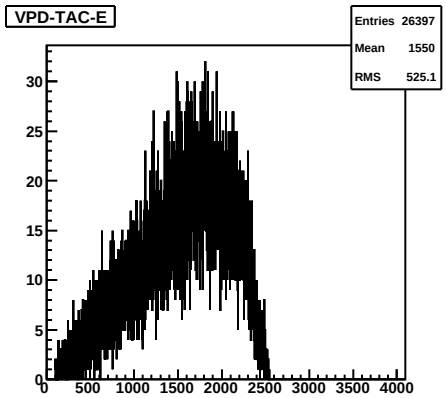

BBCsmall-TAC-W
Entries 54914
Mean 1696
RMS 506.3

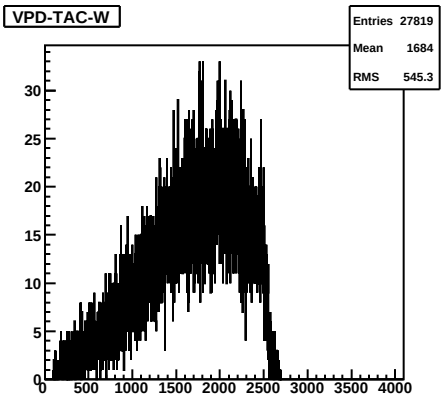

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 46636
Mean 4053
RMS 476.6

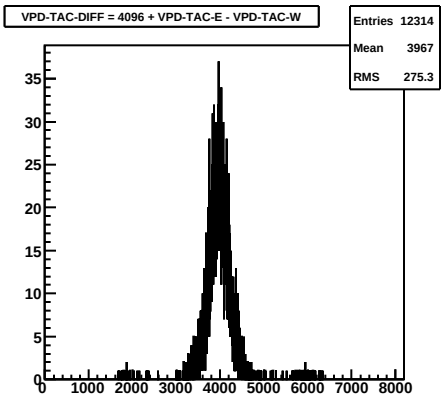

BBClarge-TAC-E
Entries 64036
Mean 1861
RMS 519.6

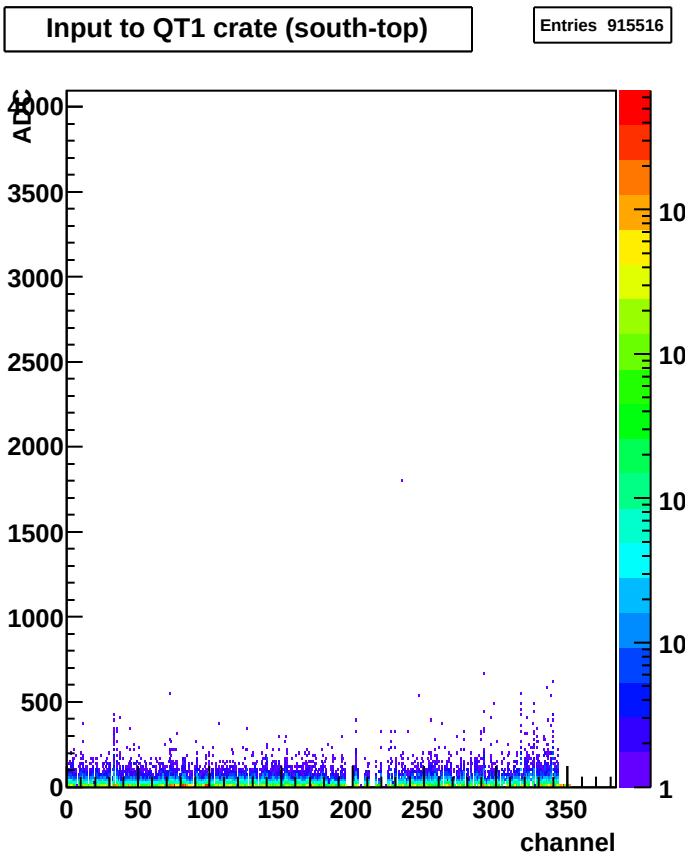

BBClarge-TAC-W
Entries 63705
Mean 1833
RMS 522.7

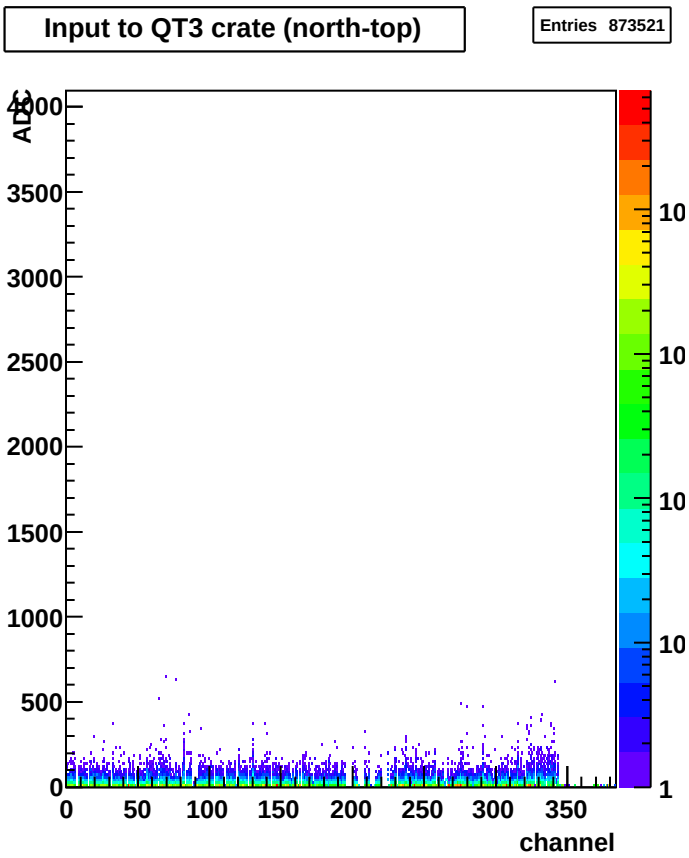

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 61167
Mean 4123
RMS 454.6

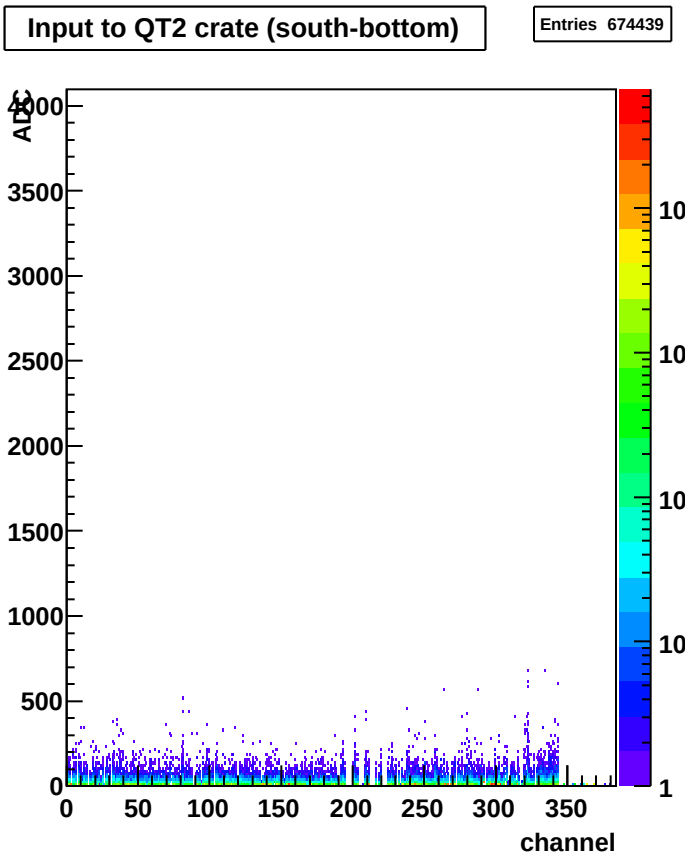

VPD-TAC-E
Entries 26397
Mean 1550
RMS 525.1

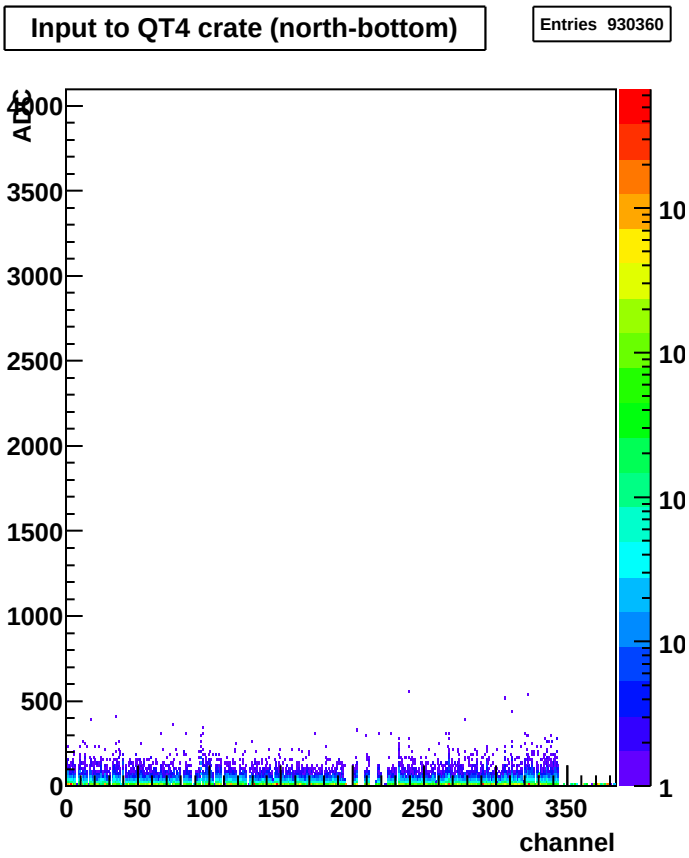

VPD-TAC-W
Entries 27819
Mean 1684
RMS 545.3

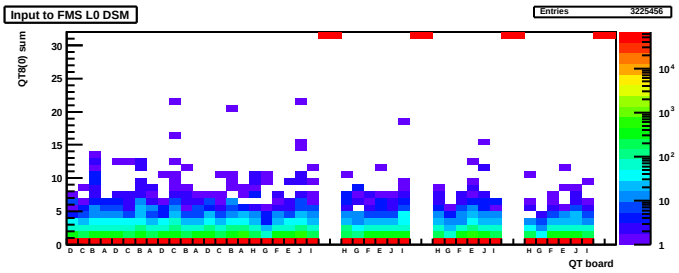

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 12314
Mean 3967
RMS 275.3

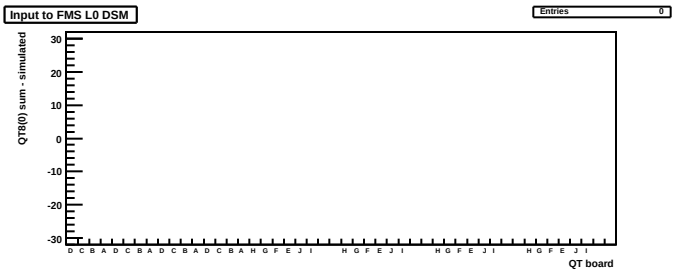

Input to QT1 crate (south-top)
Entries 915516
ADC
channel

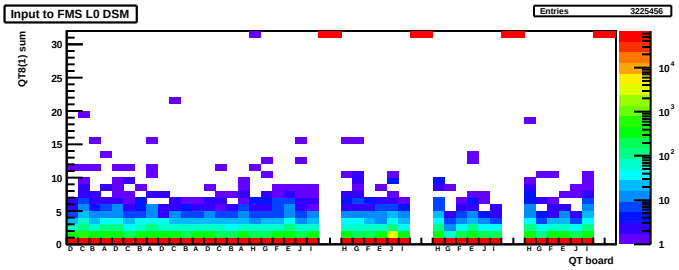

Input to QT3 crate (north-top)
Entries 873521
ADC
channel

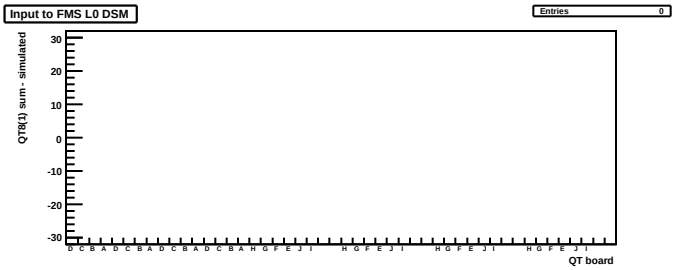

Input to QT2 crate (south-bottom)
Entries 674439
ADC
channel

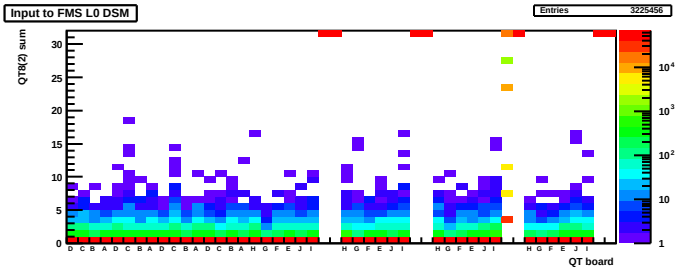

Input to QT4 crate (north-bottom)
Entries 930360
ADC
channel

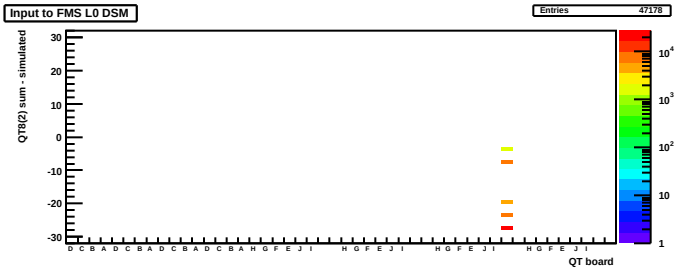

Input to FMS L0 DSM
Entries 3225456
QT8(0) sum
QT board

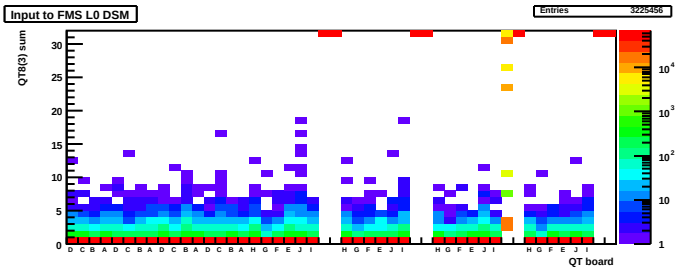

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

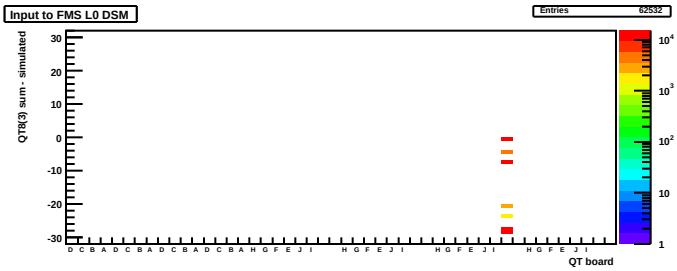

Input to FMS L0 DSM
Entries 3225456
QT8(1) sum
QT board

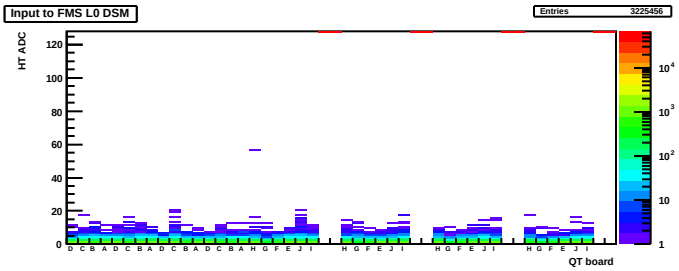

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

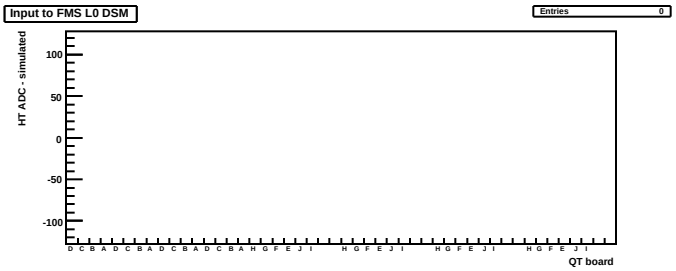

Input to FMS L0 DSM
Entries 3225456
QT8(2) sum
QT board

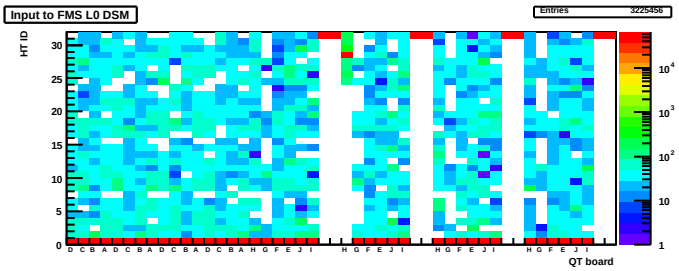

Input to FMS L0 DSM
Entries 47178
QT8(2) sum - simulated
QT board

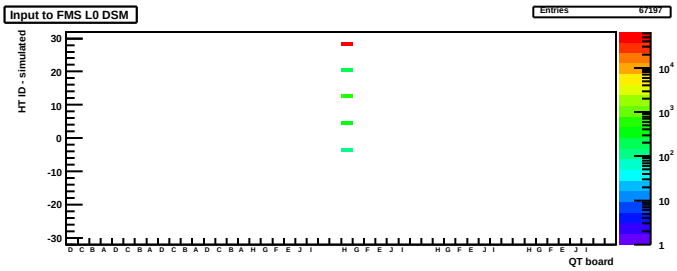

Input to FMS L0 DSM
Entries 3225456
QT8(3) sum
QT board

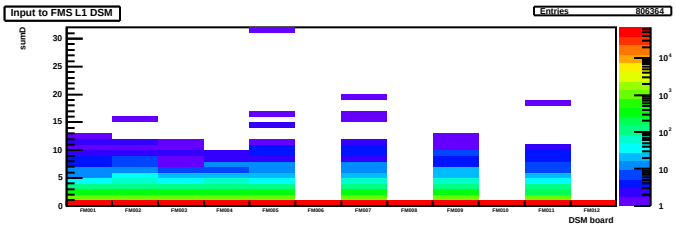

Input to FMS L0 DSM
Entries 62532
QT8(3) sum - simulated
QT board

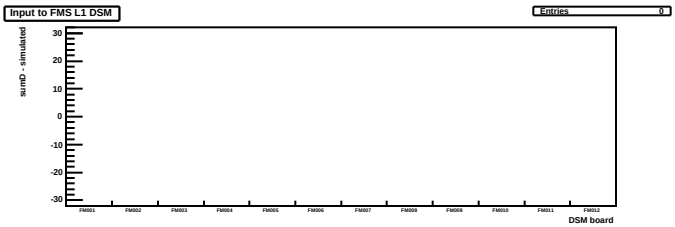

Input to FMS L0 DSM
Entries 3225456
HT ADC
QT board

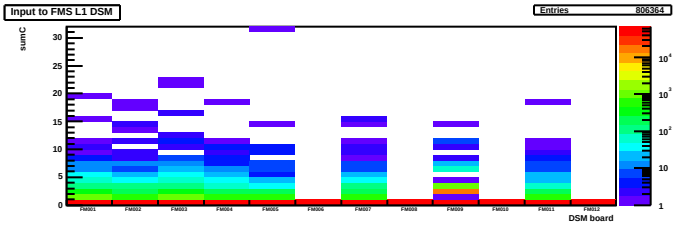

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

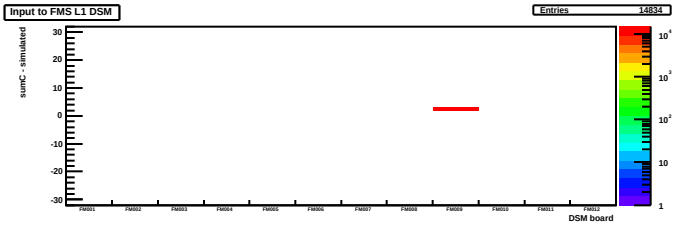

Input to FMS L0 DSM
Entries 3225456
HT ID
QT board

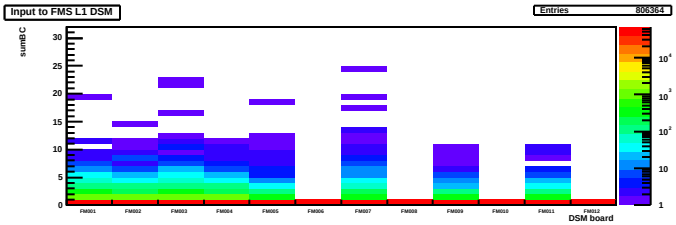

Input to FMS L0 DSM
Entries 67197
HT ID - simulated
QT board

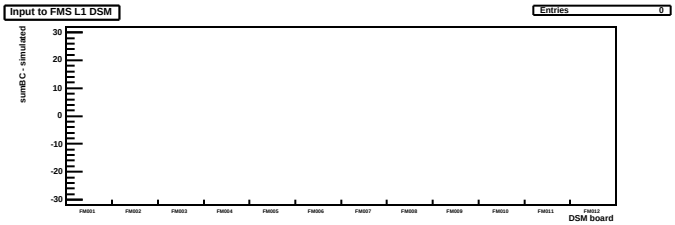

Input to FMS L1 DSM
Entries 806364
sumD
DSM board

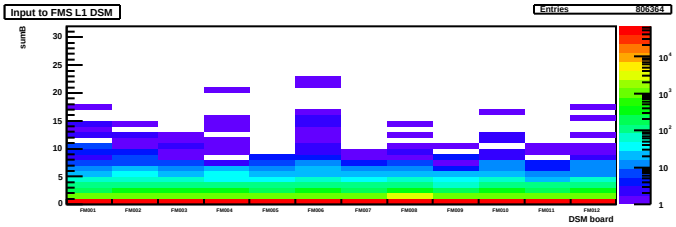

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

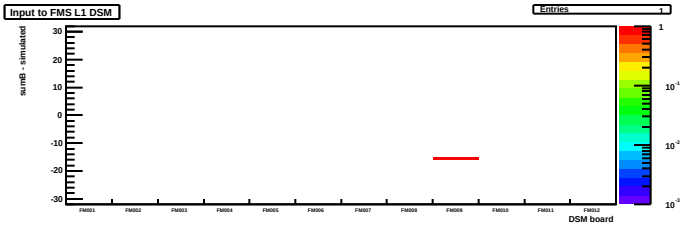

Input to FMS L1 DSM
Entries 806364
sumC
DSM board

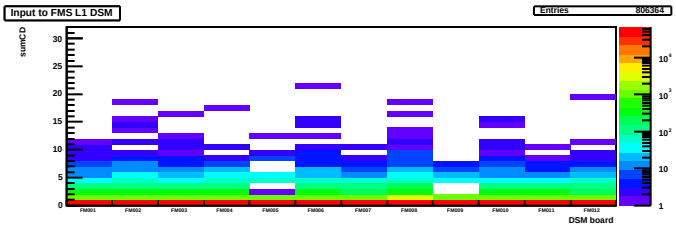

Input to FMS L1 DSM
Entries 14834
sumC - simulated
DSM board

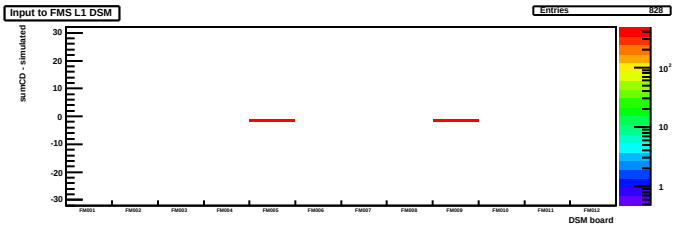

Input to FMS L1 DSM
Entries 806364
sumBC
DSM board

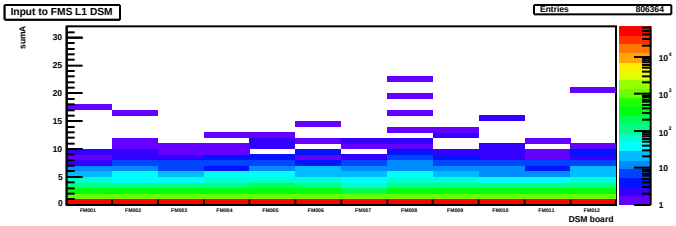

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

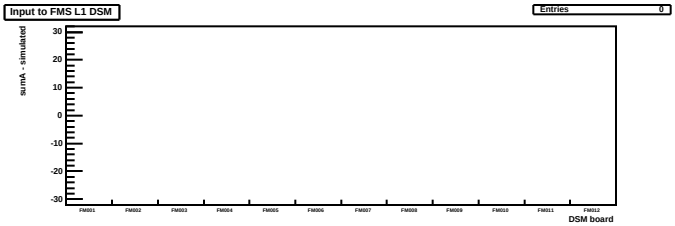

Input to FMS L1 DSM
Entries 806364
sumB
DSM board

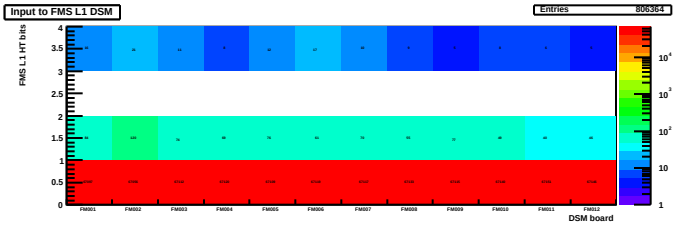

Input to FMS L1 DSM
Entries 1
sumB - simulated
DSM board

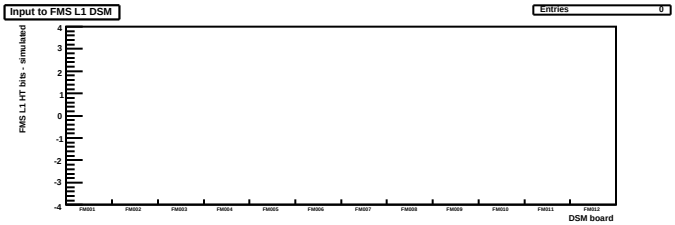

Input to FMS L1 DSM
Entries 806364
sumCD
DSM board

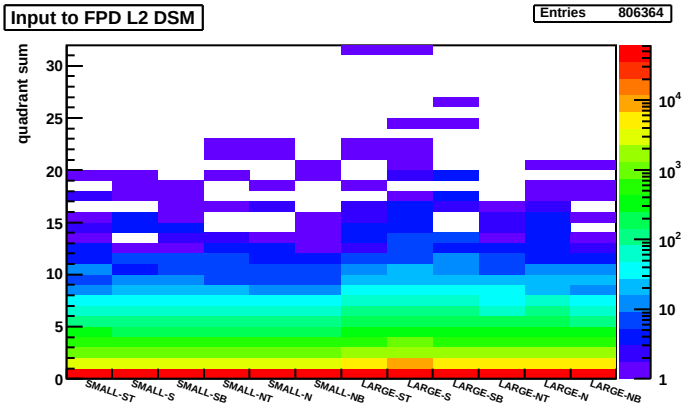

Input to FMS L1 DSM
Entries 828
sumCD - simulated
DSM board

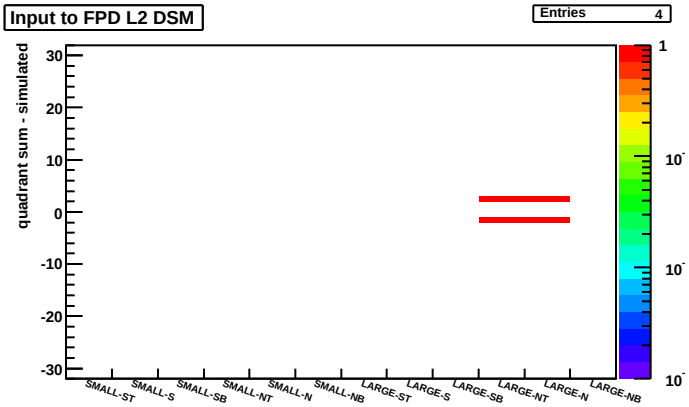

Input to FMS L1 DSM
Entries 806364
sumA
DSM board

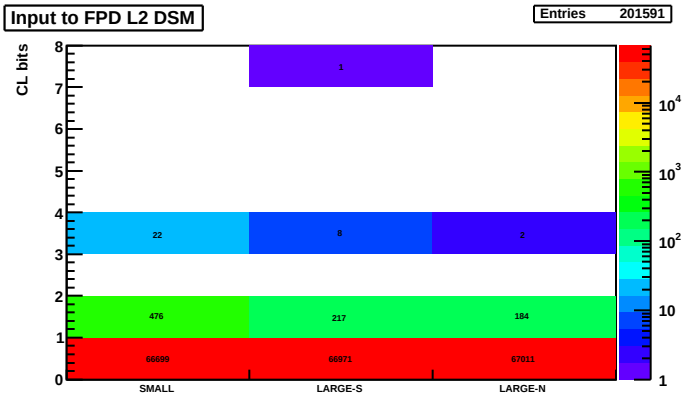

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

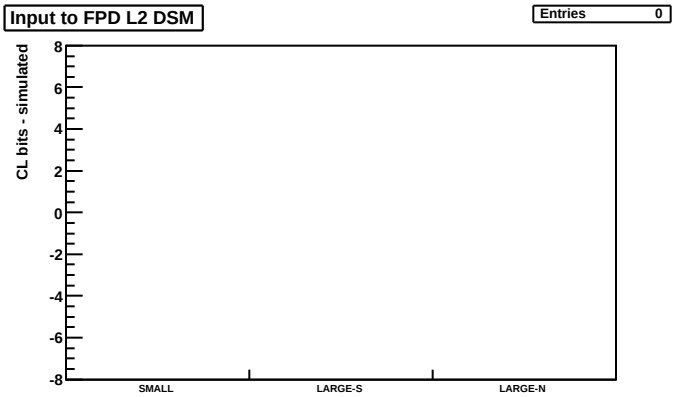

Input to FMS L1 DSM
Entries 806364
FMS L1 HT bits
DSM board

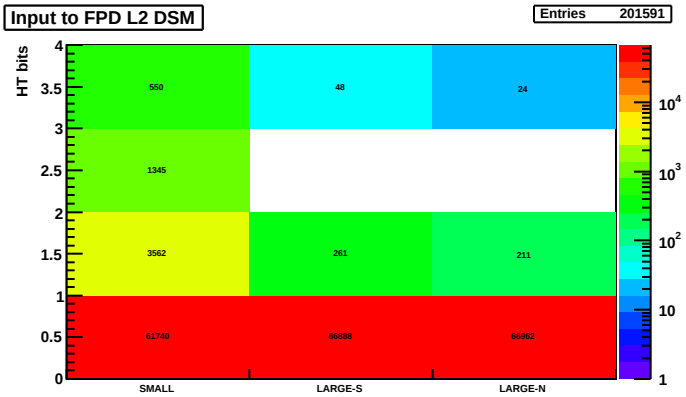

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

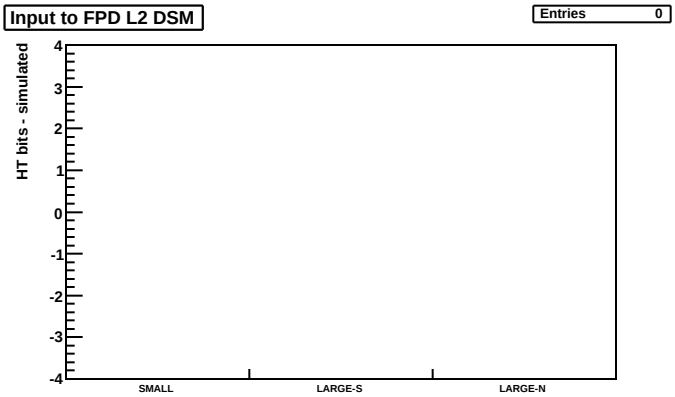

Input to FPD L2 DSM
Entries 806364
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

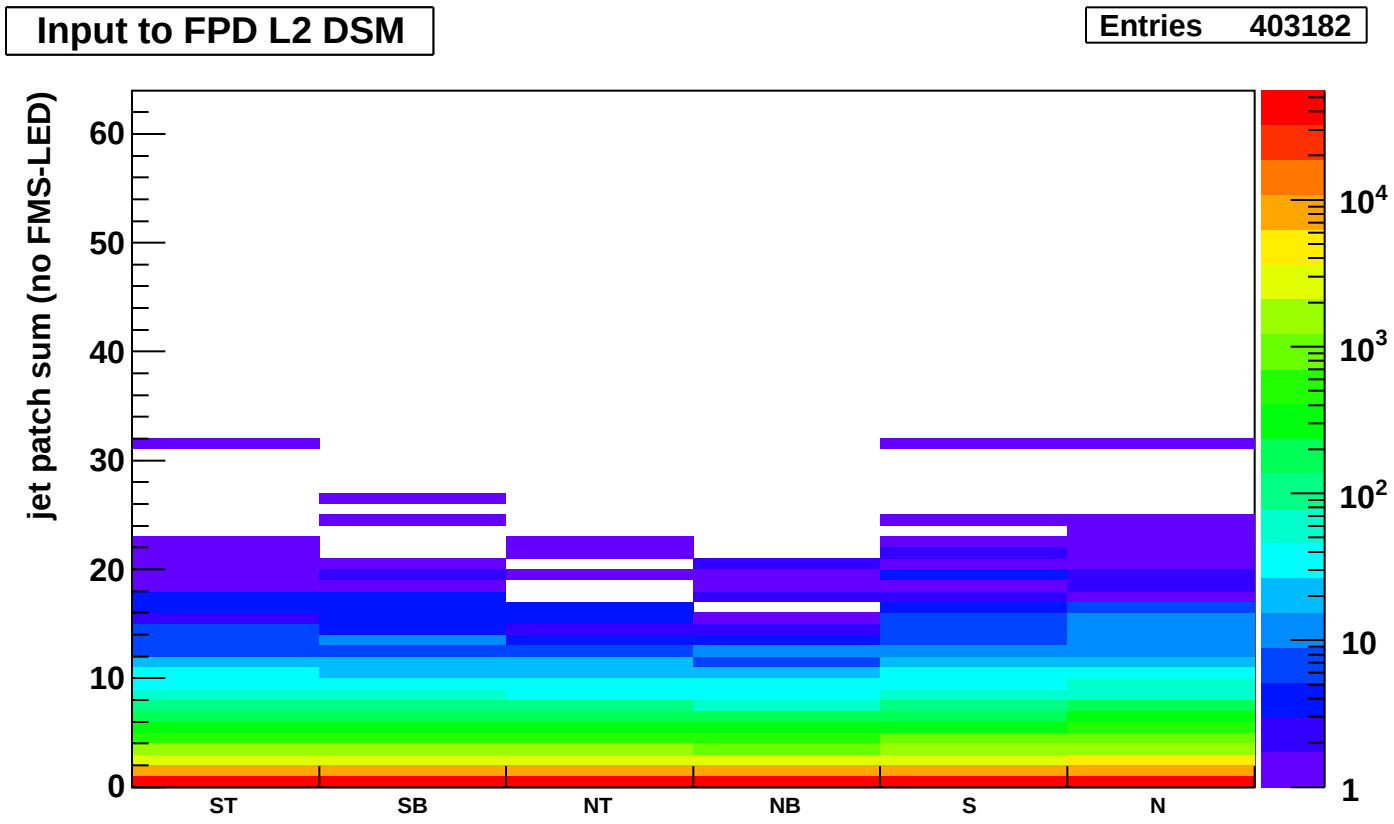

Input to FPD L2 DSM
Entries 4
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

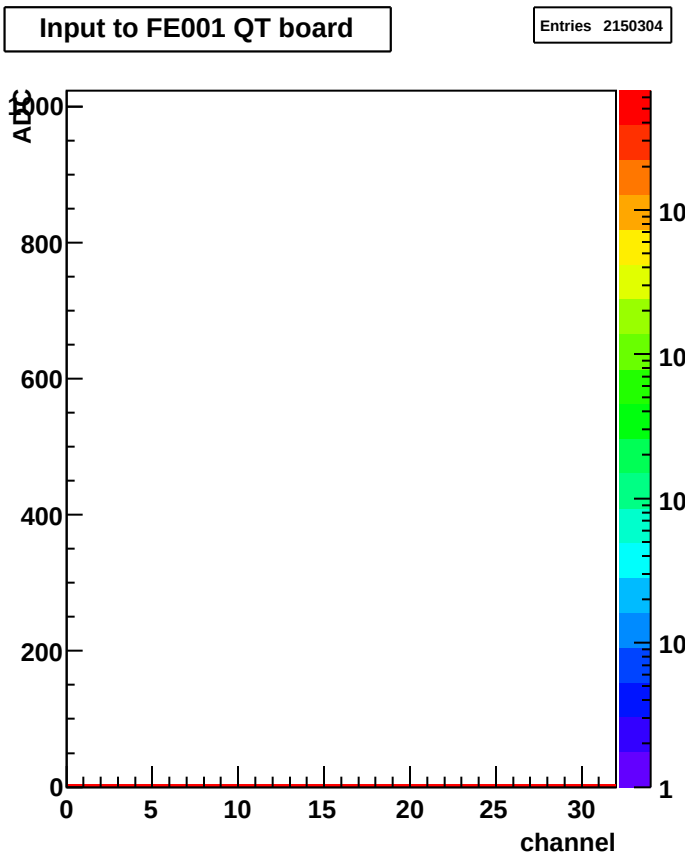

Input to FPD L2 DSM
Entries 201591
CL bits
1
22
8
2
476
217
184
66699
66971
67011
SMALL
LARGE-S
LARGE-N

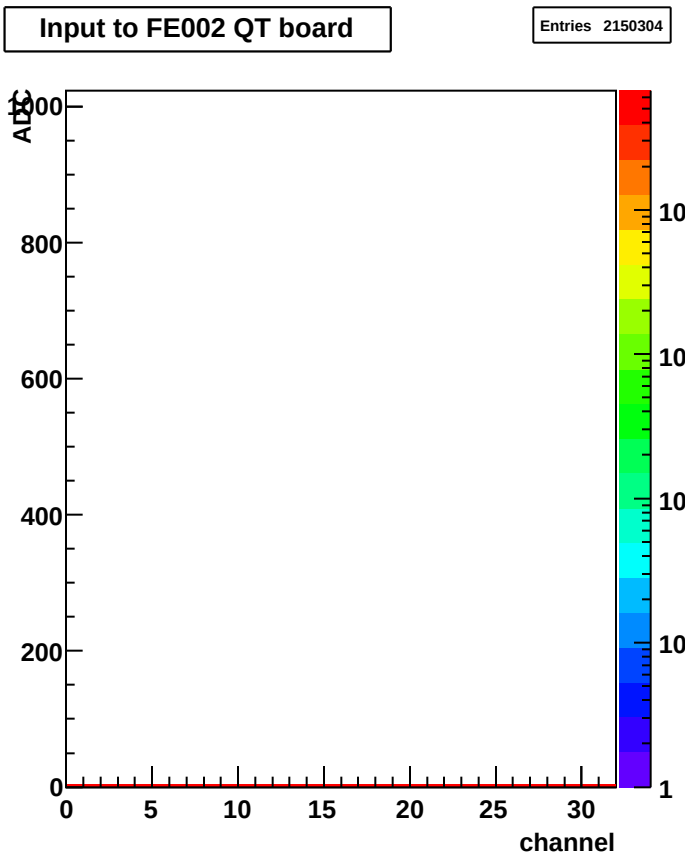

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

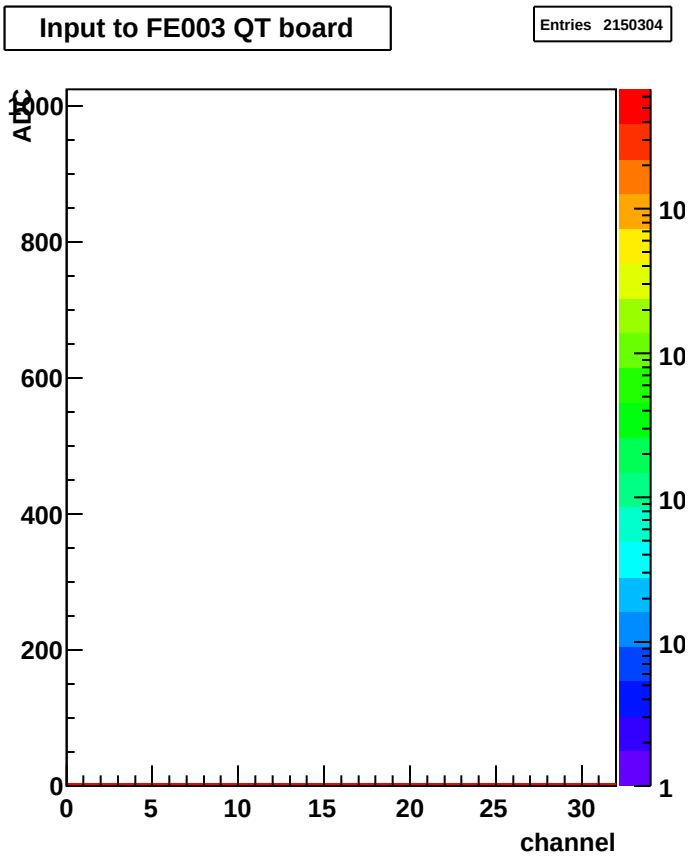

Input to FPD L2 DSM
Entries 201591
HT bits
550
48
24
1345
3562
261
211
61740
66888
66962
SMALL
LARGE-S
LARGE-N

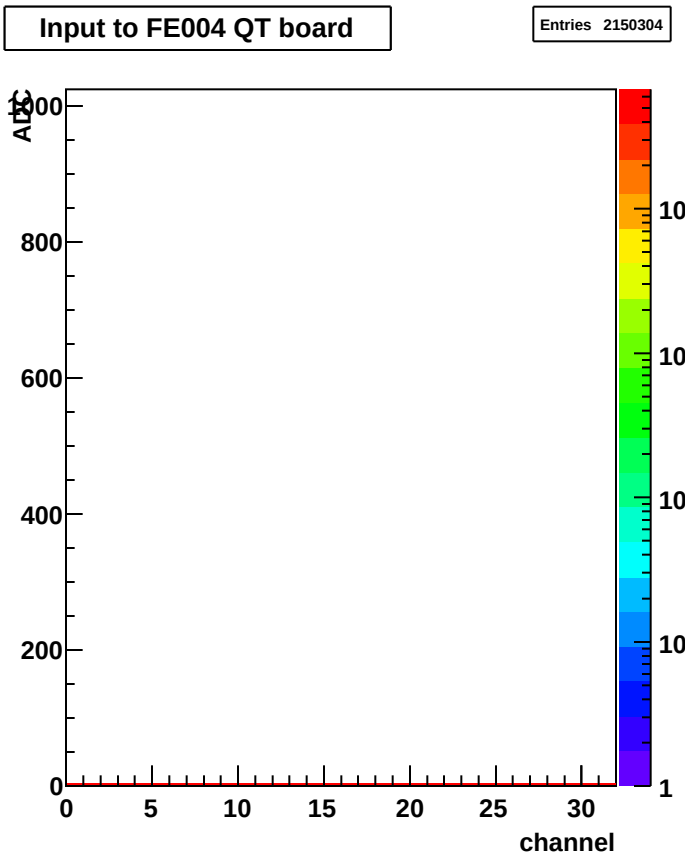

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

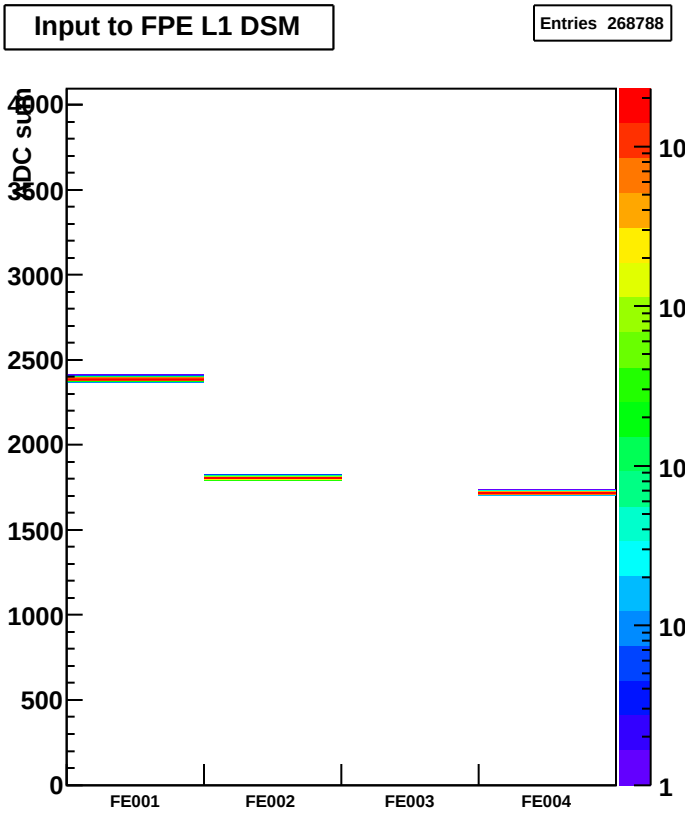

Input to FPD L2 DSM
Entries 403182
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

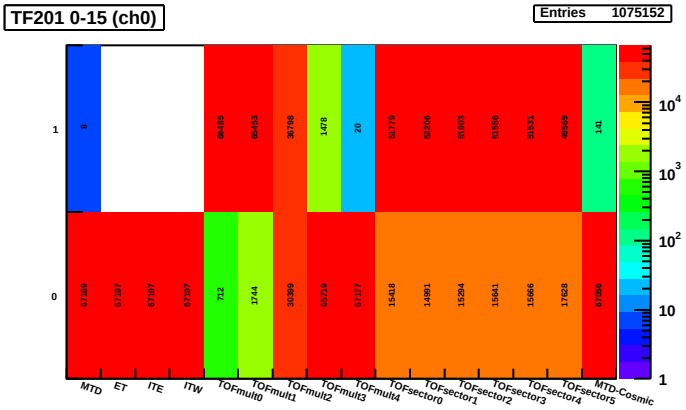

Input to FE001 QT board
Entries 2150304
ADC
channel

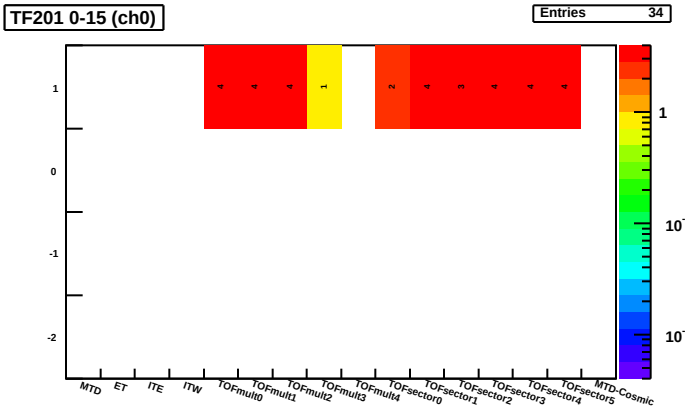

Input to FE002 QT board
Entries 2150304
ADC
channel

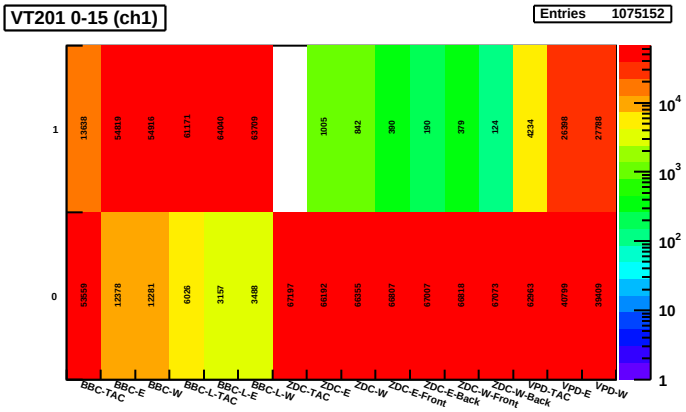

Input to FE003 QT board
Entries 2150304
ADC
channel

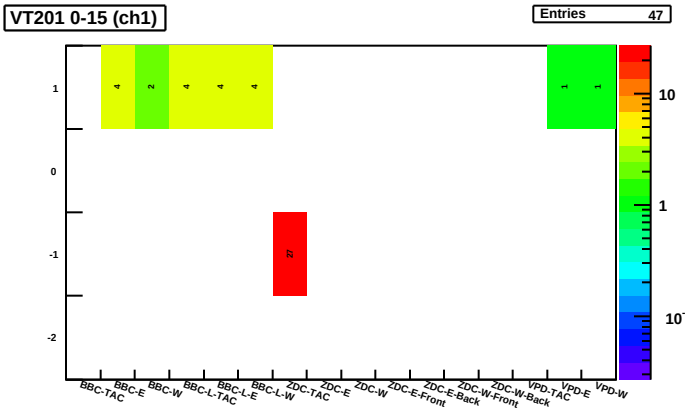

Input to FE004 QT board
Entries 2150304
ADC
channel

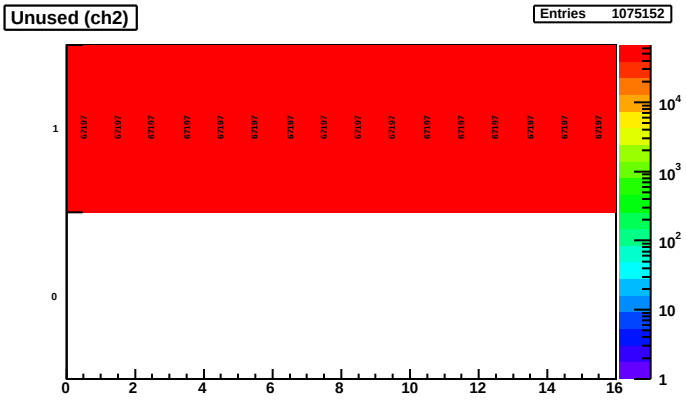

Input to FPE L1 DSM
Entries 268788
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

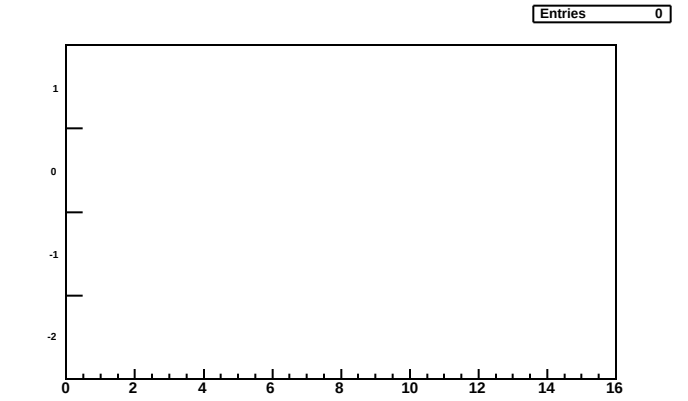

TF201 0-15 (ch0)
Entries 1075152
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

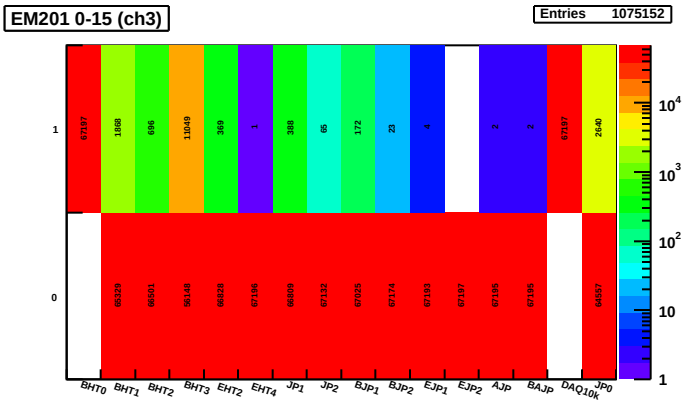

TF201 0-15 (ch0)
Entries 34
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

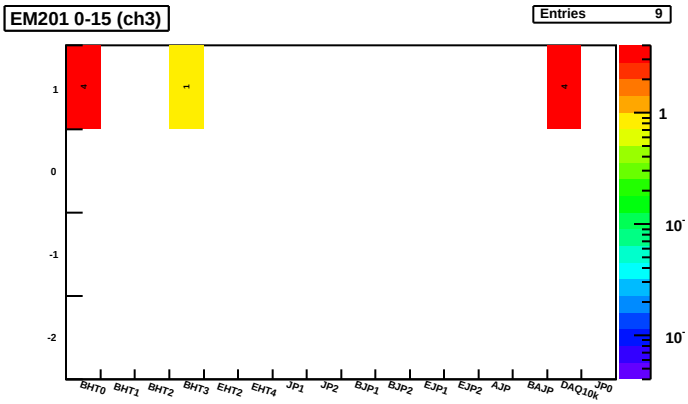

VT201 0-15 (ch1)
Entries 1075152
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 47
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1075152


Entries 0


EM201 0-15 (ch3)
Entries 1075152
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
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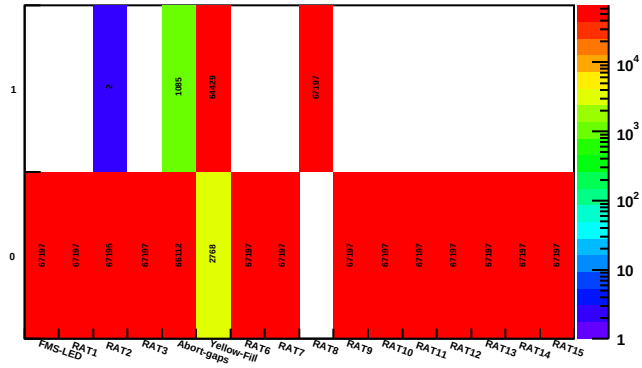
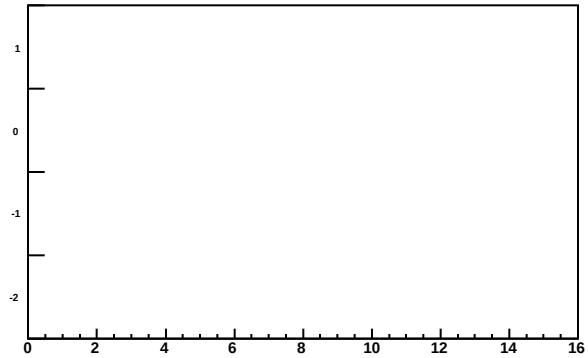
RAT board (ch4)





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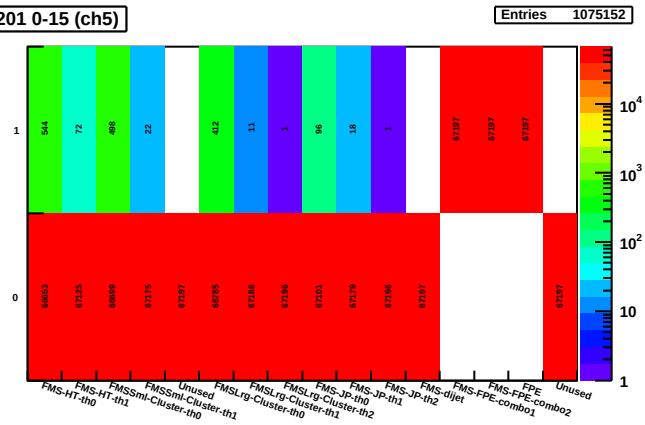

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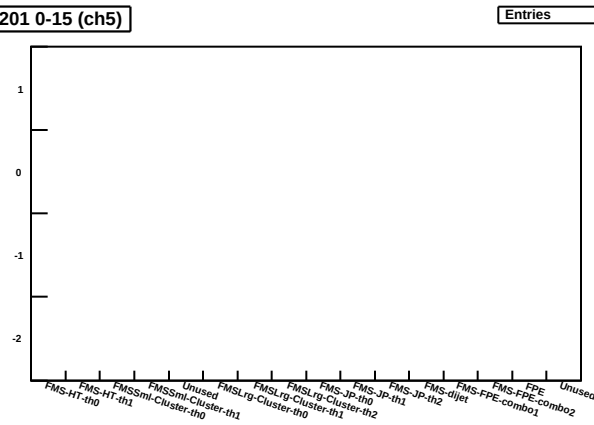

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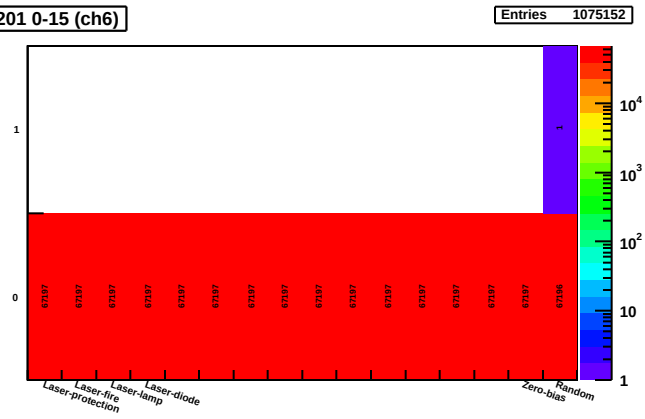

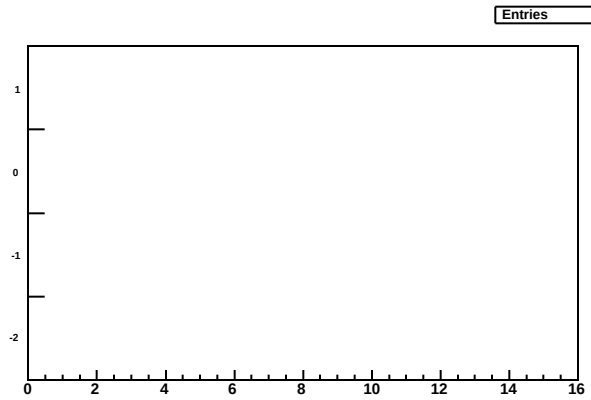
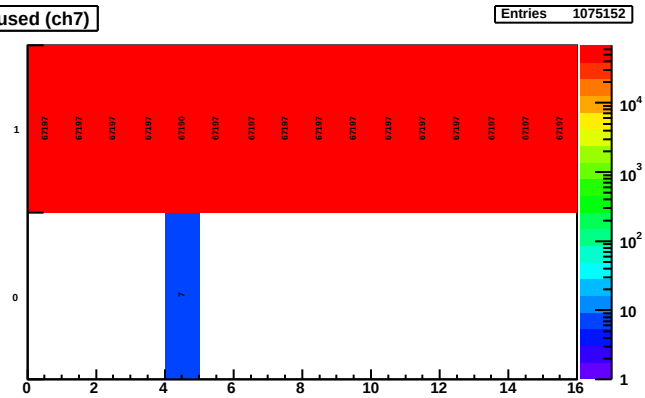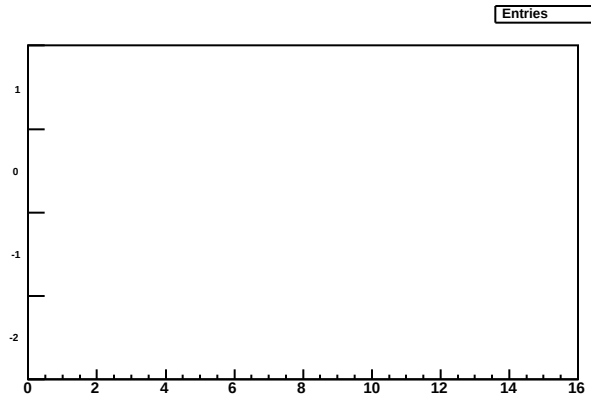
Unused (ch7)