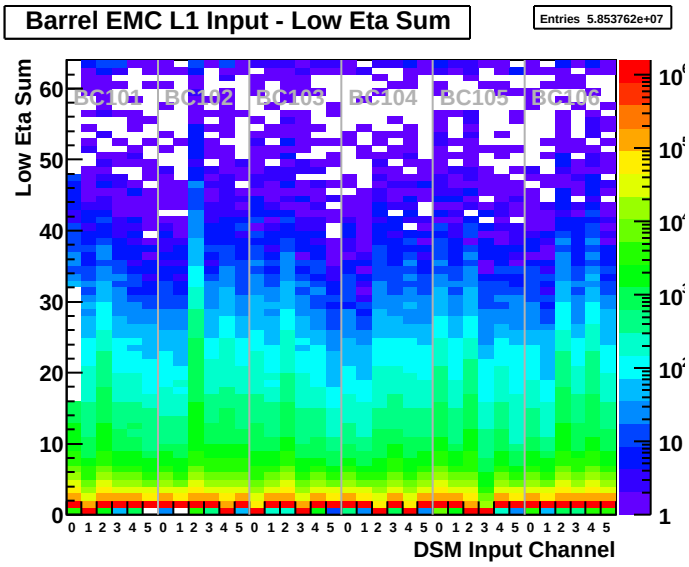
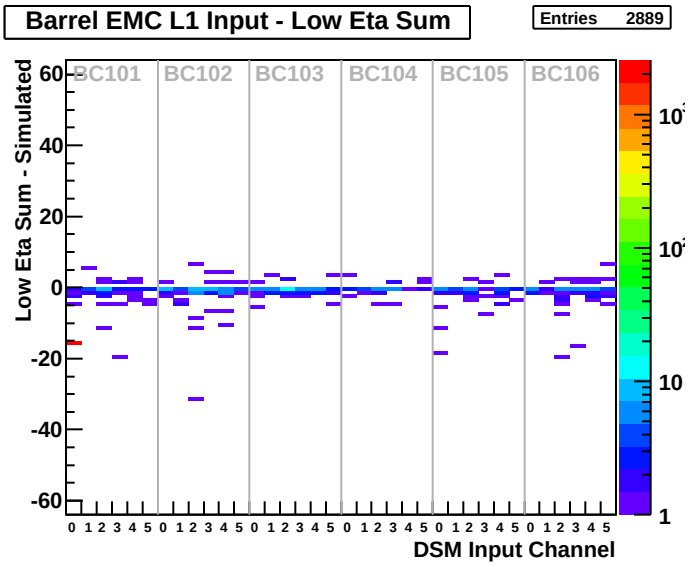
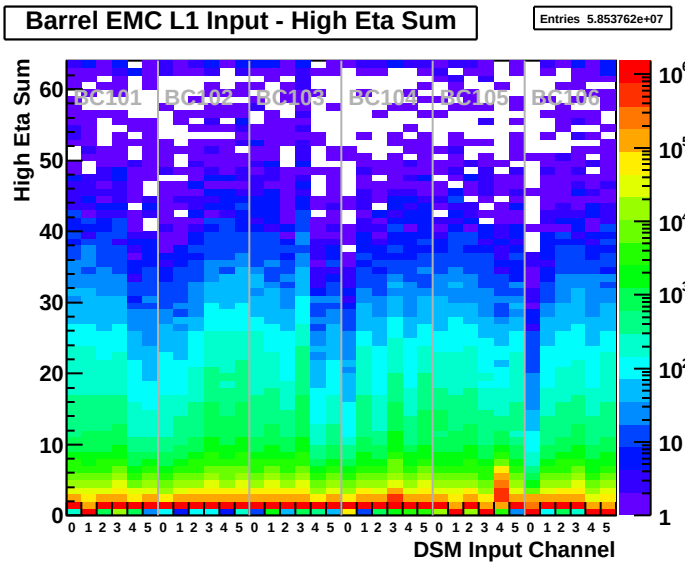
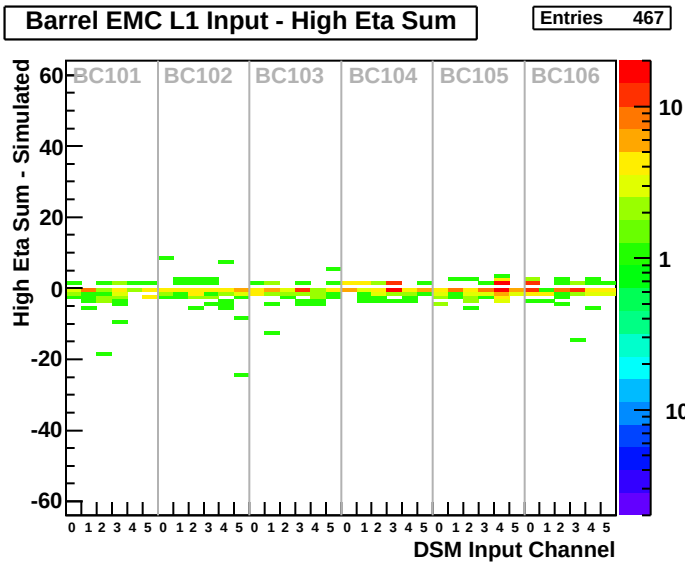
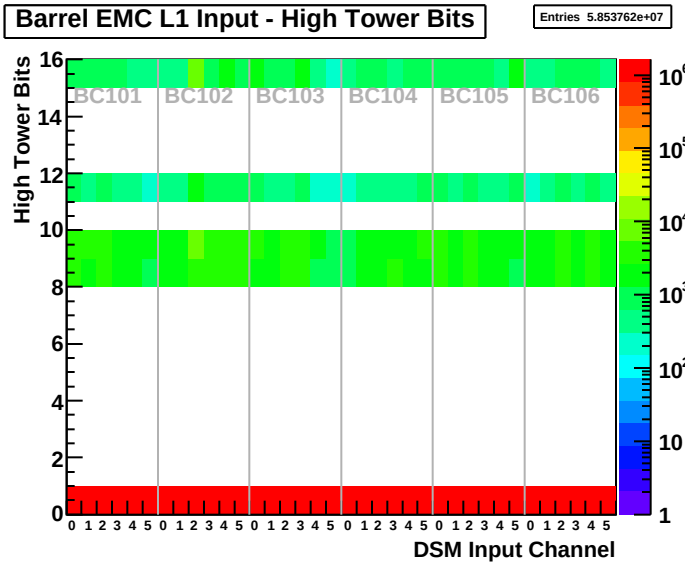
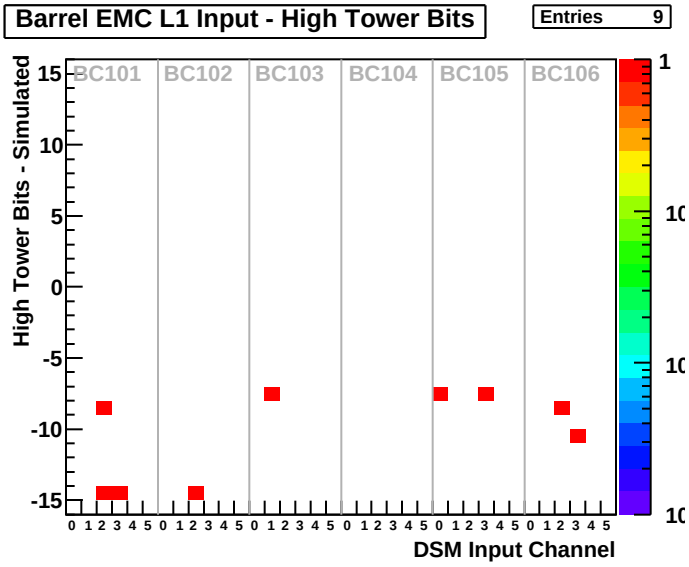

Barrel EMC L1 Input - Low Eta Sum
Entries 5.853762e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

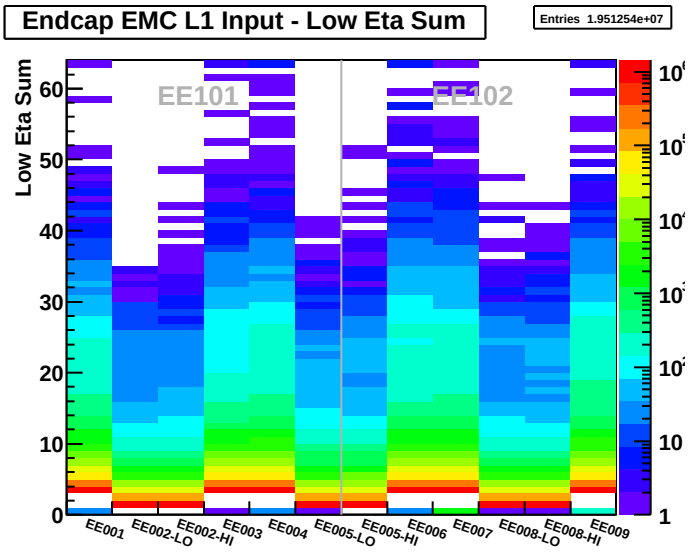

Barrel EMC L1 Input - Low Eta Sum
Entries 2889
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

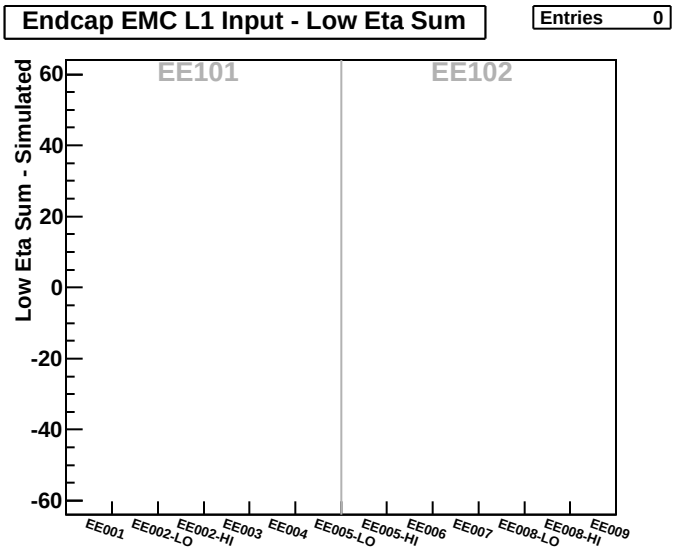

Barrel EMC L1 Input - High Eta Sum
Entries 5.853762e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

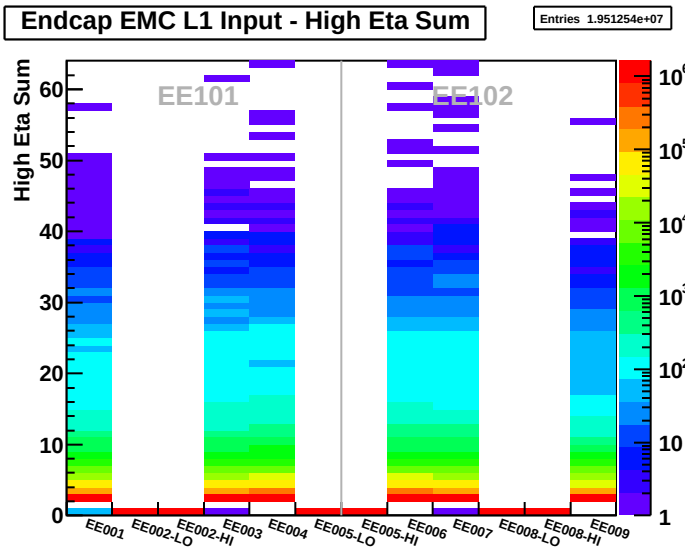

Barrel EMC L1 Input - High Eta Sum
Entries 467
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

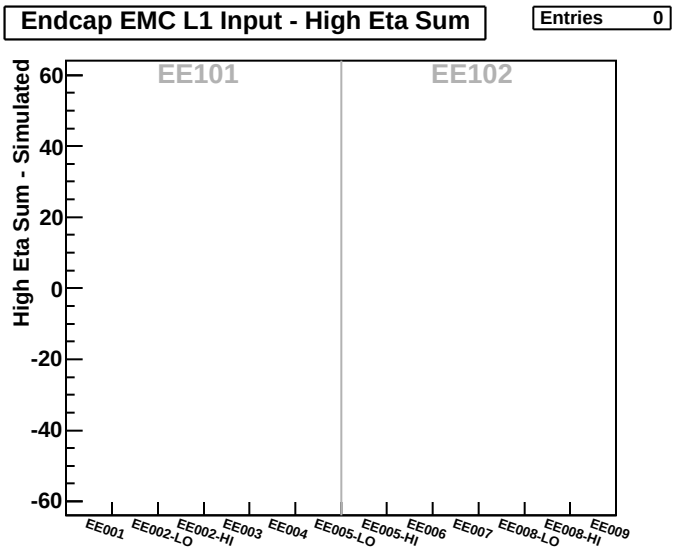

Barrel EMC L1 Input - High Tower Bits
Entries 5.853762e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

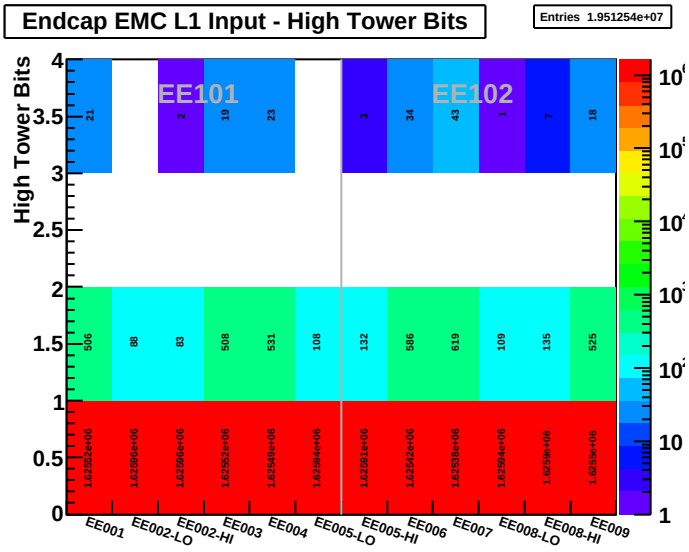

Barrel EMC L1 Input - High Tower Bits
Entries 9
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

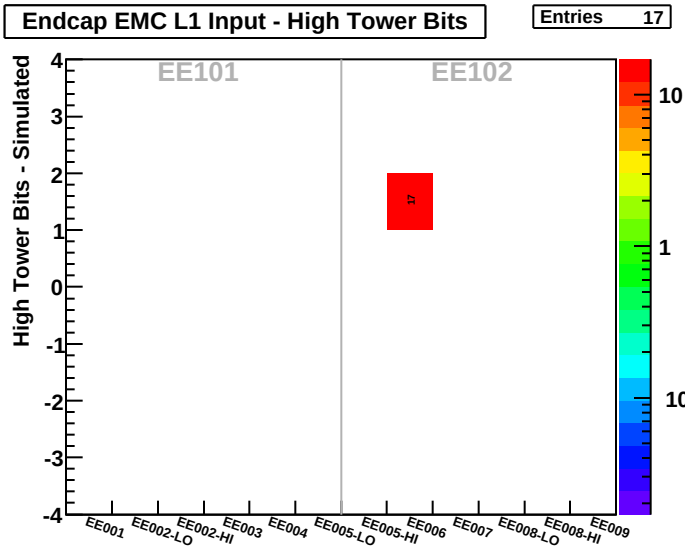

Endcap EMC L1 Input - Low Eta Sum
Entries 1.951254e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

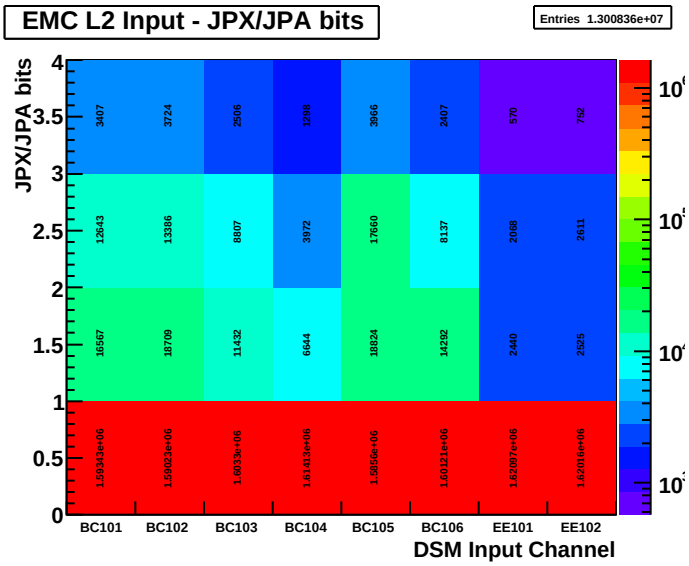

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

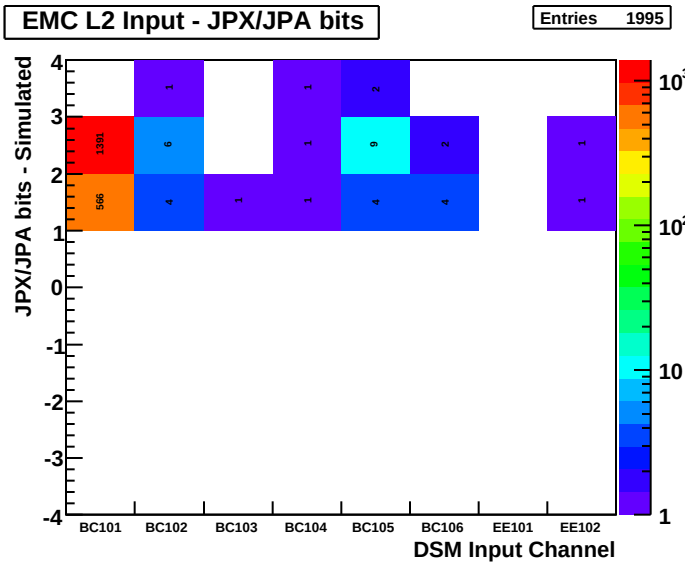

Endcap EMC L1 Input - High Eta Sum
Entries 1.951254e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

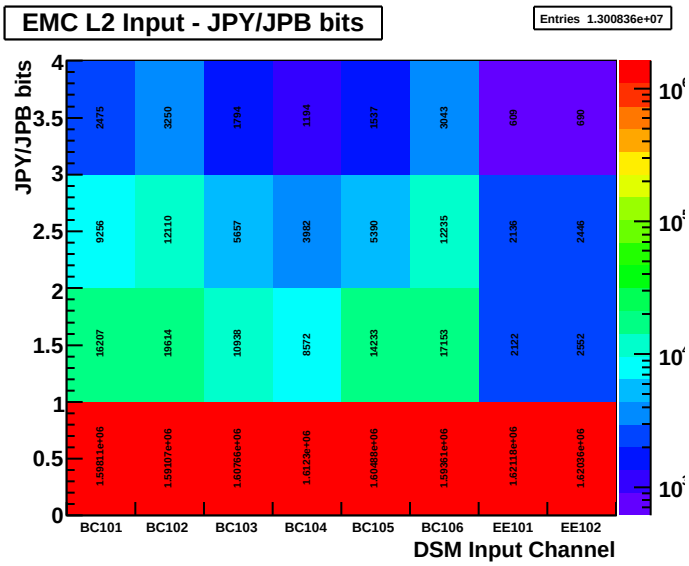

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

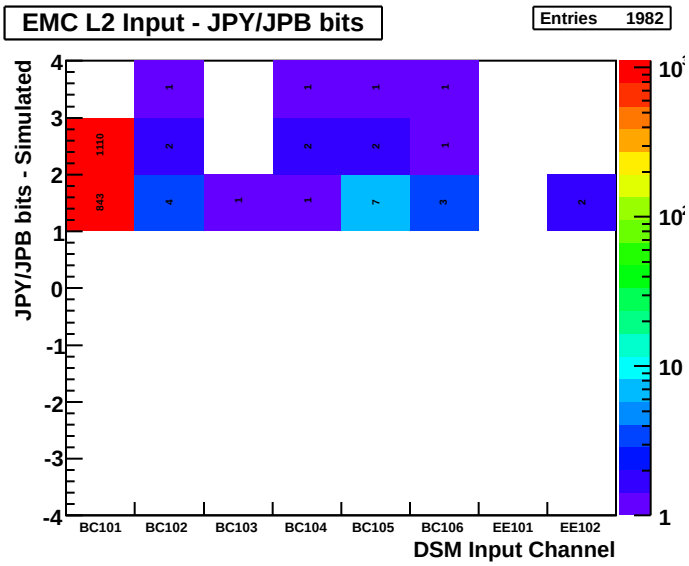

Endcap EMC L1 Input - High Tower Bits
Entries 1.951254e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

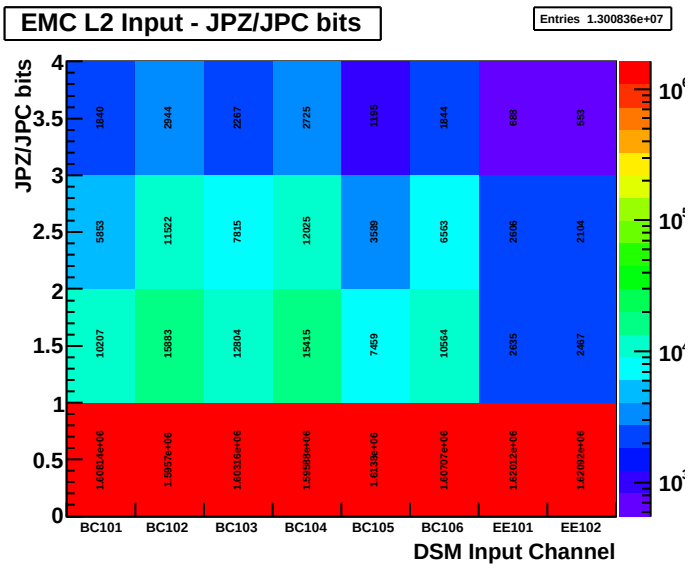

Endcap EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

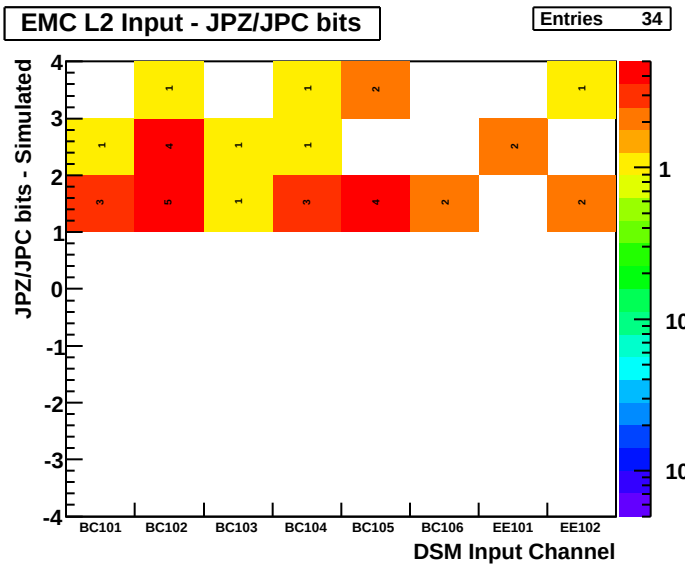

EMC L2 Input - JPX/JPA bits
Entries 1.300836e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3407 3724 2506 1238 3966 2407 570 752
12643 13386 8807 3972 17660 8137 2068 2611
16567 19709 11432 6644 18824 14292 2440 2525
1.59343e+06 1.59023e+06 1.6033e+06 1.61413e+06 1.5956e+06 1.60121e+06 1.62097e+06 1.62016e+06

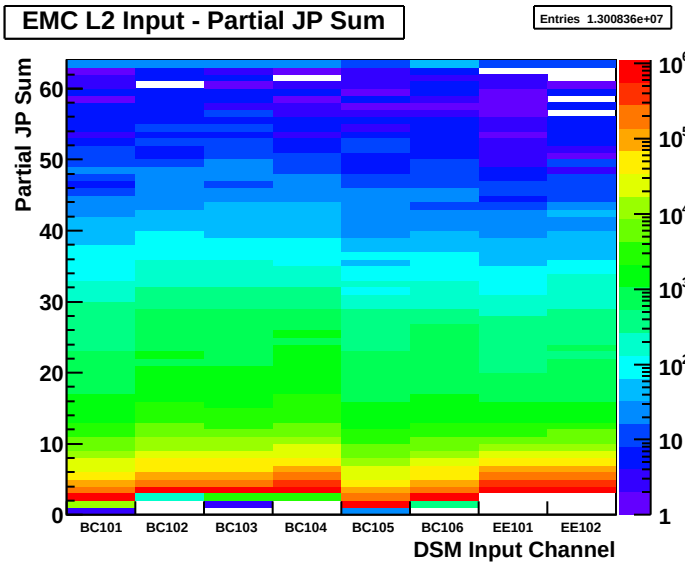

EMC L2 Input - JPX/JPA bits
Entries 1995
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1391 1 6 1 1 9 2 1
566 4 1 1 4 4 1

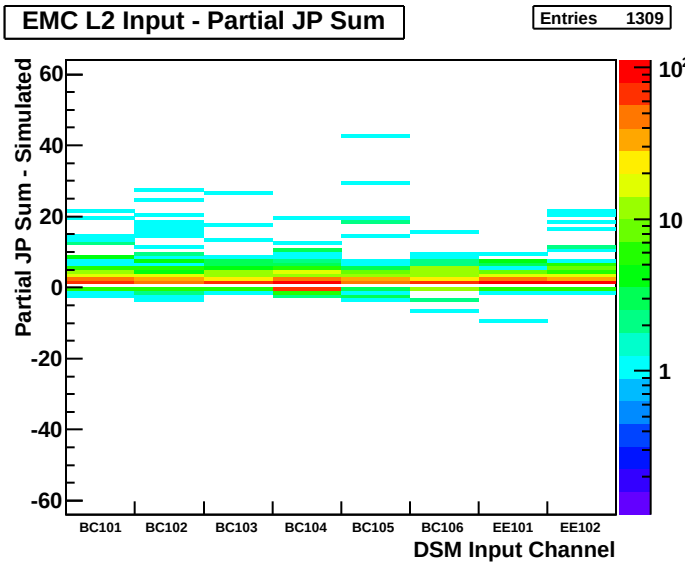

EMC L2 Input - JPY/JPB bits
Entries 1.300836e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2475 3250 1794 1194 1537 3043 699 690
9256 12110 5657 3982 5390 12235 2136 2446
16207 19614 10938 8572 14233 17153 2122 2552
1.59811e+06 1.59107e+06 1.60766e+06 1.6123e+06 1.60498e+06 1.59361e+06 1.62118e+06 1.62036e+06

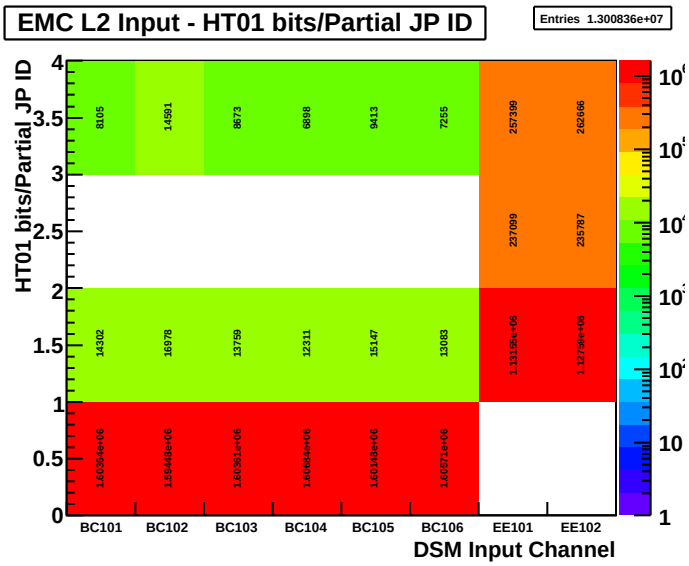

EMC L2 Input - JPY/JPB bits
Entries 1982
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1110 1 1 1 1 1
843 2 2 2 1
4 1 1 7 3 2

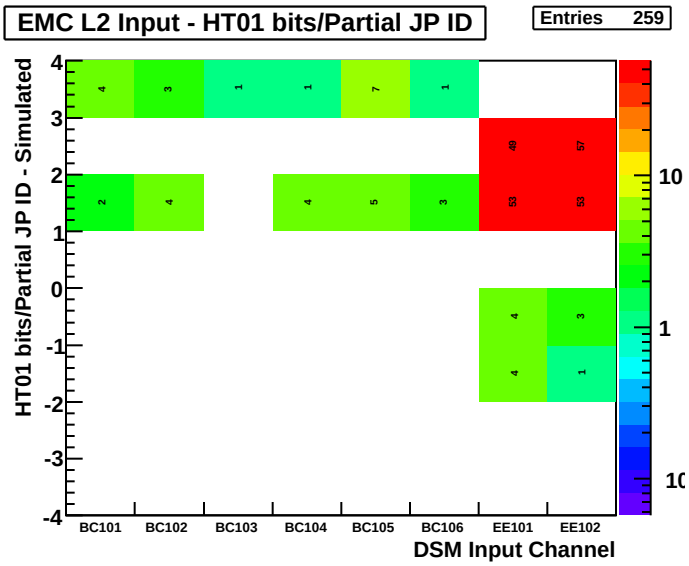

EMC L2 Input - JPZ/JPC bits
Entries 1.300836e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1040 2944 2267 2725 1195 1044 688 553
5853 11522 7815 12025 3589 6563 2606 2104
10207 15883 12804 15415 7469 10564 2635 2467
1.60814e+06 1.5957e+06 1.60316e+06 1.59593e+06 1.6138e+06 1.60707e+06 1.62012e+06 1.62092e+06

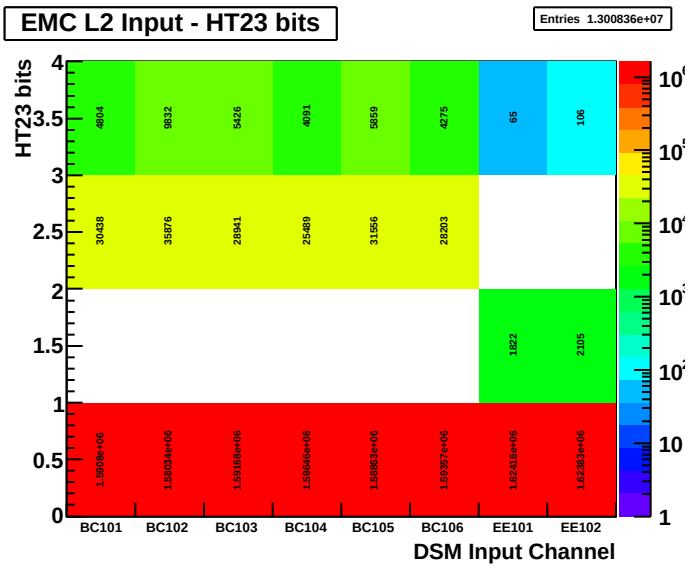

EMC L2 Input - JPZ/JPC bits
Entries 34
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 4 1 1 2 1
3 5 1 3 4 2 2

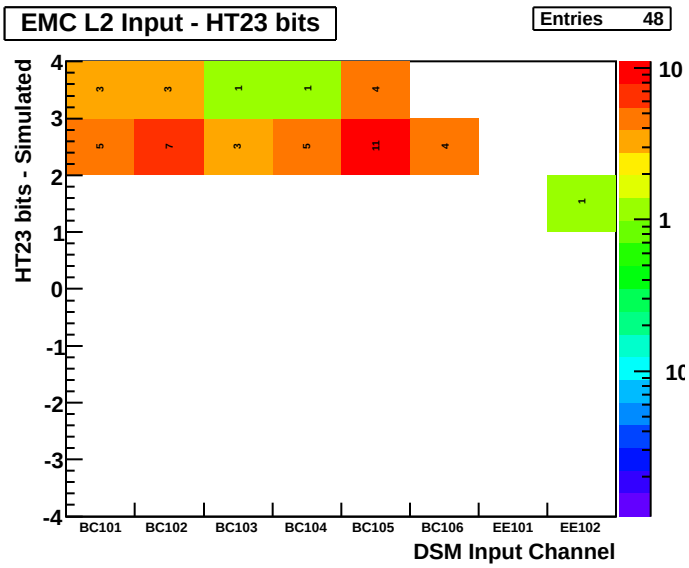

EMC L2 Input - Partial JP Sum
Entries 1.300836e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

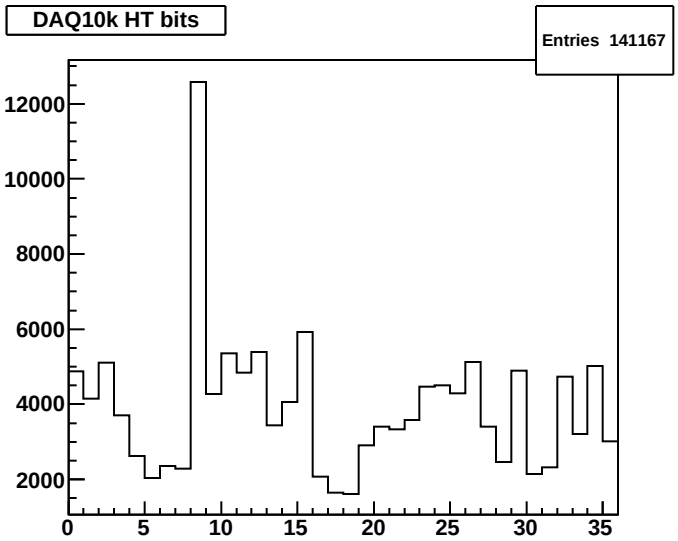

EMC L2 Input - Partial JP Sum
Entries 1309
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

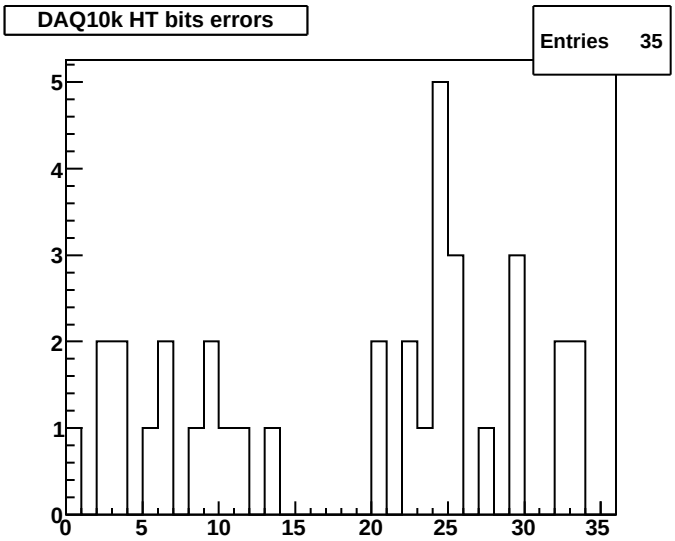

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.300836e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

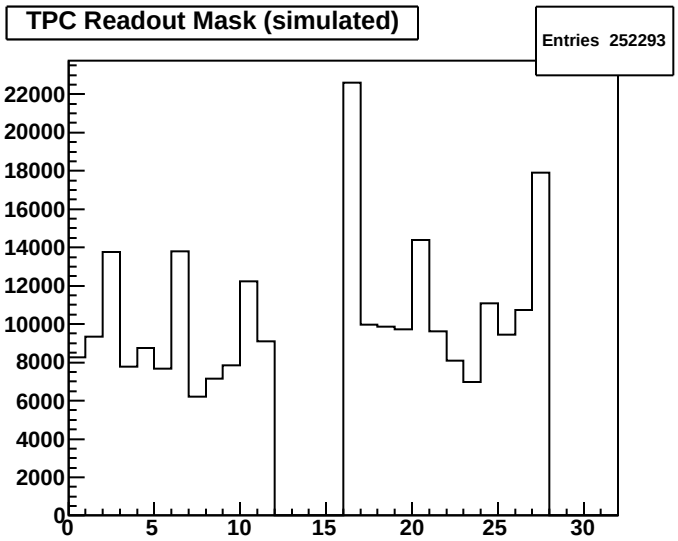

EMC L2 Input - HT01 bits/Partial JP ID
Entries 259
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

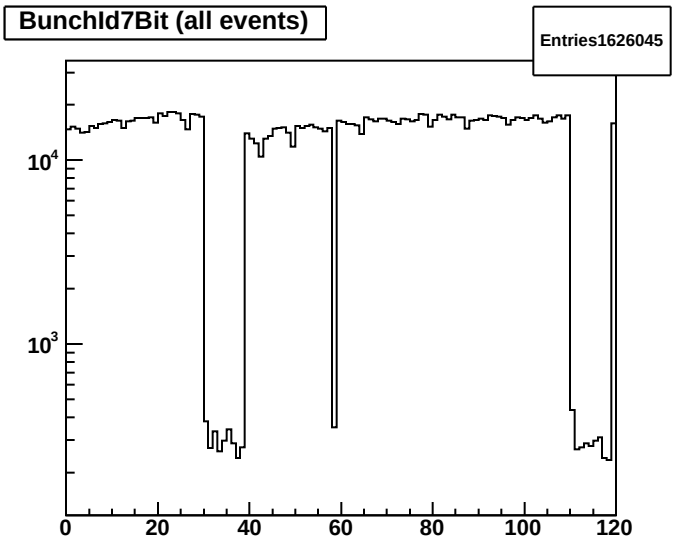

EMC L2 Input - HT23 bits
Entries 1.300836e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

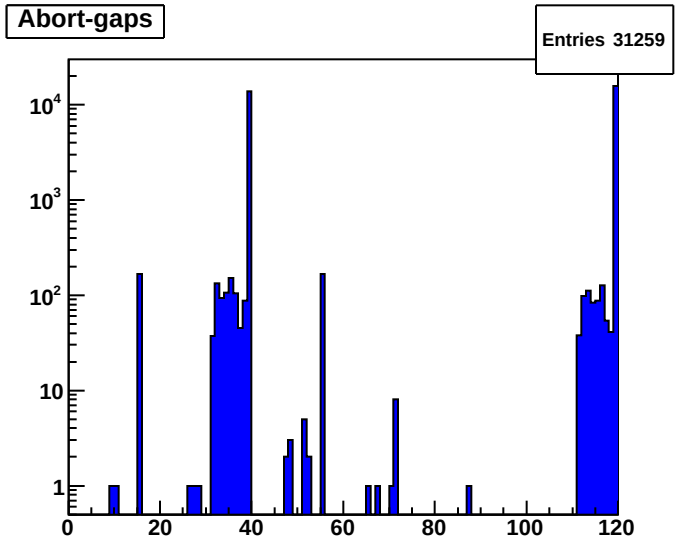

EMC L2 Input - HT23 bits
Entries 48
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

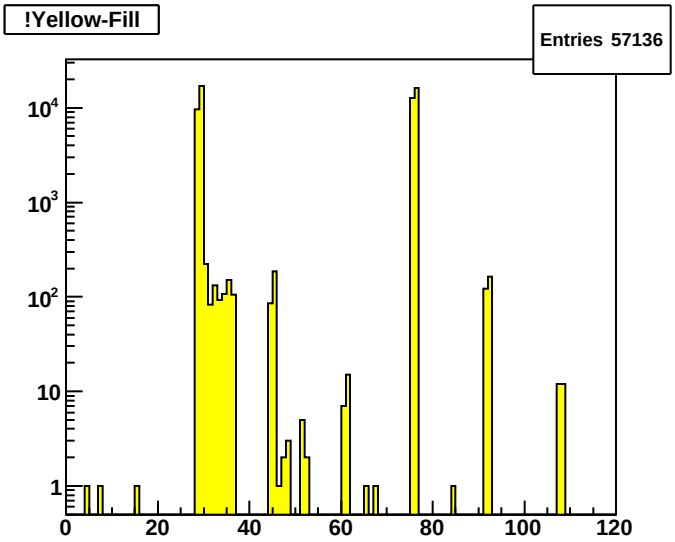

DAQ10k HT bits
Entries 141167

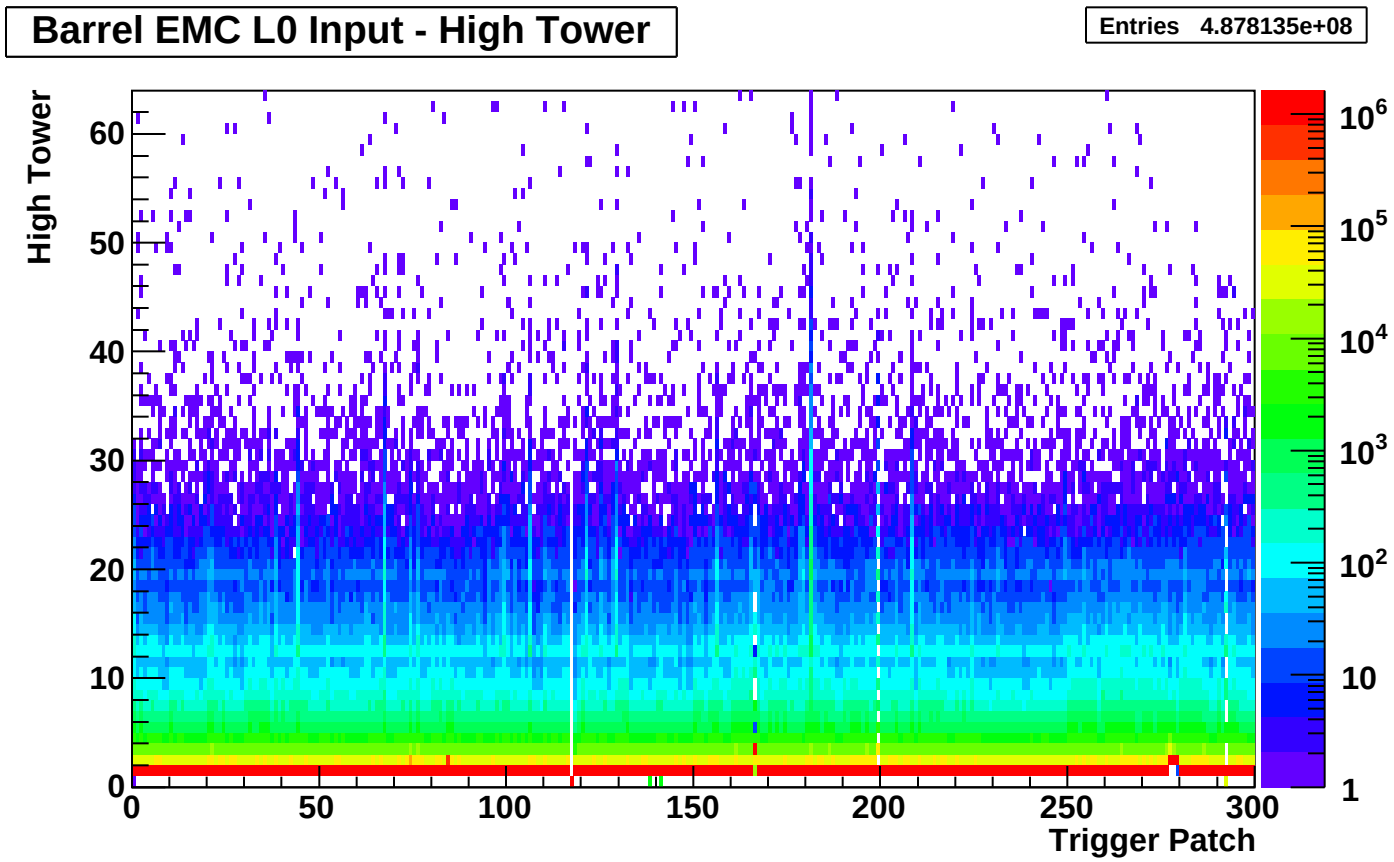

DAQ10k HT bits errors
Entries 35

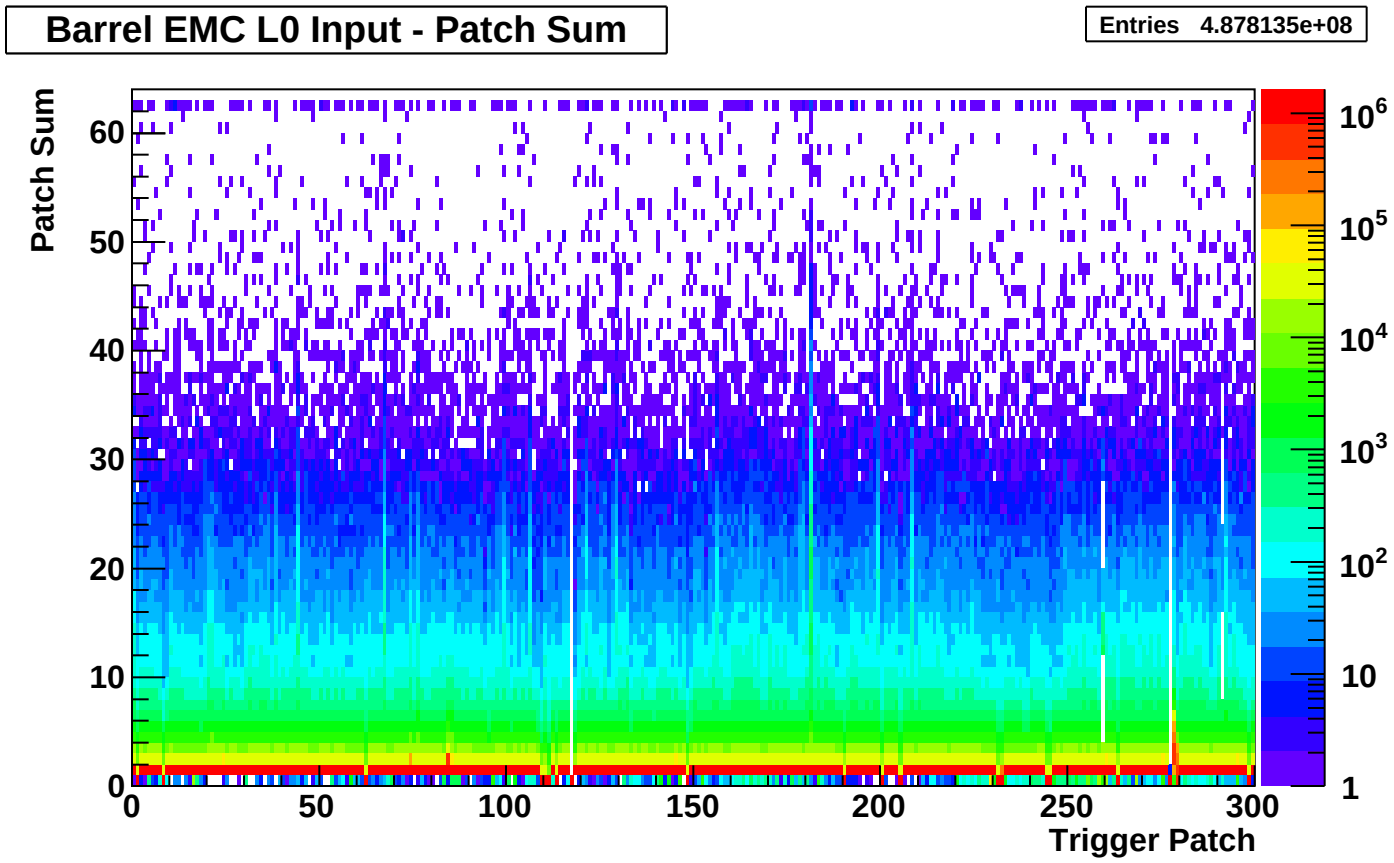

TPC Readout Mask (simulated)
Entries 252293

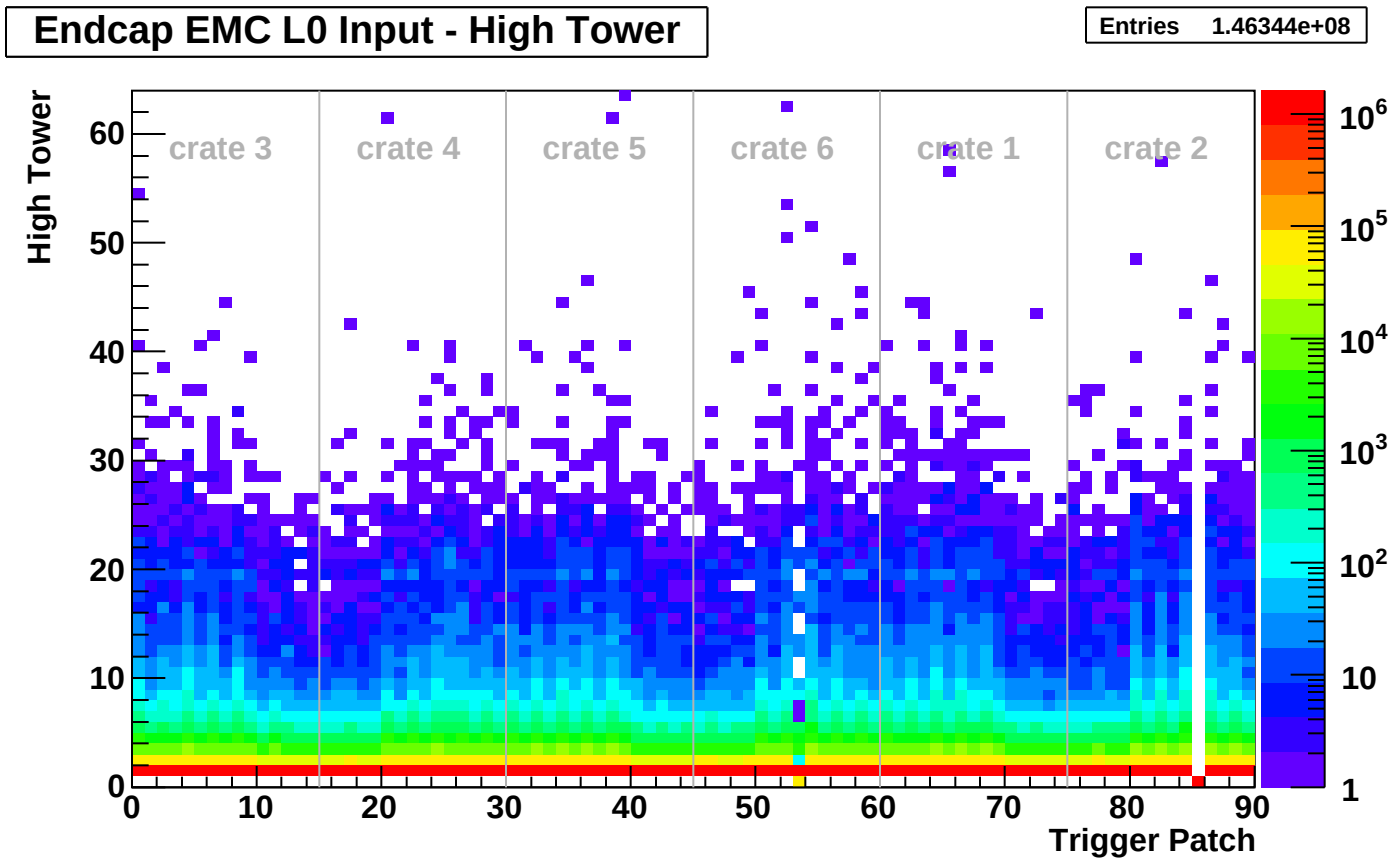

BunchId7Bit (all events)
Entries1626045

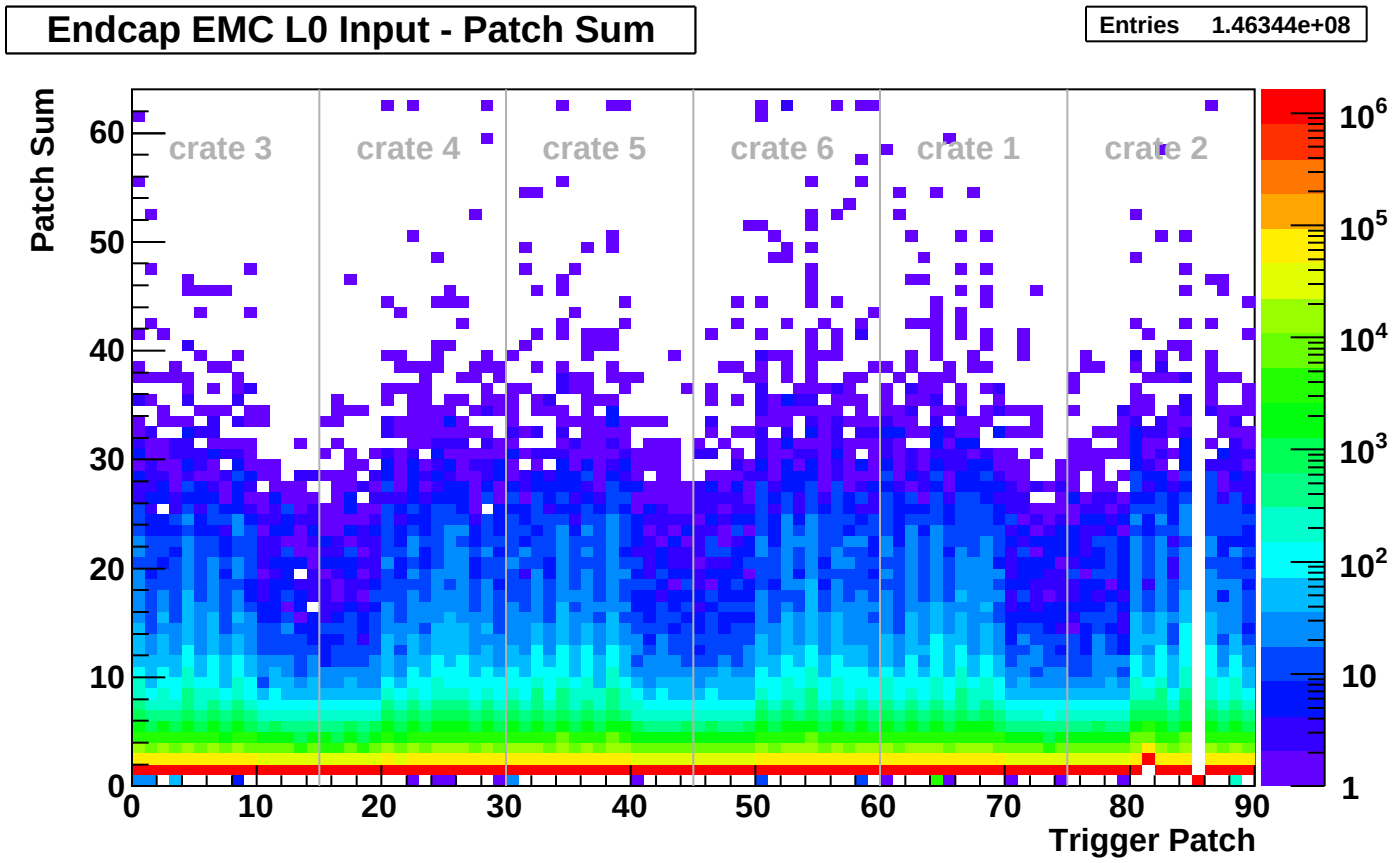

Abort-gaps
Entries 31259

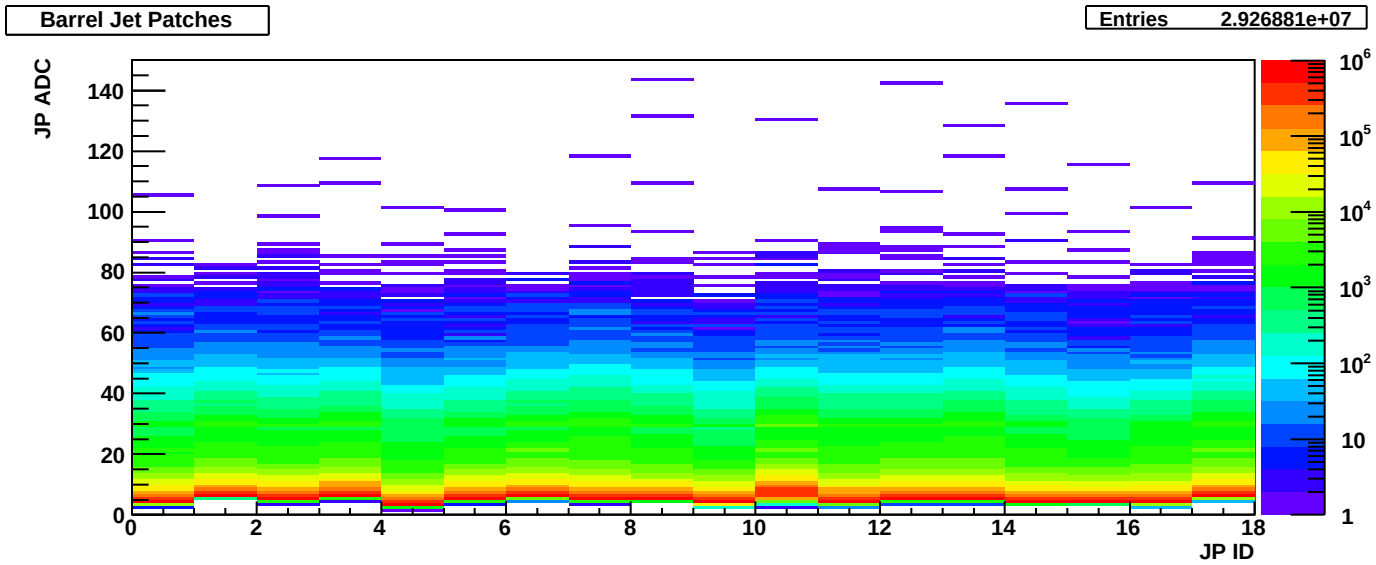

!Yellow-Fill
Entries 57136

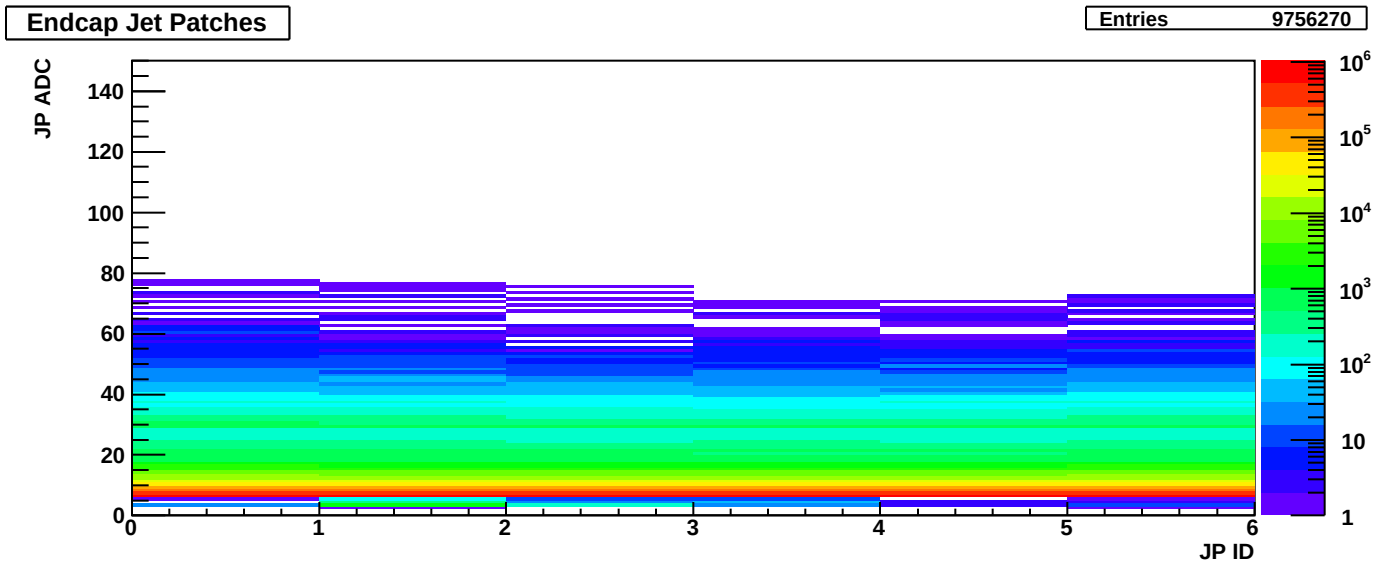

Barrel EMC L0 Input - High Tower
Entries 4.878135e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

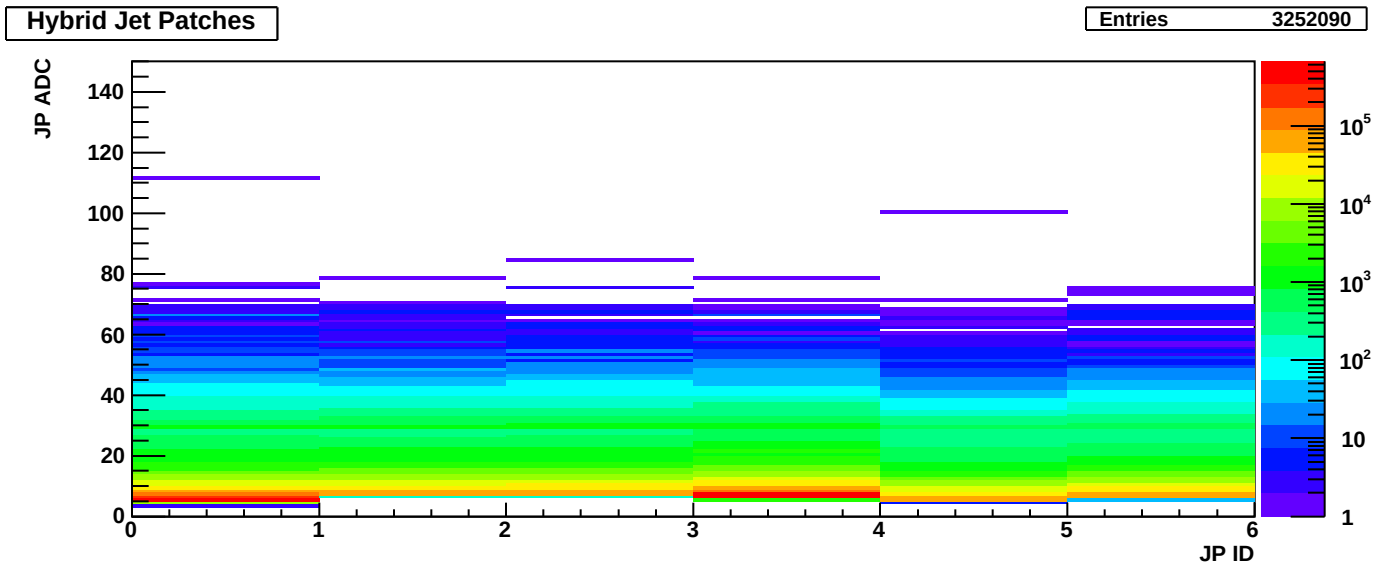

Barrel EMC L0 Input - Patch Sum
Entries 4.878135e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

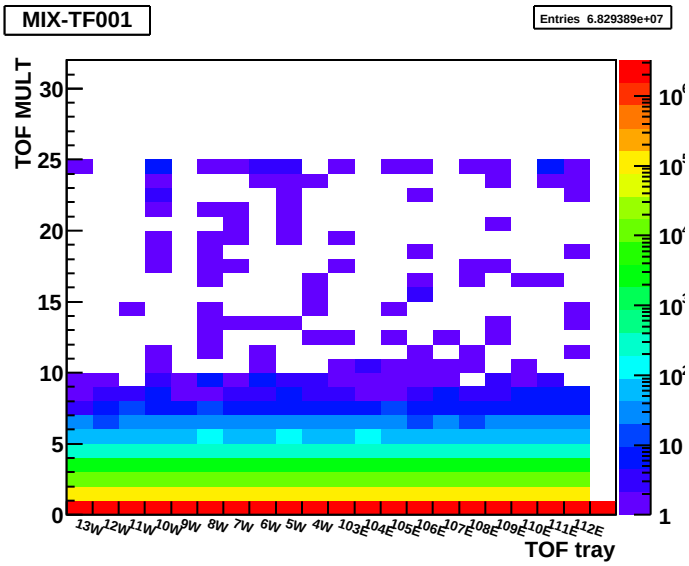

Endcap EMC L0 Input - High Tower
Entries 1.46344e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

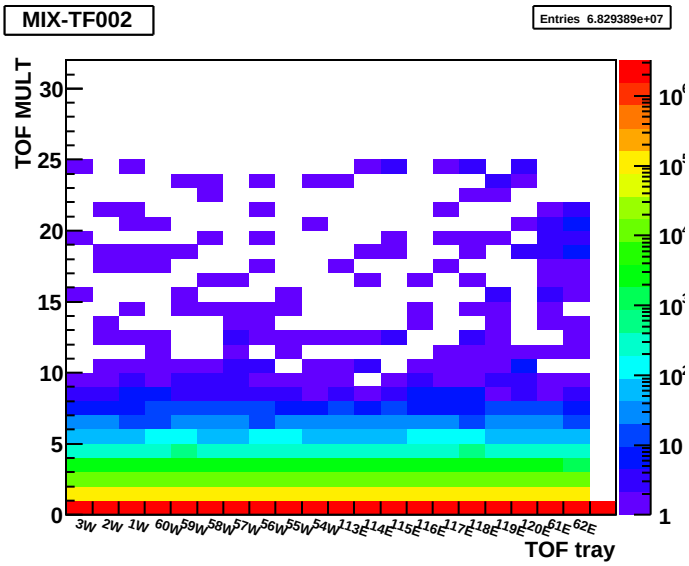

Endcap EMC L0 Input - Patch Sum
Entries 1.46344e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

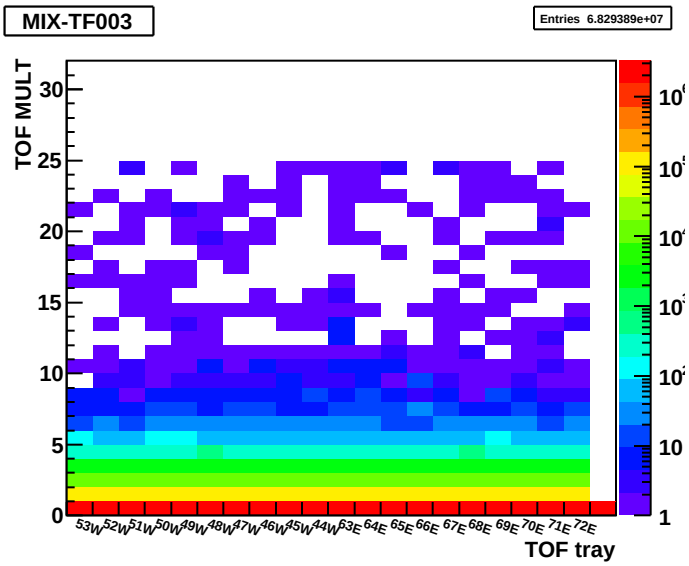

Barrel Jet Patches
Entries 2.926881e+07
JP ADC
JP ID

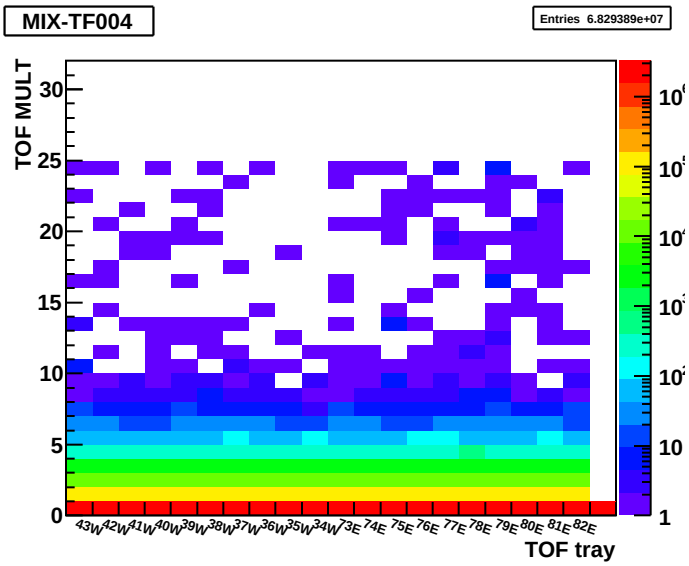

Endcap Jet Patches
Entries 9756270
JP ADC
JP ID

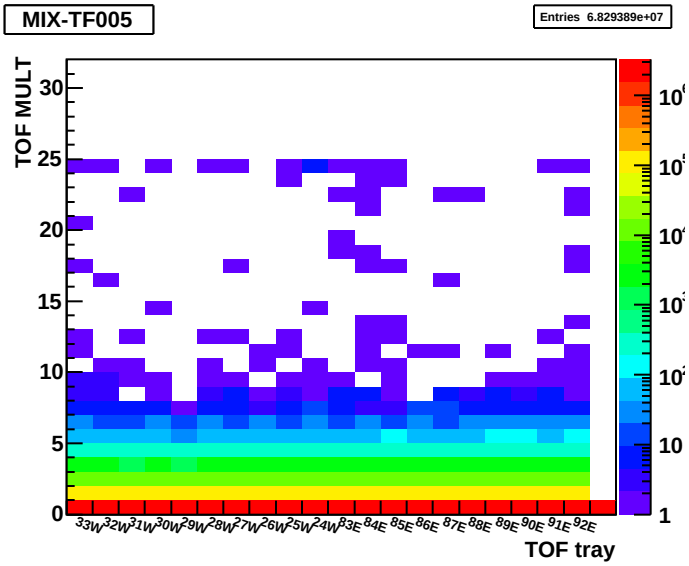

Hybrid Jet Patches
Entries 3252090
JP ADC
JP ID

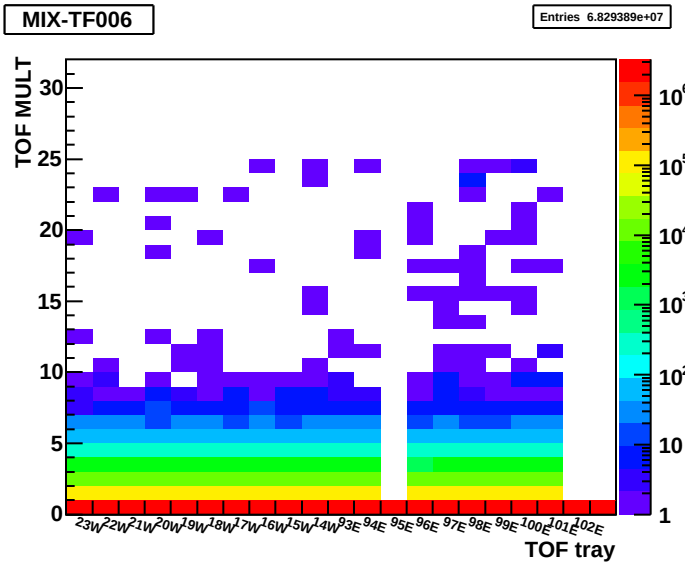

MIX-TF001
Entries 6.829389e+07
TOF MULT
TOF tray

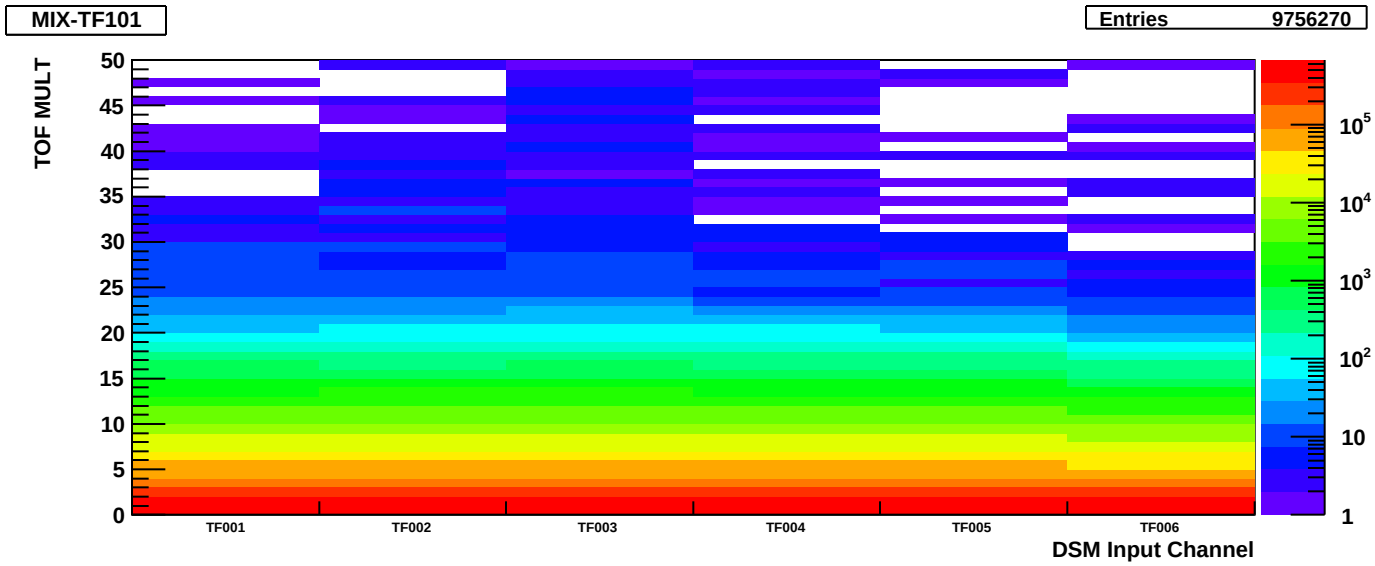

MIX-TF002
Entries 6.829389e+07
TOF MULT
TOF tray

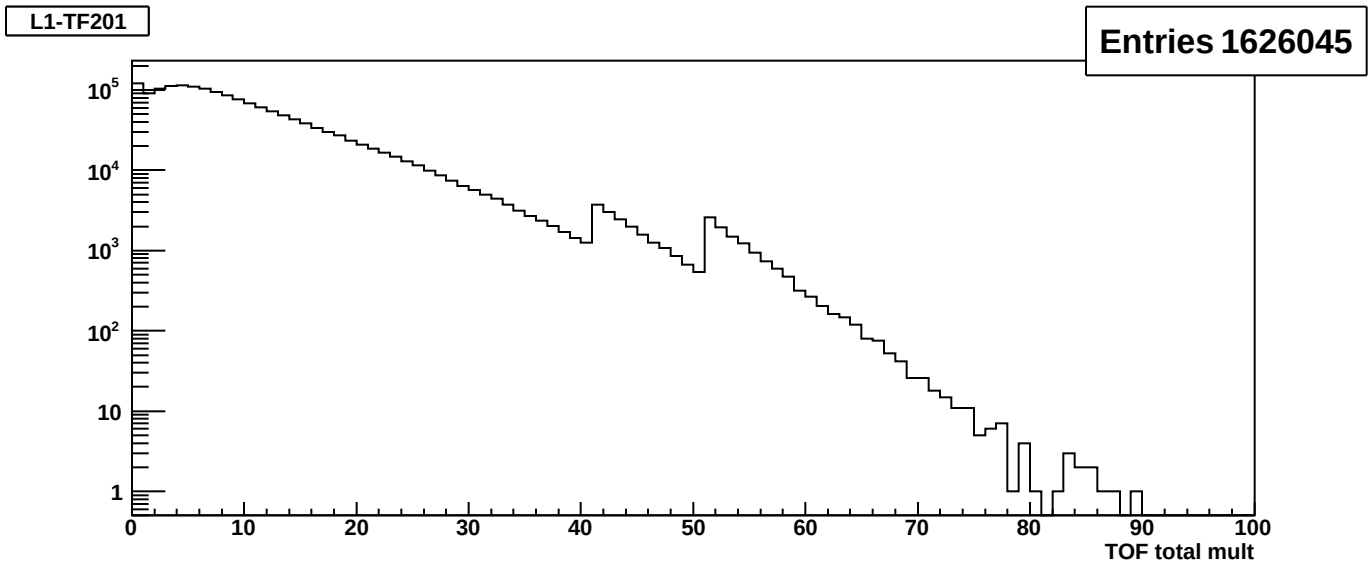

MIX-TF003
Entries 6.829389e+07
TOF MULT
TOF tray

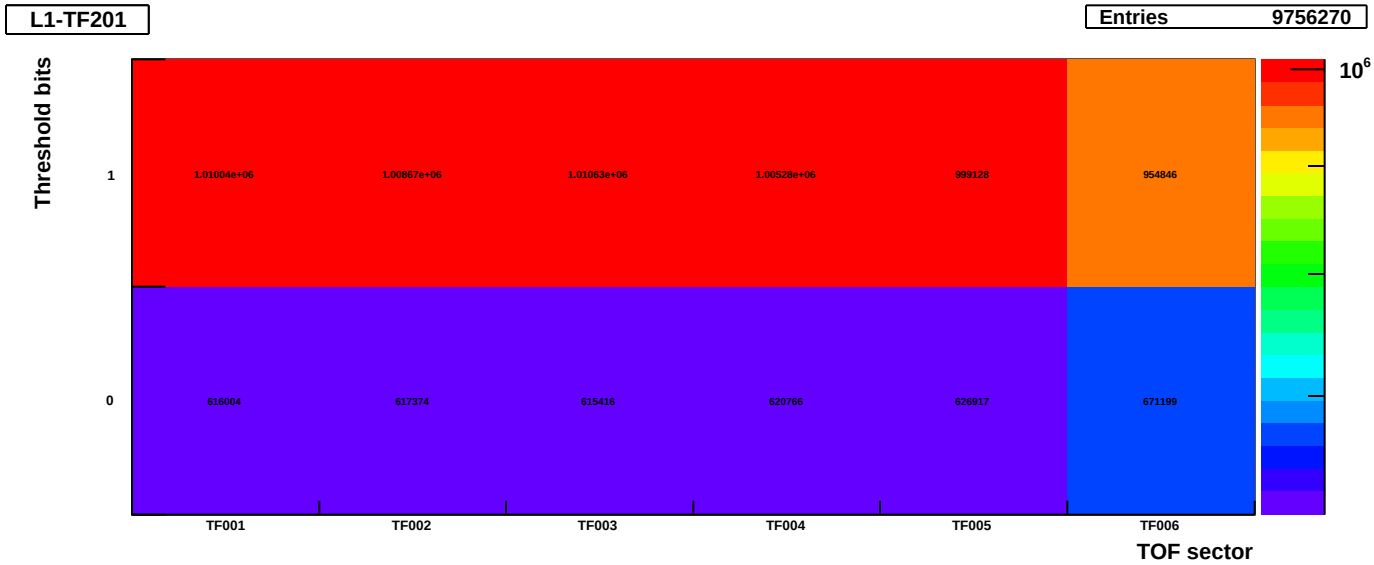

MIX-TF004
Entries 6.829389e+07
TOF MULT
TOF tray

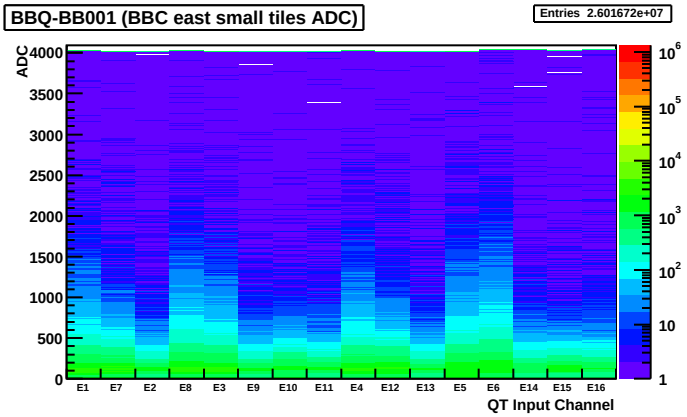

MIX-TF005
Entries 6.829389e+07
TOF MULT
TOF tray

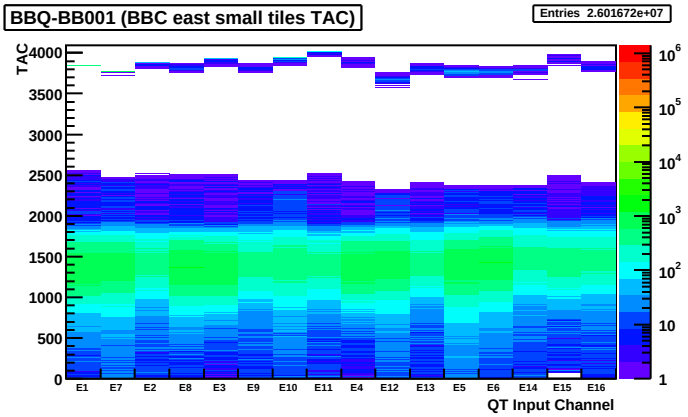

MIX-TF006
Entries 6.829389e+07
TOF MULT
TOF tray

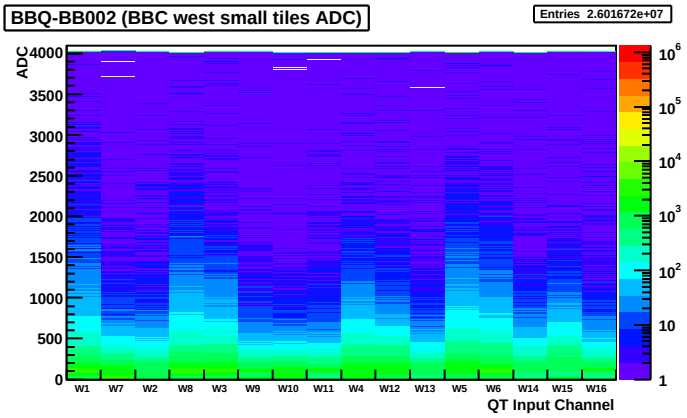

MIX-TF101
Entries 9756270
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

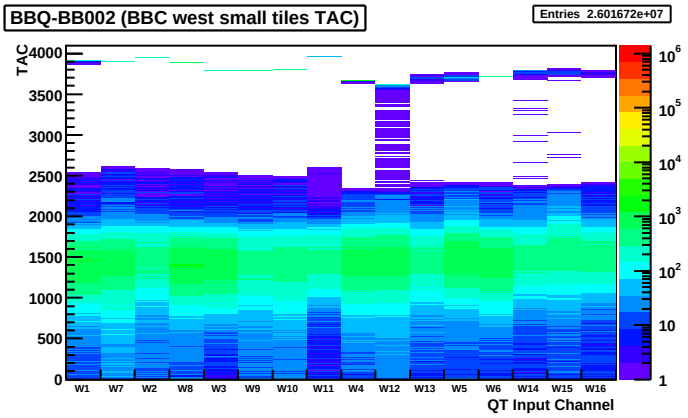

L1-TF201
Entries 1626045
TOF total mult

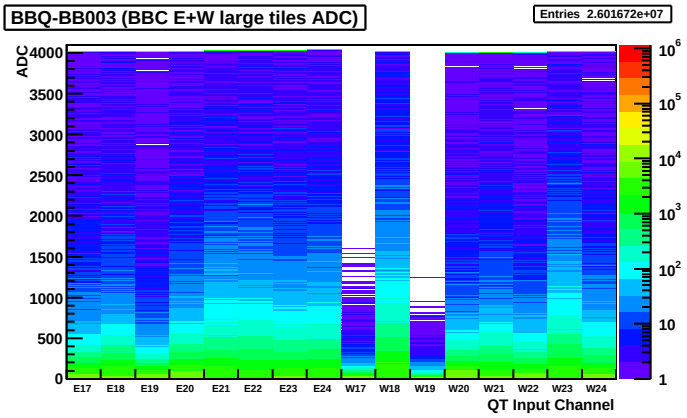

L1-TF201
Entries 9756270
Threshold bits
1.01004e+06
1.00867e+06
1.01063e+06
1.00528e+06
999128
954846
616004
617374
615416
620766
626917
671199
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

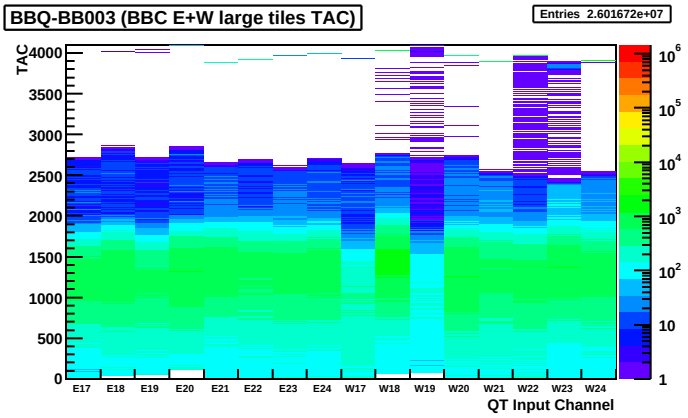

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.601672e+07
ADC
QT Input Channel

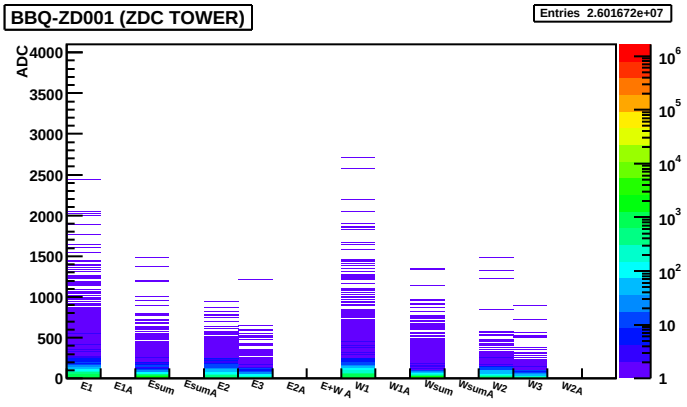

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.601672e+07
TAC
QT Input Channel

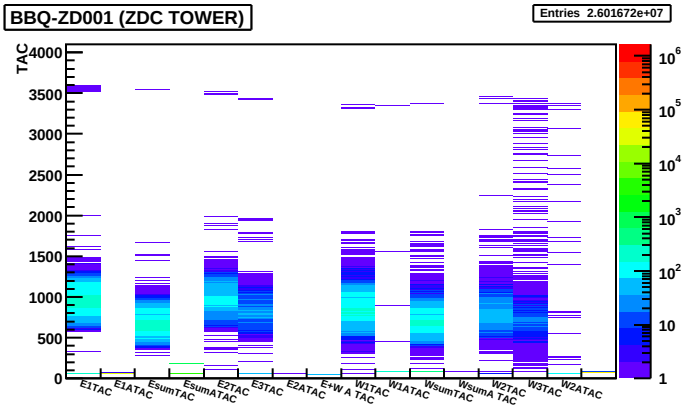

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.601672e+07
ADC
QT Input Channel

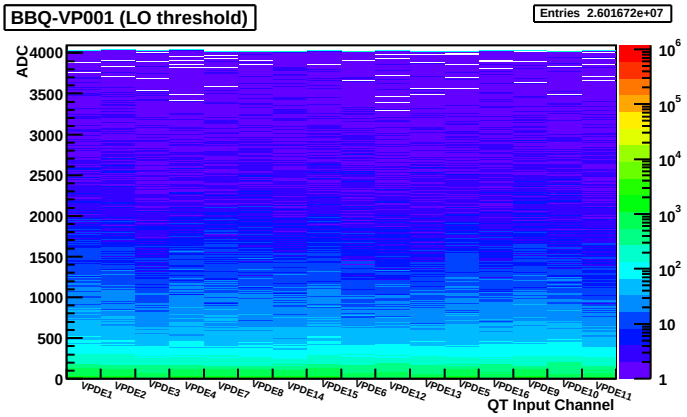

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.601672e+07
TAC
QT Input Channel

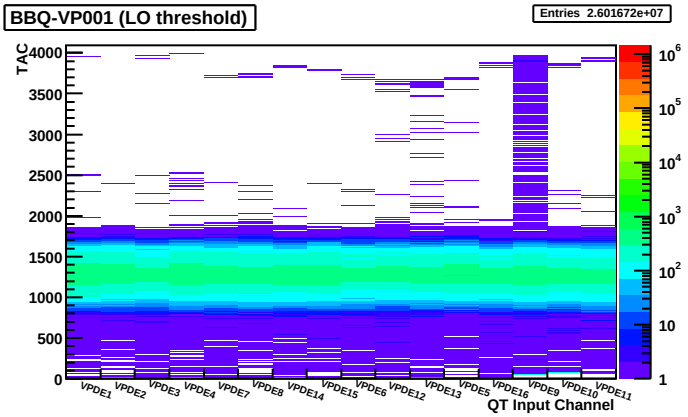

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.601672e+07
ADC
QT Input Channel

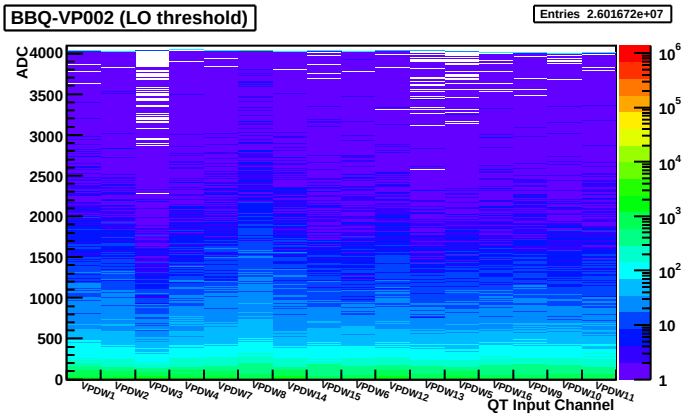

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.601672e+07
TAC
QT Input Channel

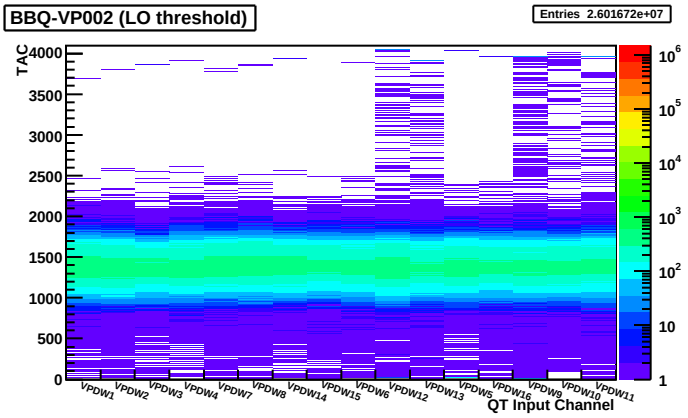

BBQ-ZD001 (ZDC TOWER)
Entries 2.601672e+07
ADC

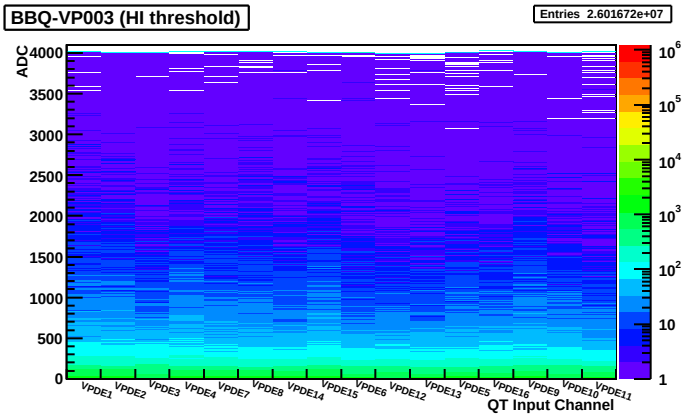

BBQ-ZD001 (ZDC TOWER)
Entries 2.601672e+07
TAC

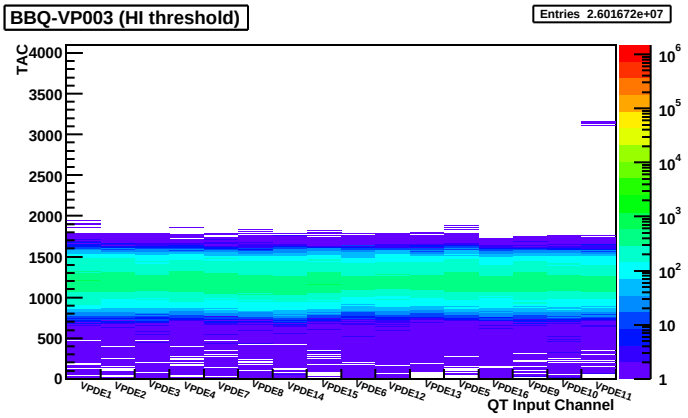

BBQ-VP001 (LO threshold)
Entries 2.601672e+07
ADC
QT Input Channel

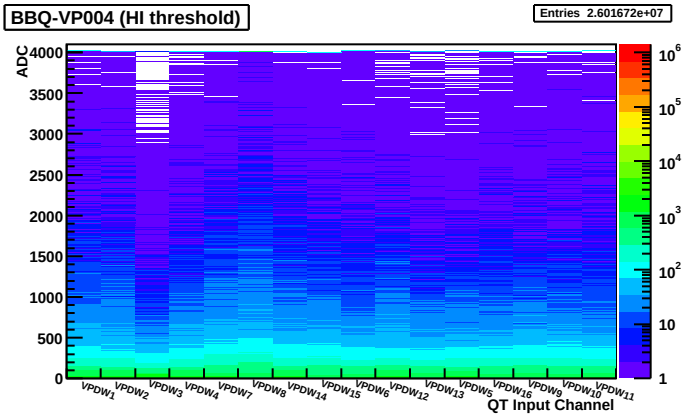

BBQ-VP001 (LO threshold)
Entries 2.601672e+07
TAC
QT Input Channel

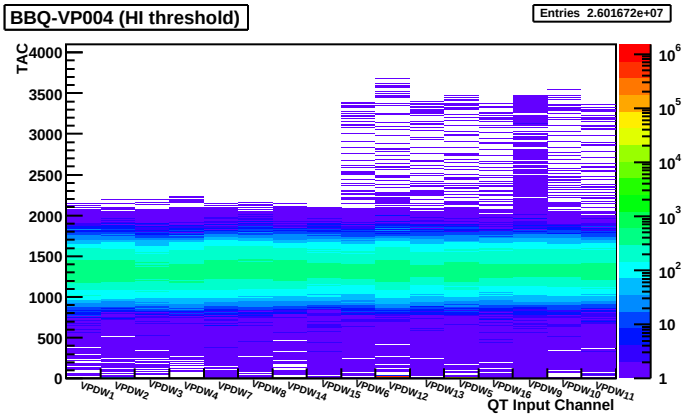

BBQ-VP002 (LO threshold)
Entries 2.601672e+07
ADC
QT Input Channel

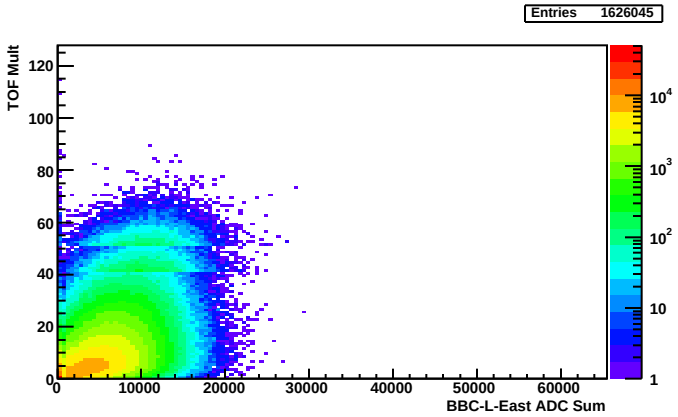

BBQ-VP002 (LO threshold)
Entries 2.601672e+07
TAC
QT Input Channel

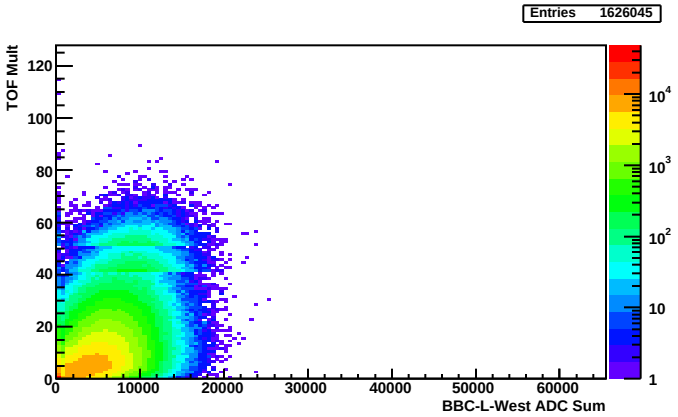

BBQ-VP003 (HI threshold)
Entries 2.601672e+07
ADC
QT Input Channel

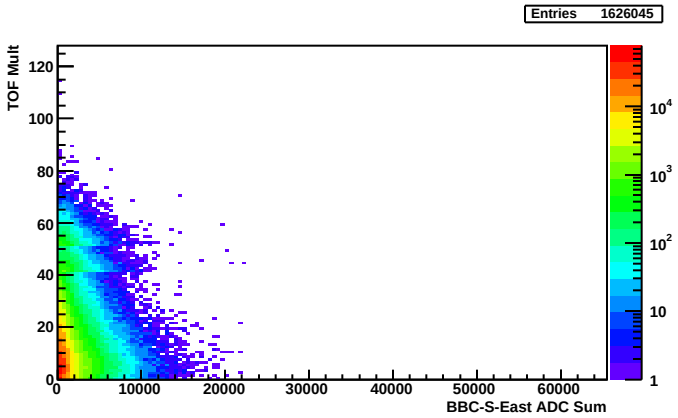

BBQ-VP003 (HI threshold)
Entries 2.601672e+07
TAC
QT Input Channel

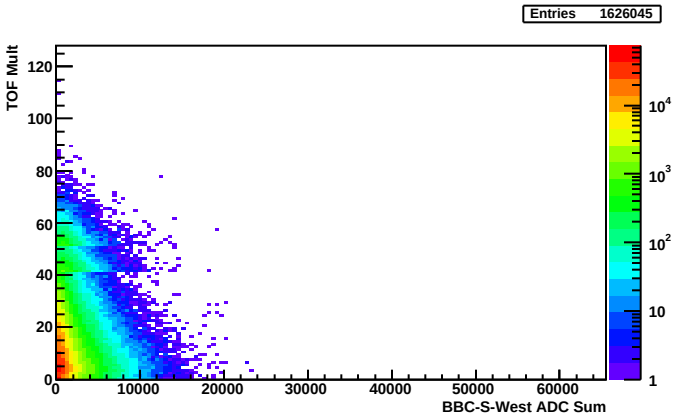

BBQ-VP004 (HI threshold)
Entries 2.601672e+07
ADC
QT Input Channel

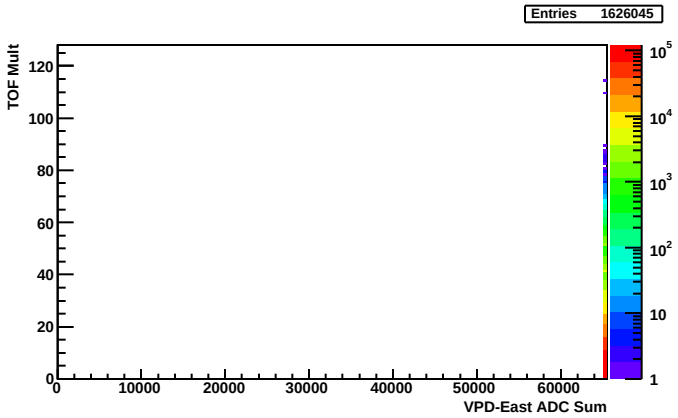

BBQ-VP004 (HI threshold)
Entries 2.601672e+07
TAC
QT Input Channel

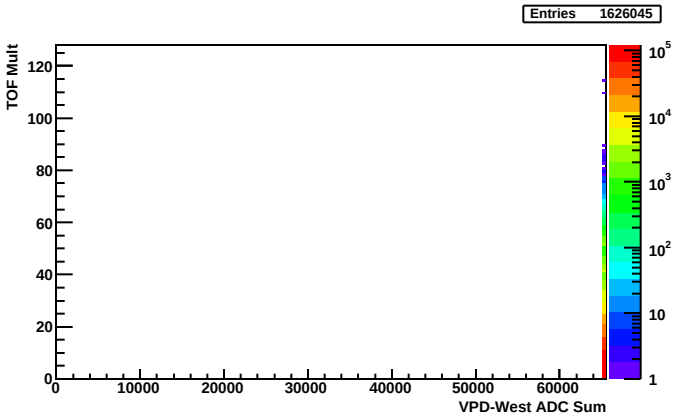

Entries 1626045
TOF Mult
BBC-L-East ADC Sum

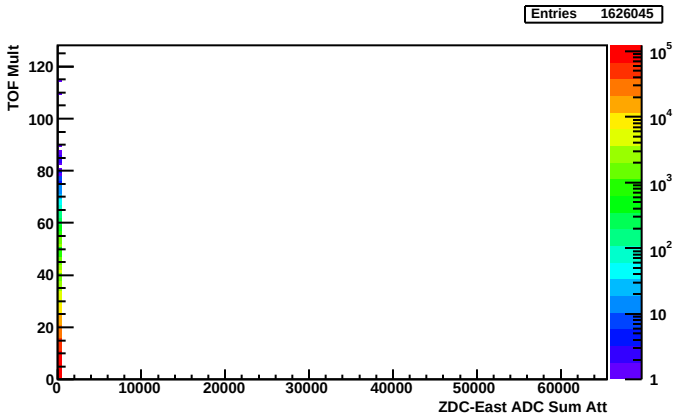

Entries 1626045
TOF Mult
BBC-L-West ADC Sum

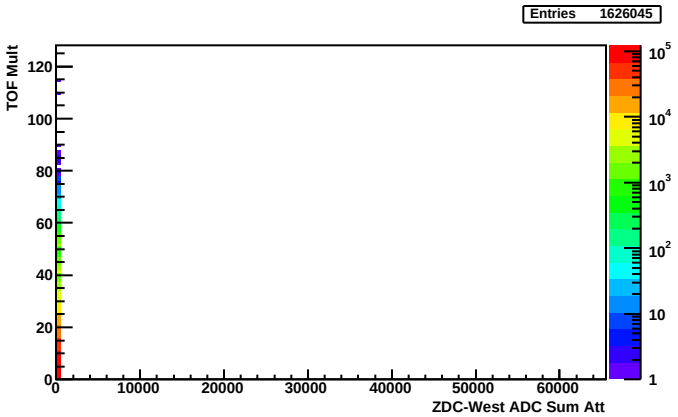

Entries 1626045
TOF Mult
BBC-S-East ADC Sum

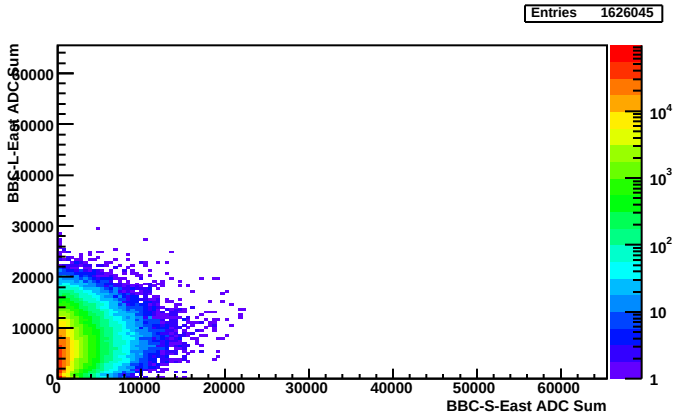

Entries 1626045
TOF Mult
BBC-S-West ADC Sum

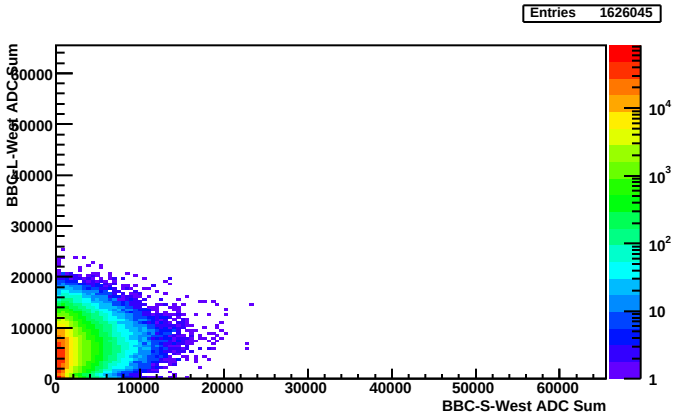

Entries 1626045
TOF Mult
VPD-East ADC Sum

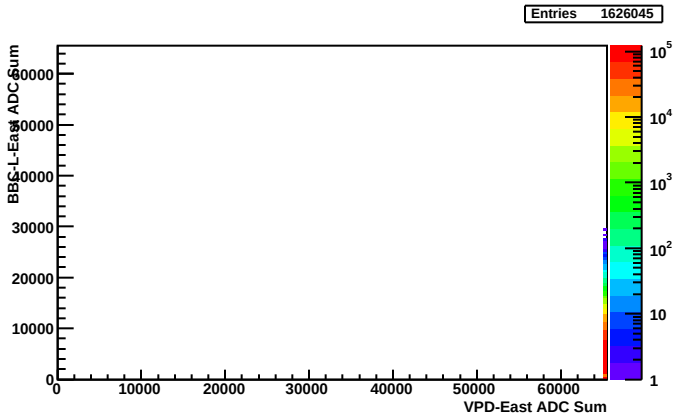

Entries 1626045
TOF Mult
VPD-West ADC Sum

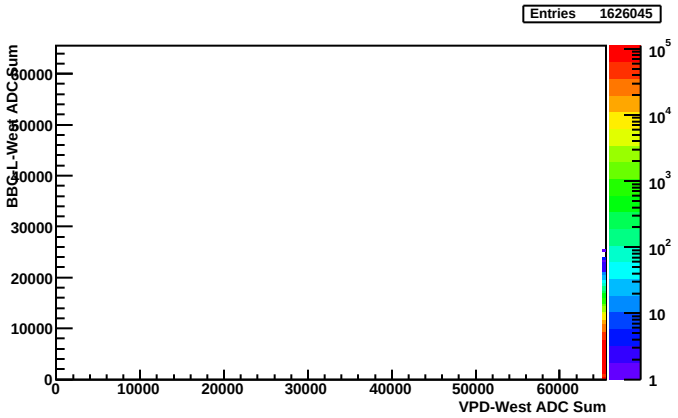

Entries 1626045
TOF Mult
ZDC-East ADC Sum Att

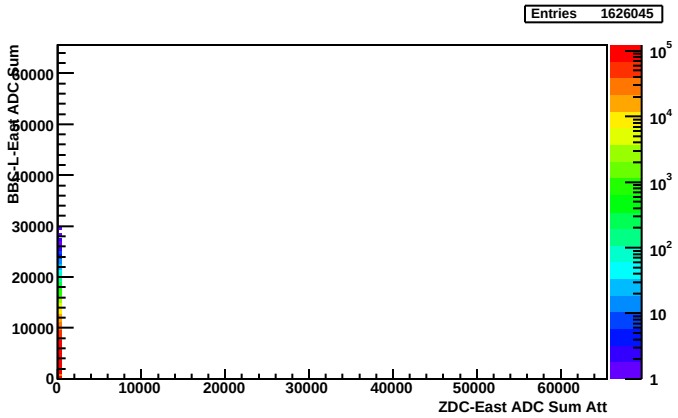

Entries 1626045
TOF Mult
ZDC-West ADC Sum Att

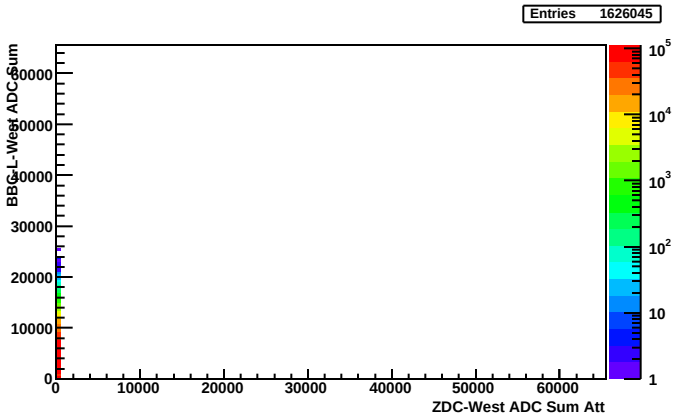

Entries 1626045
BBC-L-East ADC Sum
BBC-S-East ADC Sum

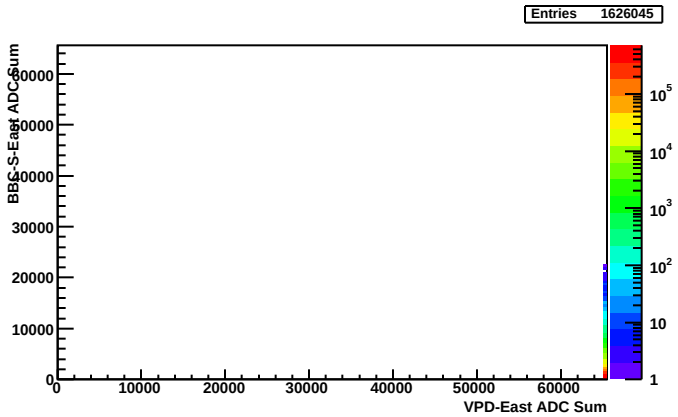

Entries 1626045
BBC-L-West ADC Sum
BBC-S-West ADC Sum

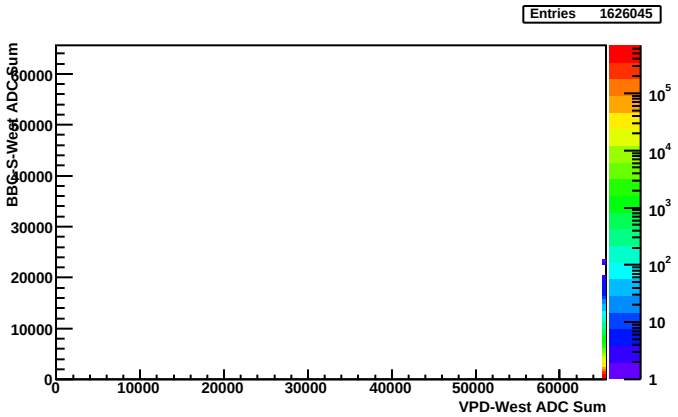

Entries 1626045
BBC-L-East ADC Sum
VPD-East ADC Sum

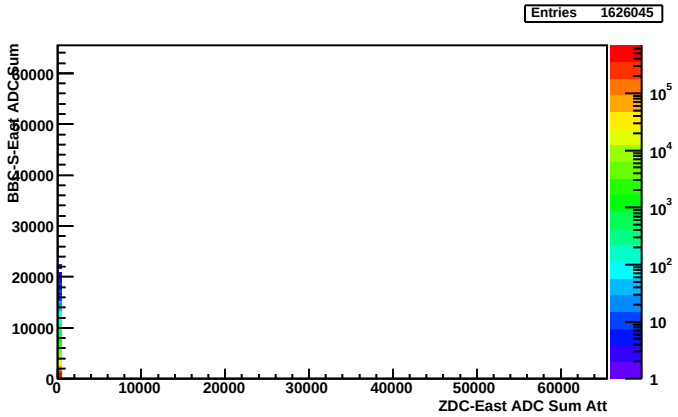

Entries 1626045
BBC-L-West ADC Sum
VPD-West ADC Sum

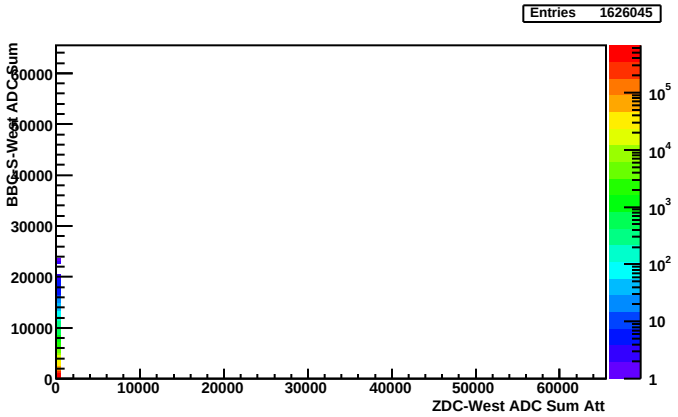

Entries 1626045
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

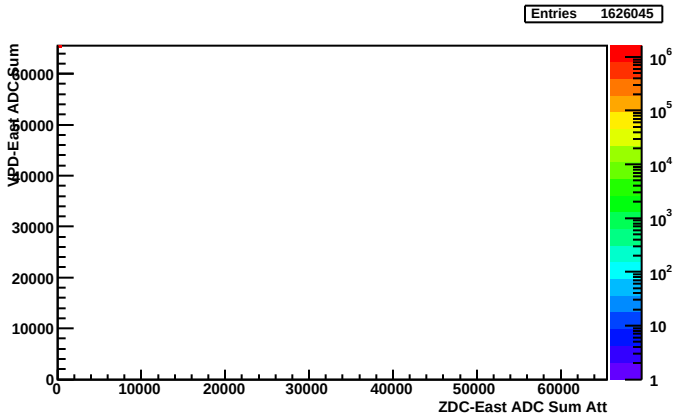

Entries 1626045
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

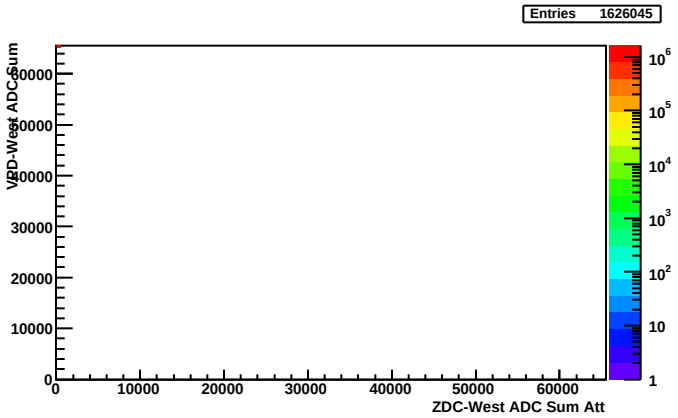

Entries 1626045
BBC-S-East ADC Sum
VPD-East ADC Sum

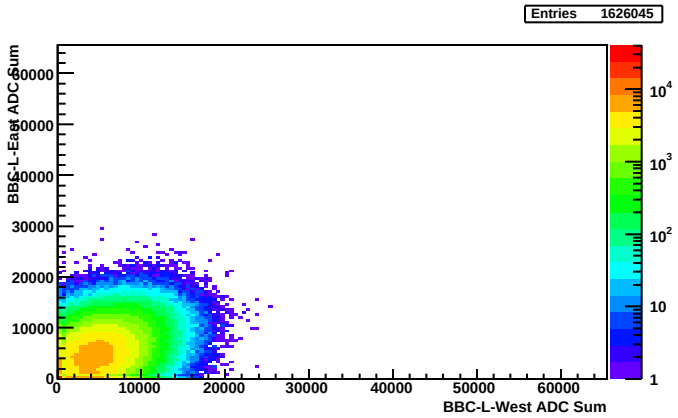

Entries 1626045
BBC-S-West ADC Sum
VPD-West ADC Sum

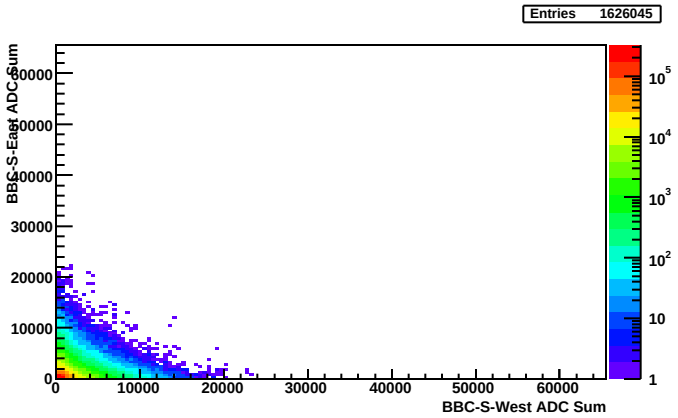

Entries 1626045
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1626045
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1626045
VPD-East ADC Sum
ZDC-East ADC Sum Att


Entries 1626045
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 1626045
BBC-L-East ADC Sum
BBC-L-West ADC Sum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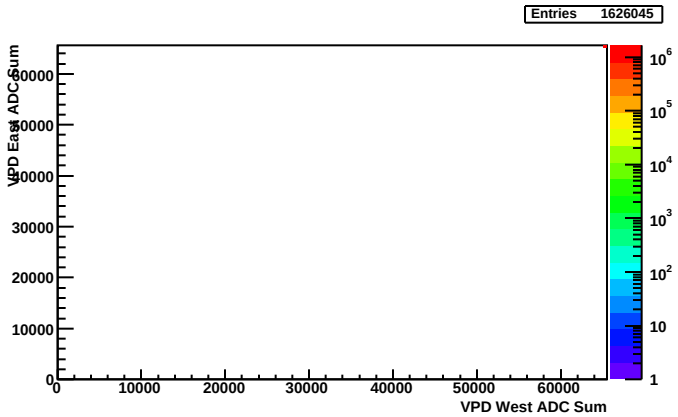
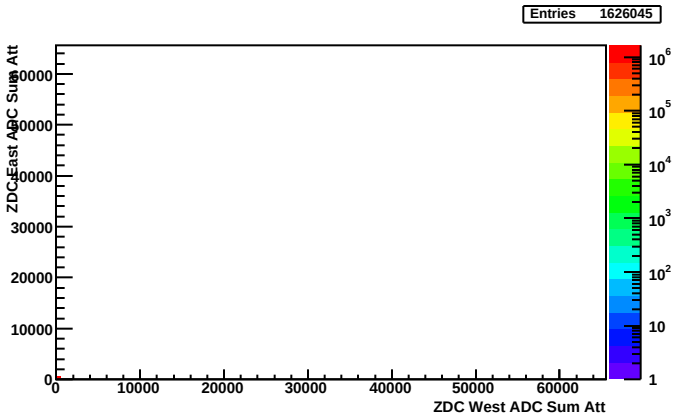
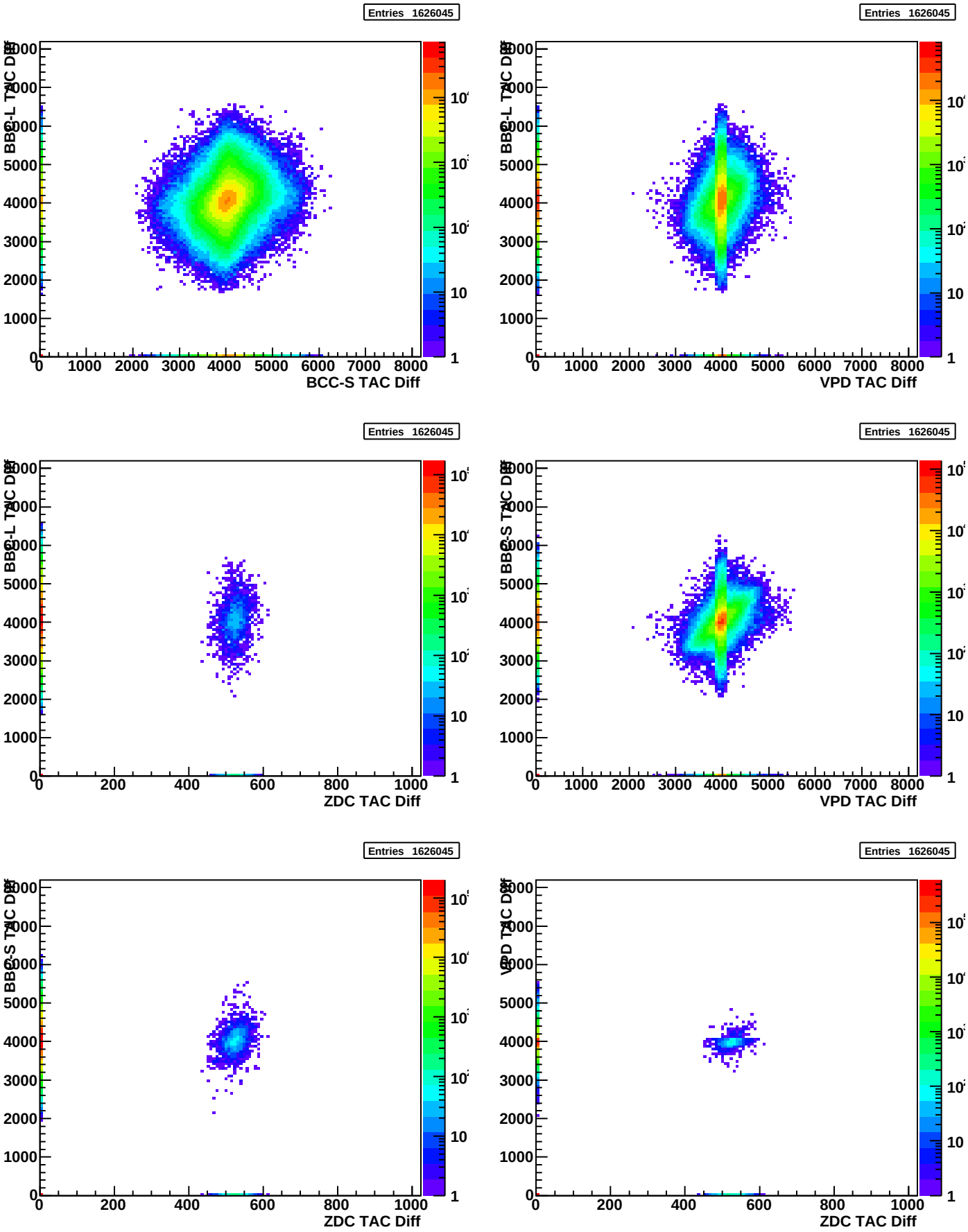
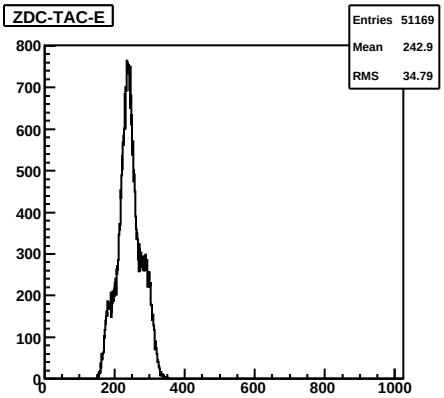
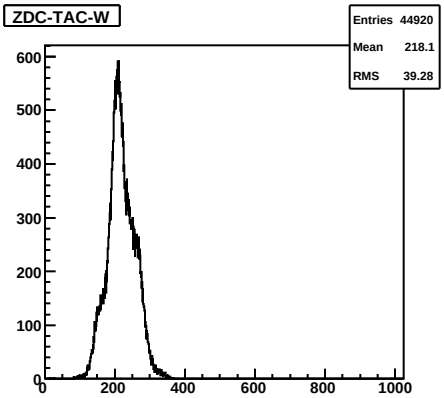
BBC-S-East ADC Sum
BBC-S-West ADC Sum

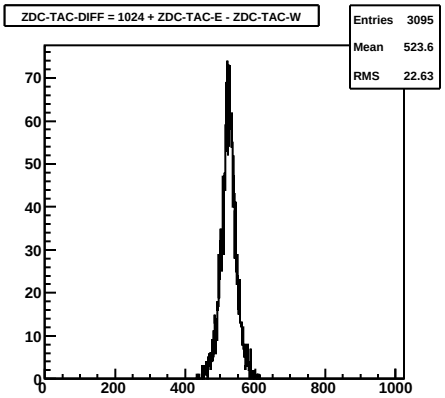

Entries 1626045
VPD East ADC Sum
VPD West ADC Sum

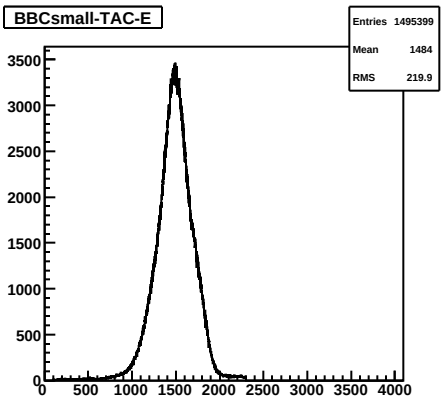

Entries 1626045
ZDC East ADC Sum Att
ZDC West ADC Sum Att

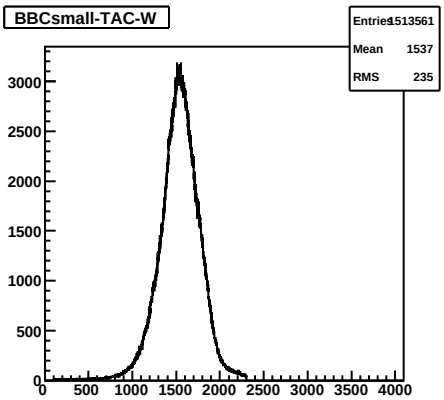

Entries 1626045
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1626045
BBC-L TAC Diff
VPD TAC Diff
Entries 1626045
BBC-L TAC Diff
ZDC TAC Diff
Entries 1626045
BBC-S TAC Diff
VPD TAC Diff
Entries 1626045
BBC-S TAC Diff
ZDC TAC Diff
Entries 1626045
VPD TAC Diff
ZDC TAC Diff

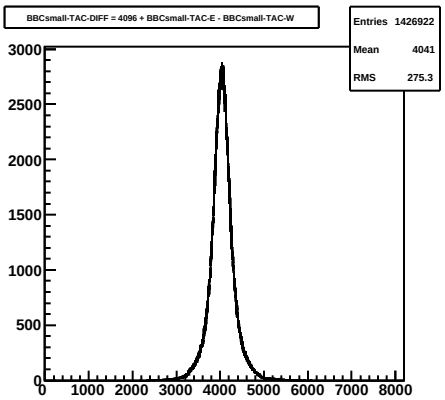

ZDC-TAC-E
Entries 51169
Mean 242.9
RMS 34.79

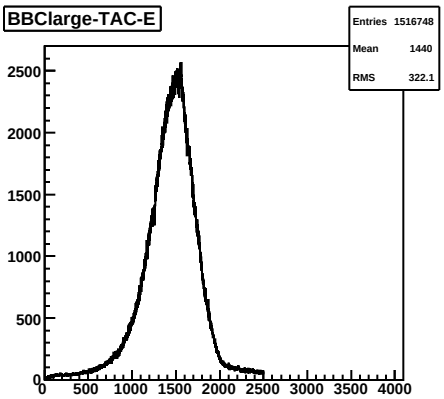

ZDC-TAC-W
Entries 44920
Mean 218.1
RMS 39.28

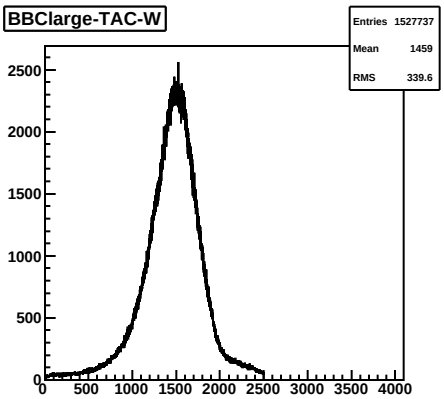

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3095
Mean 523.6
RMS 22.63

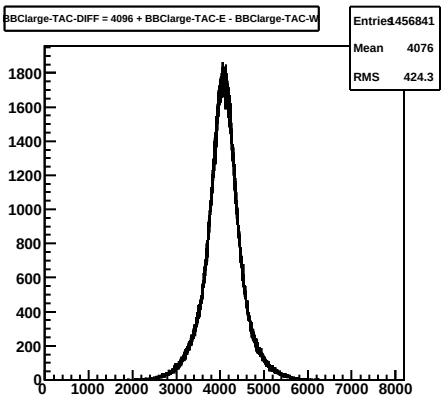

BBCsmall-TAC-E
Entries 1495399
Mean 1484
RMS 219.9

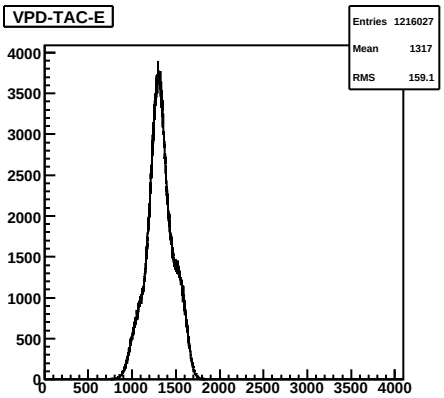

BBCsmall-TAC-W
Entries 1513561
Mean 1537
RMS 235

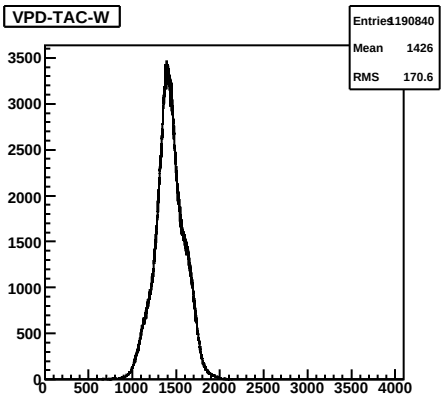

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1426922
Mean 4041
RMS 275.3

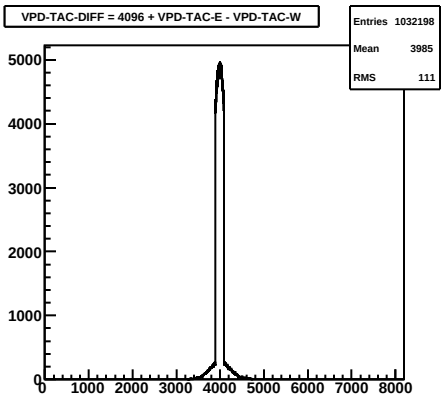

BBClarge-TAC-E
Entries 1516748
Mean 1440
RMS 322.1

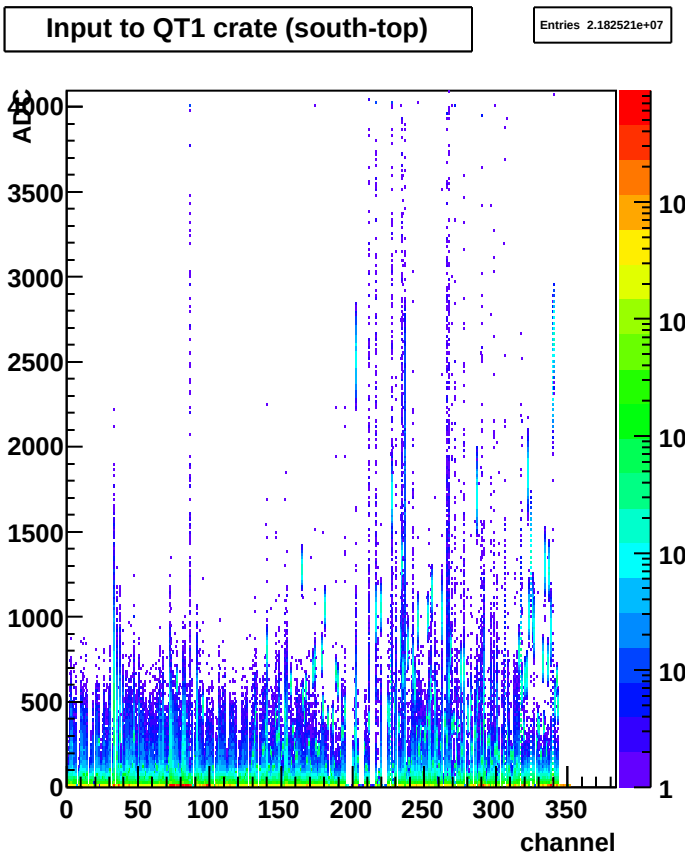

BBClarge-TAC-W
Entries 1527737
Mean 1459
RMS 339.6

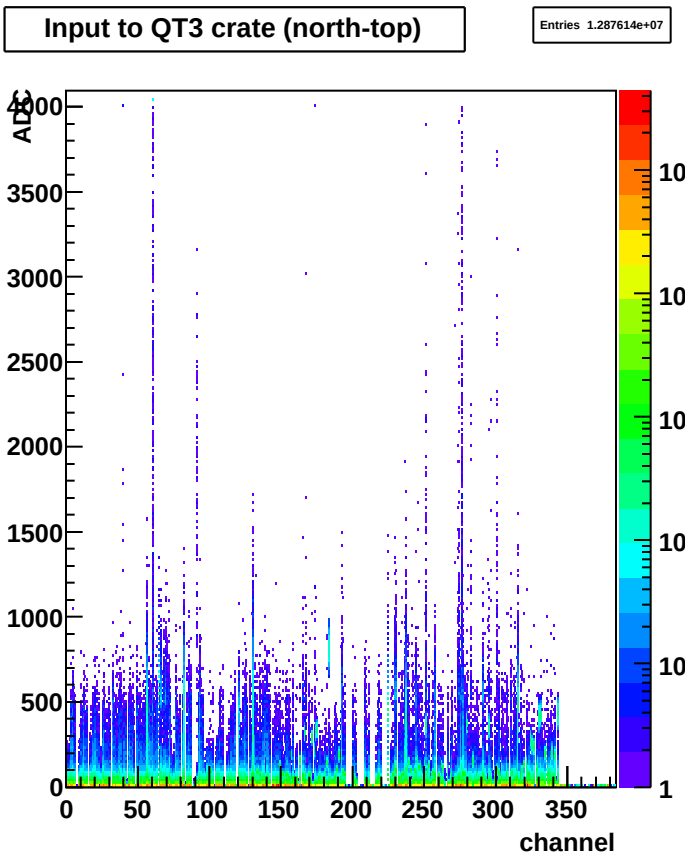

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1456841
Mean 4076
RMS 424.3

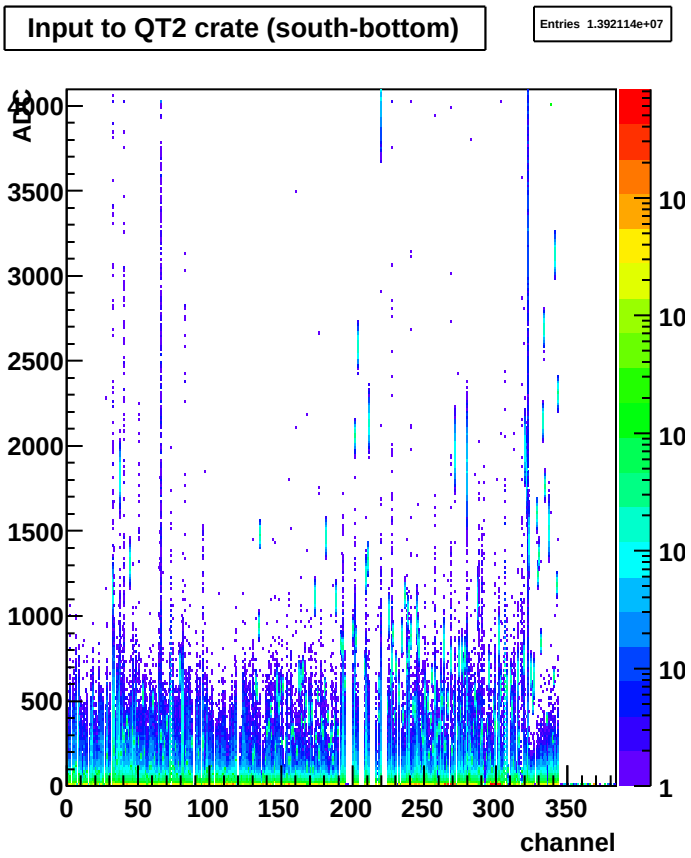

VPD-TAC-E
Entries 1216027
Mean 1317
RMS 159.1

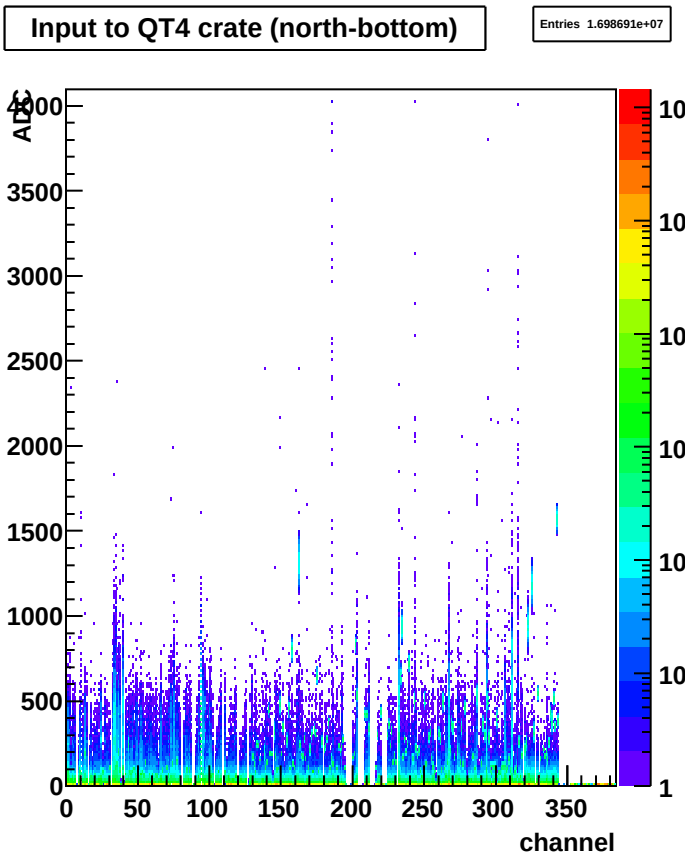

VPD-TAC-W
Entries 1190840
Mean 1426
RMS 170.6

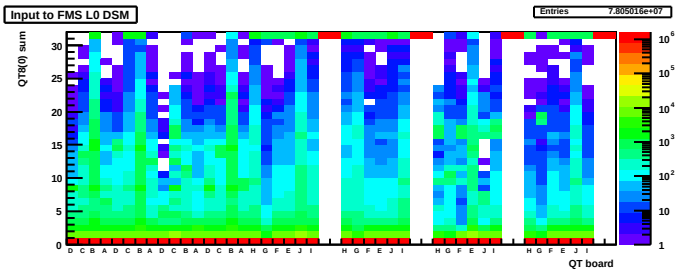

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1032198
Mean 3985
RMS 111

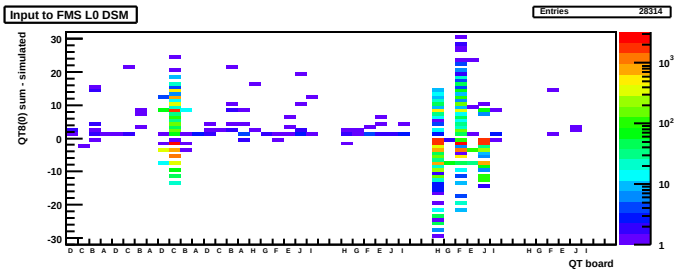

Input to QT1 crate (south-top)
Entries 2.182521e+07
ADC
channel

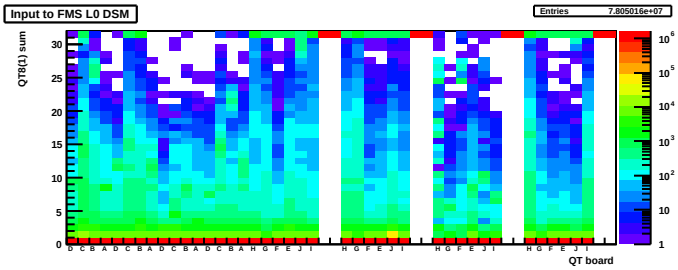

Input to QT3 crate (north-top)
Entries 1.287614e+07
ADC
channel

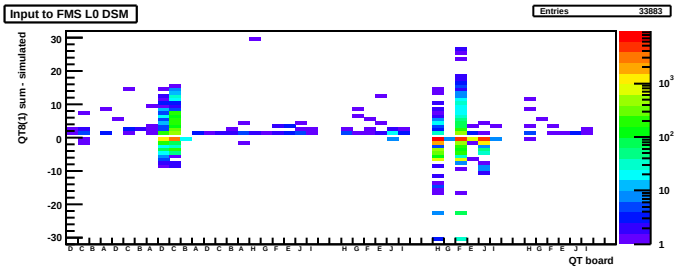

Input to QT2 crate (south-bottom)
Entries 1.392114e+07
ADC
channel

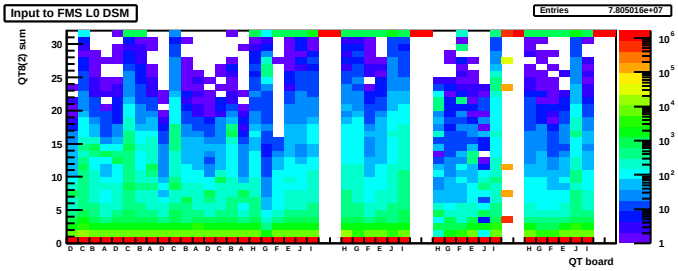

Input to QT4 crate (north-bottom)
Entries 1.698691e+07
ADC
channel

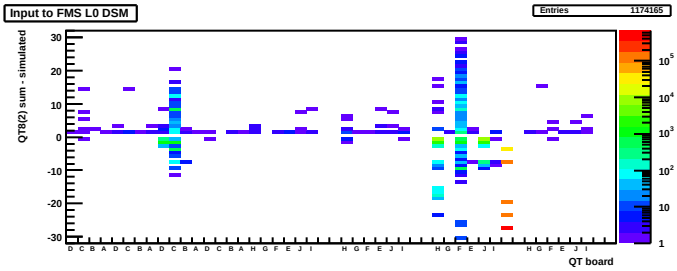

Input to FMS L0 DSM
Entries 7.805016e+07
QT8(0) sum
QT board

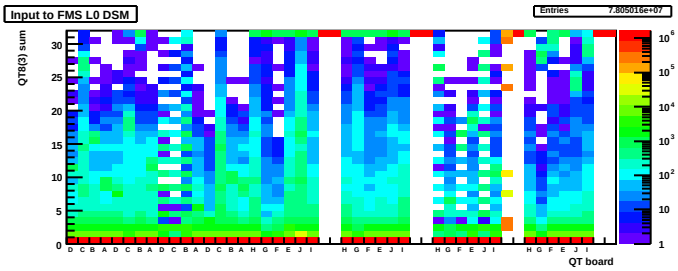

Input to FMS L0 DSM
Entries 28314
QT8(0) sum - simulated
QT board

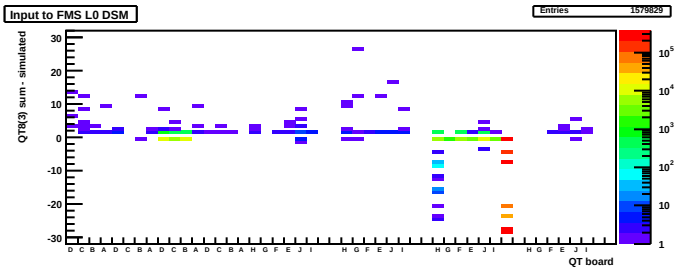

Input to FMS L0 DSM
Entries 7.805016e+07
QT8(1) sum
QT board

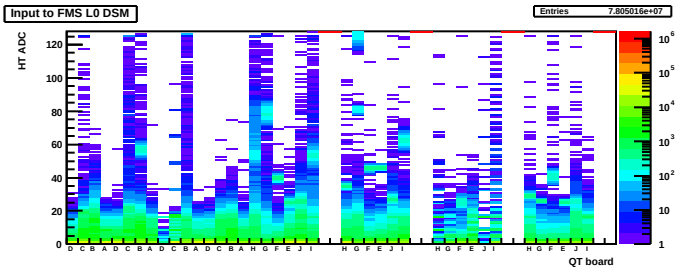

Input to FMS L0 DSM
Entries 33883
QT8(1) sum - simulated
QT board

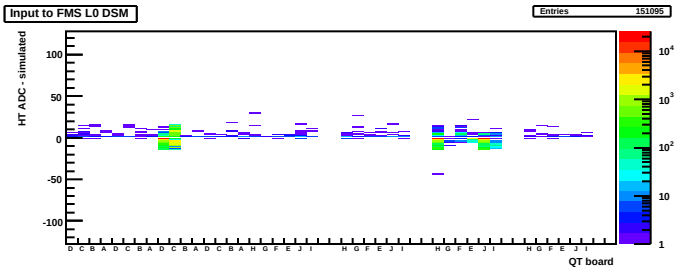

Input to FMS L0 DSM
Entries 7.805016e+07
QT8(2) sum
QT board

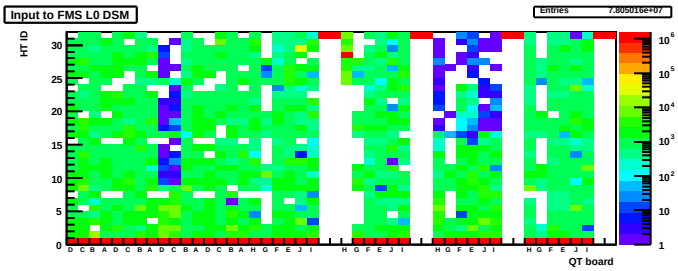

Input to FMS L0 DSM
Entries 1174165
QT8(2) sum - simulated
QT board

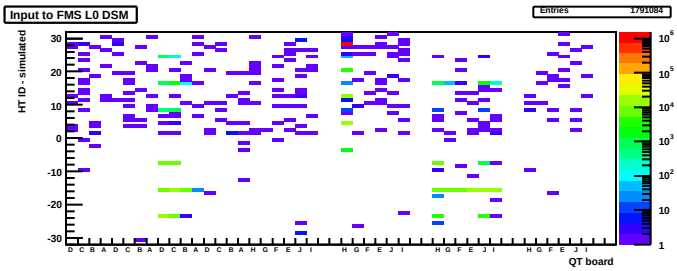

Input to FMS L0 DSM
Entries 7.805016e+07
QT8(3) sum
QT board

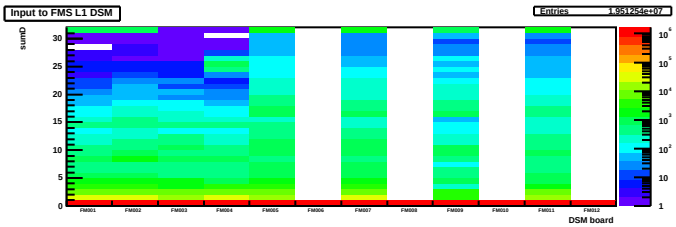

Input to FMS L0 DSM
Entries 1579829
QT8(3) sum - simulated
QT board

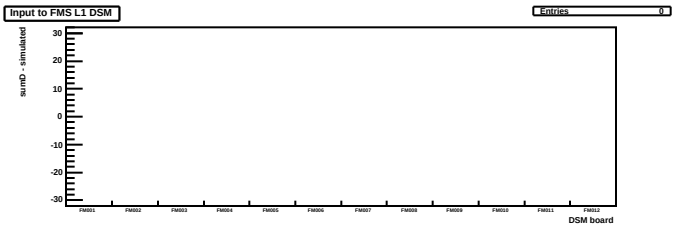

Input to FMS L0 DSM
Entries 7.805016e+07
HT ADC
QT board

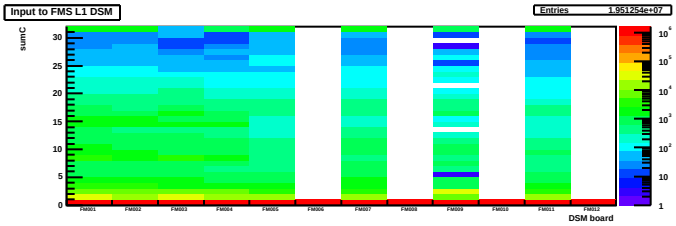

Input to FMS L0 DSM
Entries 151095
HT ADC - simulated
QT board

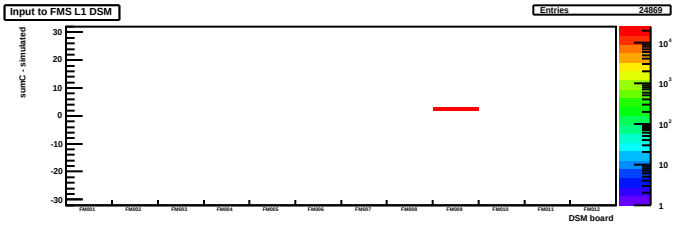

Input to FMS L0 DSM
Entries 7.805016e+07
HT ID
QT board

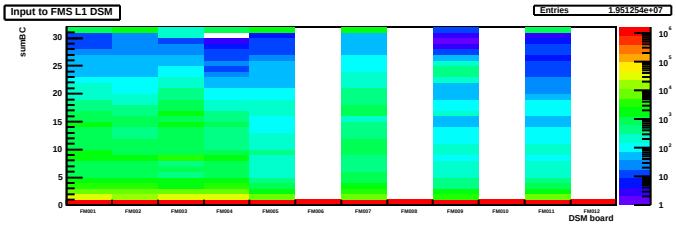

Input to FMS L0 DSM
Entries 1791084
HT ID - simulated
QT board

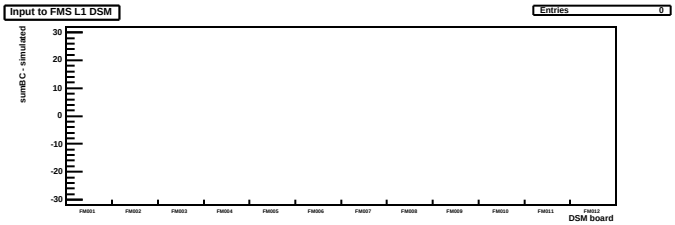

Input to FMS L1 DSM
Entries 1.951254e+07
sumD
DSM board

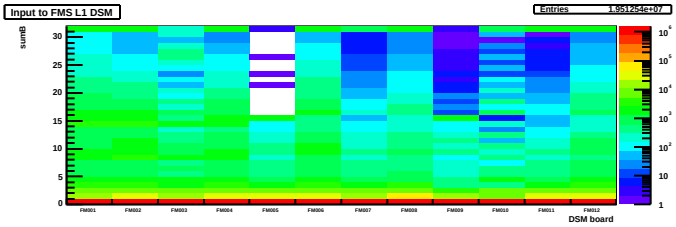

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

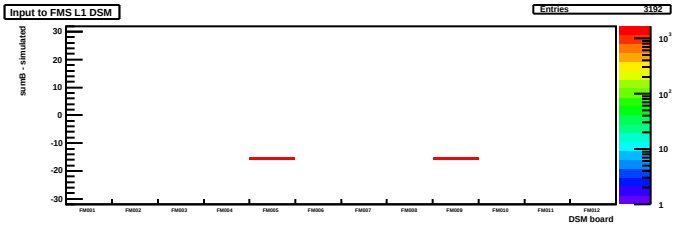

Input to FMS L1 DSM
Entries 1.951254e+07
sumC
DSM board

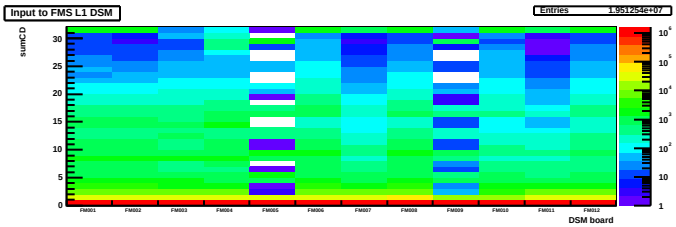

Input to FMS L1 DSM
Entries 24869
sumC - simulated
DSM board

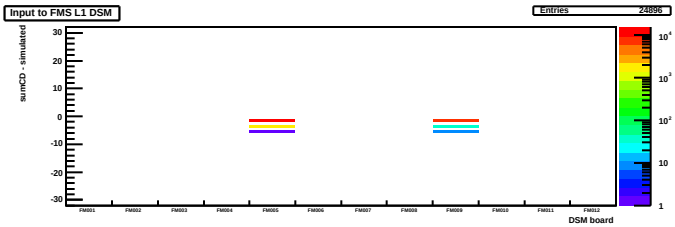

Input to FMS L1 DSM
Entries 1.951254e+07
sumBC
DSM board

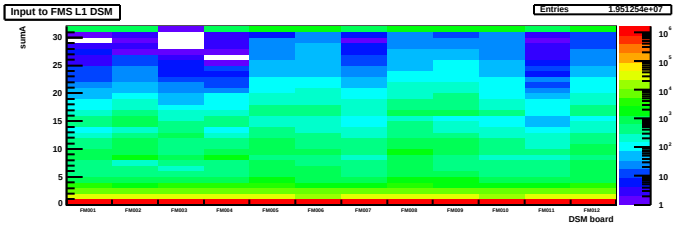

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

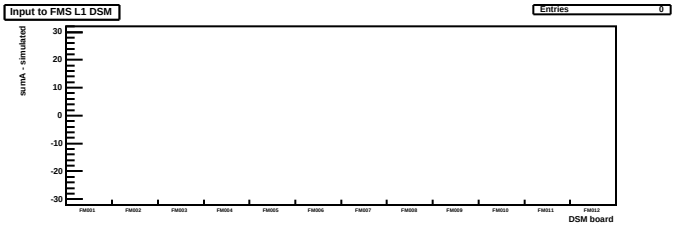

Input to FMS L1 DSM
Entries 1.951254e+07
sumB
DSM board

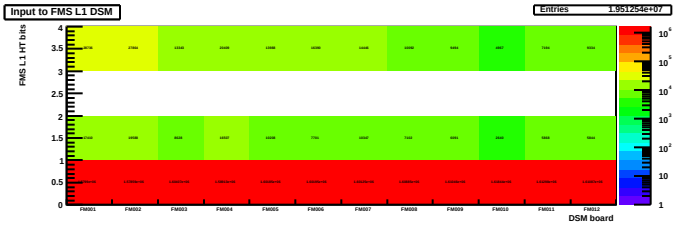

Input to FMS L1 DSM
Entries 3192
sumB - simulated
DSM board

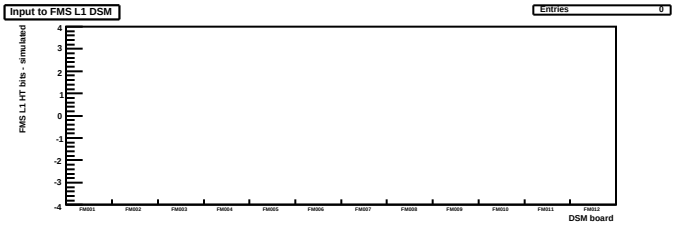

Input to FMS L1 DSM
Entries 1.951254e+07
sumCD
DSM board

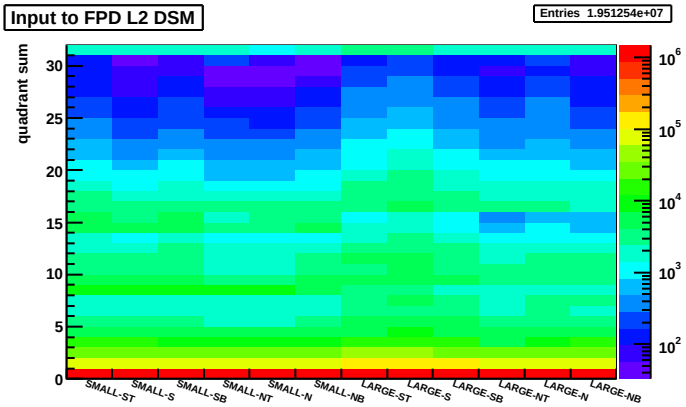

Input to FMS L1 DSM
Entries 24896
sumCD - simulated
DSM board

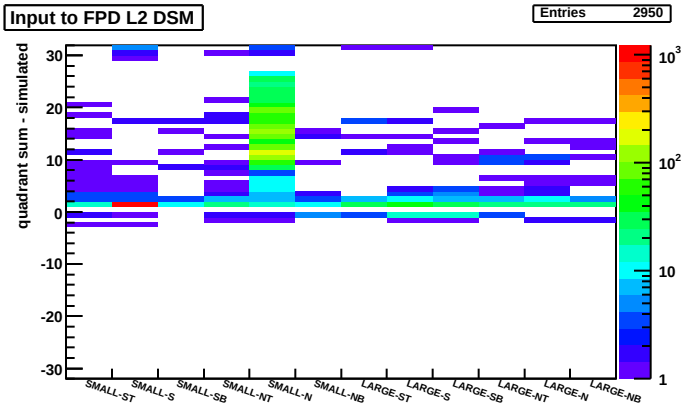

Input to FMS L1 DSM
Entries 1.951254e+07
sumA
DSM board

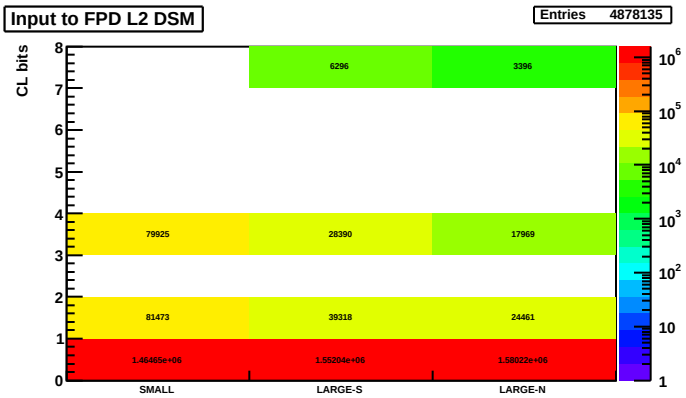

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

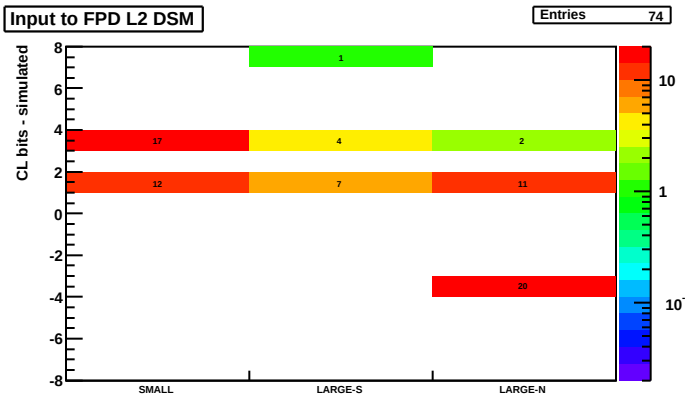

Input to FMS L1 DSM
Entries 1.951254e+07
FMS L1 HT bits
DSM board

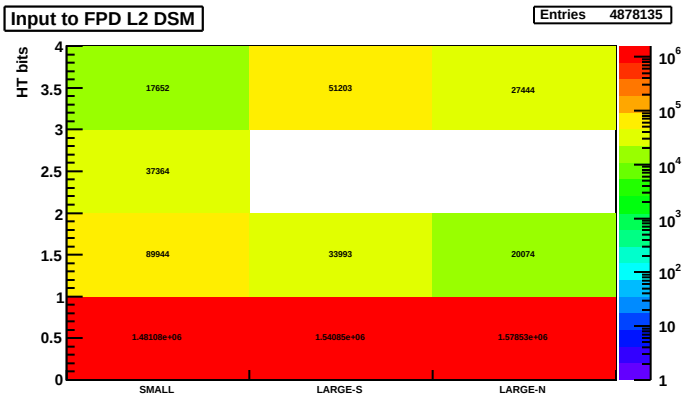

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

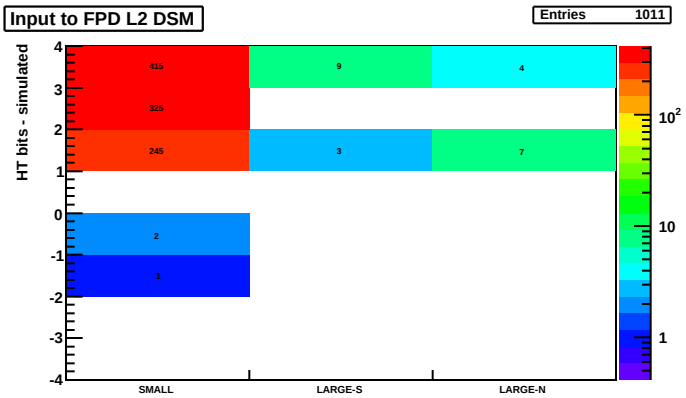

Input to FPD L2 DSM
Entries 1.951254e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

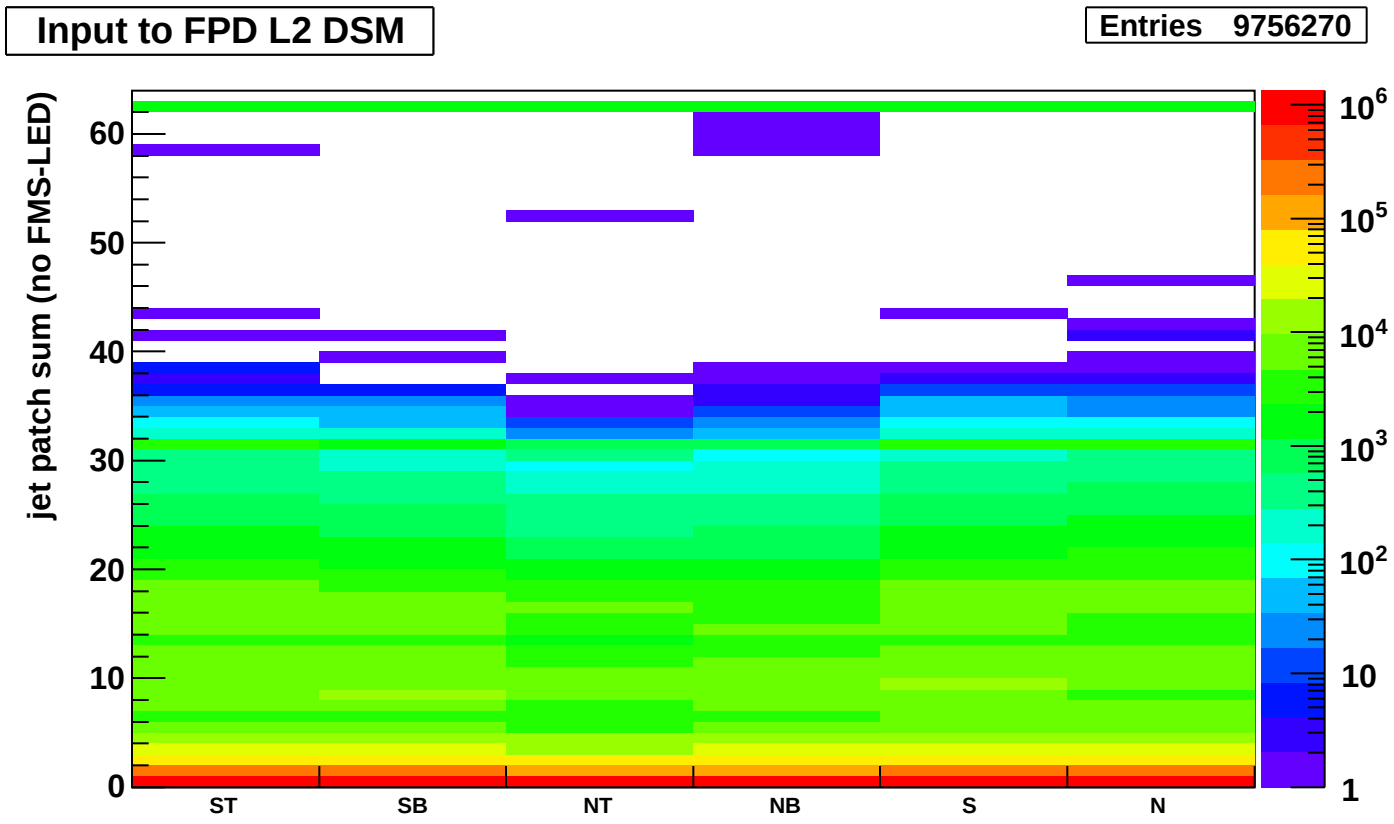

Input to FPD L2 DSM
Entries 2950
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

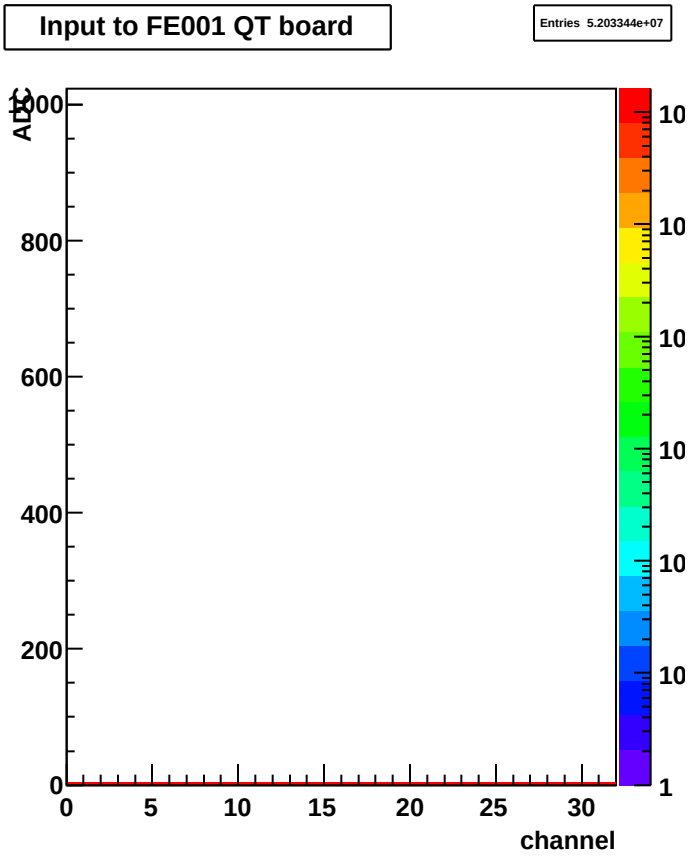

Input to FPD L2 DSM
Entries 4878135
CL bits
6296
3396
79925
28390
17969
81473
39318
24461
1.46465e+06
1.55204e+06
1.58022e+06
SMALL
LARGE-S
LARGE-N

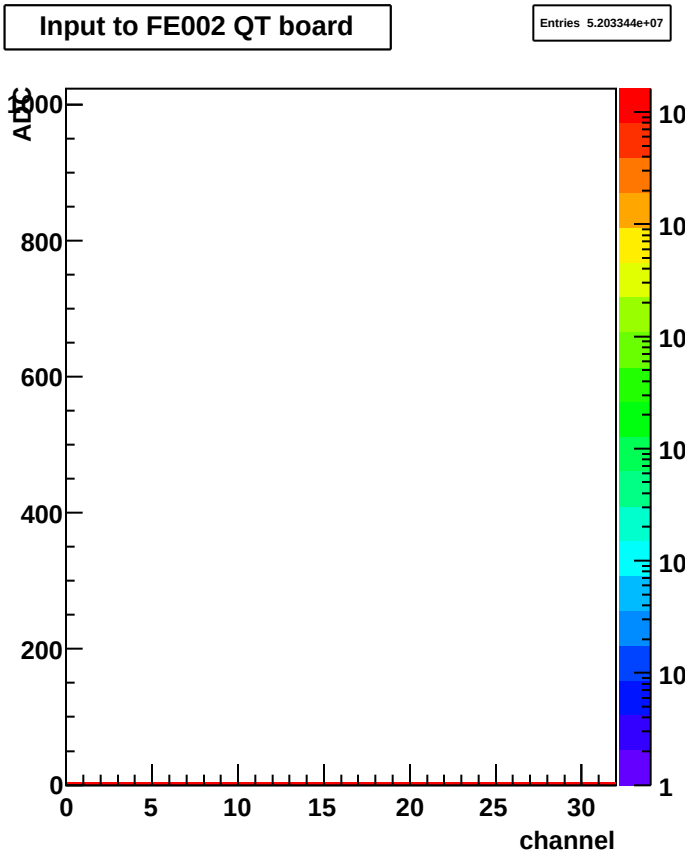

Input to FPD L2 DSM
Entries 74
CL bits - simulated
1
17
4
2
12
7
11
20
SMALL
LARGE-S
LARGE-N

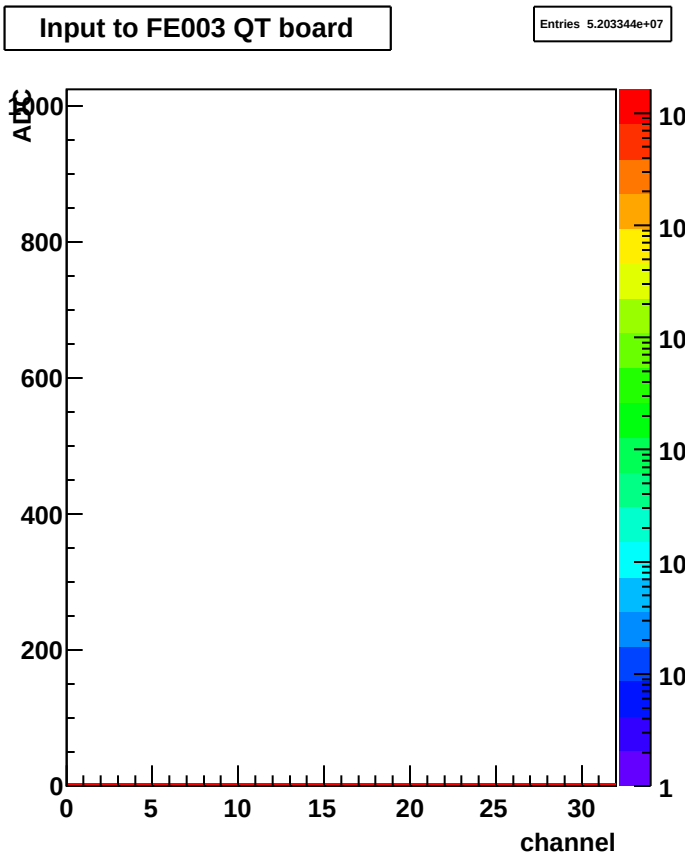

Input to FPD L2 DSM
Entries 4878135
HT bits
17652
51203
27444
37364
89944
33993
20074
1.48108e+06
1.54085e+06
1.57853e+06
SMALL
LARGE-S
LARGE-N

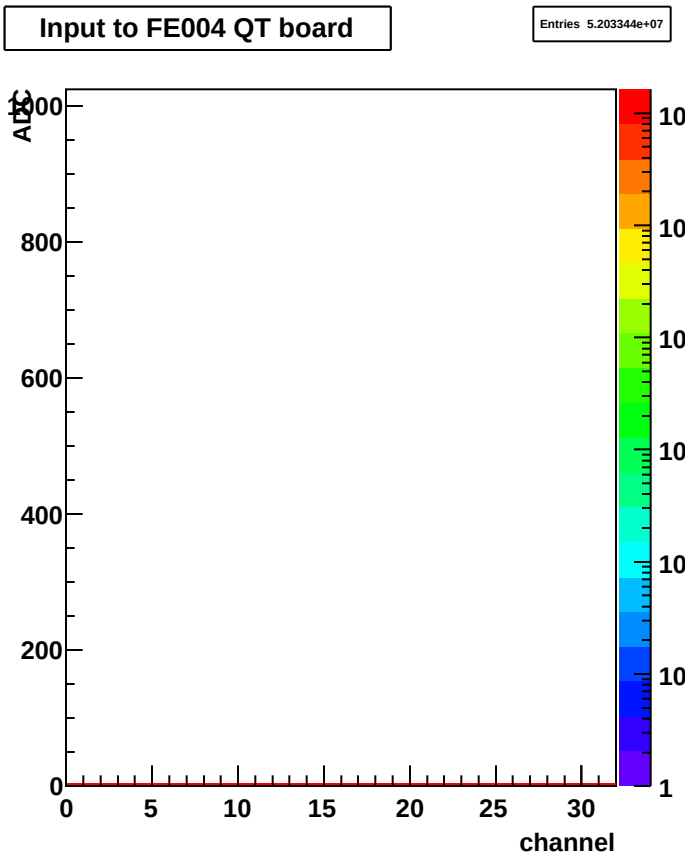

Input to FPD L2 DSM
Entries 1011
HT bits - simulated
415
9
4
325
245
3
7
2
1
SMALL
LARGE-S
LARGE-N

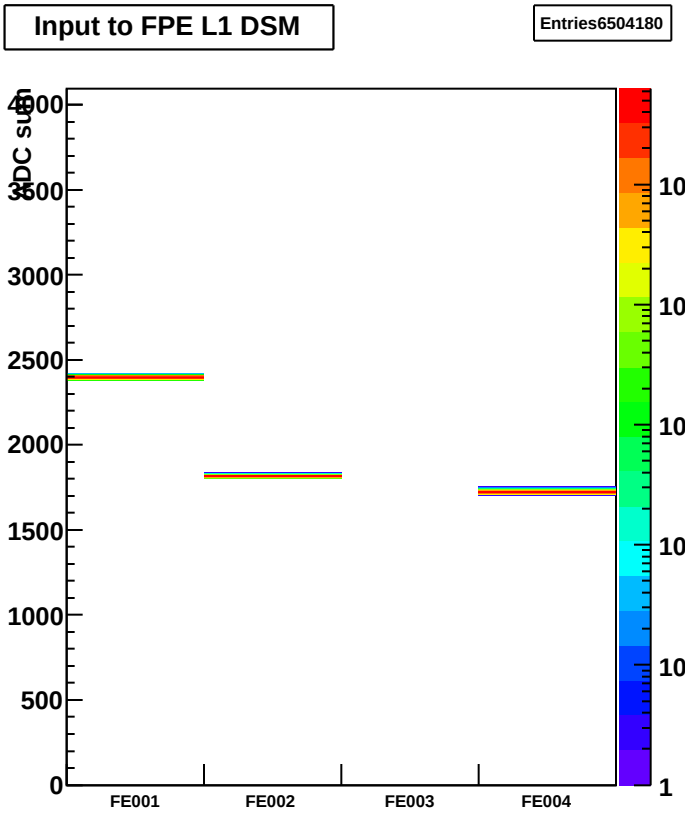

Input to FPD L2 DSM
Entries 9756270
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 5.203344e+07
ADC
channel

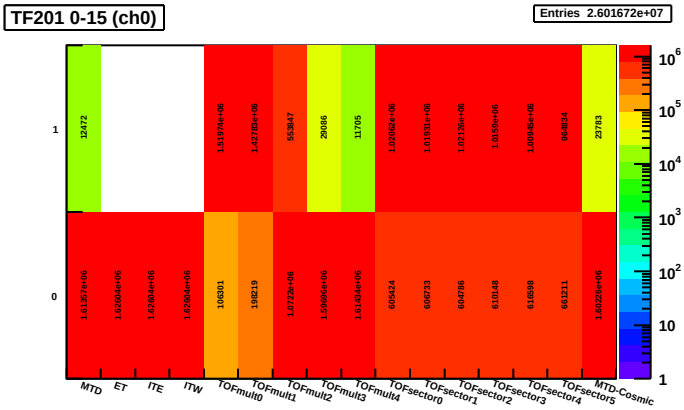

Input to FE002 QT board
Entries 5.203344e+07
ADC
channel


Input to FE003 QT board
Entries 5.203344e+07
ADC
channel

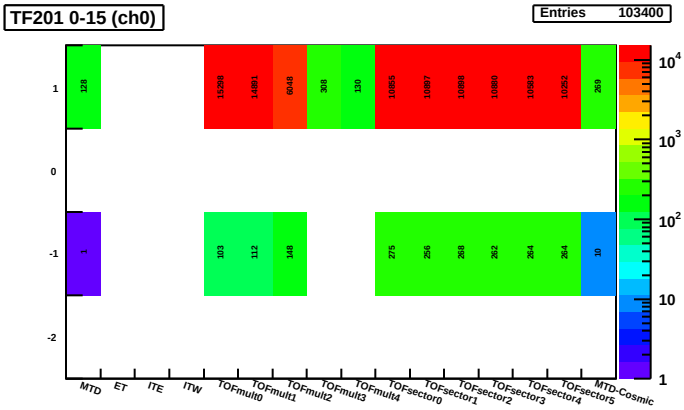

Input to FE004 QT board
Entries 5.203344e+07
ADC
channel


Input to FPE L1 DSM
Entries6504180
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

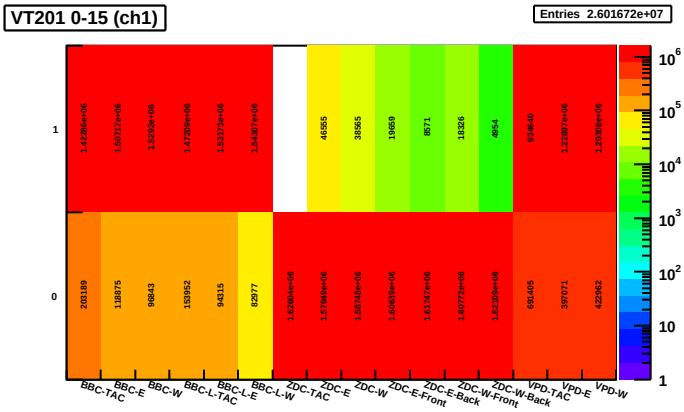
TF201 0-15 (ch0)



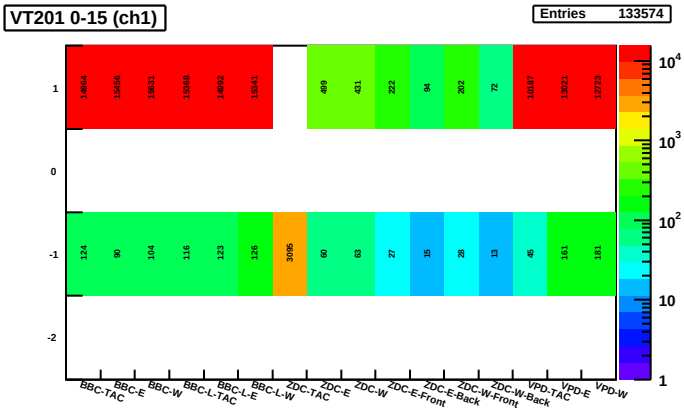
TF201 0-15 (ch0)



VT201 0-15 (ch1)



VT201 0-15 (ch1)



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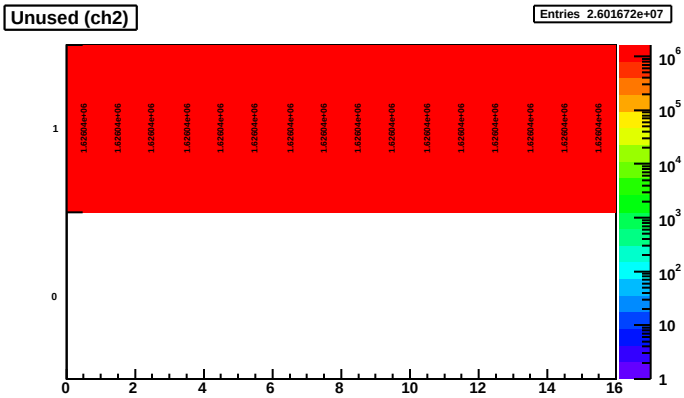
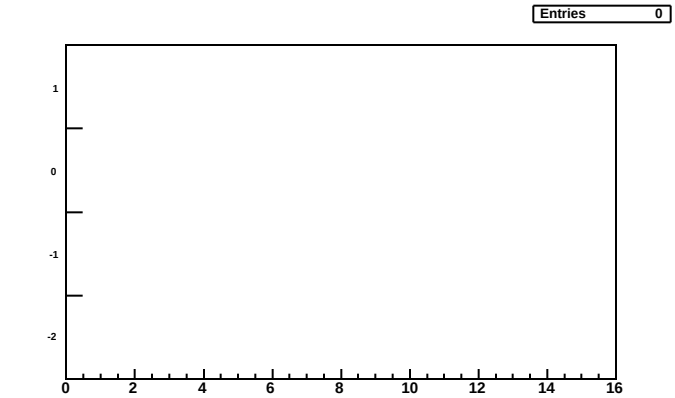
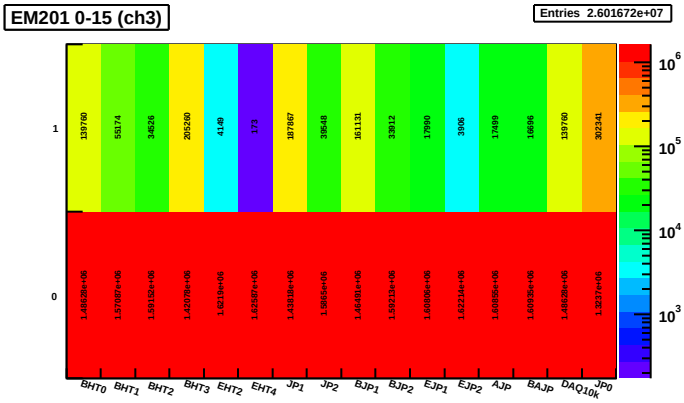




EM201 0-15 (ch3)



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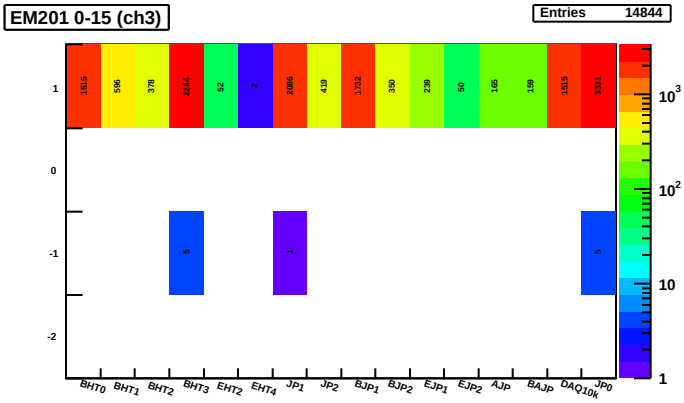
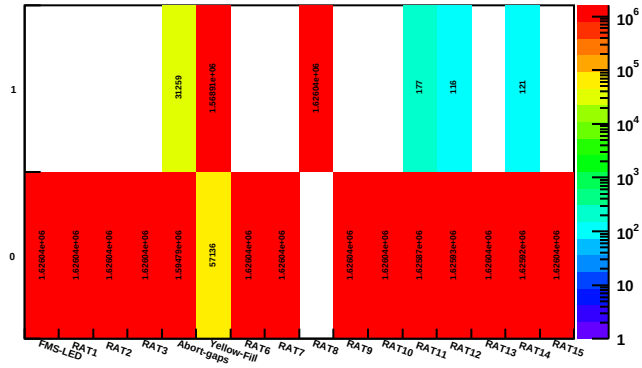
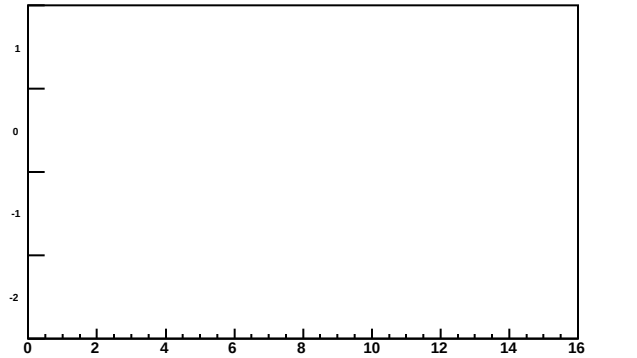
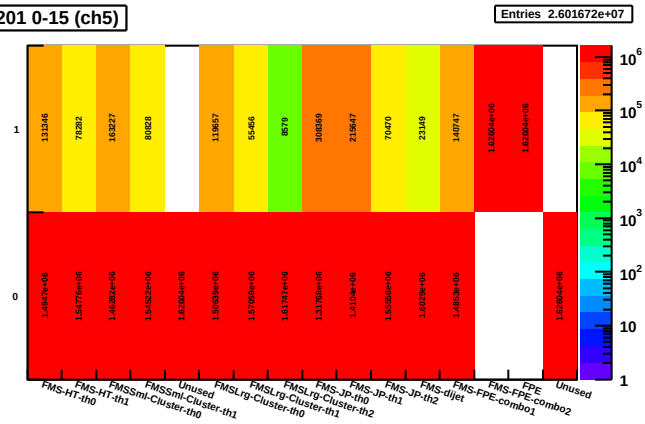


RAT board (ch4)

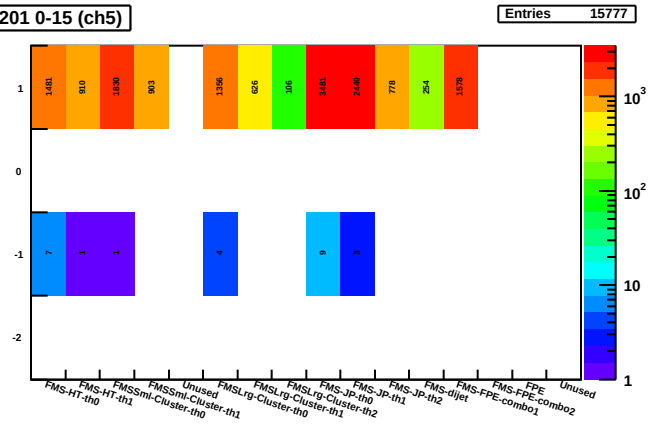




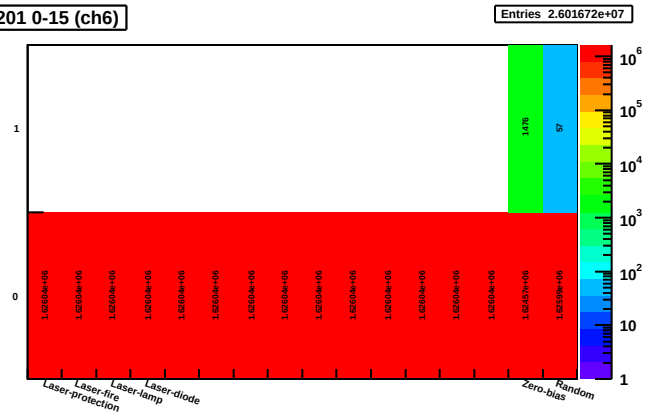
FP201 0-15 (ch5)

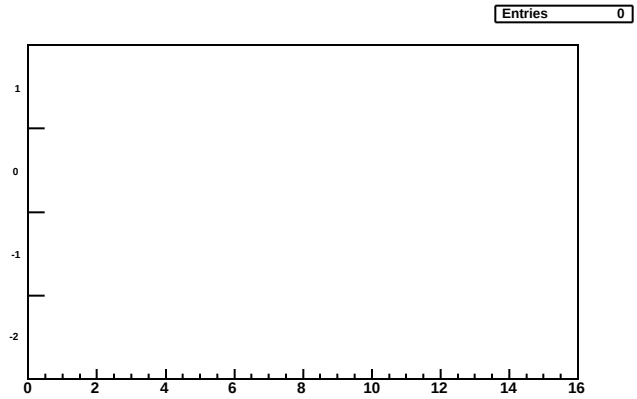
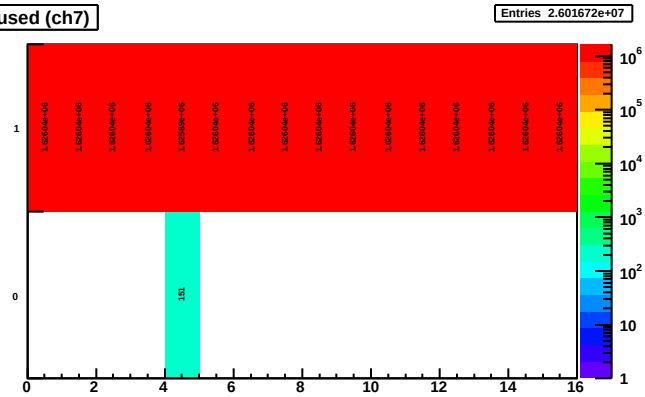
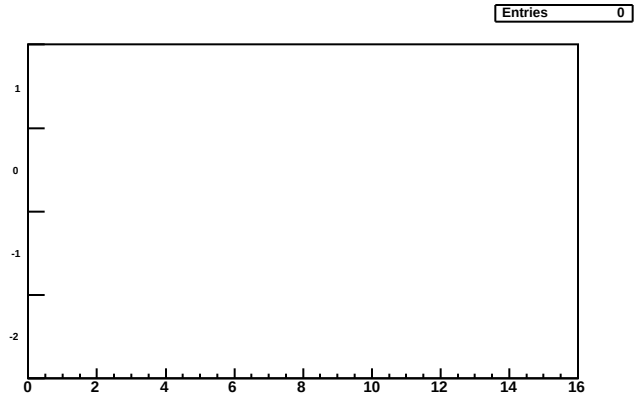


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)