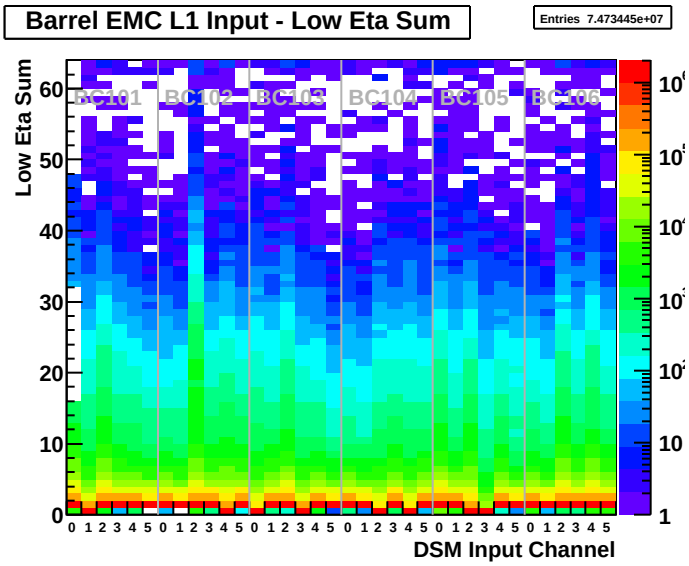
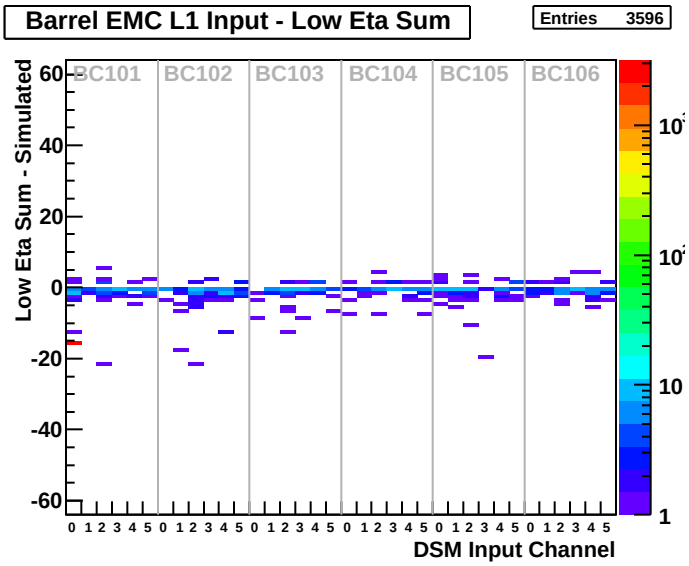
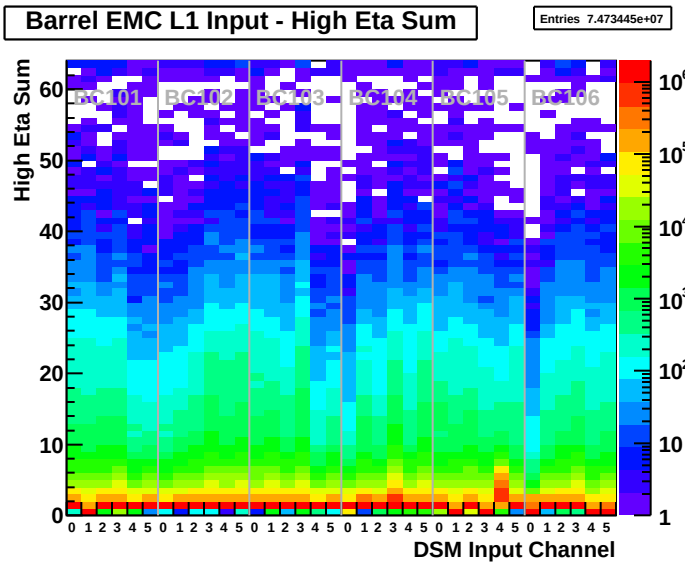
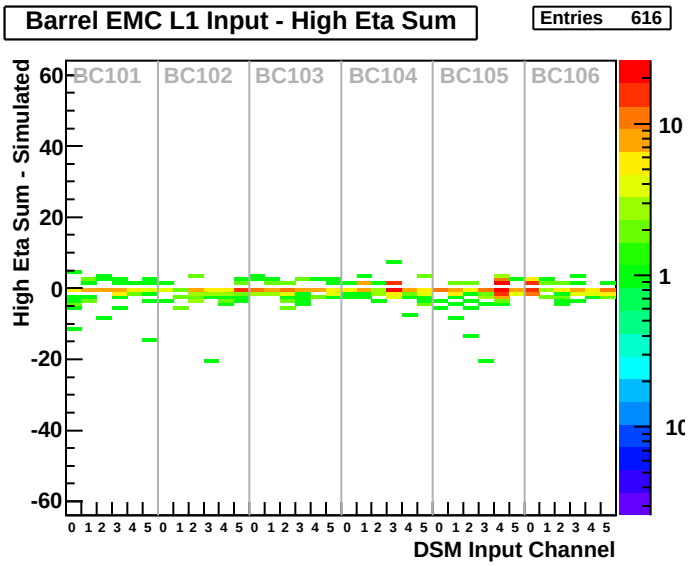
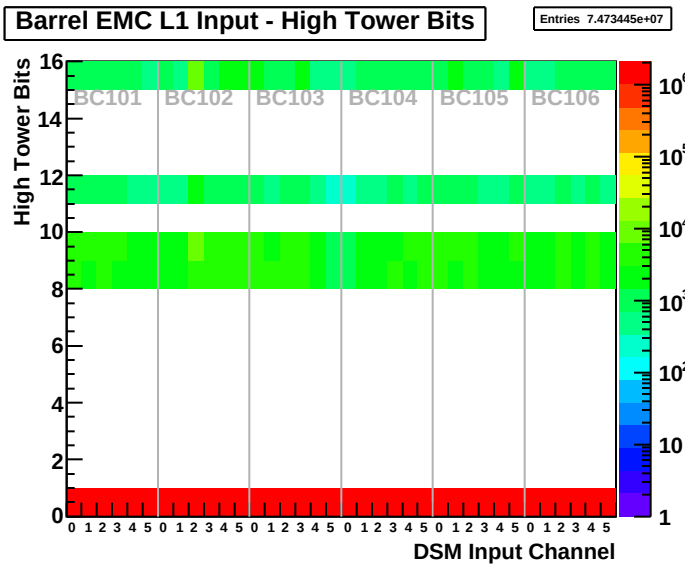
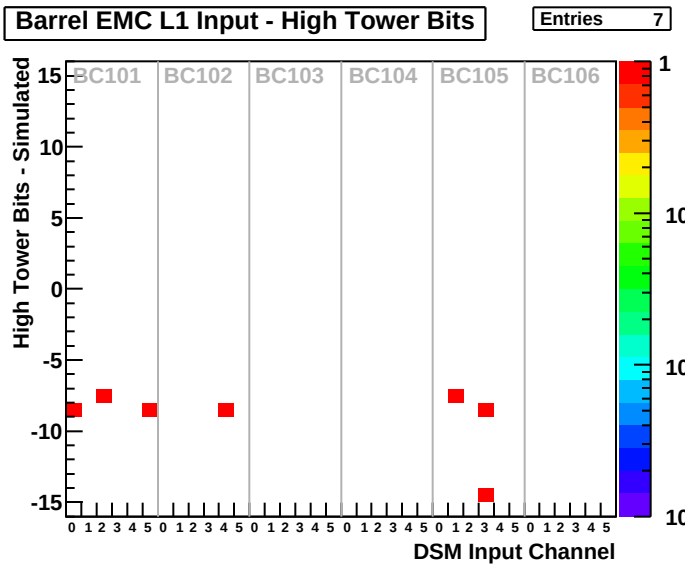

Barrel EMC L1 Input - Low Eta Sum
Entries 7.473445e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

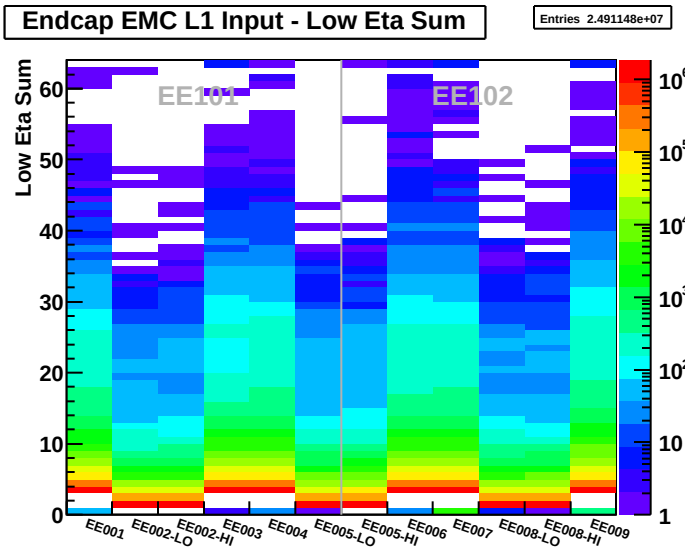

Barrel EMC L1 Input - Low Eta Sum
Entries 3596
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

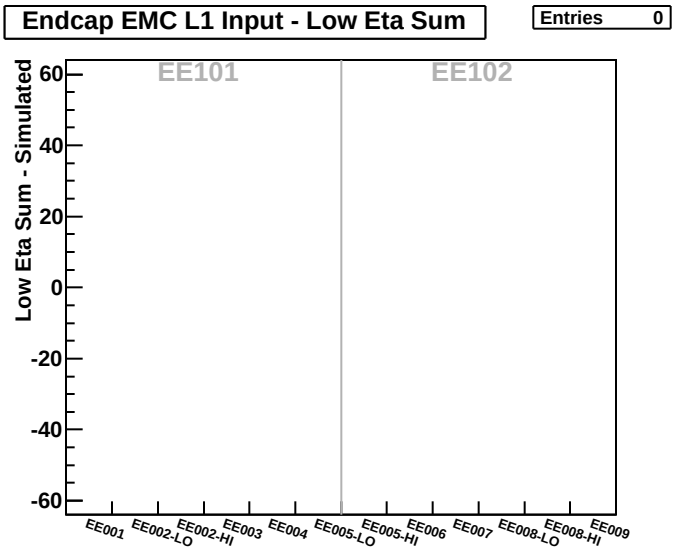

Barrel EMC L1 Input - High Eta Sum
Entries 7.473445e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

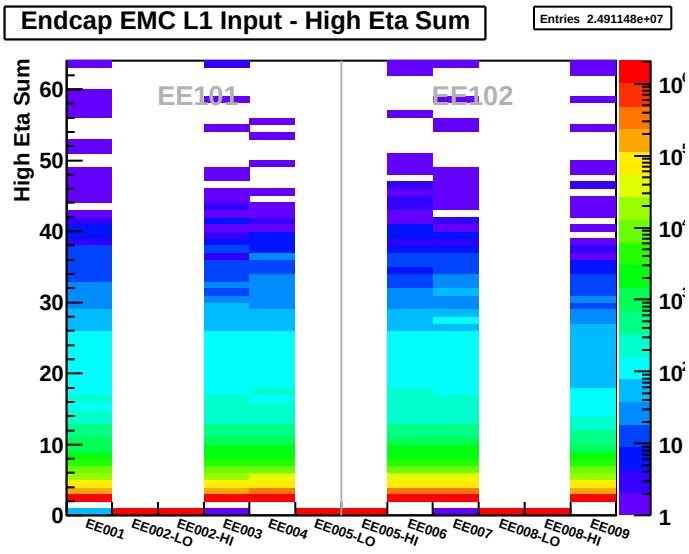

Barrel EMC L1 Input - High Eta Sum
Entries 616
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

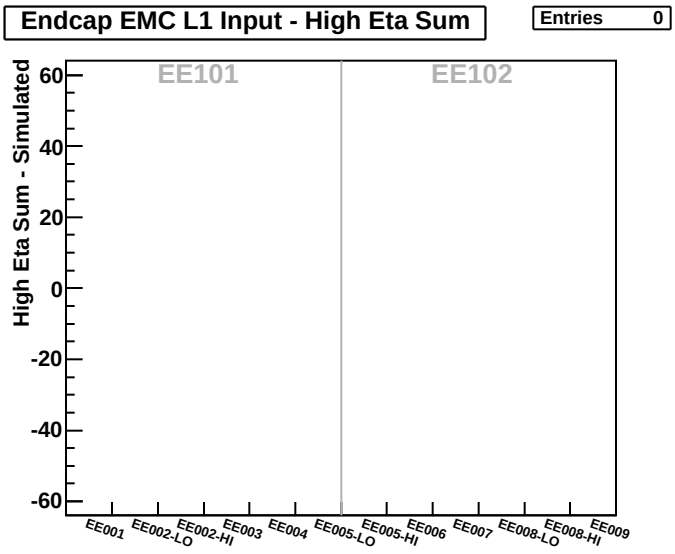

Barrel EMC L1 Input - High Tower Bits
Entries 7.473445e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

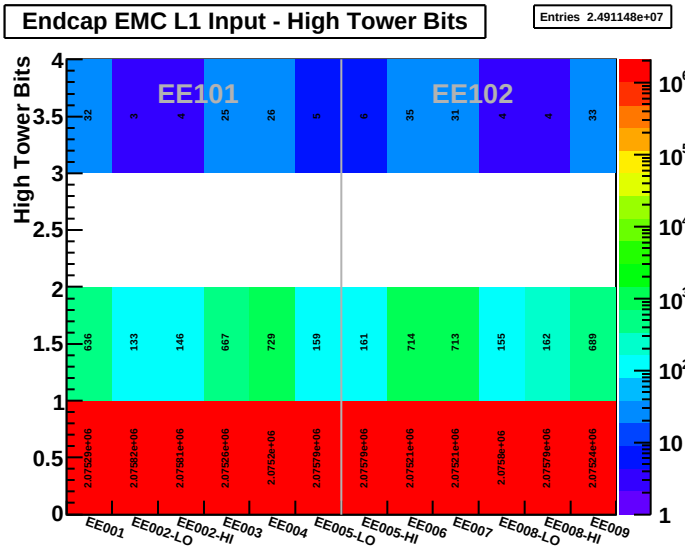

Barrel EMC L1 Input - High Tower Bits
Entries 7
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

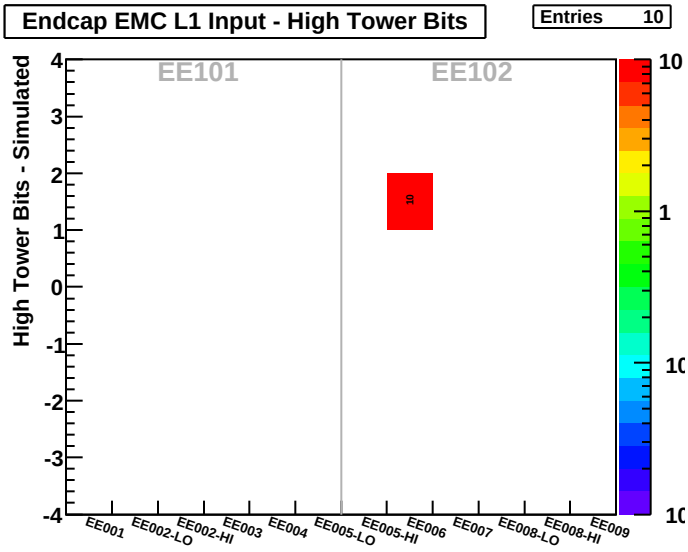

Endcap EMC L1 Input - Low Eta Sum
Entries 2.491148e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

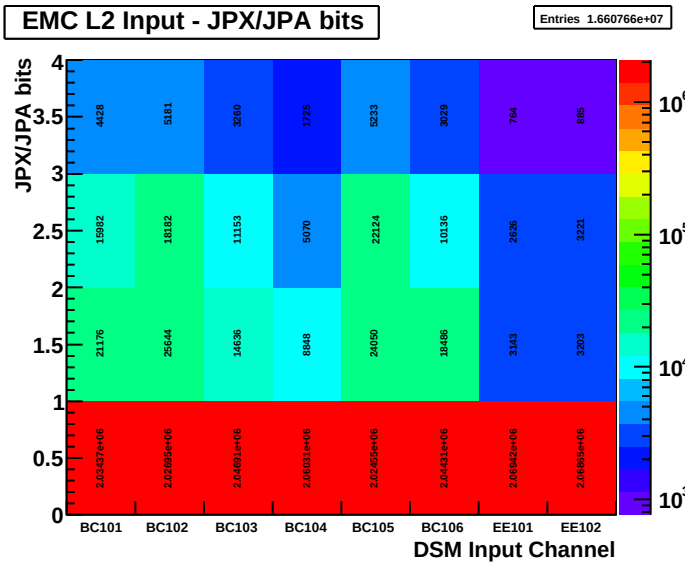

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

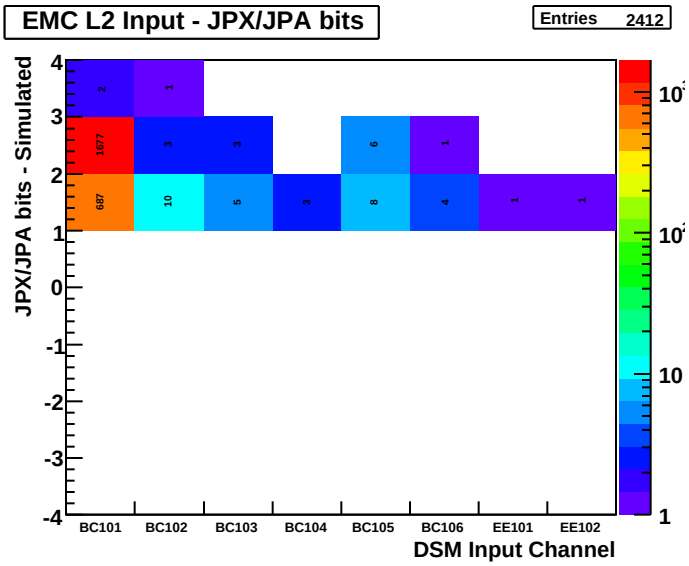

Endcap EMC L1 Input - High Eta Sum
Entries 2.491148e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

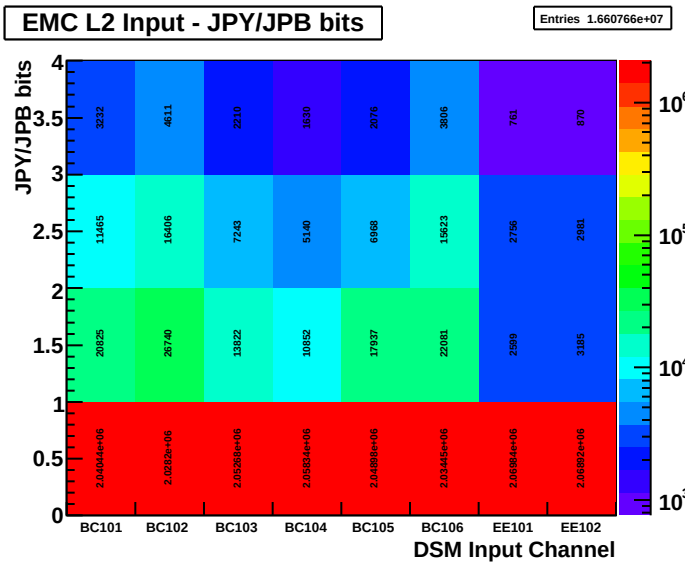

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

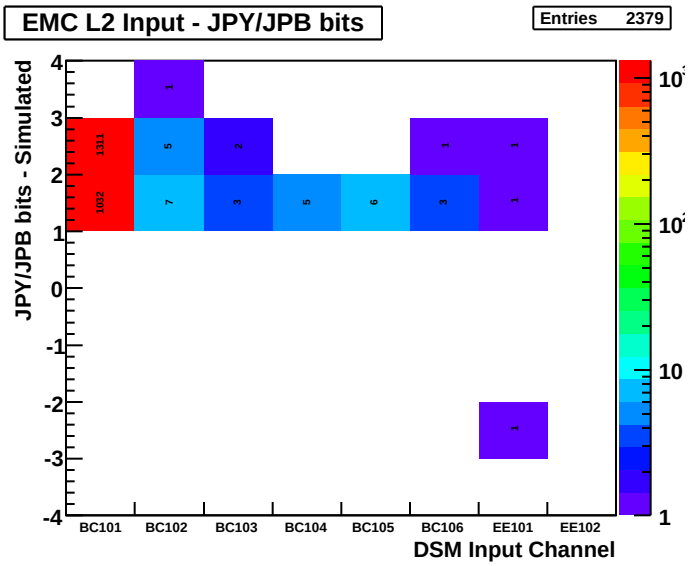

Endcap EMC L1 Input - High Tower Bits
Entries 2.491148e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

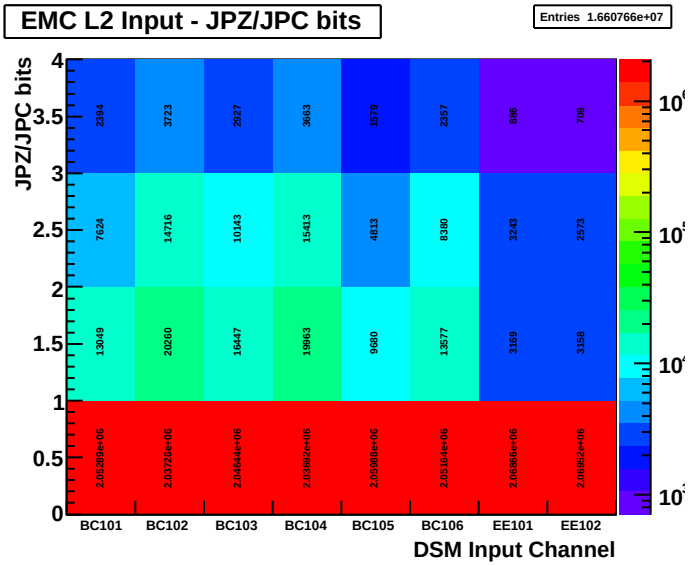

Endcap EMC L1 Input - High Tower Bits
Entries 10
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

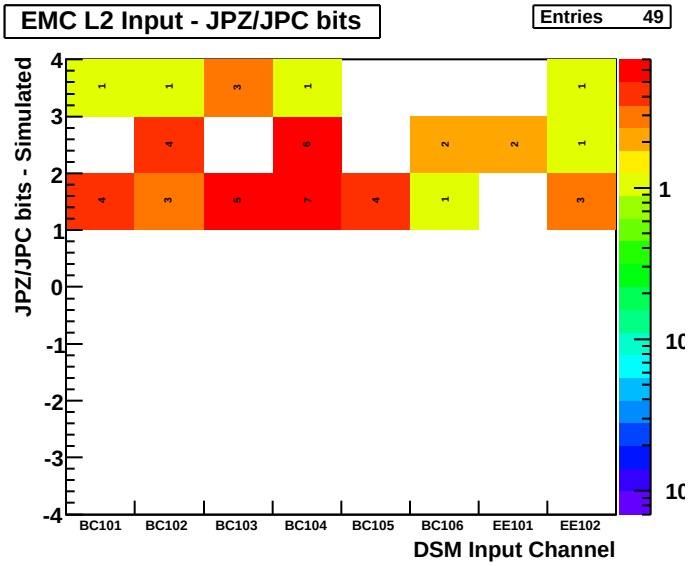

EMC L2 Input - JPX/JPA bits
Entries 1.660766e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

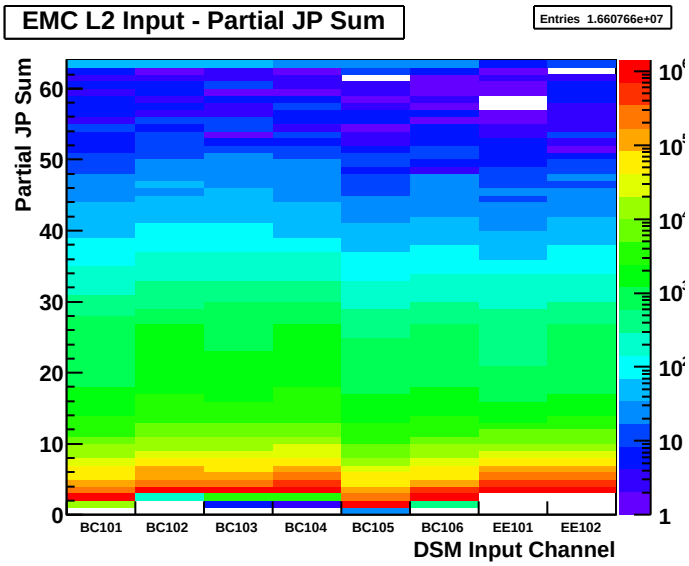

EMC L2 Input - JPX/JPA bits
Entries 2412
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

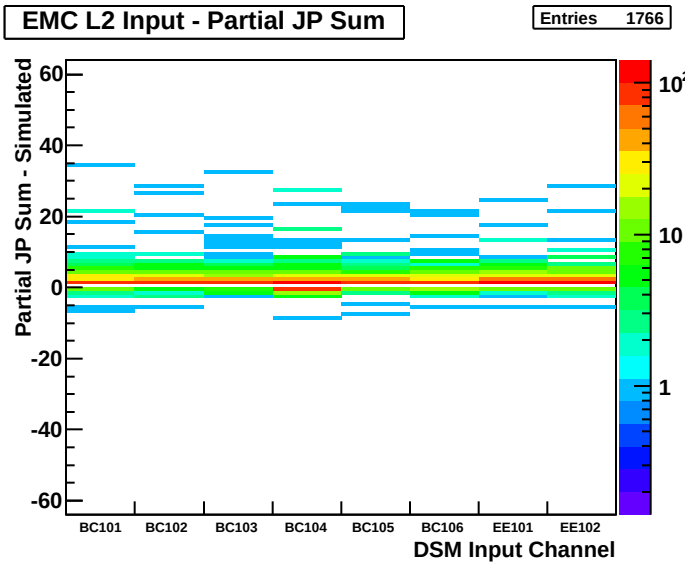

EMC L2 Input - JPY/JPB bits
Entries 1.660766e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

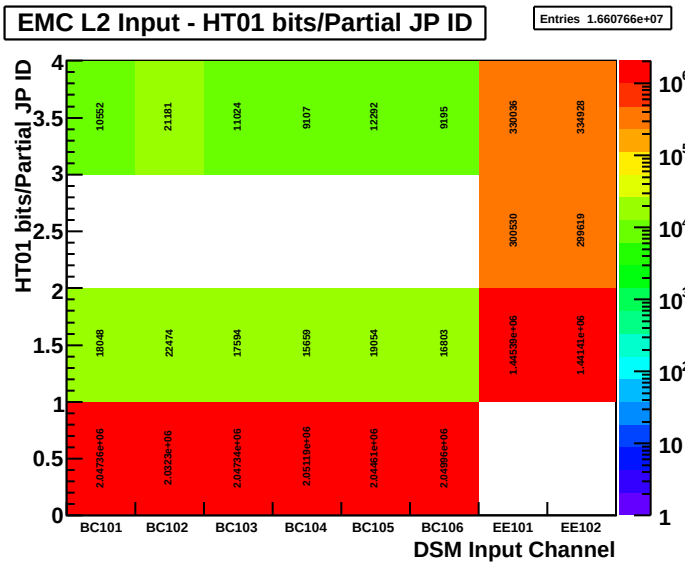

EMC L2 Input - JPY/JPB bits
Entries 2379
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

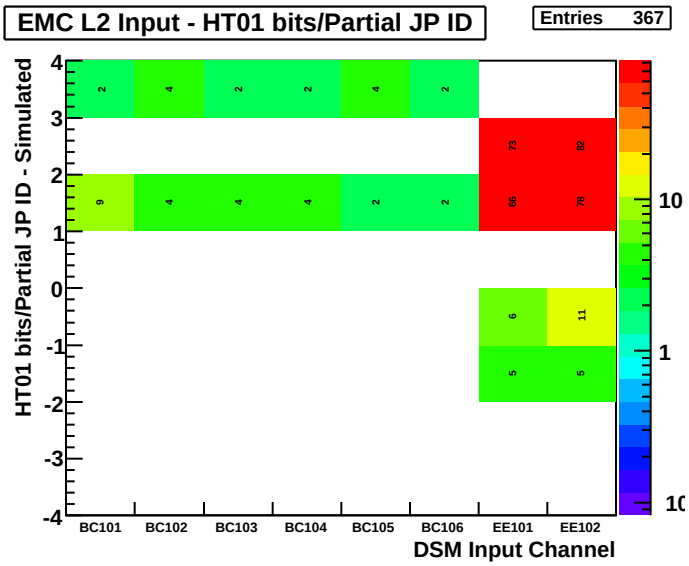

EMC L2 Input - JPZ/JPC bits
Entries 1.660766e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

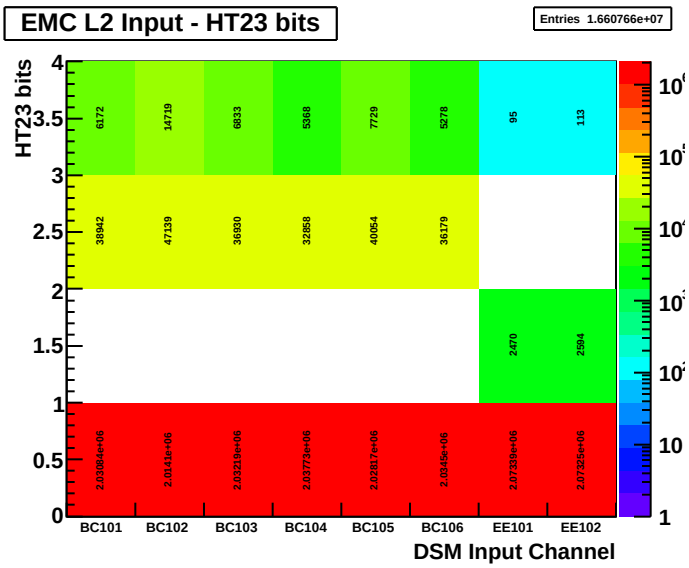

EMC L2 Input - JPZ/JPC bits
Entries 49
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

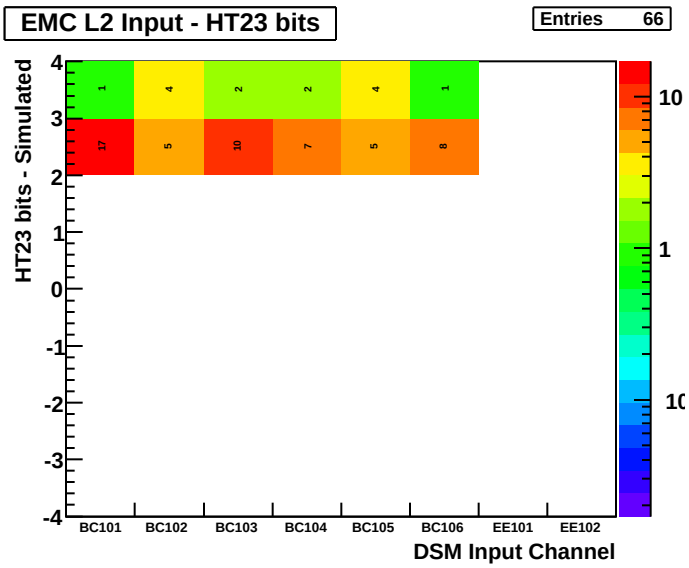

EMC L2 Input - Partial JP Sum
Entries 1.660766e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

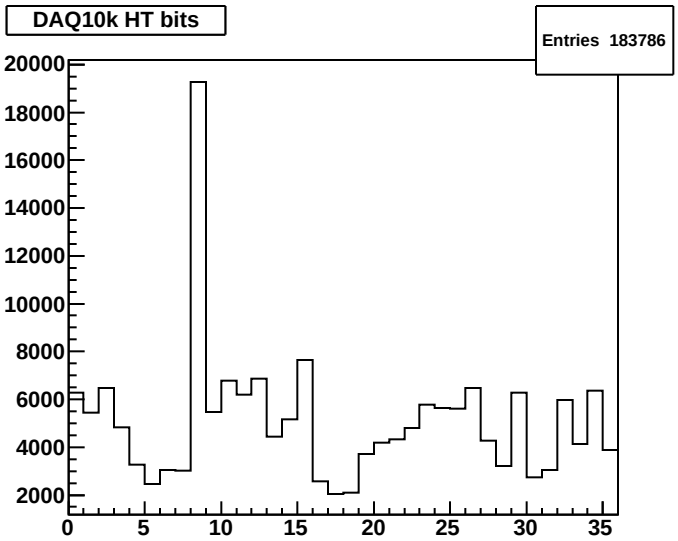

EMC L2 Input - Partial JP Sum
Entries 1766
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

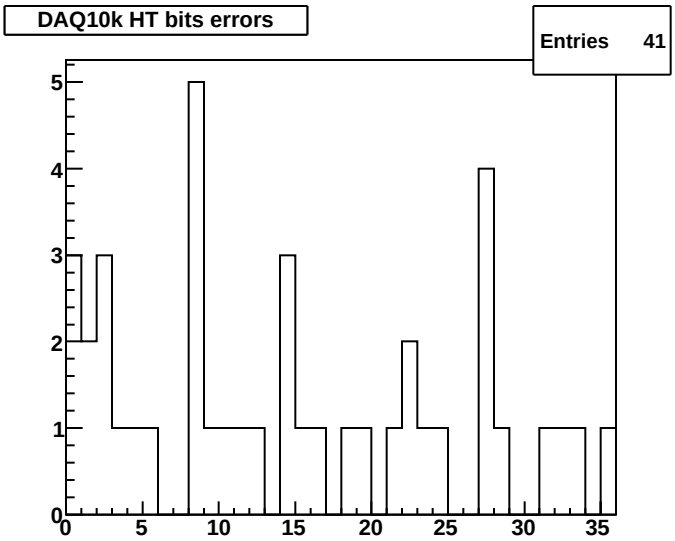

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.660766e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

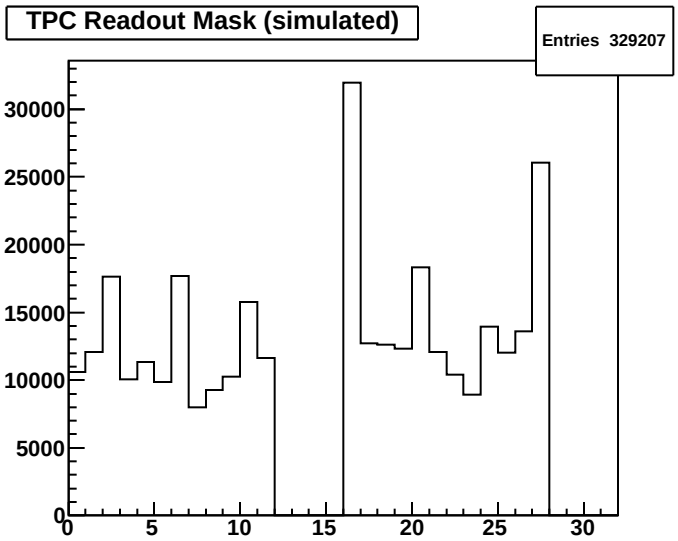

EMC L2 Input - HT01 bits/Partial JP ID
Entries 367
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

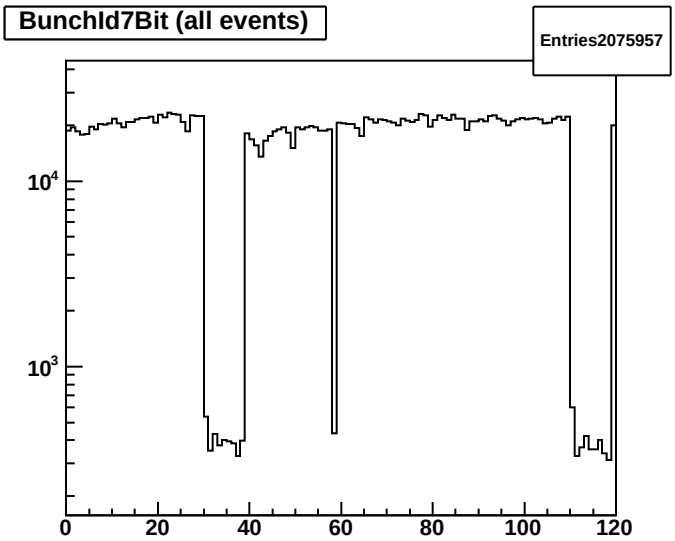

EMC L2 Input - HT23 bits
Entries 1.660766e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

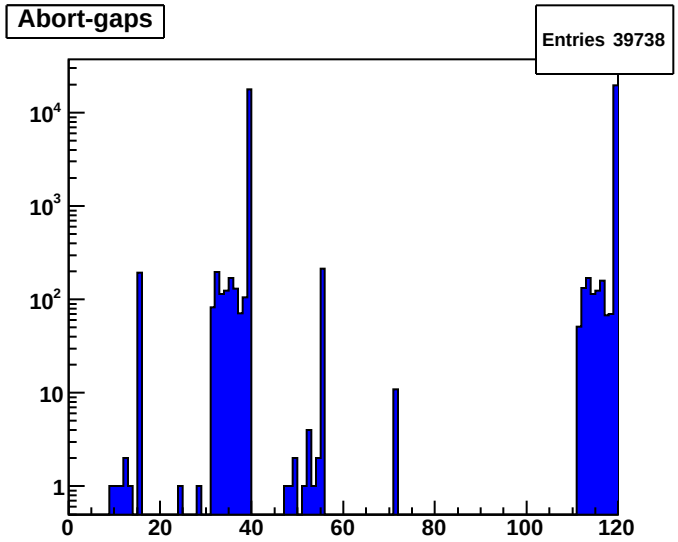

EMC L2 Input - HT23 bits
Entries 66
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

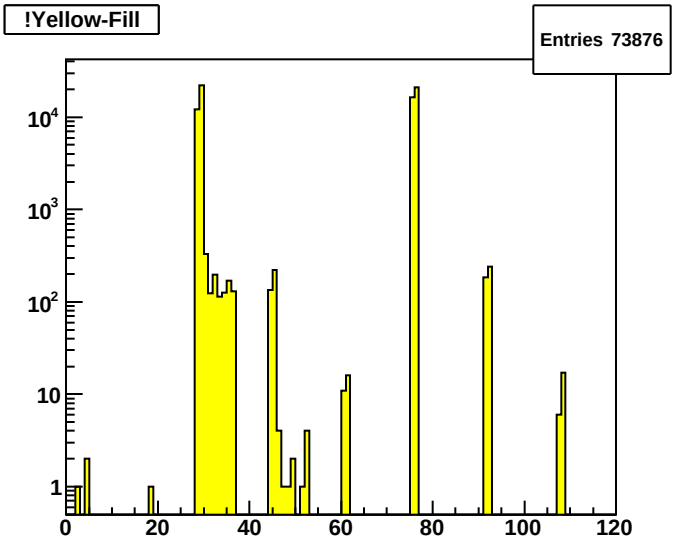

DAQ10k HT bits
Entries 183786

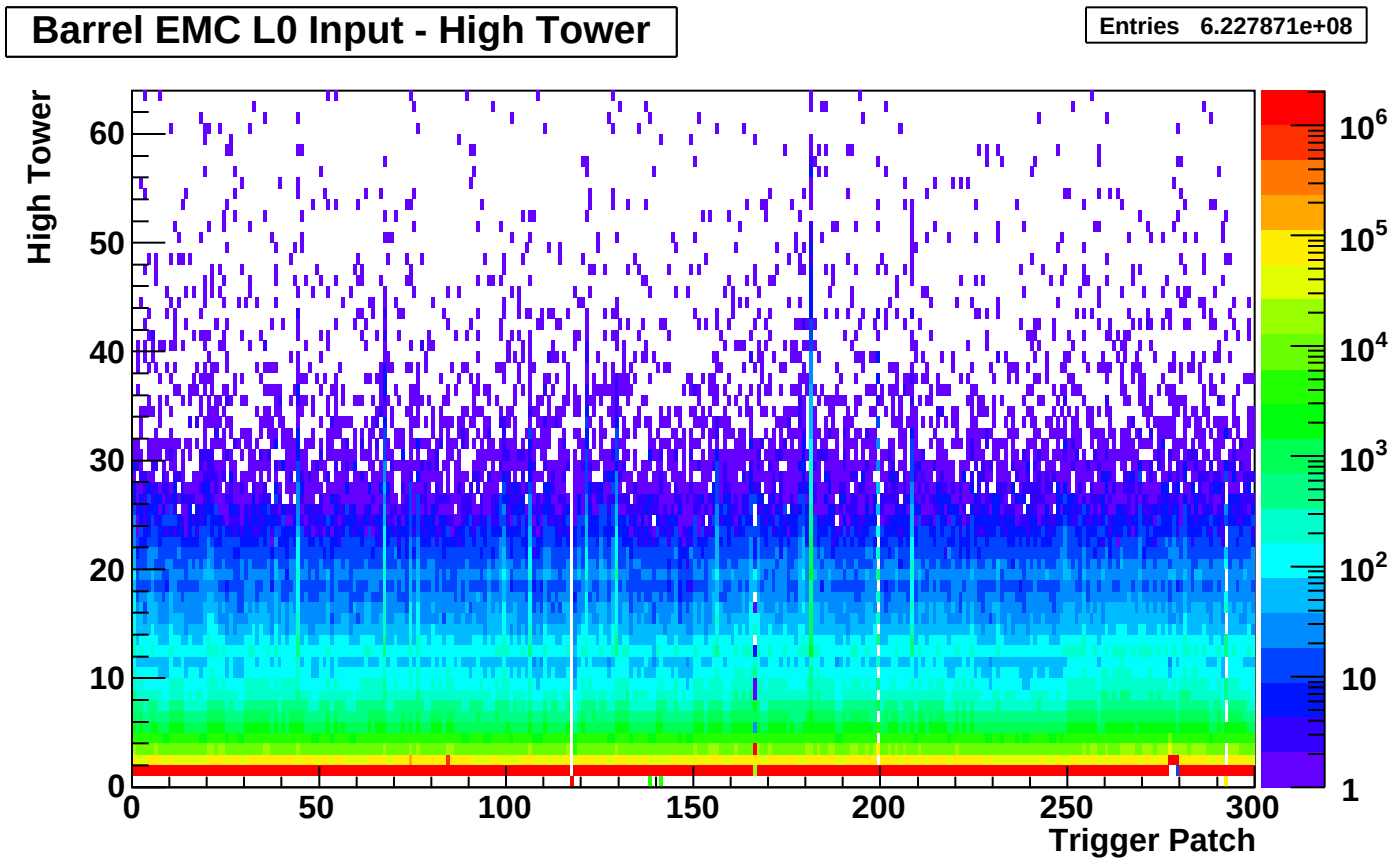

DAQ10k HT bits errors
Entries 41

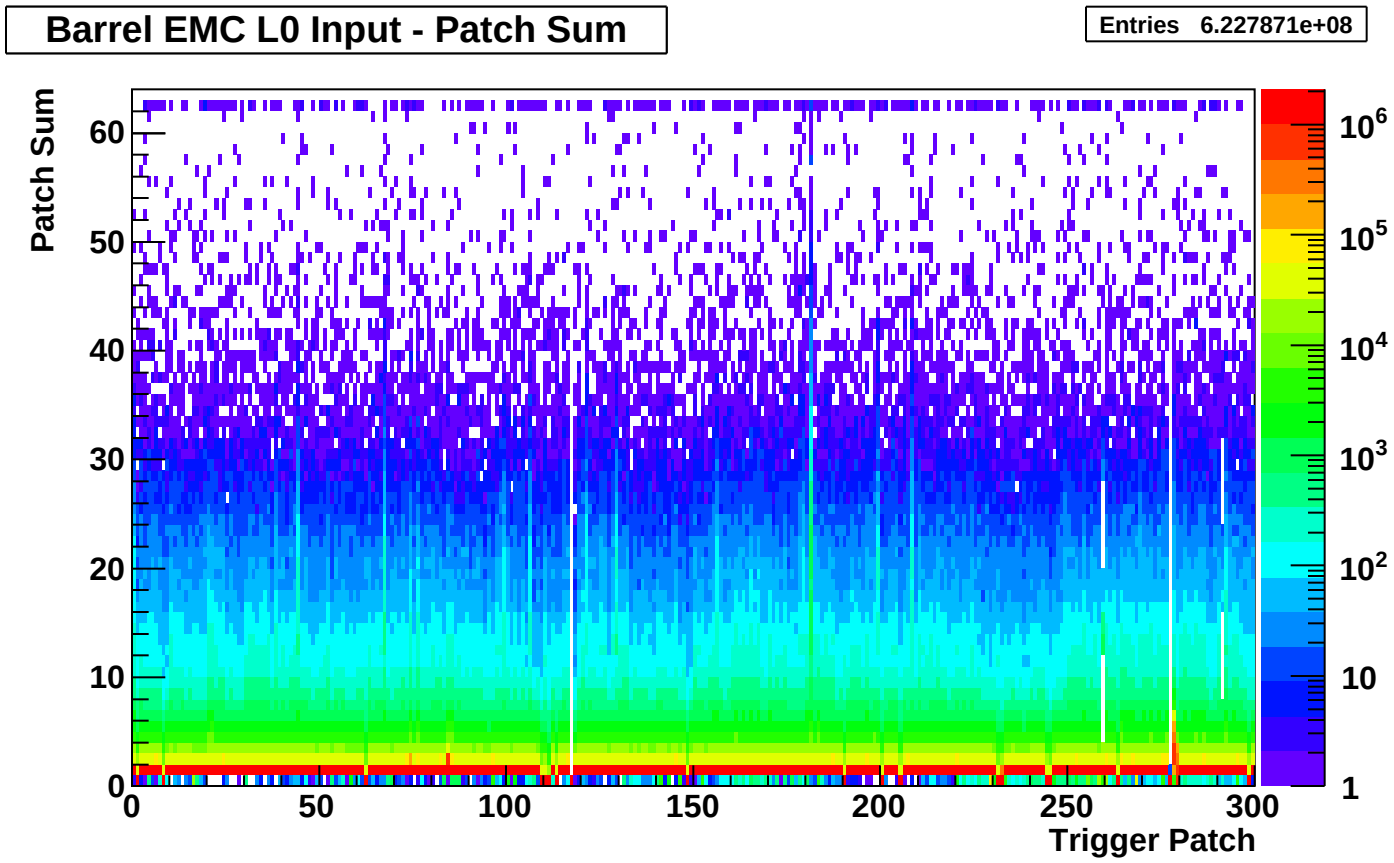

TPC Readout Mask (simulated)
Entries 329207

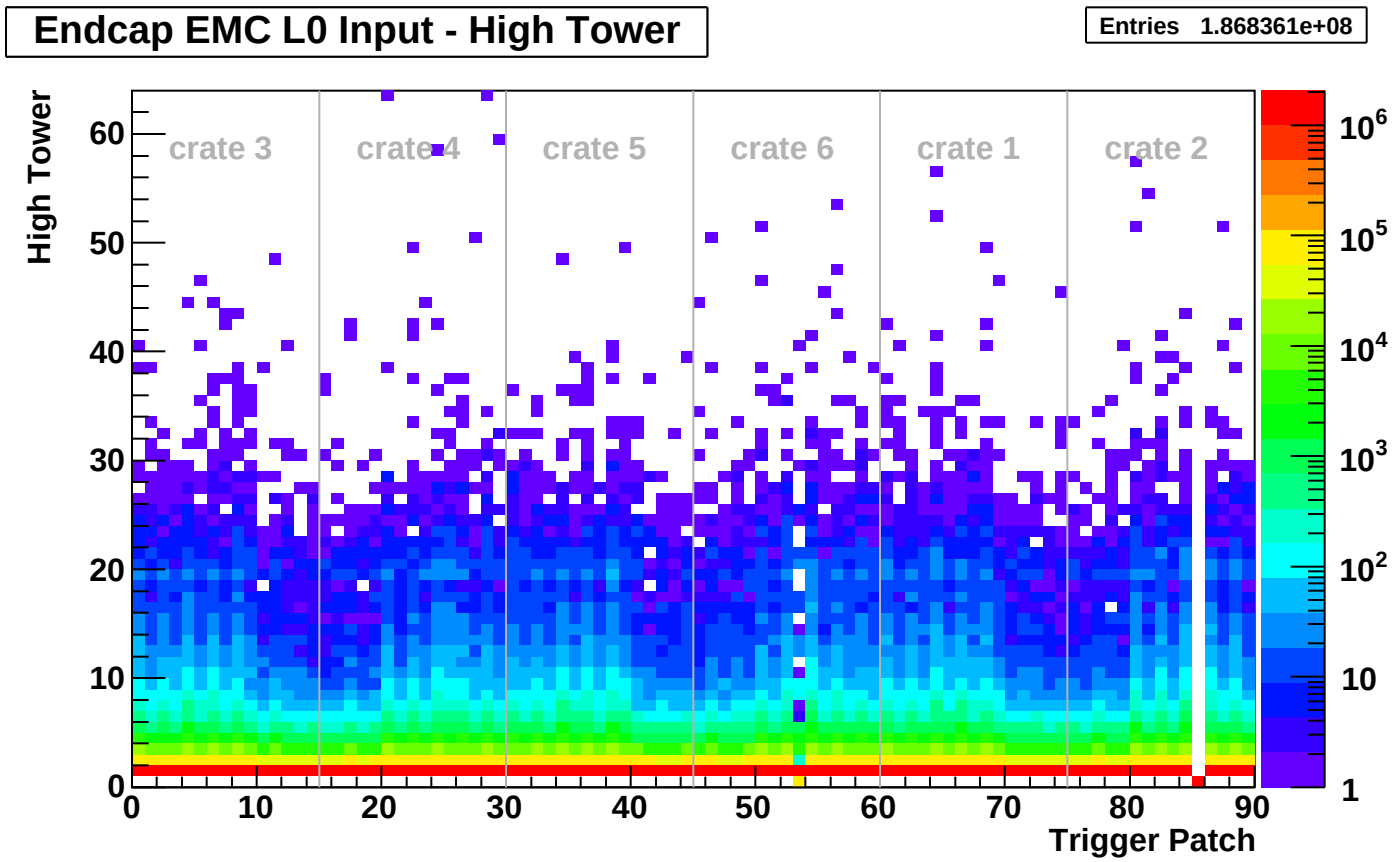

BunchId7Bit (all events)
Entries2075957

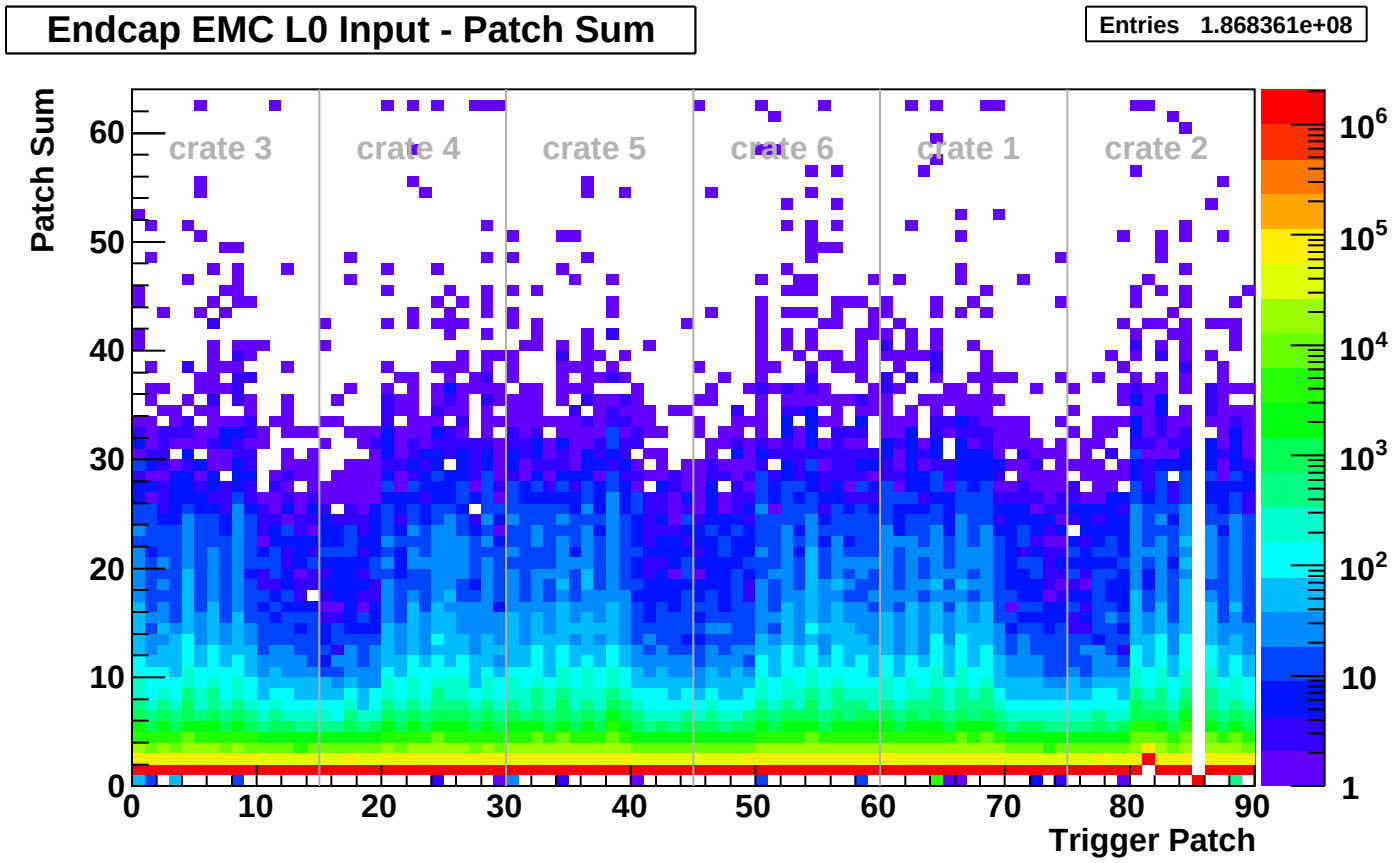

Abort-gaps
Entries 39738

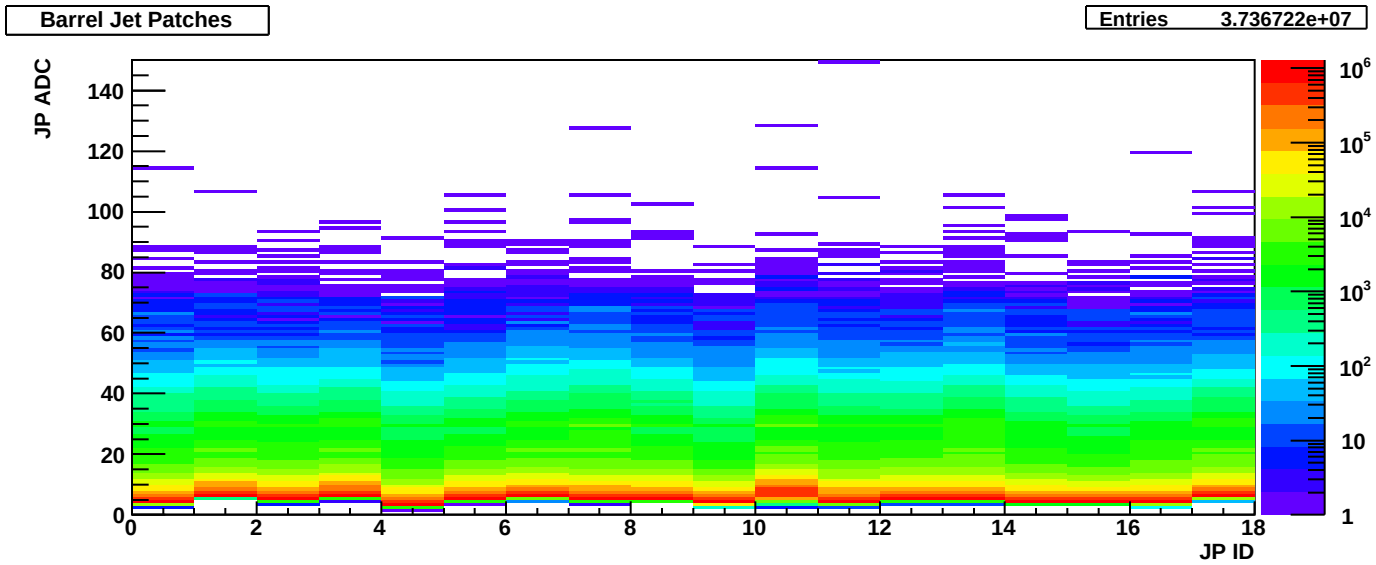

!Yellow-Fill
Entries 73876

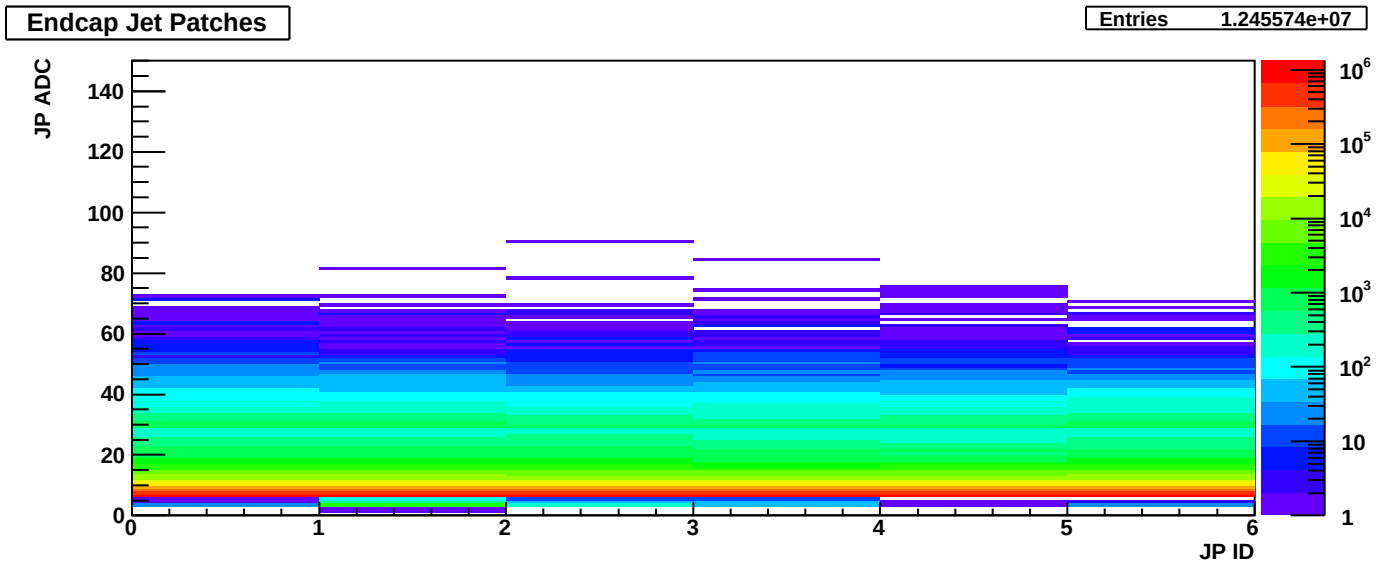

Barrel EMC L0 Input - High Tower
Entries 6.227871e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

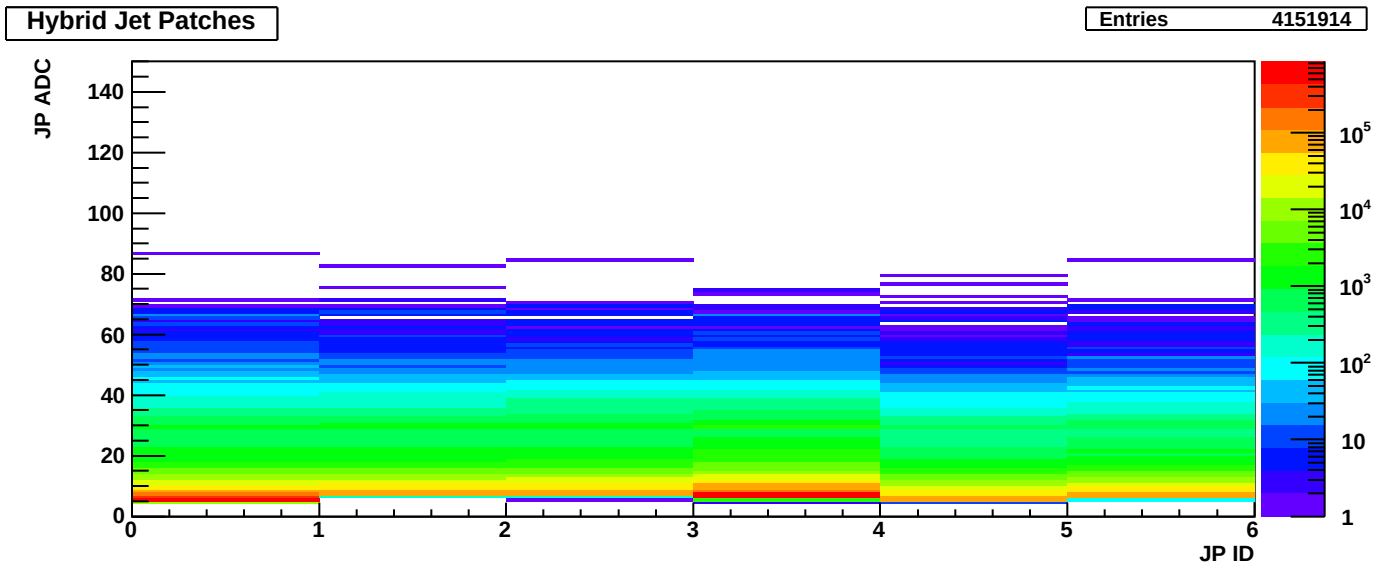

Barrel EMC L0 Input - Patch Sum
Entries 6.227871e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

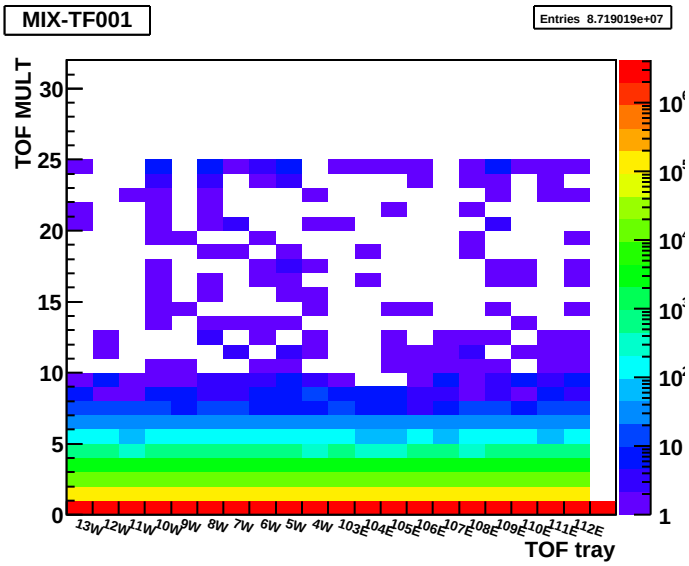

Endcap EMC L0 Input - High Tower
Entries 1.868361e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

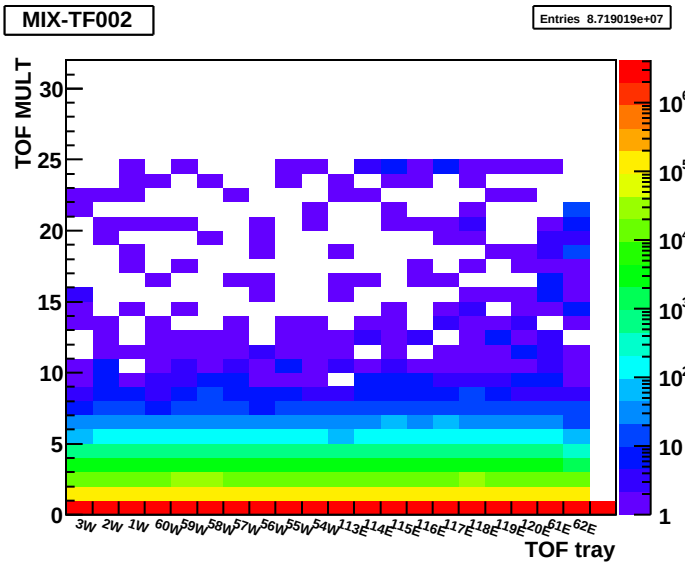

Endcap EMC L0 Input - Patch Sum
Entries 1.868361e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

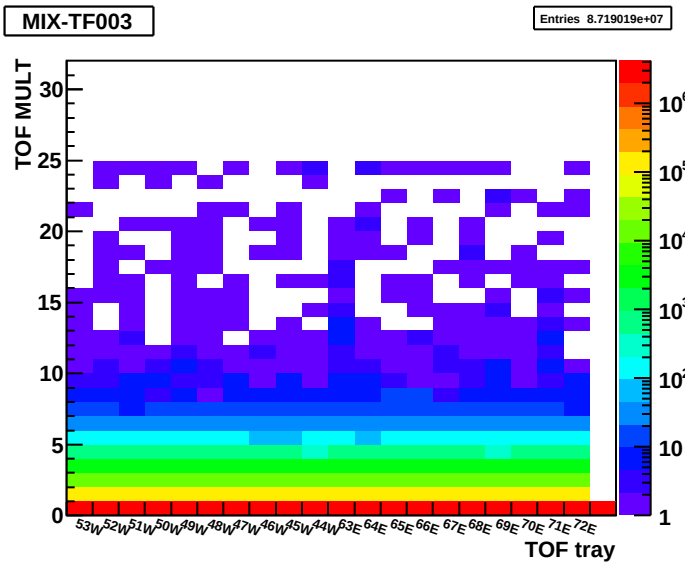

Barrel Jet Patches
Entries 3.736722e+07
JP ADC
JP ID

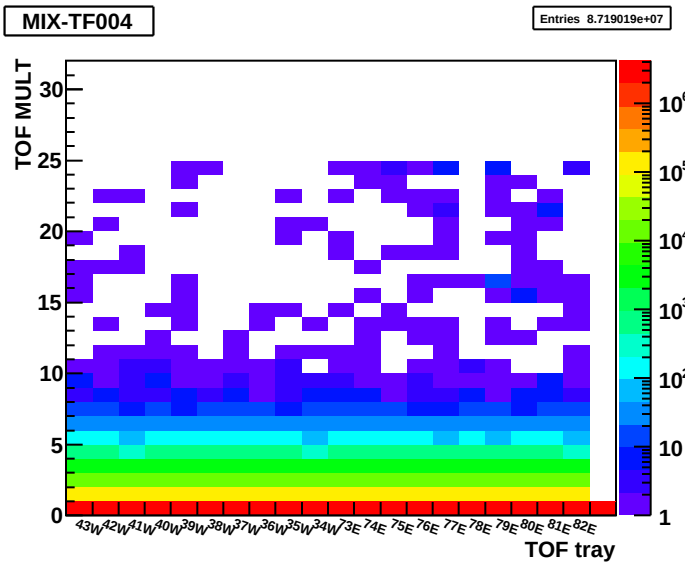

Endcap Jet Patches
Entries 1.245574e+07
JP ADC
JP ID

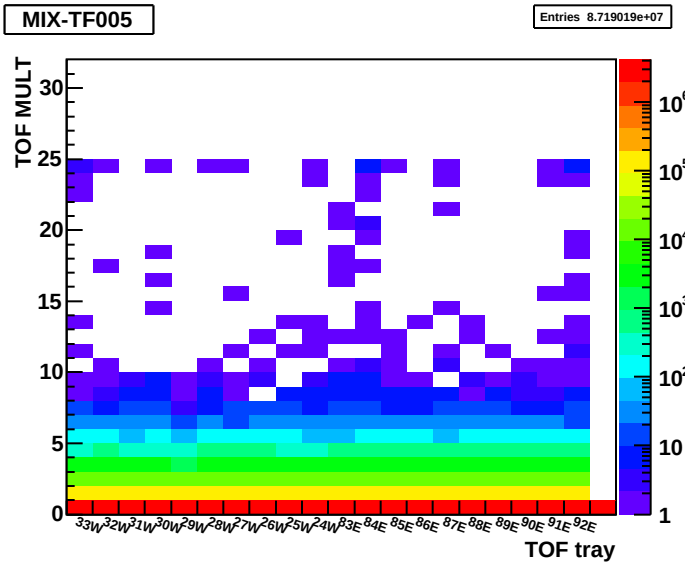

Hybrid Jet Patches
Entries 4151914
JP ADC
JP ID

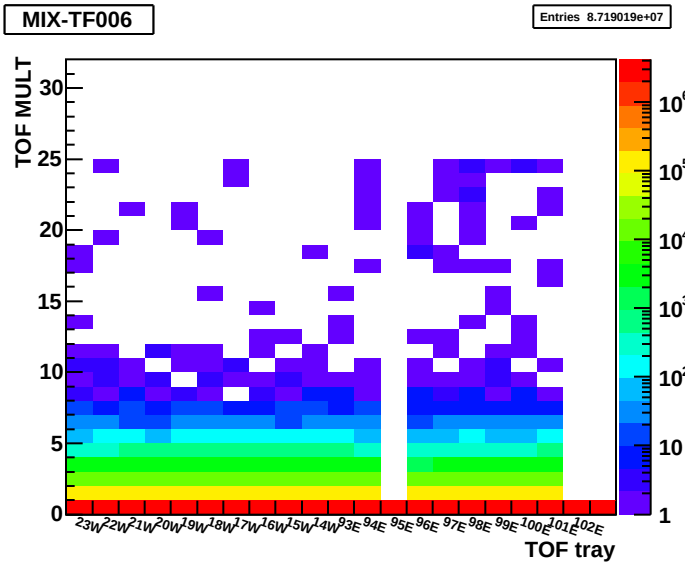

MIX-TF001
Entries 8.719019e+07
TOF MULT
TOF tray

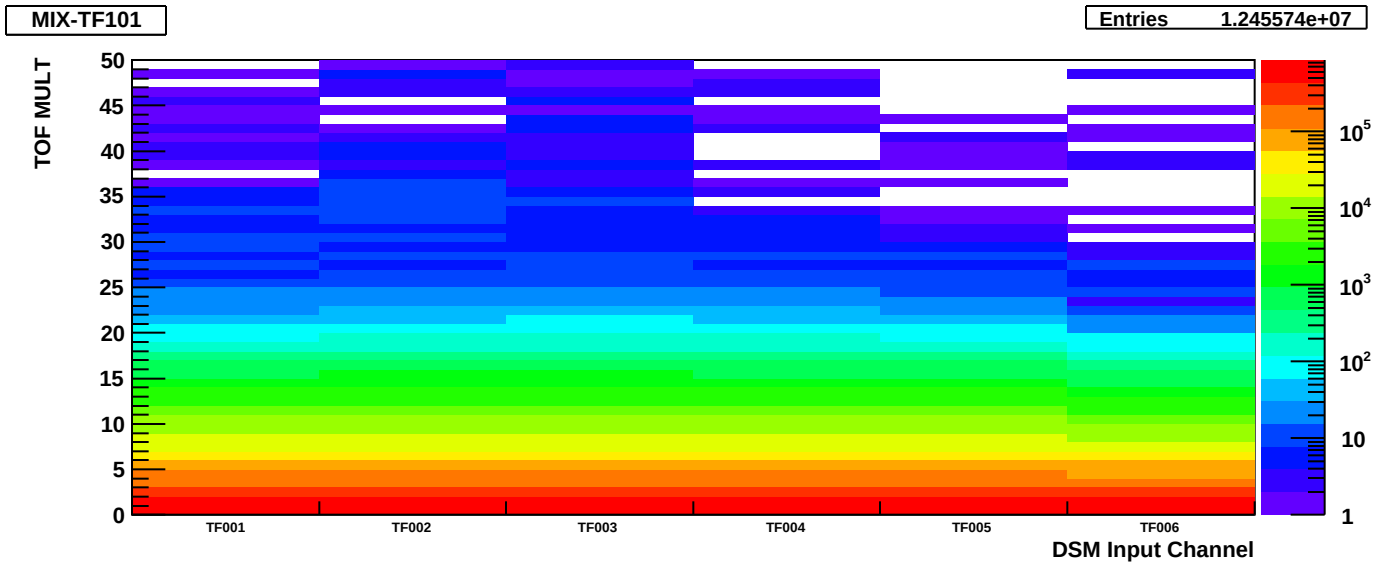

MIX-TF002
Entries 8.719019e+07
TOF MULT
TOF tray

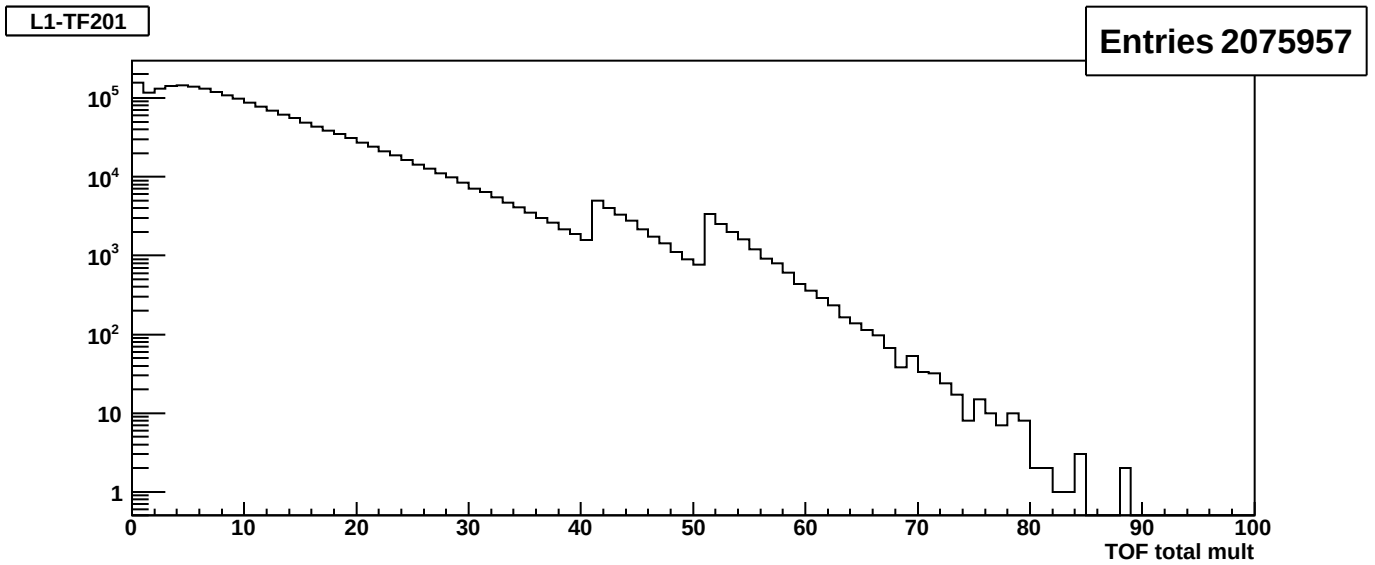

MIX-TF003
Entries 8.719019e+07
TOF MULT
TOF tray

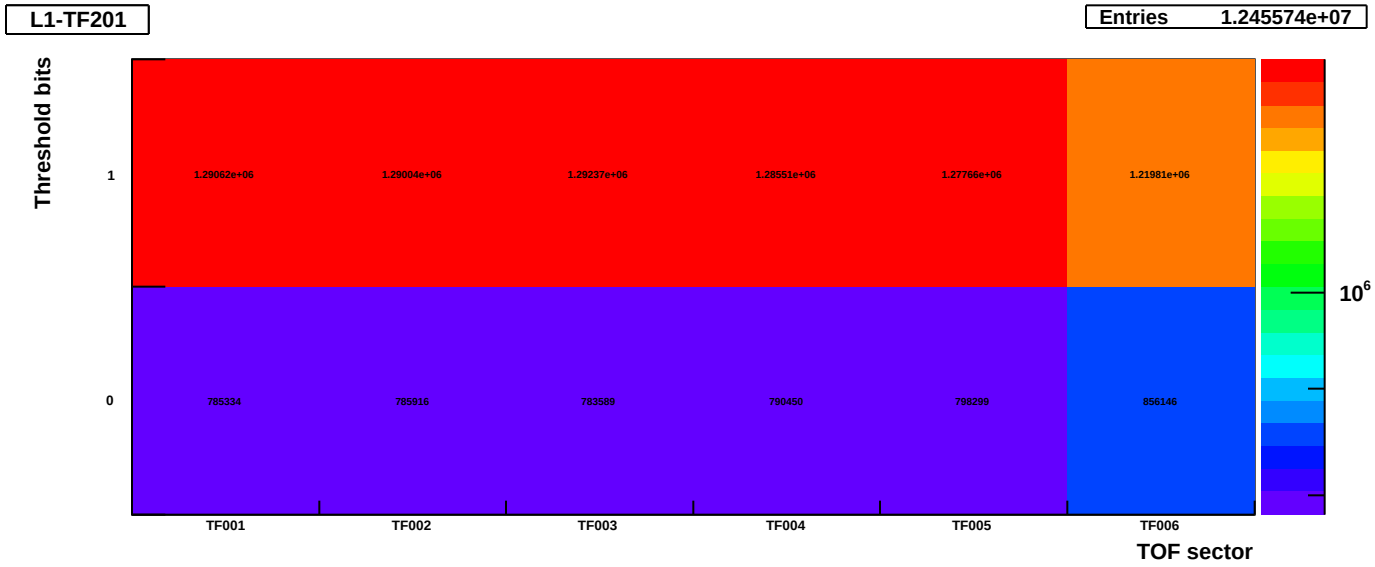

MIX-TF004
Entries 8.719019e+07
TOF MULT
TOF tray

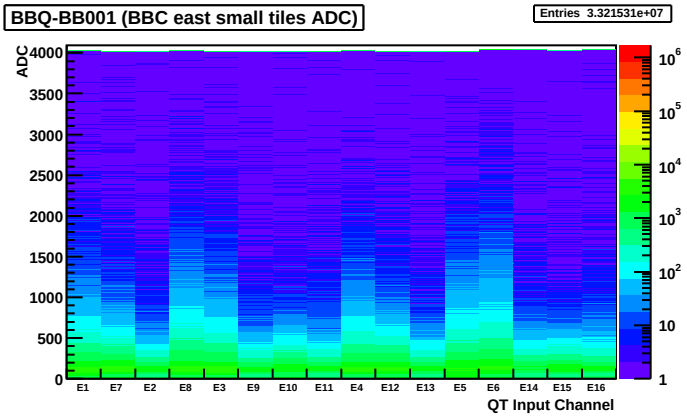

MIX-TF005
Entries 8.719019e+07
TOF MULT
TOF tray

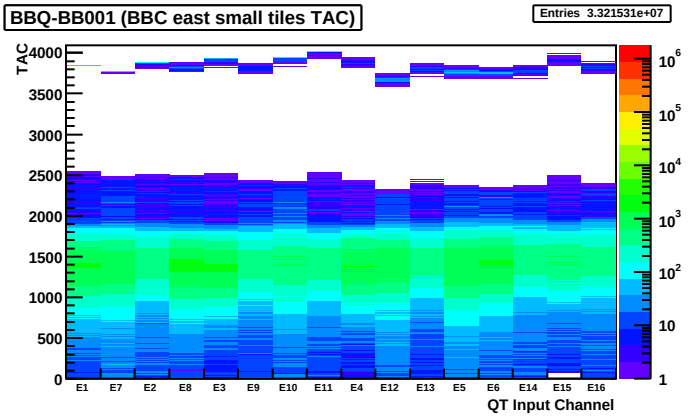

MIX-TF006
Entries 8.719019e+07
TOF MULT
TOF tray

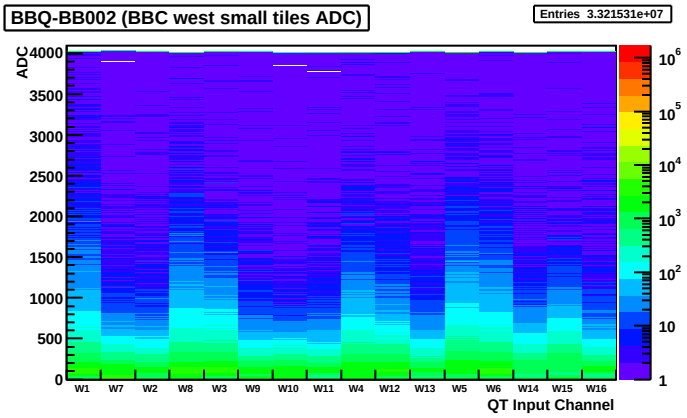

MIX-TF101
Entries 1.245574e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

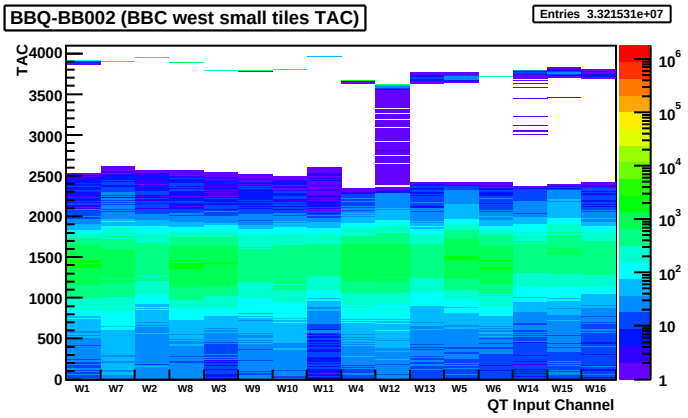

L1-TF201
Entries 2075957
TOF total mult

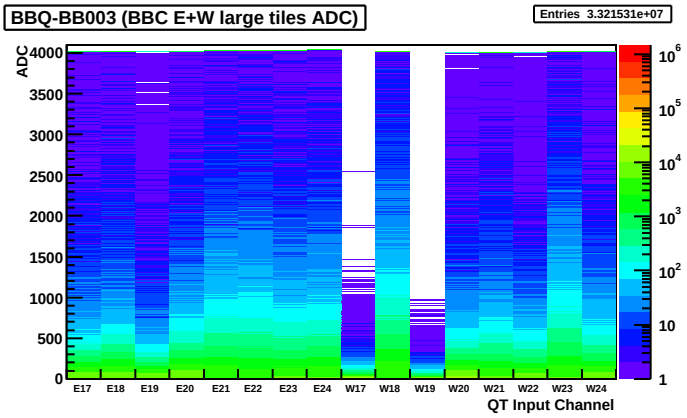

L1-TF201
Entries 1.245574e+07
Threshold bits
1.29062e+06
1.29004e+06
1.29237e+06
1.28551e+06
1.27766e+06
1.21981e+06
785334
785916
783589
790450
798299
856146
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

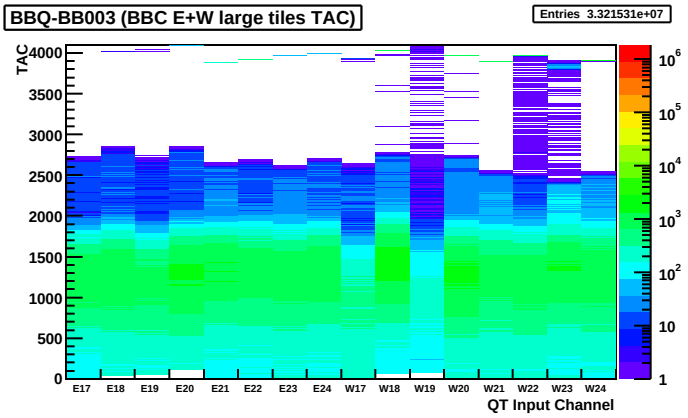

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.321531e+07
ADC
QT Input Channel

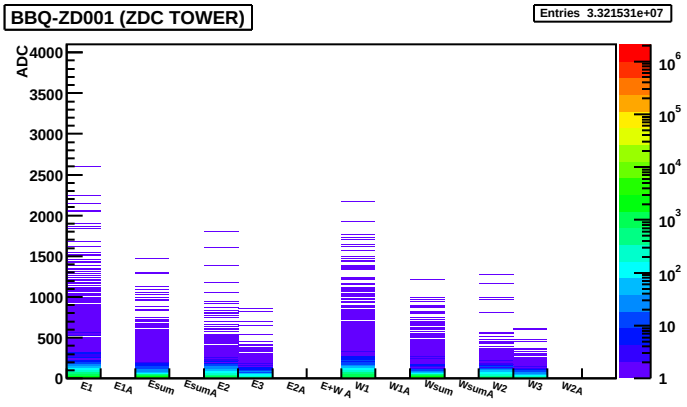

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.321531e+07
TAC
QT Input Channel

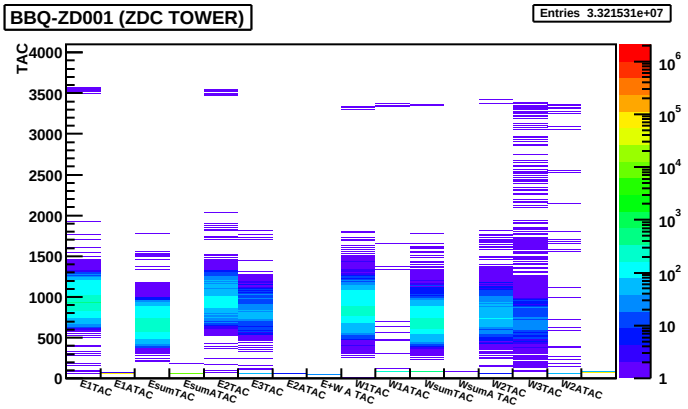

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.321531e+07
ADC
QT Input Channel

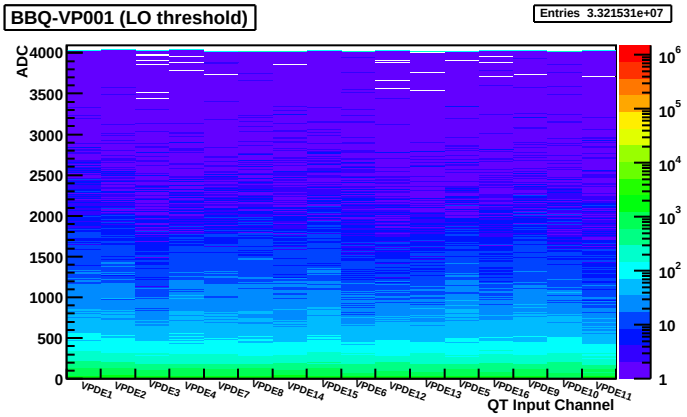

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.321531e+07
TAC
QT Input Channel

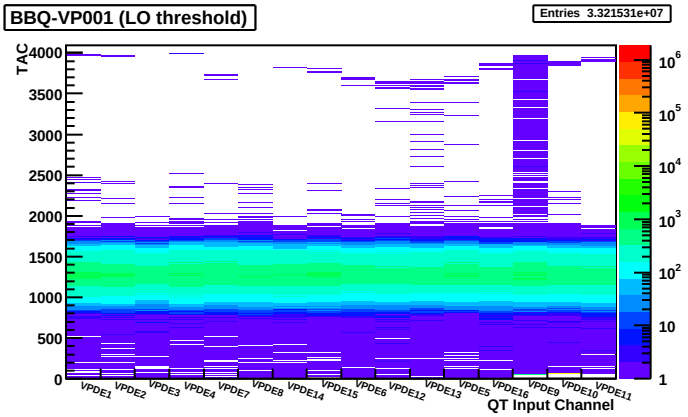

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.321531e+07
ADC
QT Input Channel

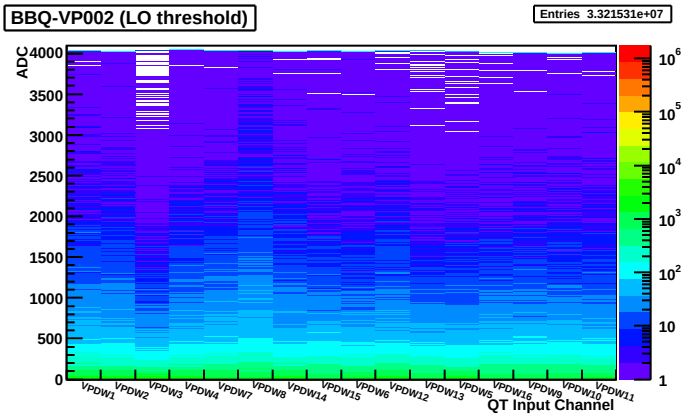

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.321531e+07
TAC
QT Input Channel

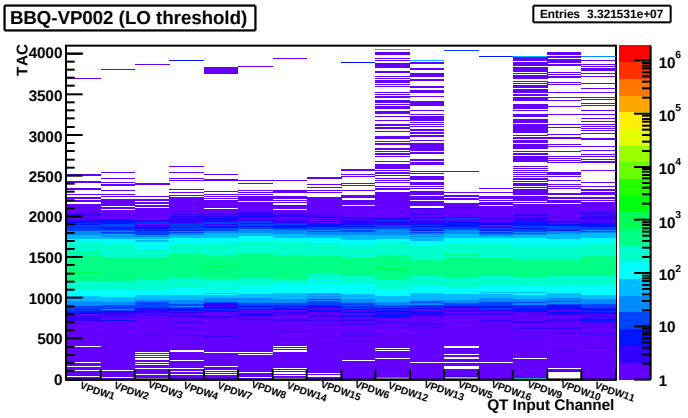

BBQ-ZD001 (ZDC TOWER)
Entries 3.321531e+07
ADC

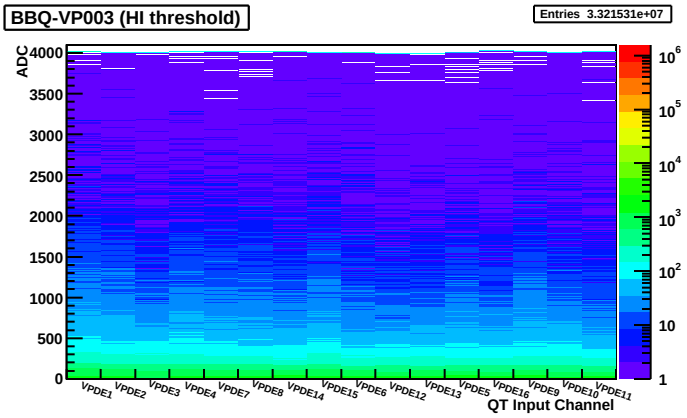

BBQ-ZD001 (ZDC TOWER)
Entries 3.321531e+07
TAC

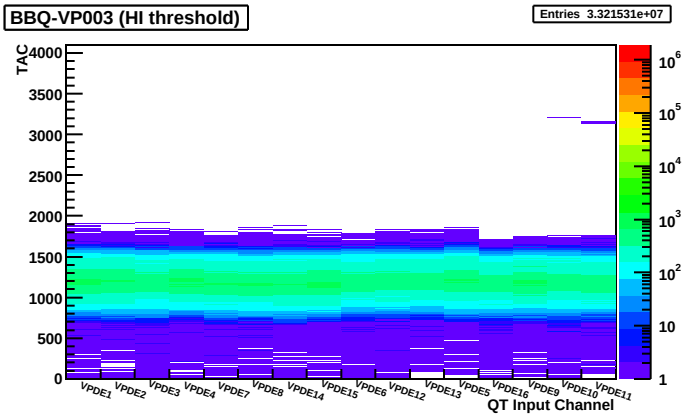

BBQ-VP001 (LO threshold)
Entries 3.321531e+07
ADC
QT Input Channel

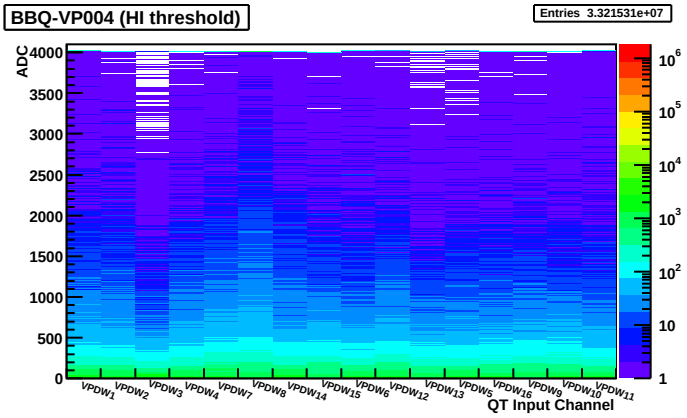

BBQ-VP001 (LO threshold)
Entries 3.321531e+07
TAC
QT Input Channel

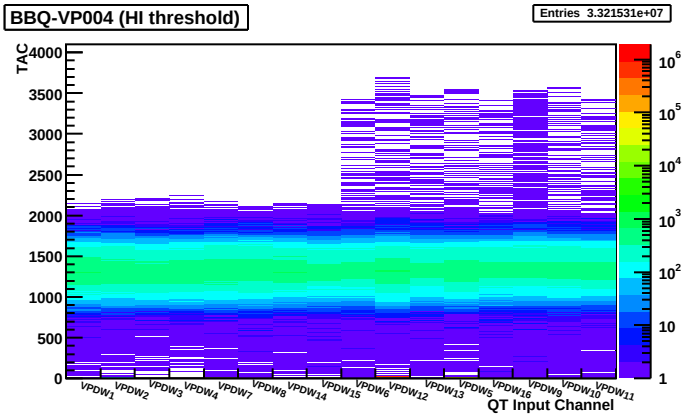

BBQ-VP002 (LO threshold)
Entries 3.321531e+07
ADC
QT Input Channel

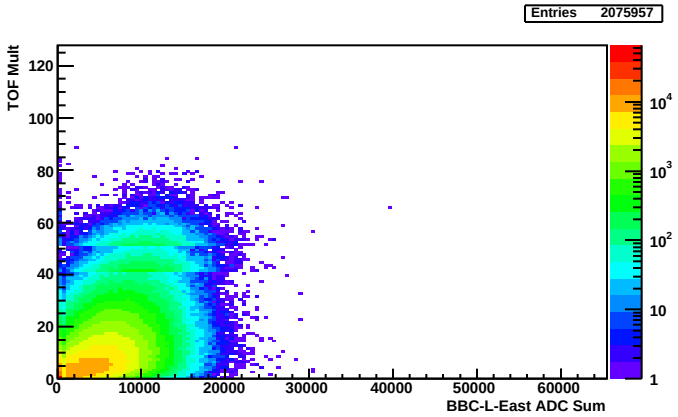

BBQ-VP002 (LO threshold)
Entries 3.321531e+07
TAC
QT Input Channel

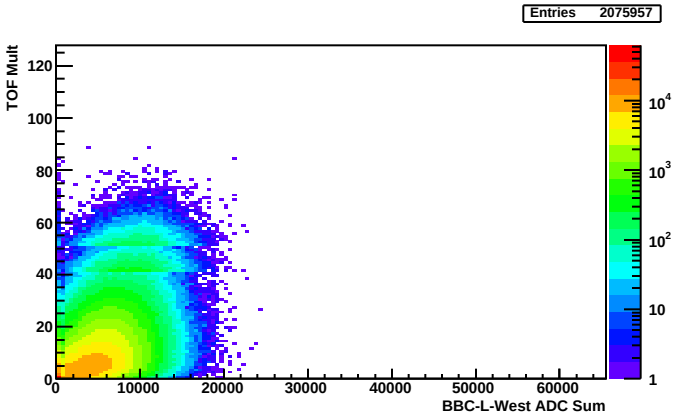

BBQ-VP003 (HI threshold)
Entries 3.321531e+07
ADC
QT Input Channel

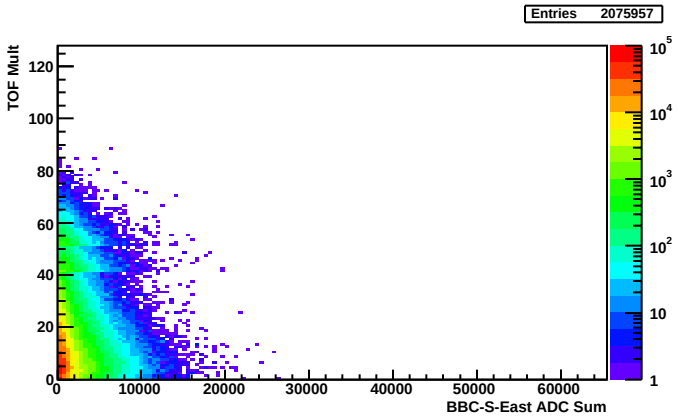

BBQ-VP003 (HI threshold)
Entries 3.321531e+07
TAC
QT Input Channel

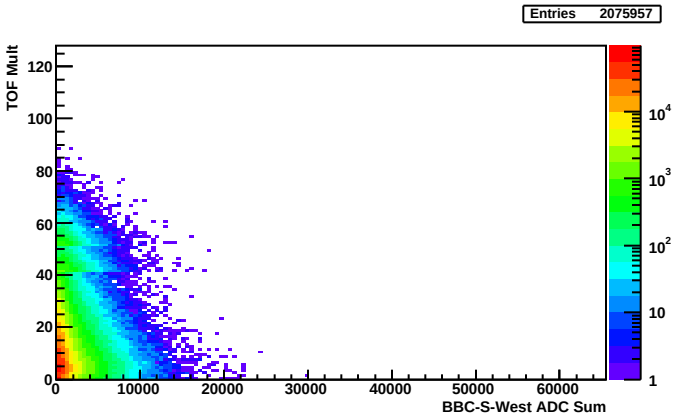

BBQ-VP004 (HI threshold)
Entries 3.321531e+07
ADC
QT Input Channel

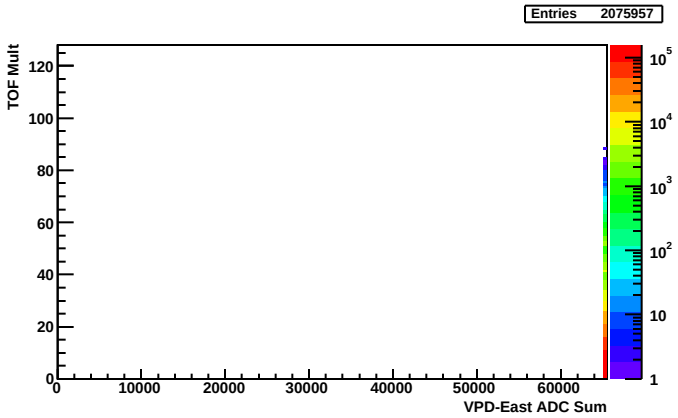

BBQ-VP004 (HI threshold)
Entries 3.321531e+07
TAC
QT Input Channel

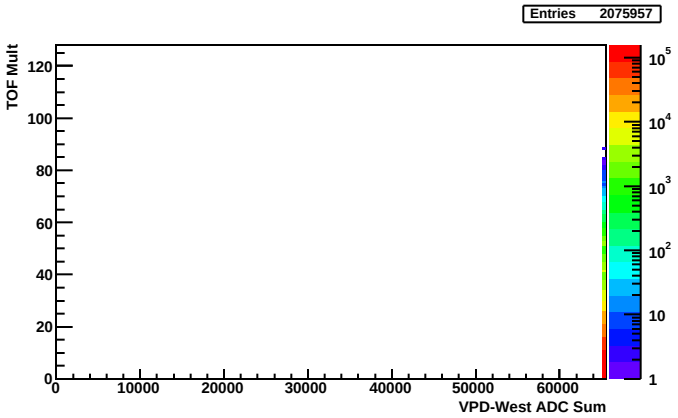

Entries 2075957
TOF Mult
BBC-L-East ADC Sum

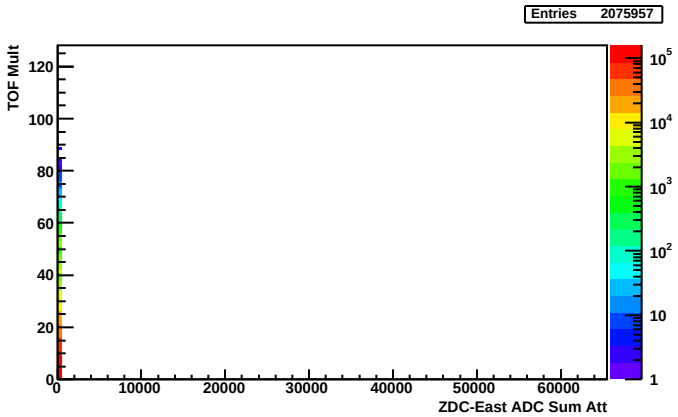

Entries 2075957
TOF Mult
BBC-L-West ADC Sum

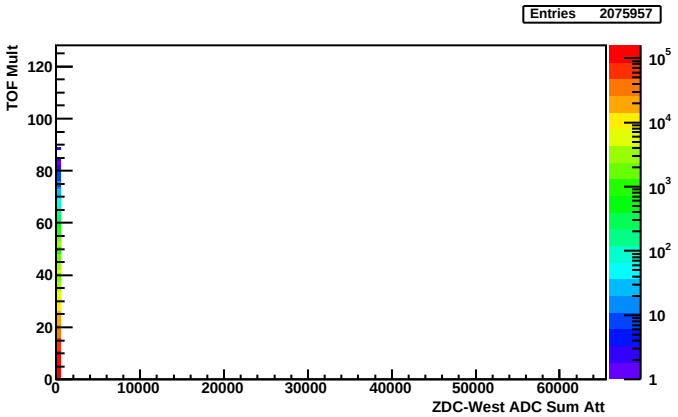

Entries 2075957
TOF Mult
BBC-S-East ADC Sum

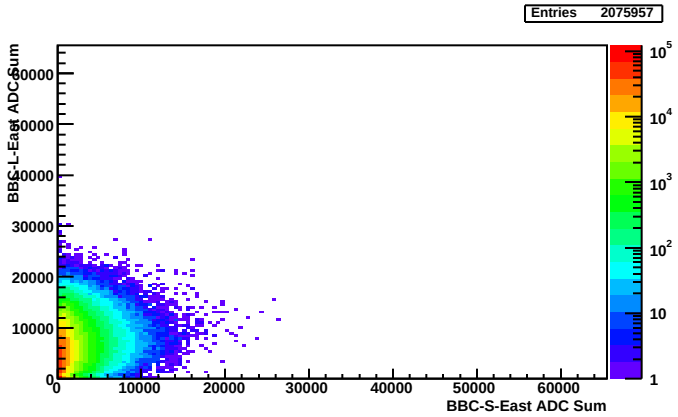

Entries 2075957
TOF Mult
BBC-S-West ADC Sum

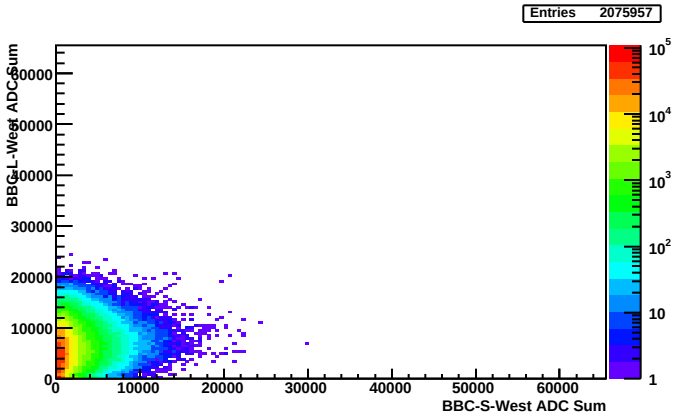

Entries 2075957
TOF Mult
VPD-East ADC Sum

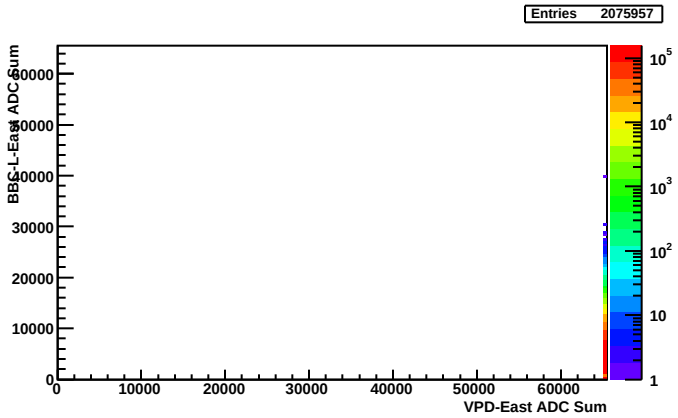

Entries 2075957
TOF Mult
VPD-West ADC Sum

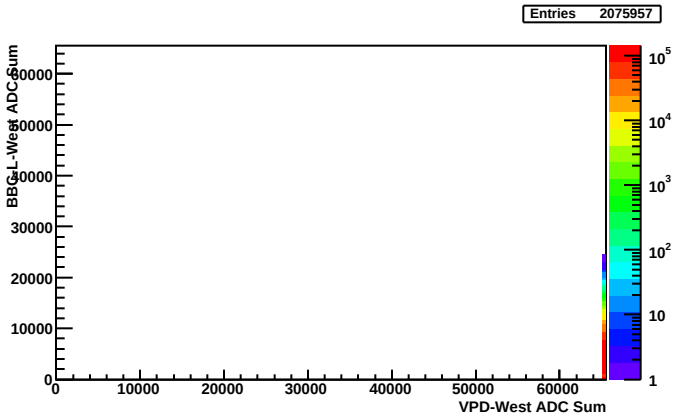

Entries 2075957
TOF Mult
ZDC-East ADC Sum Att

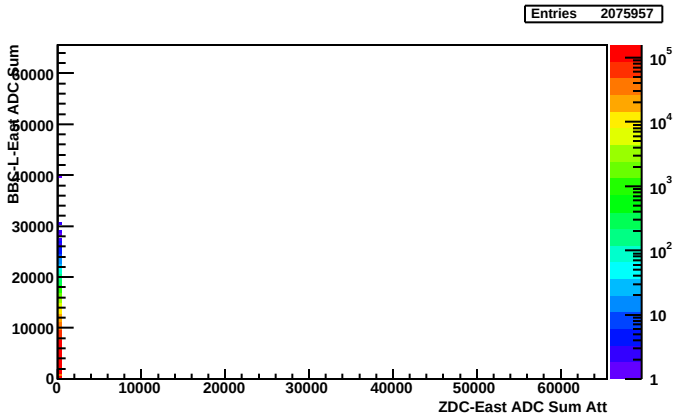

Entries 2075957
TOF Mult
ZDC-West ADC Sum Att


Entries 2075957
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2075957
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 2075957
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 2075957
BBC-L-West ADC Sum
VPD-West ADC Sum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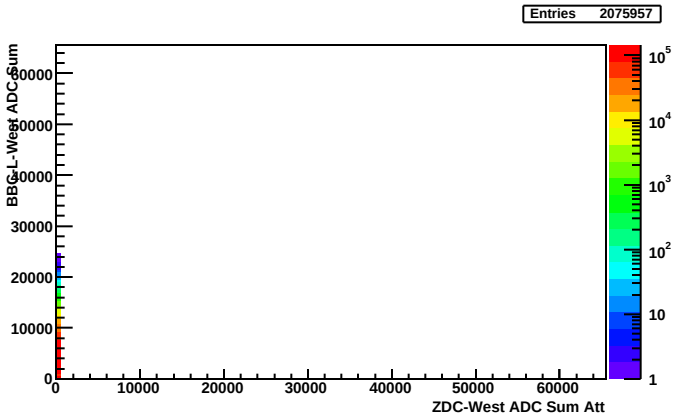
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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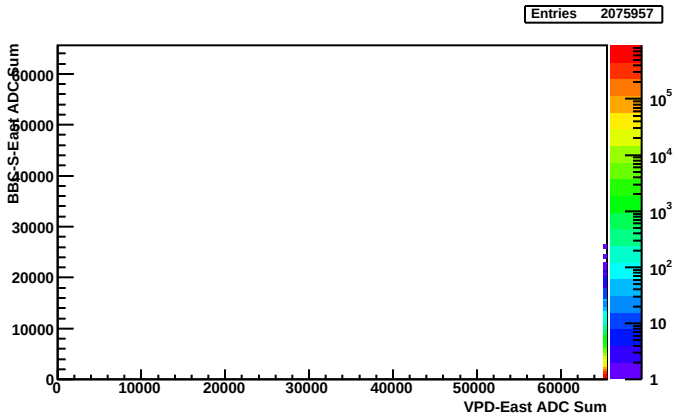
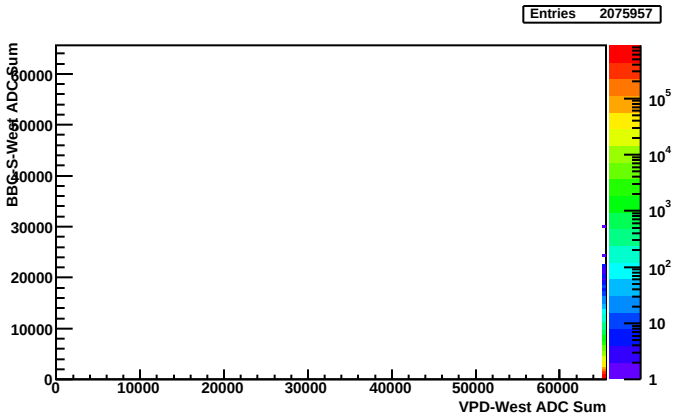
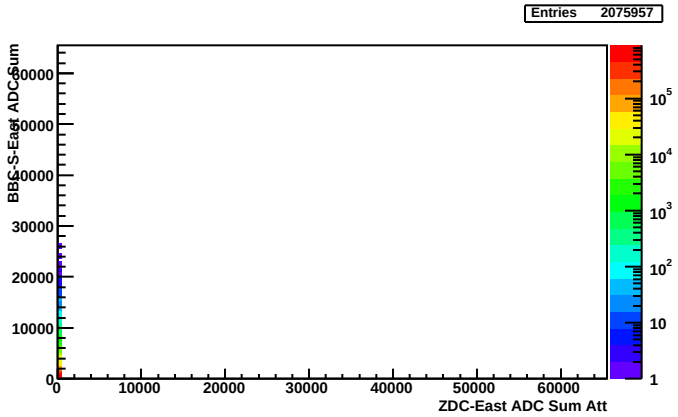
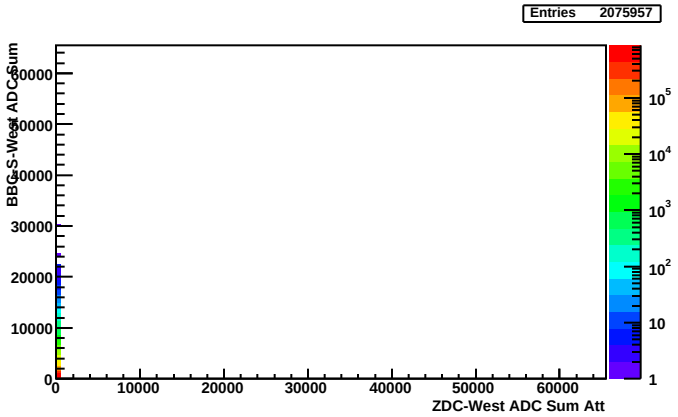
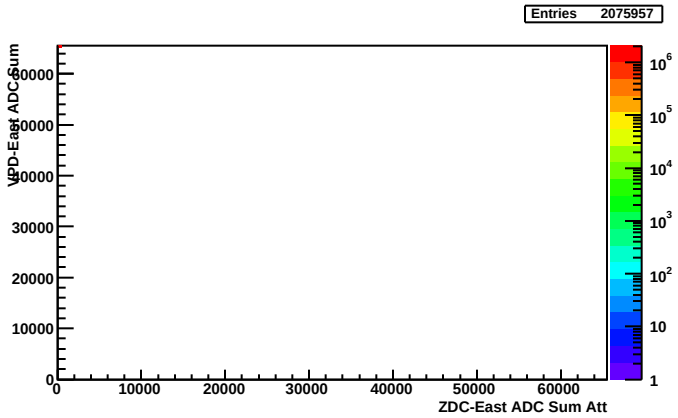
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 2075957
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2075957
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2075957
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2075957
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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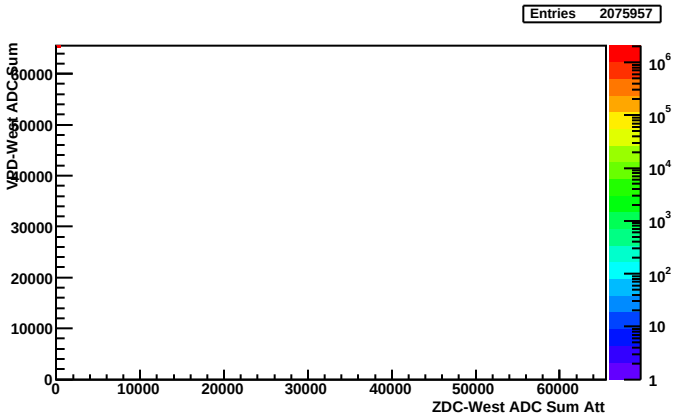
VPD-East ADC Sum
ZDC-East ADC Sum Att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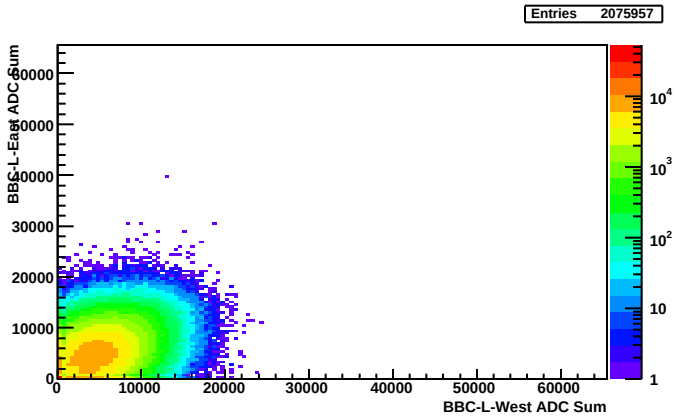
VPD-West ADC Sum
ZDC-West ADC Sum Att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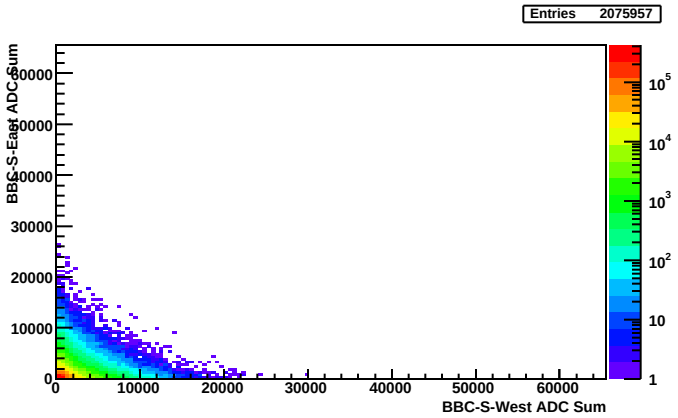
BBC-L-East ADC Sum
BBC-L-West ADC Sum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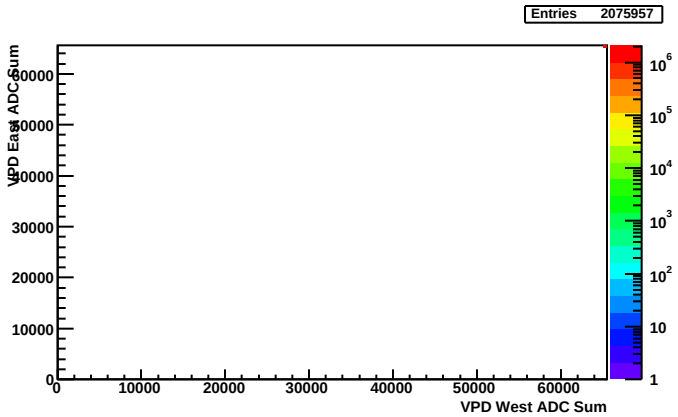
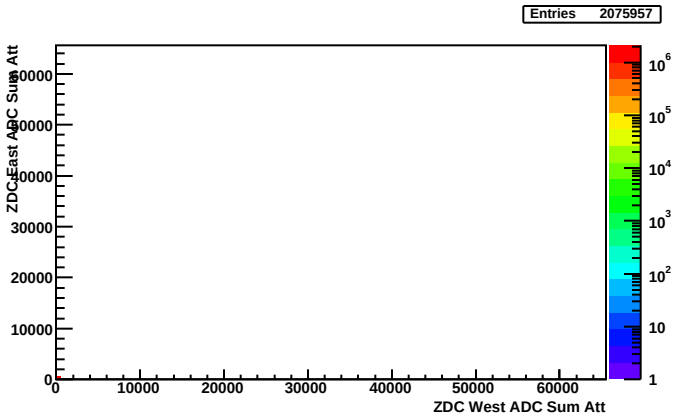
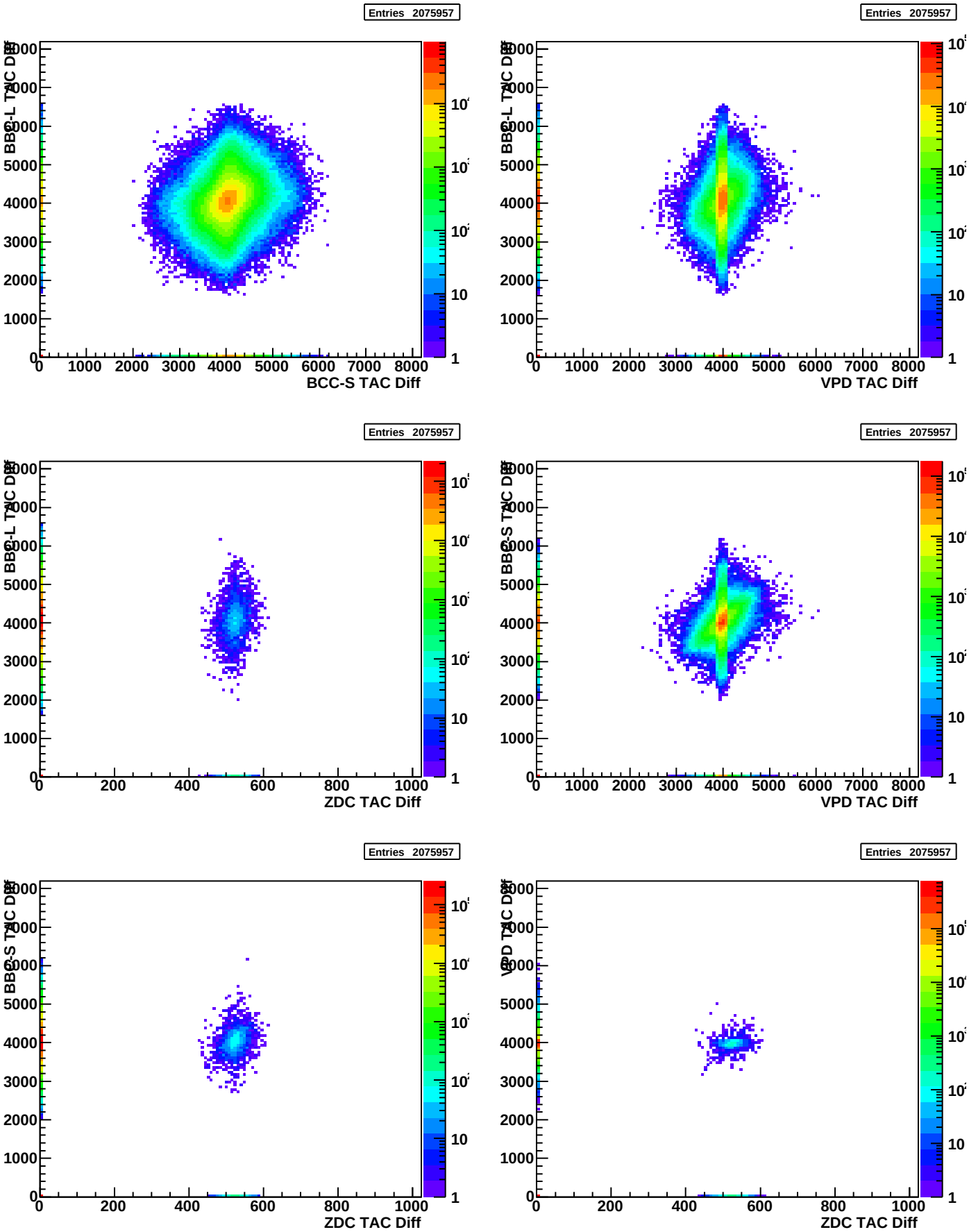
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 2075957
VPD East ADC Sum
VPD West ADC Sum


Entries 2075957
ZDC-East ADC Sum Att
ZDC West ADC Sum Att


Entries 2075957
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2075957
BBC-L TAC Diff
VPD TAC Diff
Entries 2075957
BBC-L TAC Diff
ZDC TAC Diff
Entries 2075957
BBC-S TAC Diff
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Entries 2075957
BBC-S TAC Diff
ZDC TAC Diff
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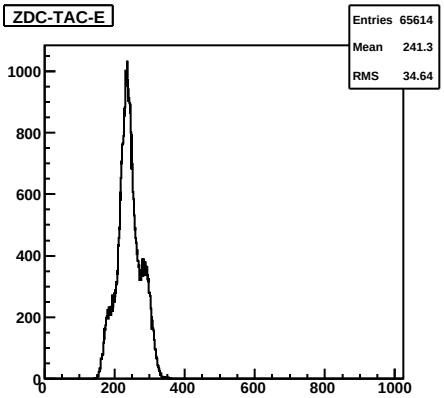
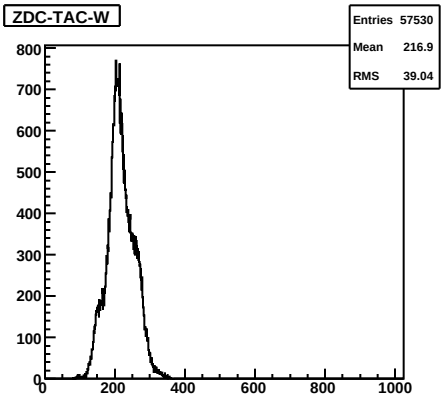
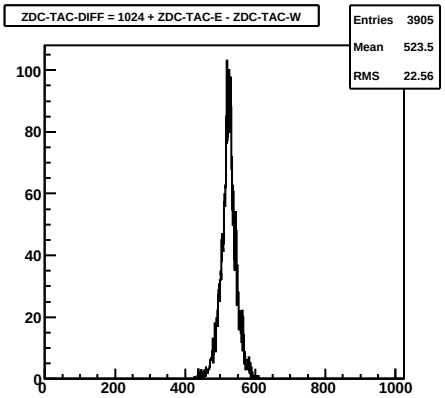
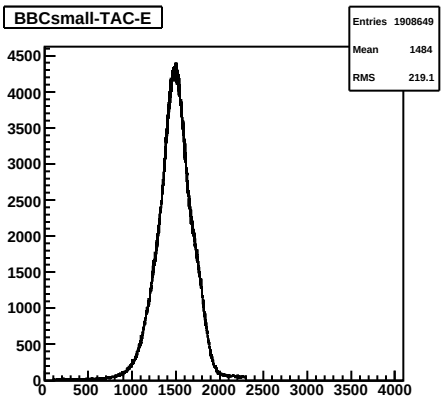
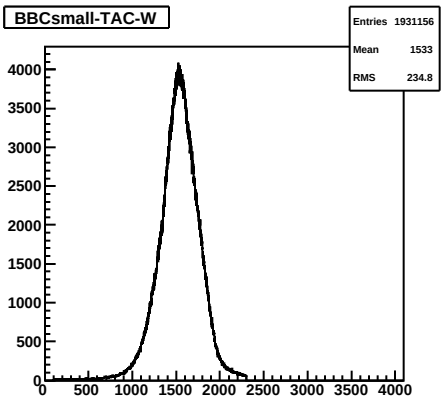
VPD TAC Diff
ZDC TAC Diff

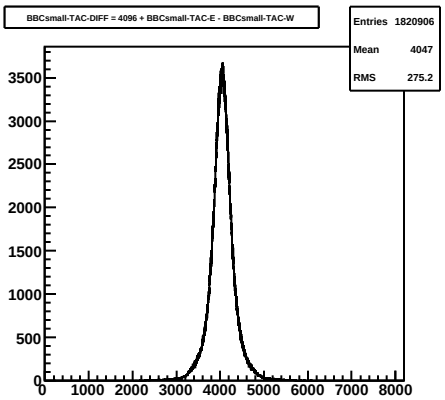

ZDC-TAC-E
Entries 65614
Mean 241.3
RMS 34.64

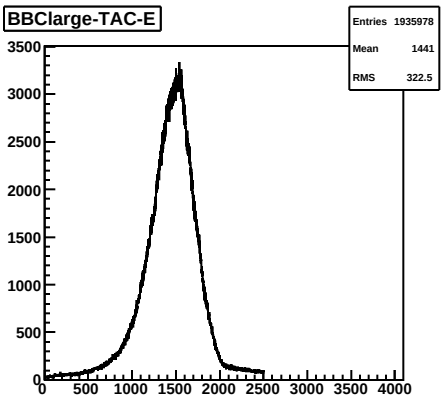

ZDC-TAC-W
Entries 57530
Mean 216.9
RMS 39.04

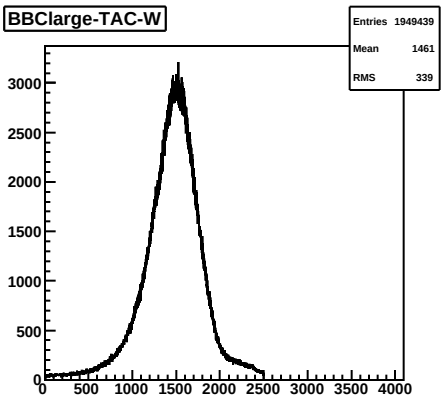

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3905
Mean 523.5
RMS 22.56

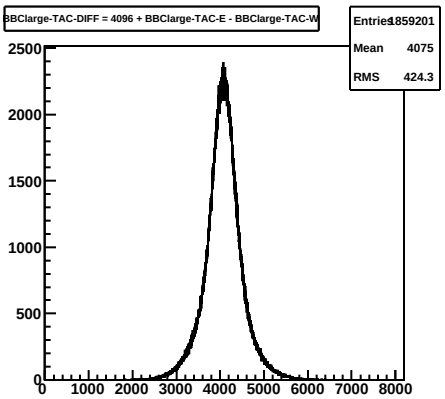

BBCsmall-TAC-E
Entries 1908649
Mean 1484
RMS 219.1

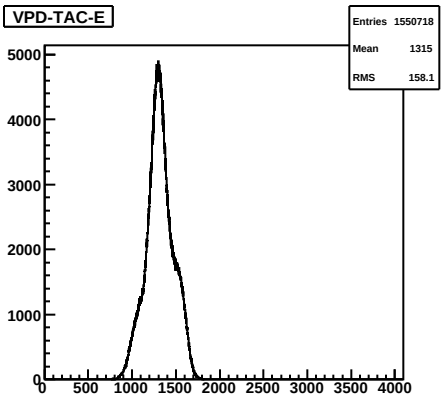

BBCsmall-TAC-W
Entries 1931156
Mean 1533
RMS 234.8

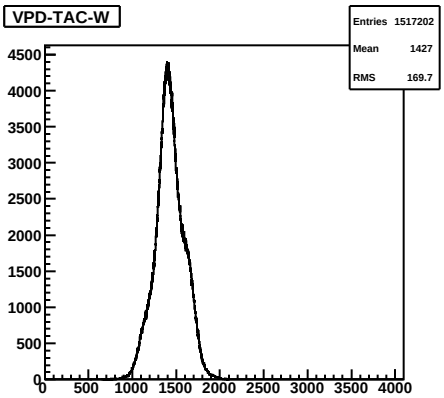

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1820906
Mean 4047
RMS 275.2

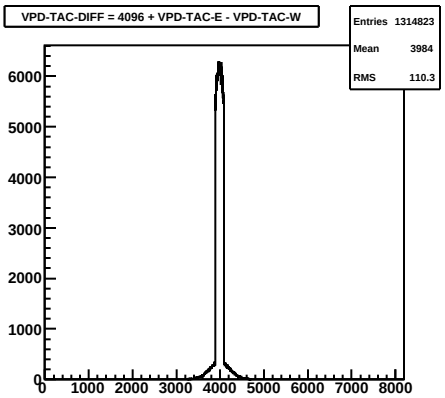

BBClarge-TAC-E
Entries 1935978
Mean 1441
RMS 322.5

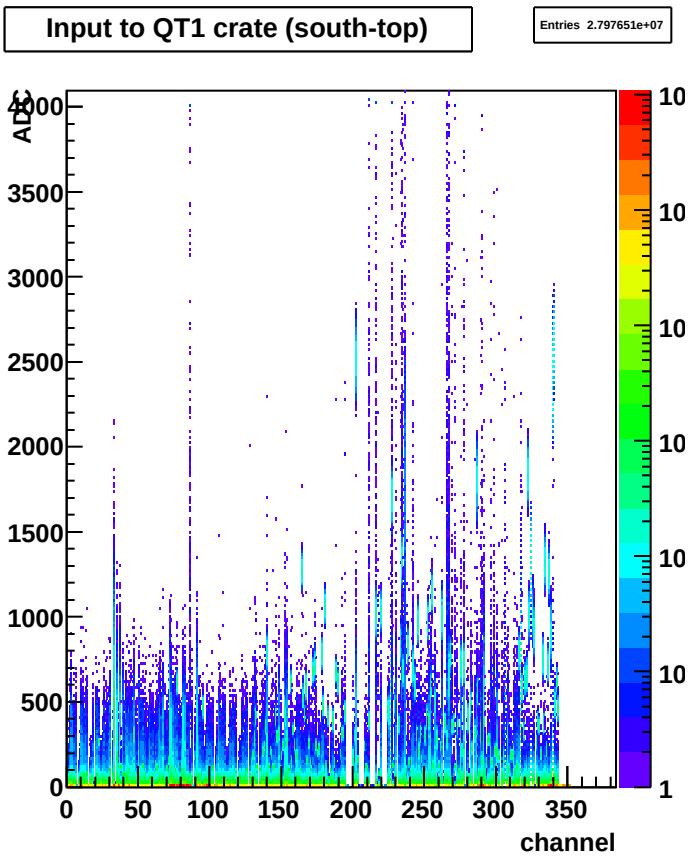

BBClarge-TAC-W
Entries 1949439
Mean 1461
RMS 339

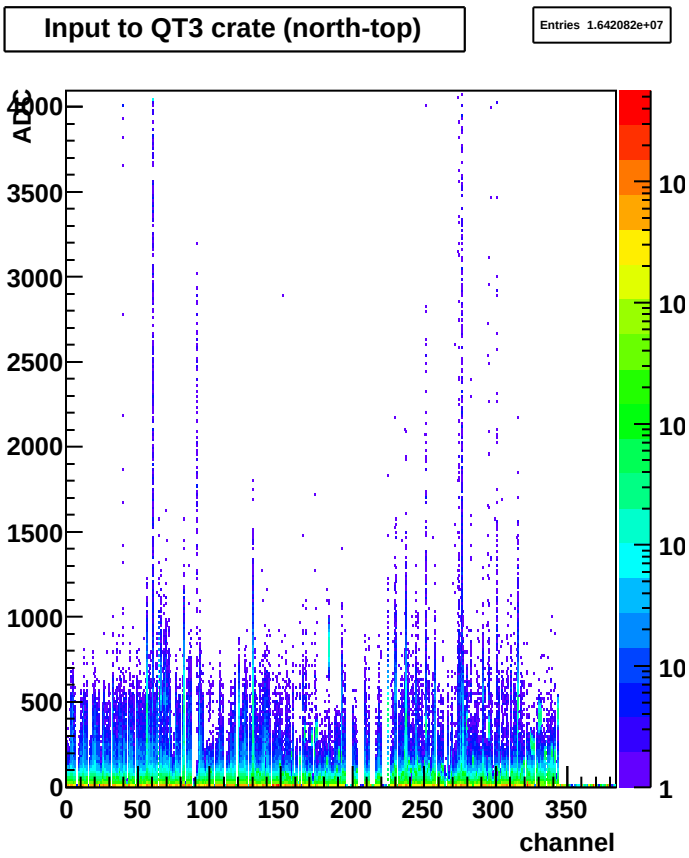

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1859201
Mean 4075
RMS 424.3

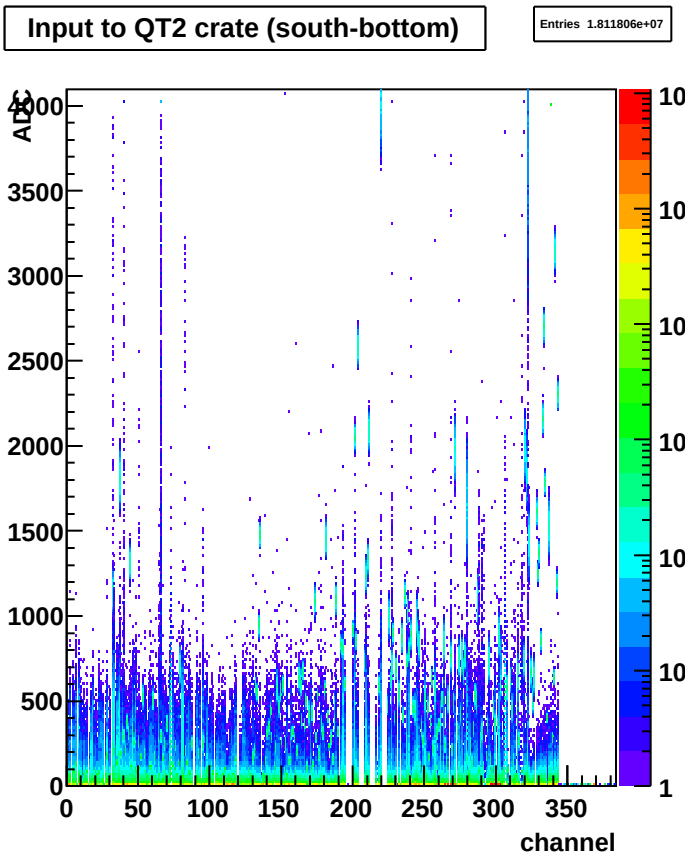

VPD-TAC-E
Entries 1550718
Mean 1315
RMS 158.1

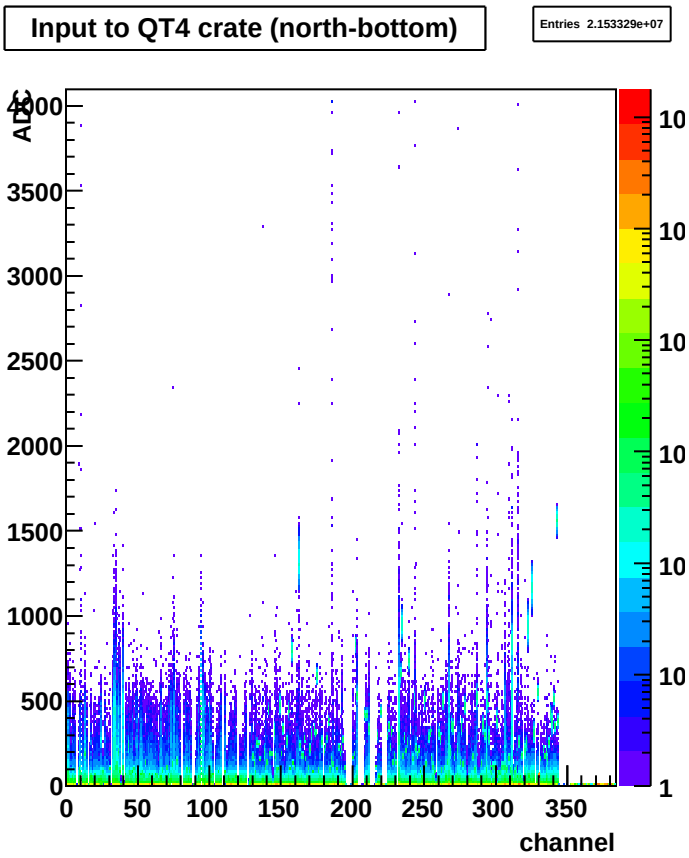

VPD-TAC-W
Entries 1517202
Mean 1427
RMS 169.7

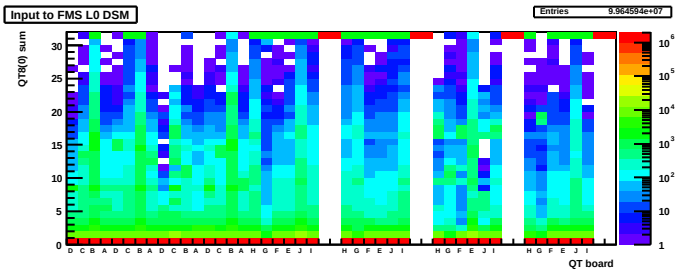

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1314823
Mean 3984
RMS 110.3

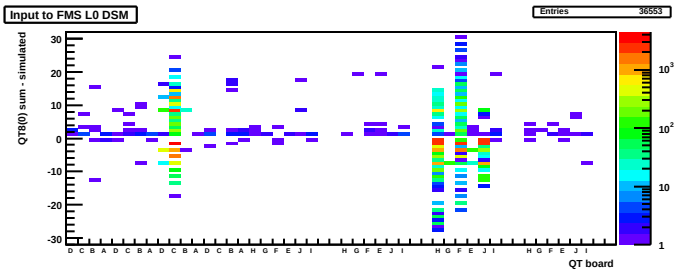

Input to QT1 crate (south-top)
Entries 2.797651e+07
ADC
channel

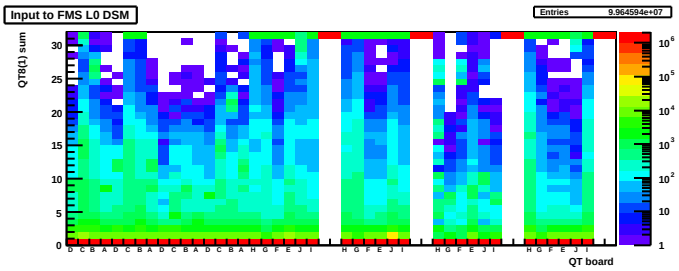

Input to QT3 crate (north-top)
Entries 1.642082e+07
ADC
channel

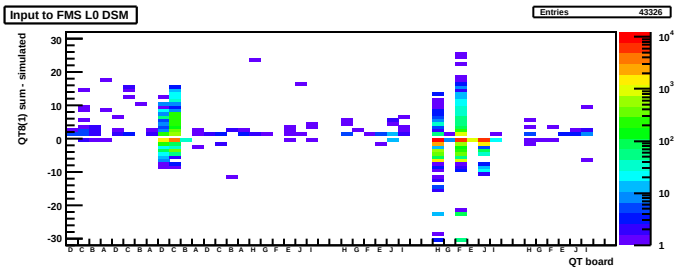

Input to QT2 crate (south-bottom)
Entries 1.811806e+07
ADC
channel

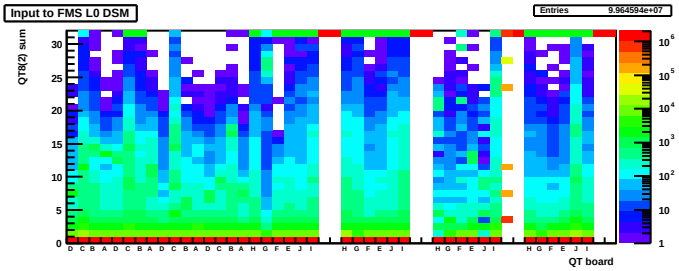

Input to QT4 crate (north-bottom)
Entries 2.153329e+07
ADC
channel

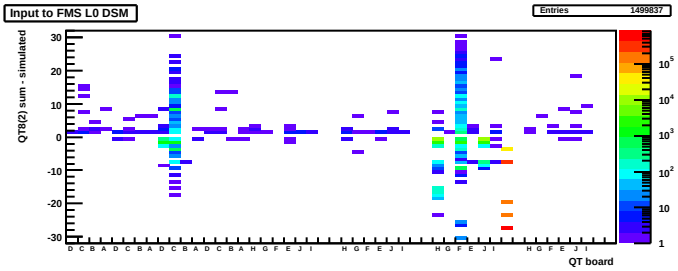

Input to FMS L0 DSM
Entries 9.964594e+07
QT8(0) sum
QT board

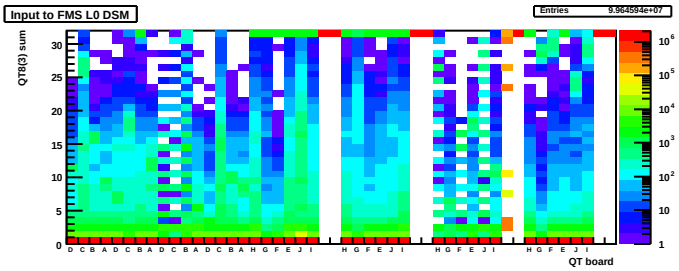

Input to FMS L0 DSM
Entries 36553
QT8(0) sum - simulated
QT board

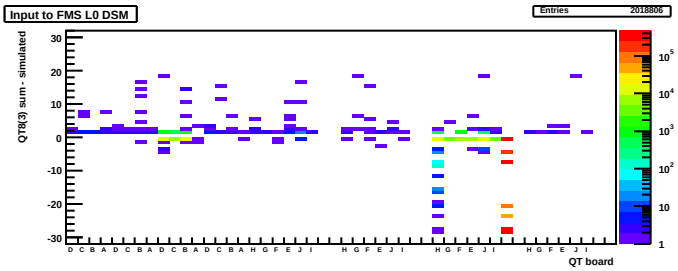

Input to FMS L0 DSM
Entries 9.964594e+07
QT8(1) sum
QT board

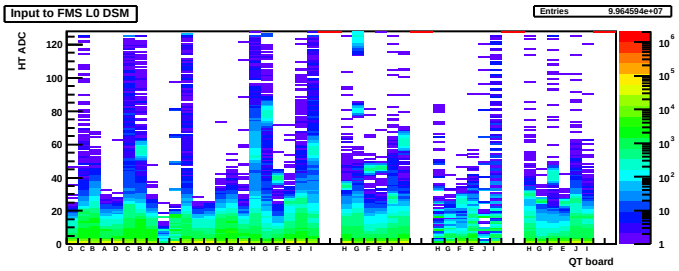

Input to FMS L0 DSM
Entries 43326
QT8(1) sum - simulated
QT board

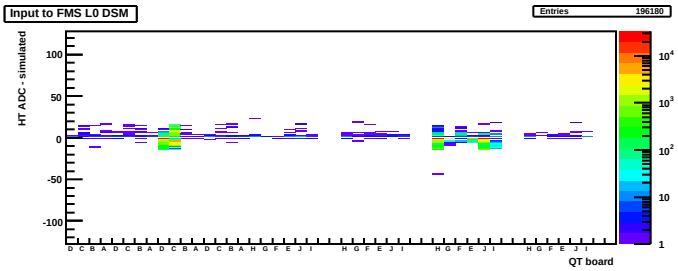

Input to FMS L0 DSM
Entries 9.964594e+07
QT8(2) sum
QT board

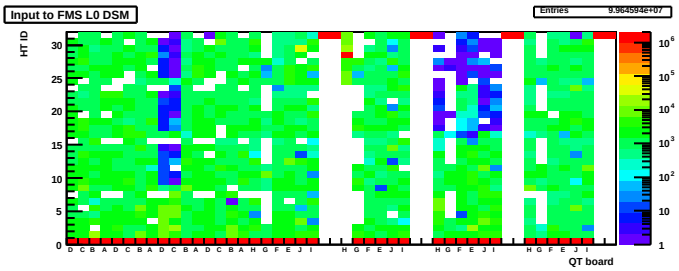

Input to FMS L0 DSM
Entries 1499837
QT8(2) sum - simulated
QT board

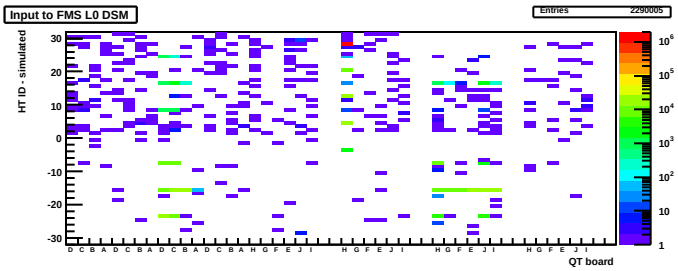

Input to FMS L0 DSM
Entries 9.964594e+07
QT8(3) sum
QT board

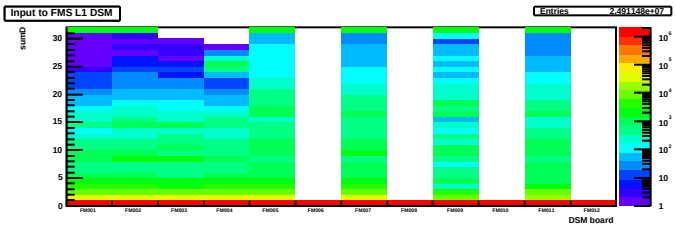

Input to FMS L0 DSM
Entries 2018806
QT8(3) sum - simulated
QT board

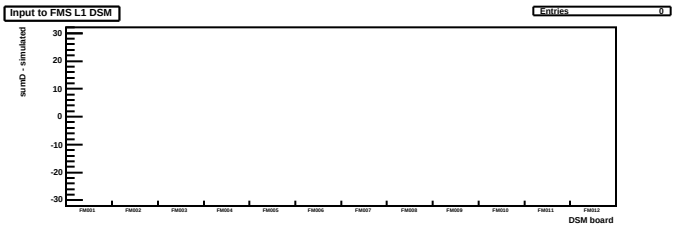

Input to FMS L0 DSM
Entries 9.964594e+07
HT ADC
QT board

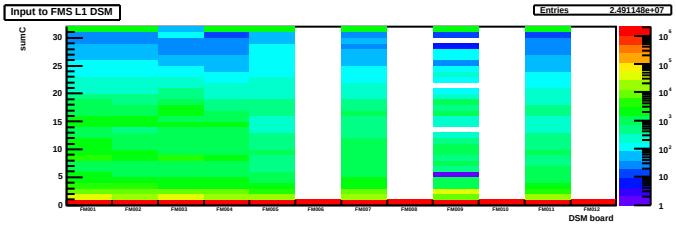

Input to FMS L0 DSM
Entries 196180
HT ADC - simulated
QT board

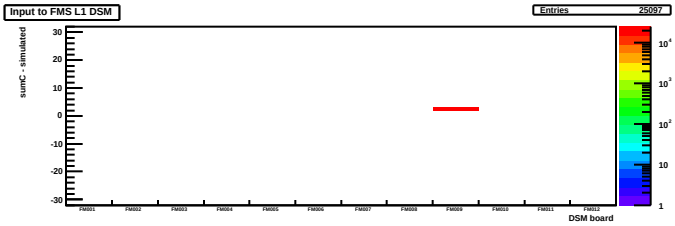

Input to FMS L0 DSM
Entries 9.964594e+07
HT ID
QT board

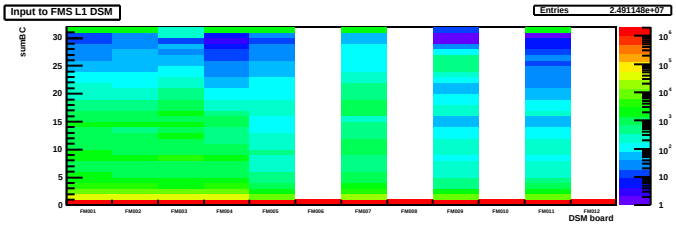

Input to FMS L0 DSM
Entries 2290005
HT ID - simulated
QT board

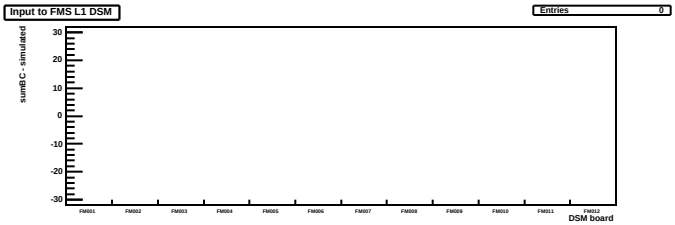

Input to FMS L1 DSM
Entries 2.491148e+07
sumD
DSM board

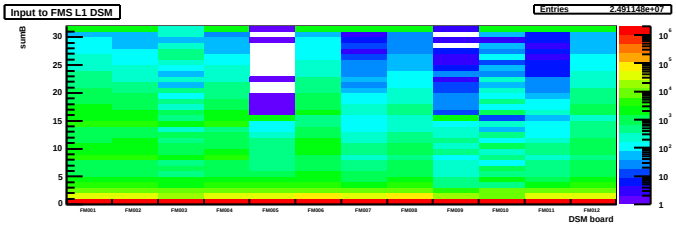

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

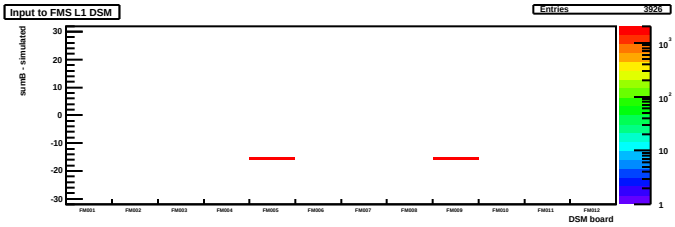

Input to FMS L1 DSM
Entries 2.491148e+07
sumC
DSM board

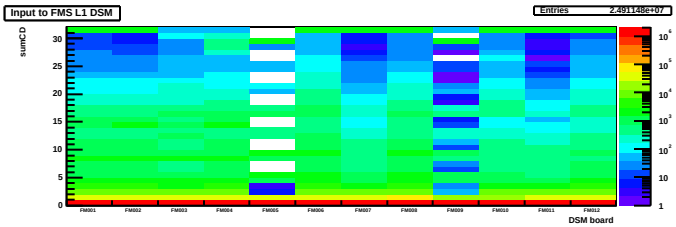

Input to FMS L1 DSM
Entries 25097
sumC - simulated
DSM board

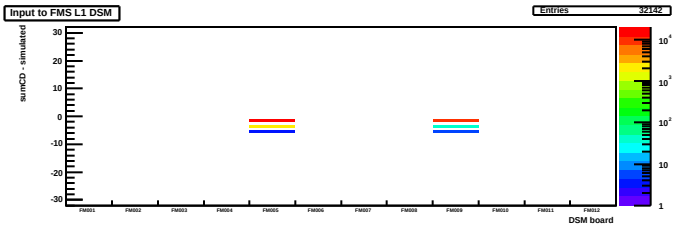

Input to FMS L1 DSM
Entries 2.491148e+07
sumBC
DSM board

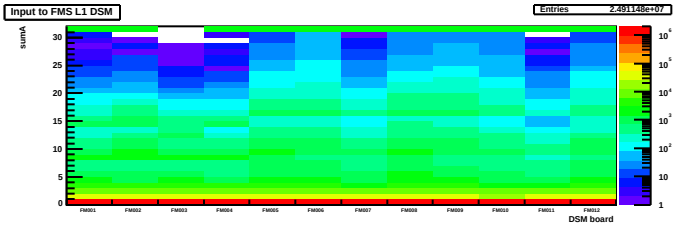

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

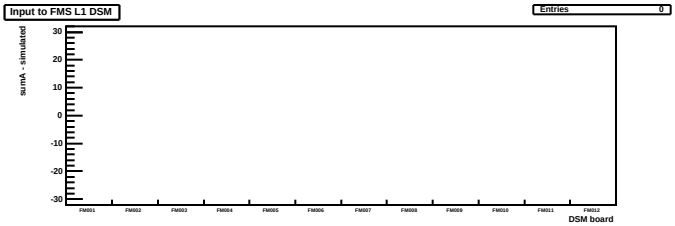

Input to FMS L1 DSM
Entries 2.491148e+07
sumB
DSM board

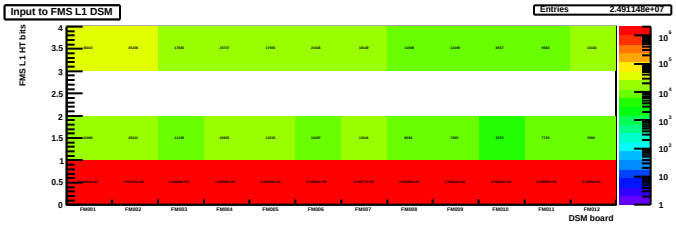

Input to FMS L1 DSM
Entries 3926
sumB - simulated
DSM board

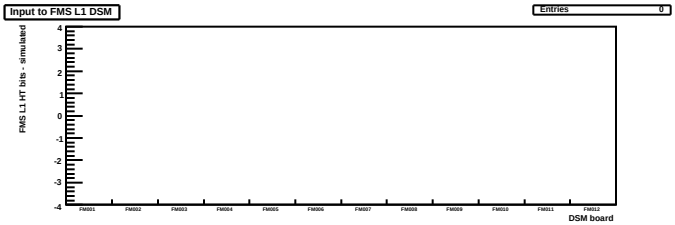

Input to FMS L1 DSM
Entries 2.491148e+07
sumCD
DSM board

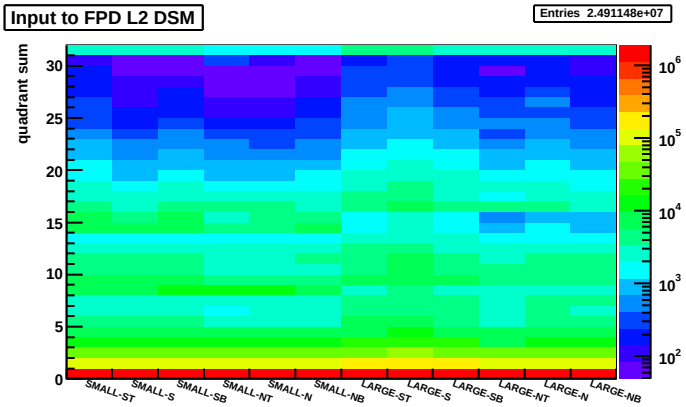

Input to FMS L1 DSM
Entries 32142
sumCD - simulated
DSM board

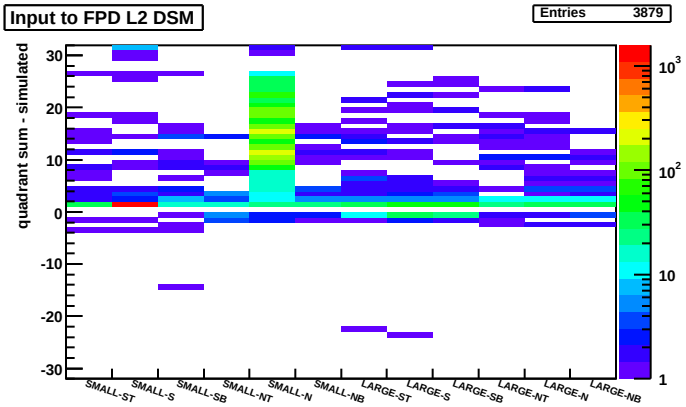

Input to FMS L1 DSM
Entries 2.491148e+07
sumA
DSM board

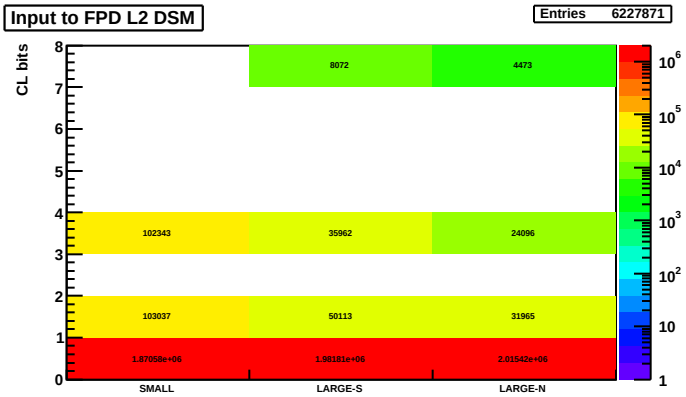

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

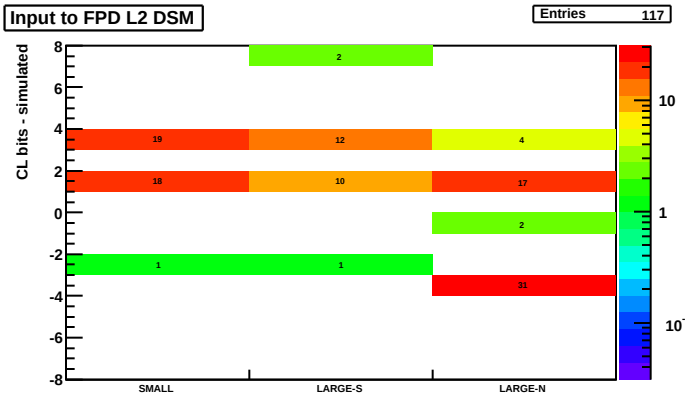

Input to FMS L1 DSM
Entries 2.491148e+07
FMS L1 HT bits
DSM board

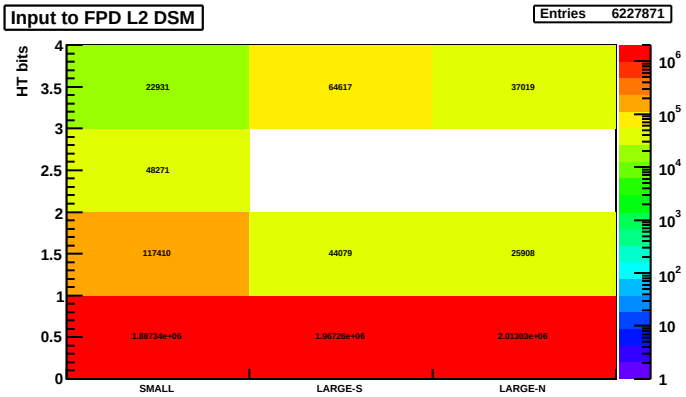

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

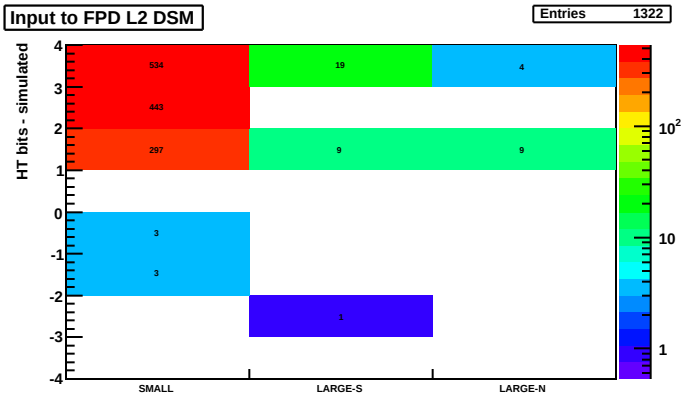

Input to FPD L2 DSM
Entries 2.491148e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

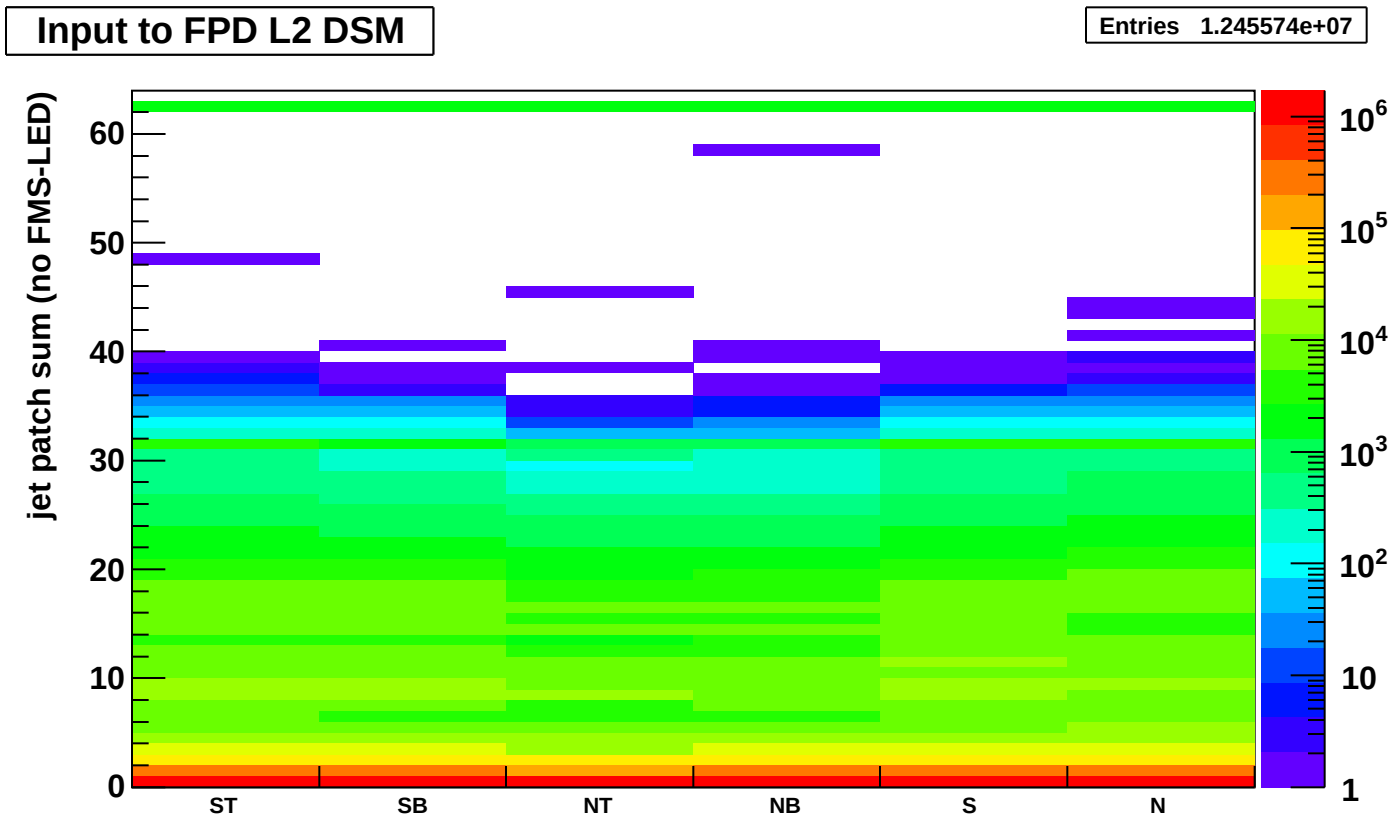

Input to FPD L2 DSM
Entries 3879
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

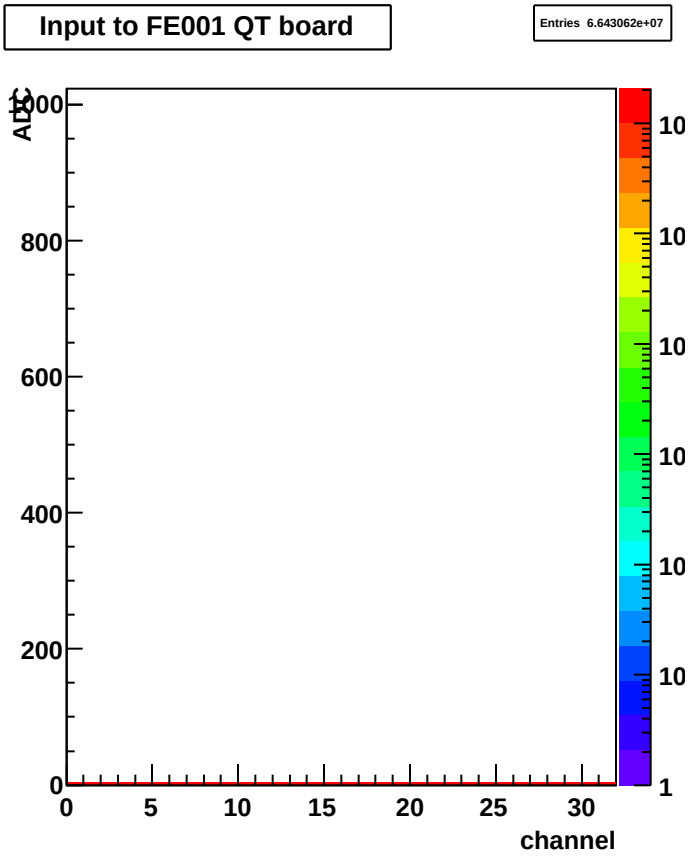

Input to FPD L2 DSM
Entries 6227871
CL bits
8072
4473
102343
35962
24096
103037
50113
31965
1.87058e+06
1.98181e+06
2.01542e+06
SMALL
LARGE-S
LARGE-N

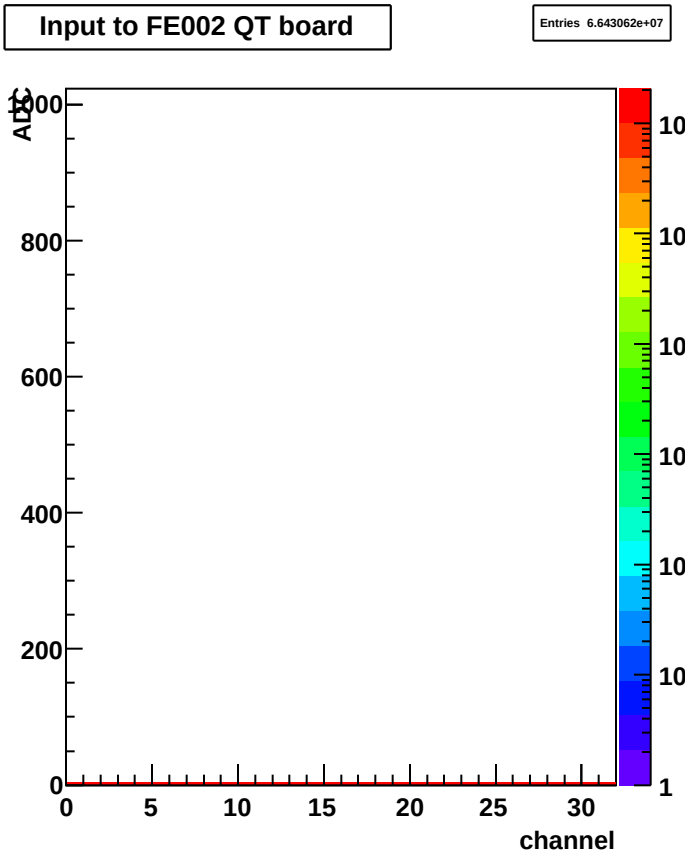

Input to FPD L2 DSM
Entries 117
CL bits - simulated
2
19
12
4
18
10
17
2
1
1
31
SMALL
LARGE-S
LARGE-N

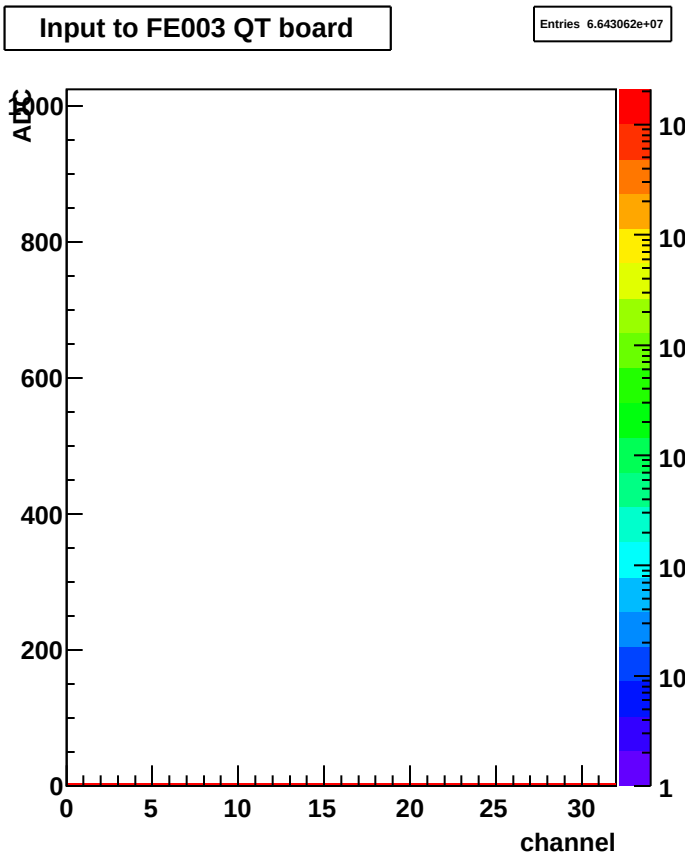

Input to FPD L2 DSM
Entries 6227871
HT bits
22931
64617
37019
48271
117410
44079
25908
1.88734e+06
1.96726e+06
2.01303e+06
SMALL
LARGE-S
LARGE-N

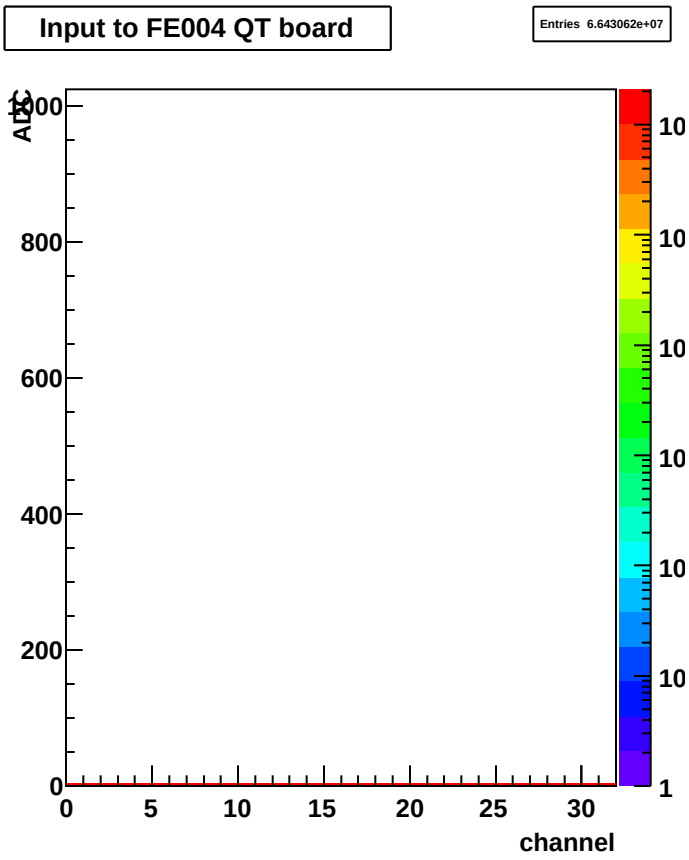

Input to FPD L2 DSM
Entries 1322
HT bits - simulated
534
19
4
443
297
9
9
3
3
1
SMALL
LARGE-S
LARGE-N

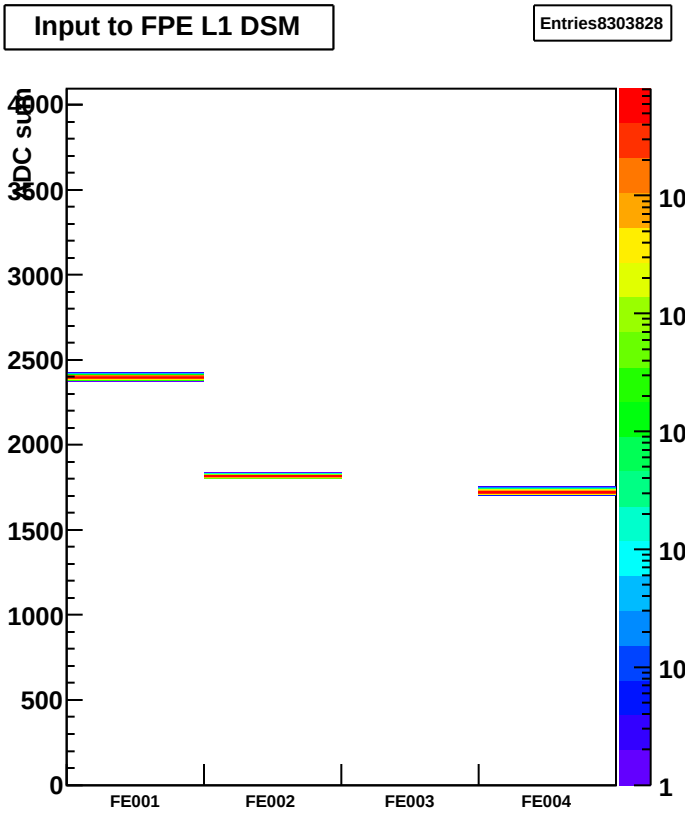

Input to FPD L2 DSM
Entries 1.245574e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

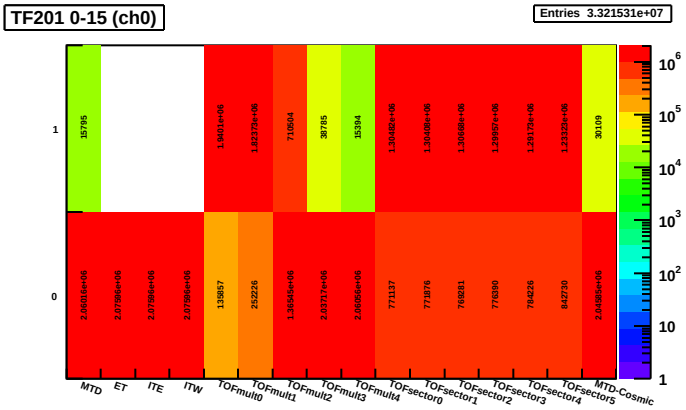

Input to FE001 QT board
Entries 6.643062e+07
ADC
channel

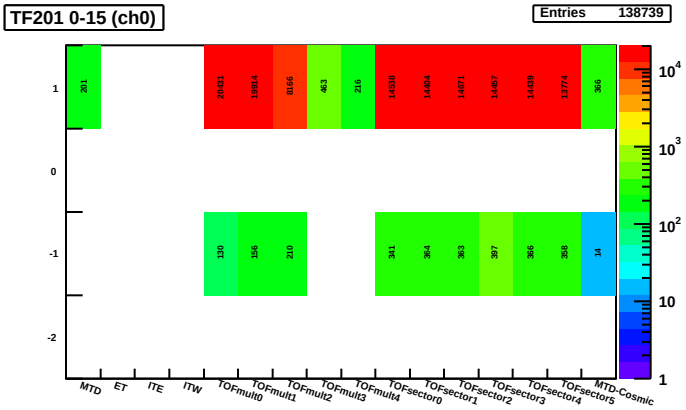

Input to FE002 QT board
Entries 6.643062e+07
ADC
channel

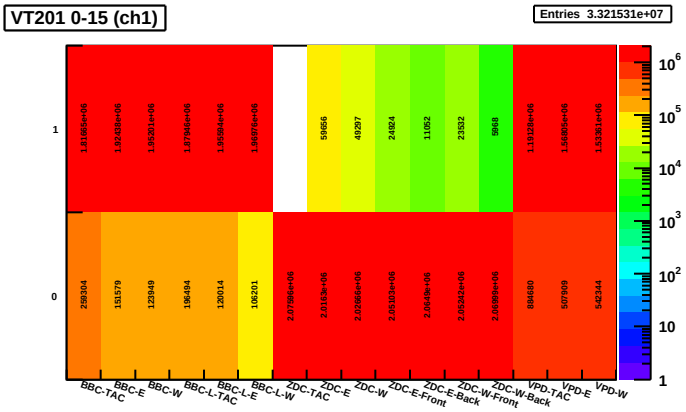

Input to FE003 QT board
Entries 6.643062e+07
ADC
channel

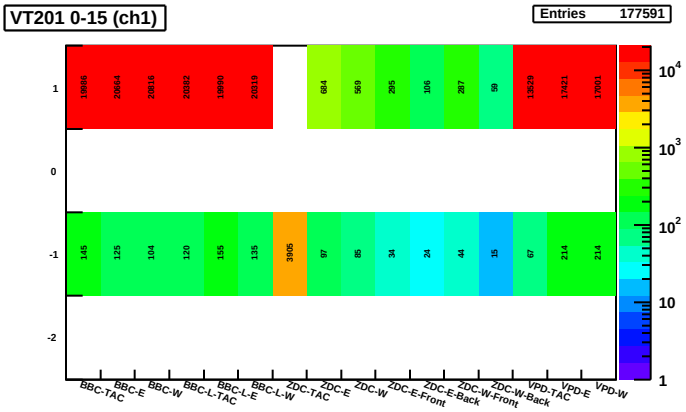

Input to FE004 QT board
Entries 6.643062e+07
ADC
channel


Input to FPE L1 DSM
Entries8303828
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 3.321531e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 138739
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3.321531e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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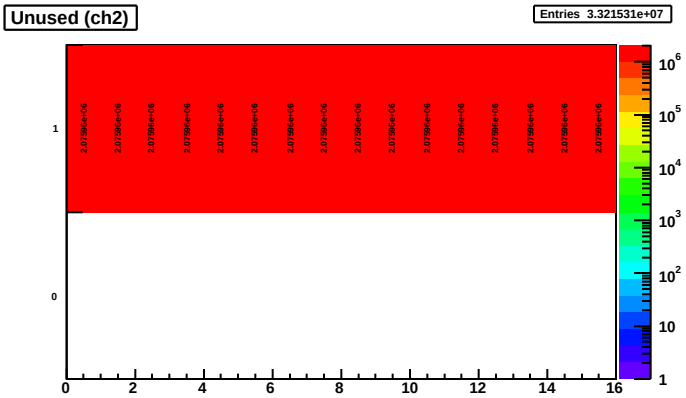
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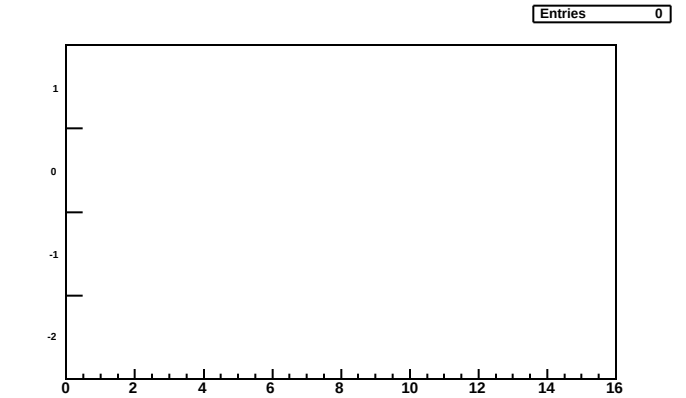
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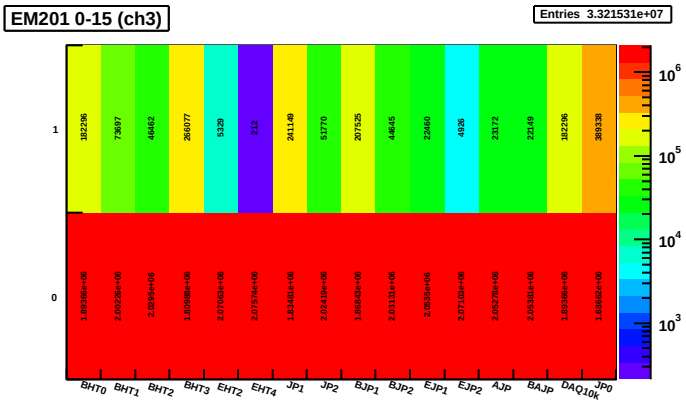
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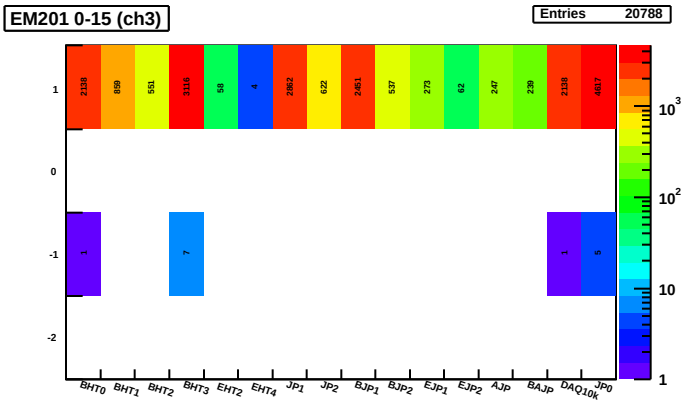
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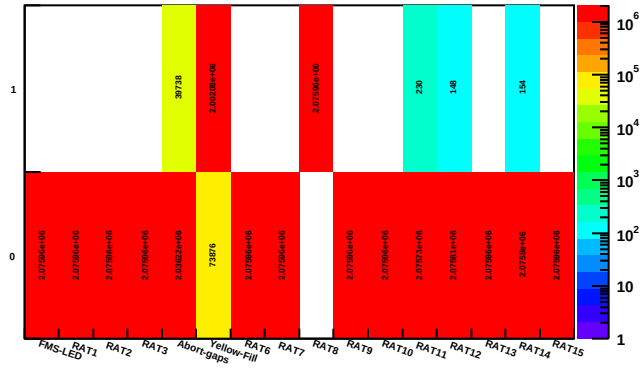
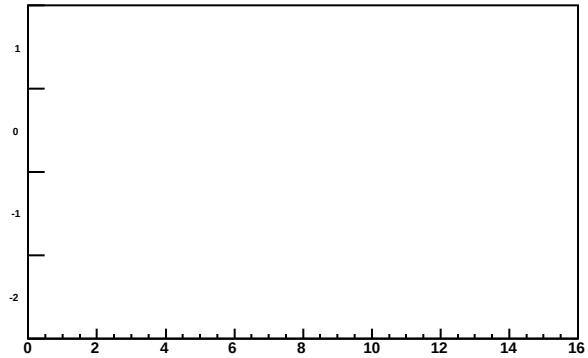
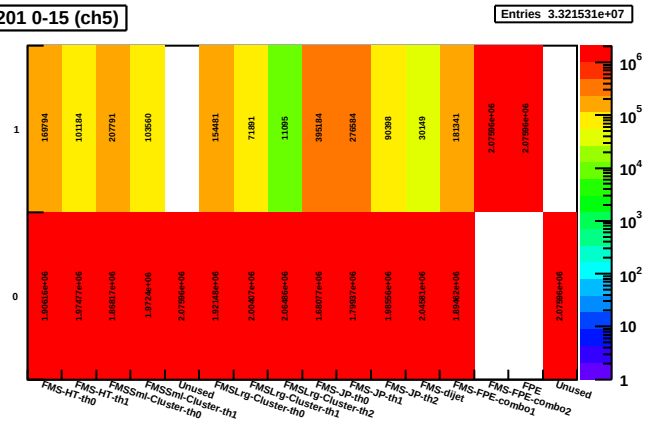
RAT board (ch4)





FP201 0-15 (ch5)



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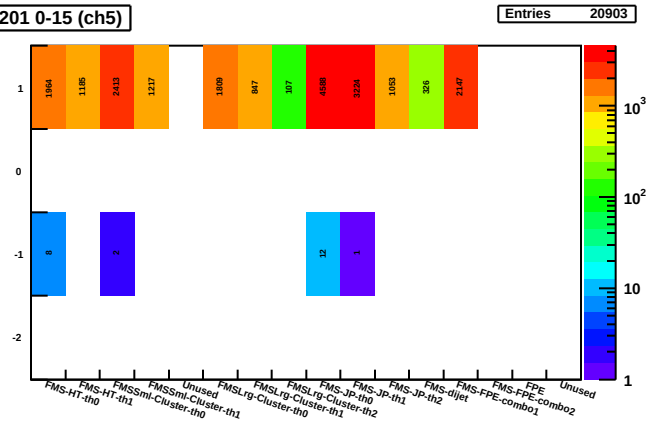
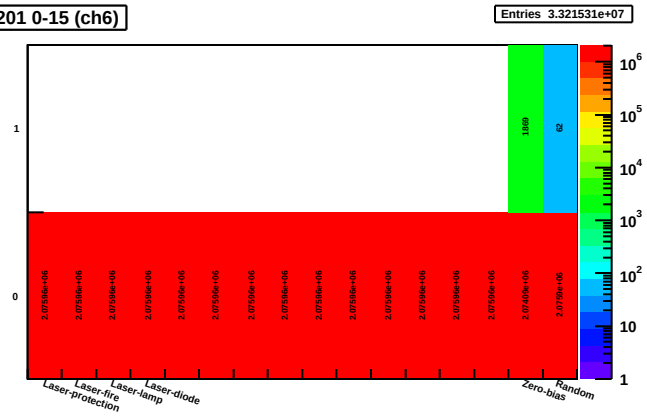
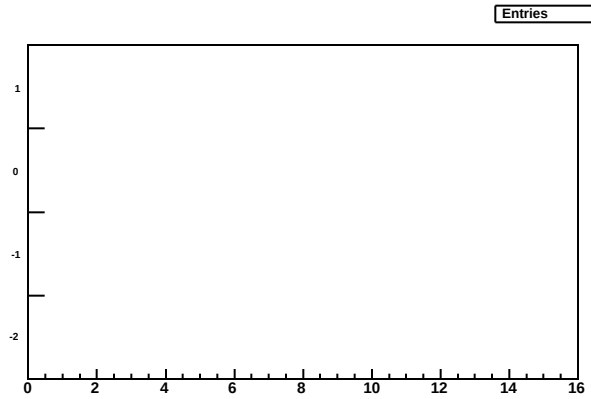
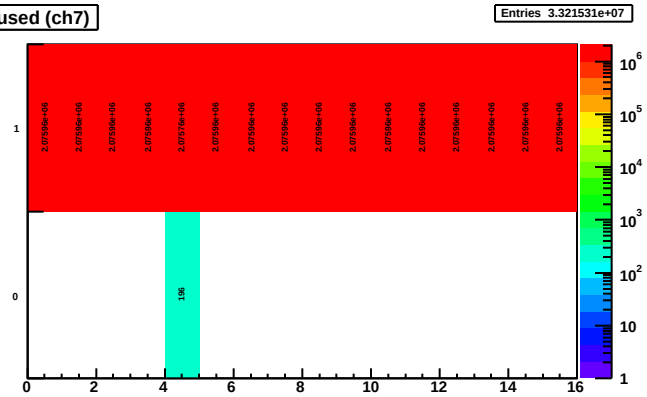
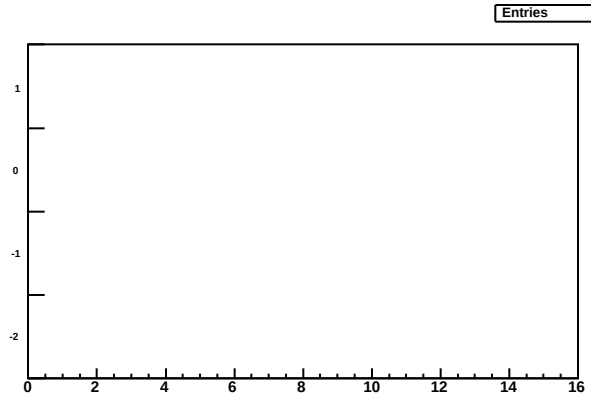


ST201 0-15 (ch6)





Unused (ch7)