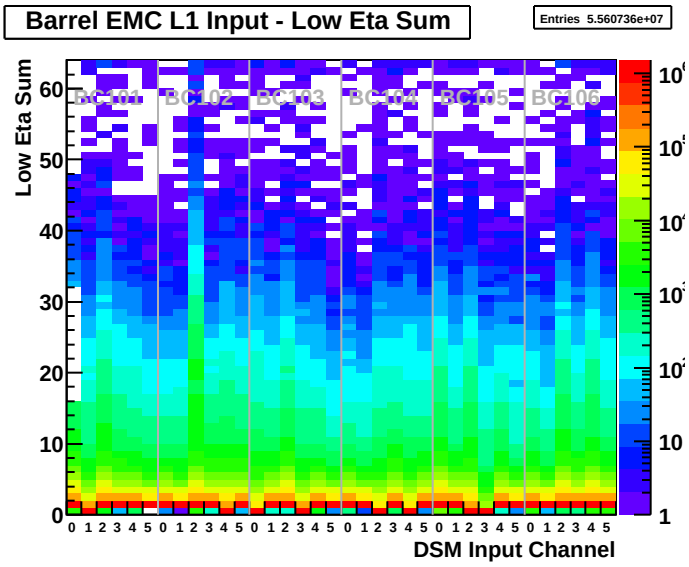
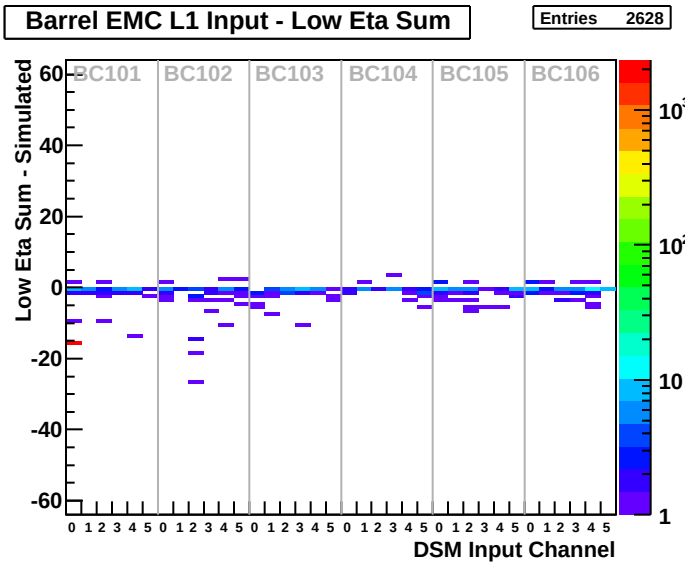
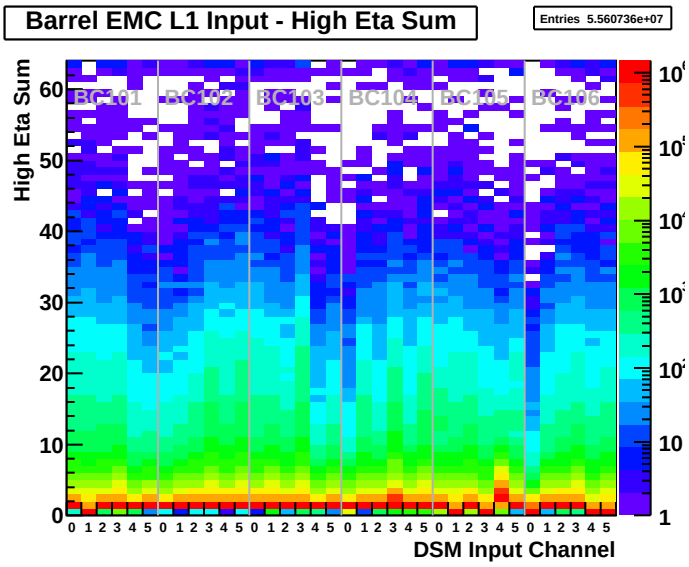
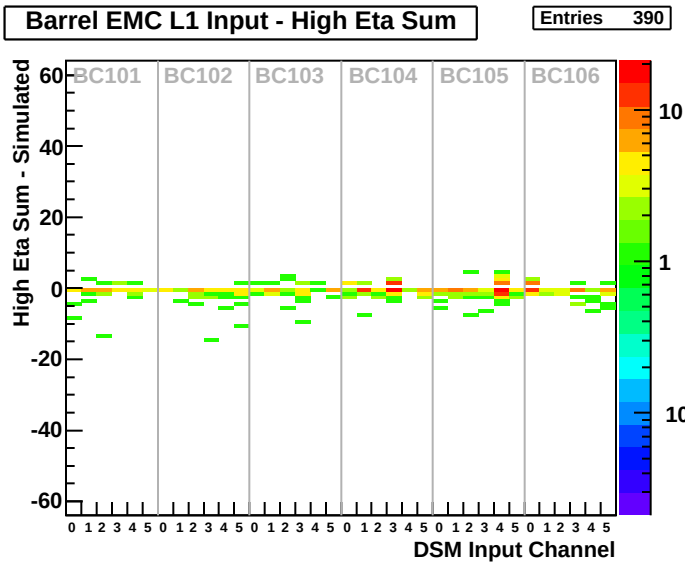
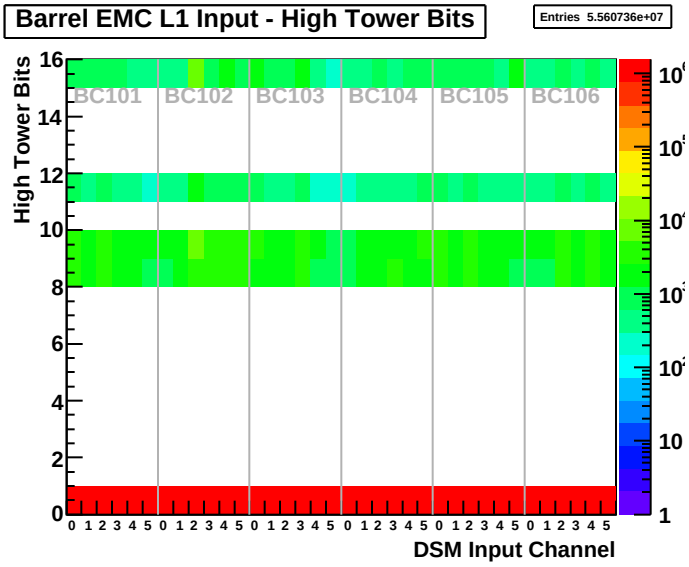
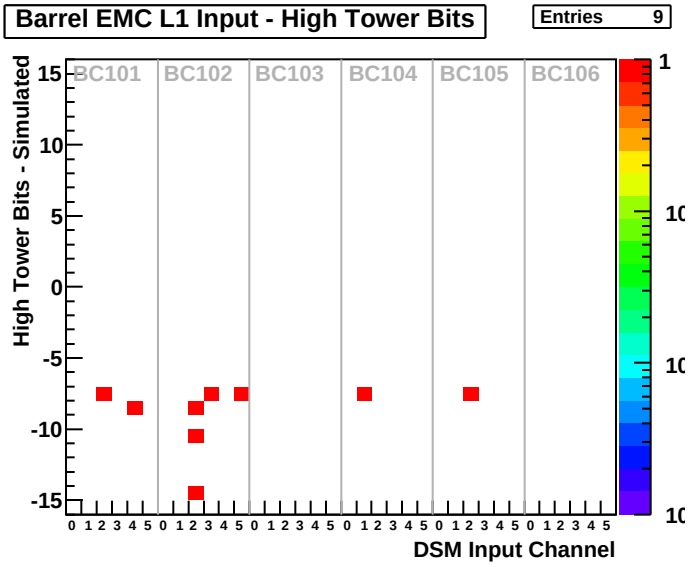

Barrel EMC L1 Input - Low Eta Sum
Entries 5.560736e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

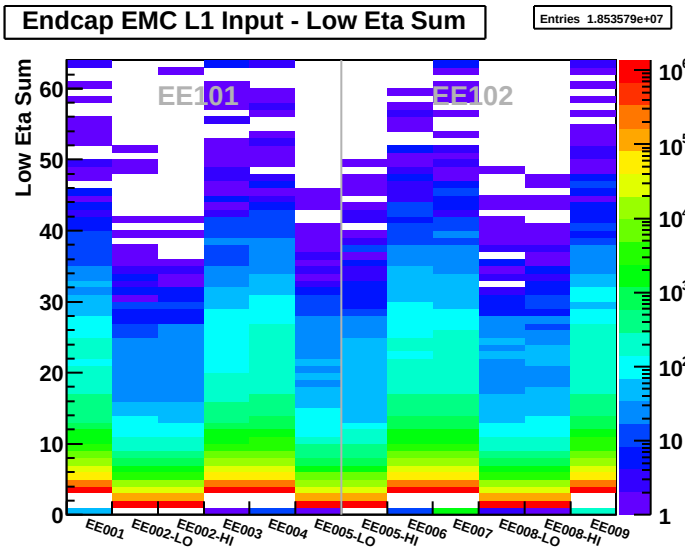

Barrel EMC L1 Input - Low Eta Sum
Entries 2628
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

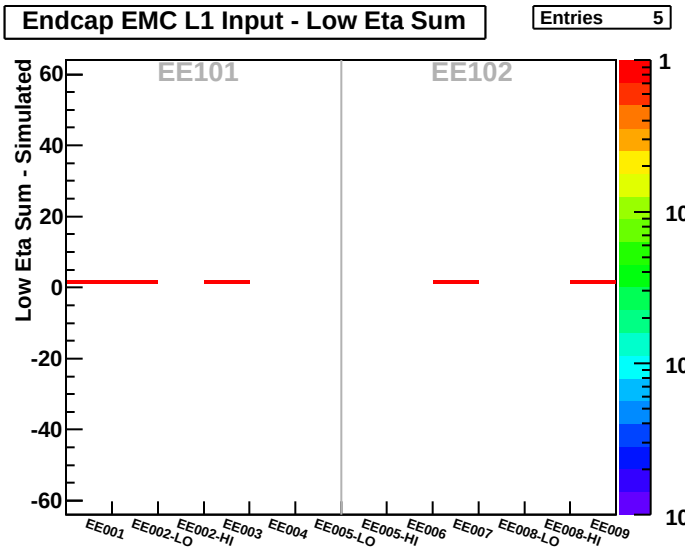

Barrel EMC L1 Input - High Eta Sum
Entries 5.560736e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

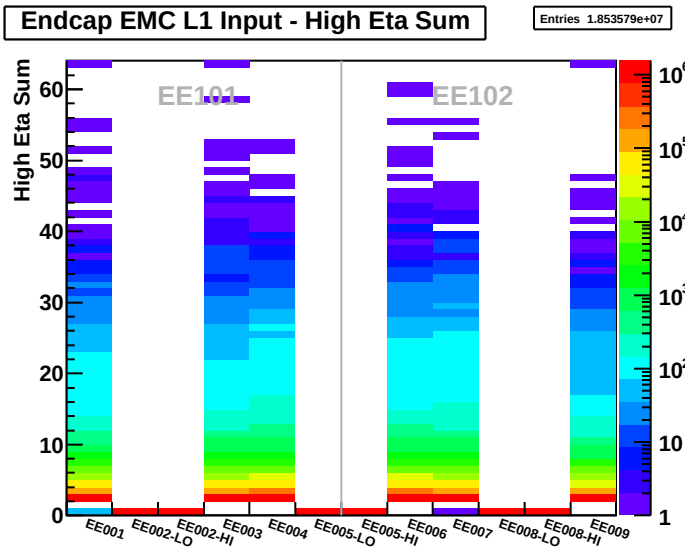

Barrel EMC L1 Input - High Eta Sum
Entries 390
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

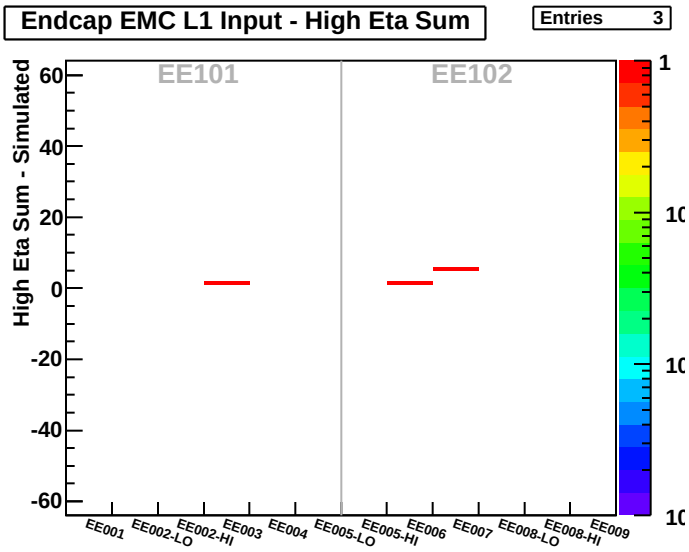

Barrel EMC L1 Input - High Tower Bits
Entries 5.560736e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

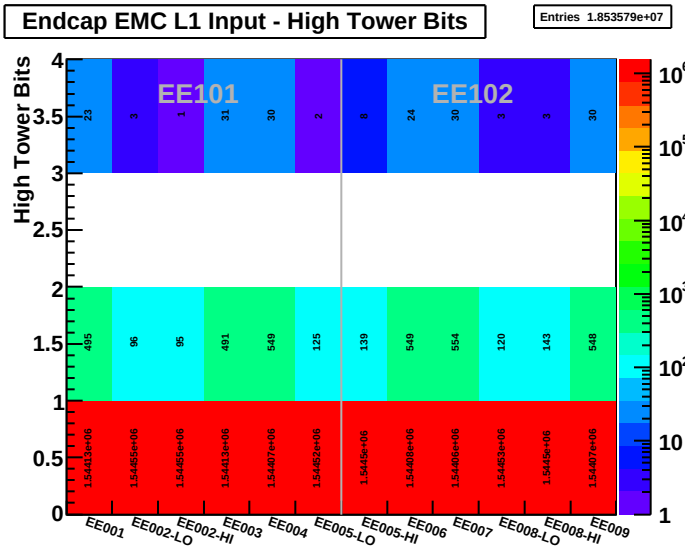

Barrel EMC L1 Input - High Tower Bits
Entries 9
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

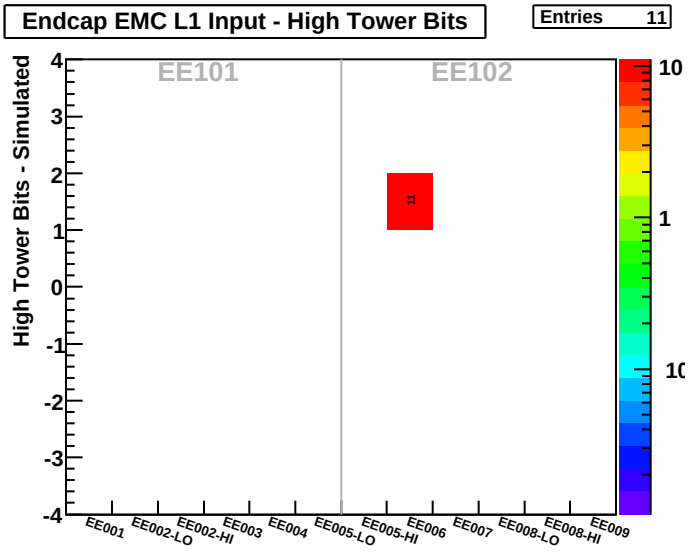

Endcap EMC L1 Input - Low Eta Sum
Entries 1.853579e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

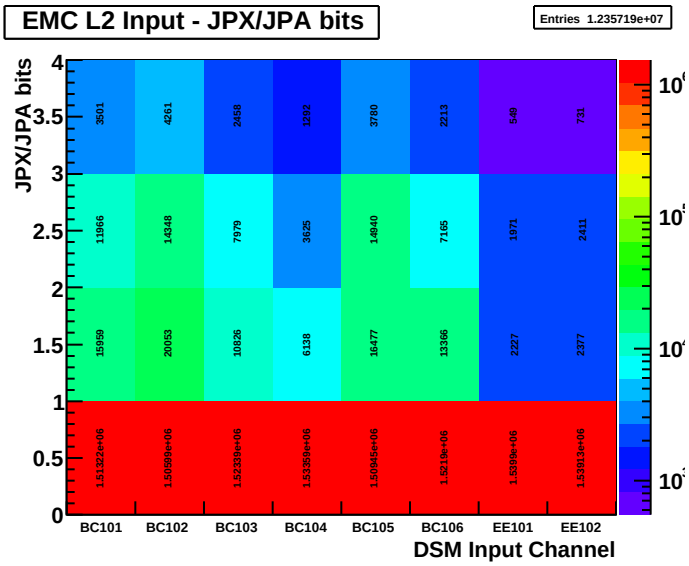

Endcap EMC L1 Input - Low Eta Sum
Entries 5
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

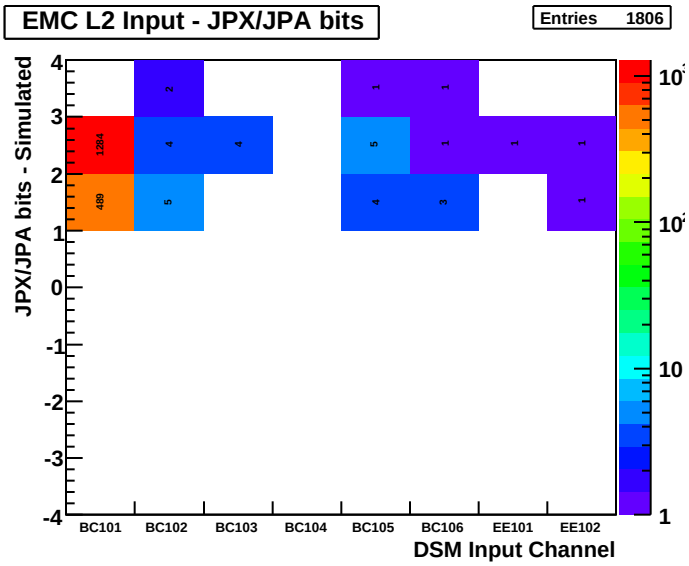

Endcap EMC L1 Input - High Eta Sum
Entries 1.853579e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

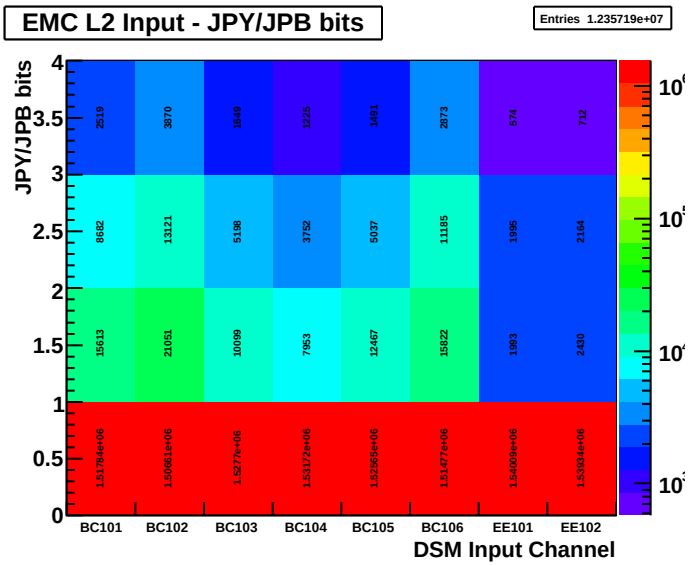

Endcap EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

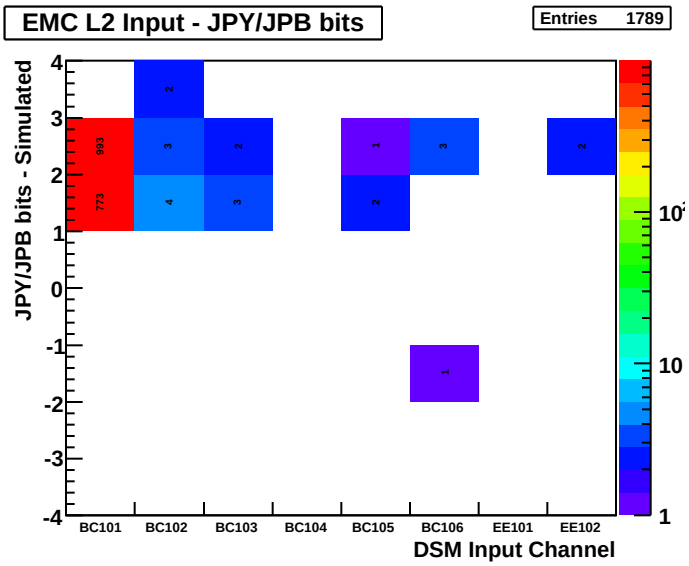

Endcap EMC L1 Input - High Tower Bits
Entries 1.853579e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

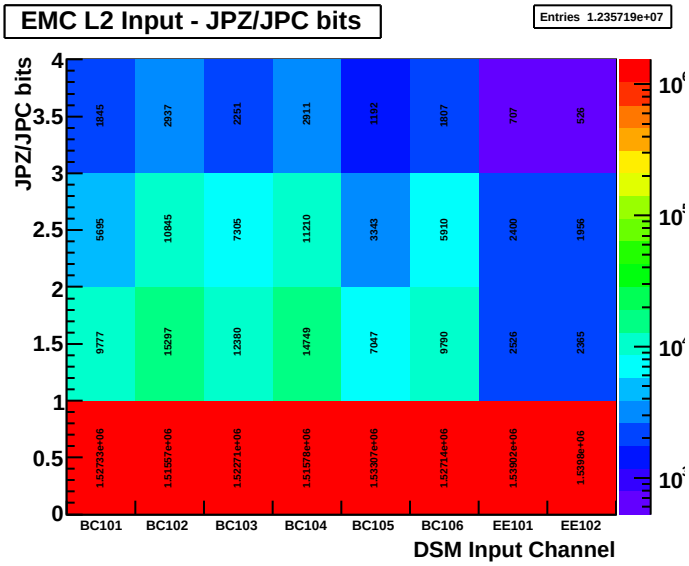

Endcap EMC L1 Input - High Tower Bits
Entries 11
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

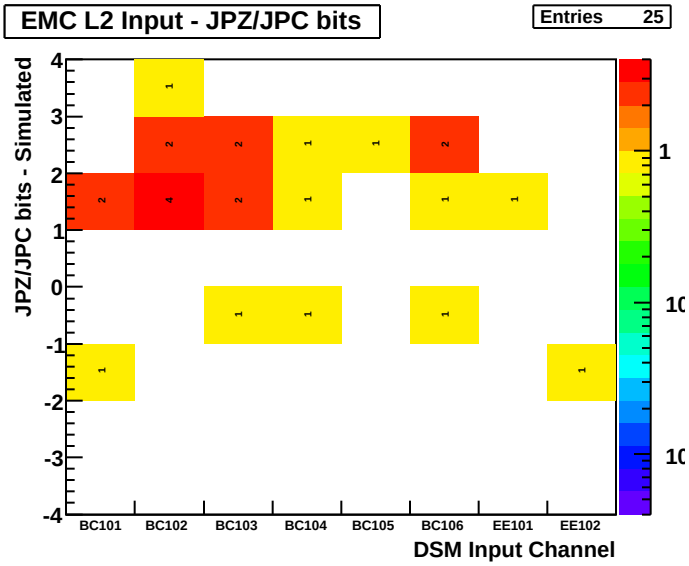

EMC L2 Input - JPX/JPA bits
Entries 1.235719e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

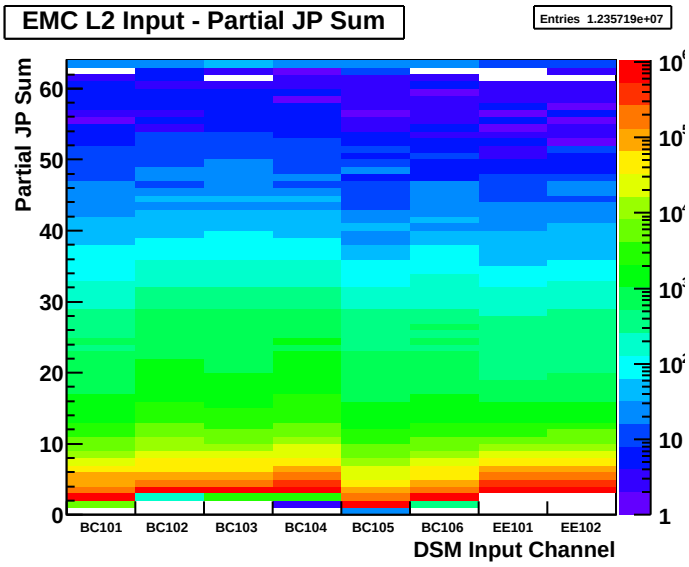

EMC L2 Input - JPX/JPA bits
Entries 1806
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

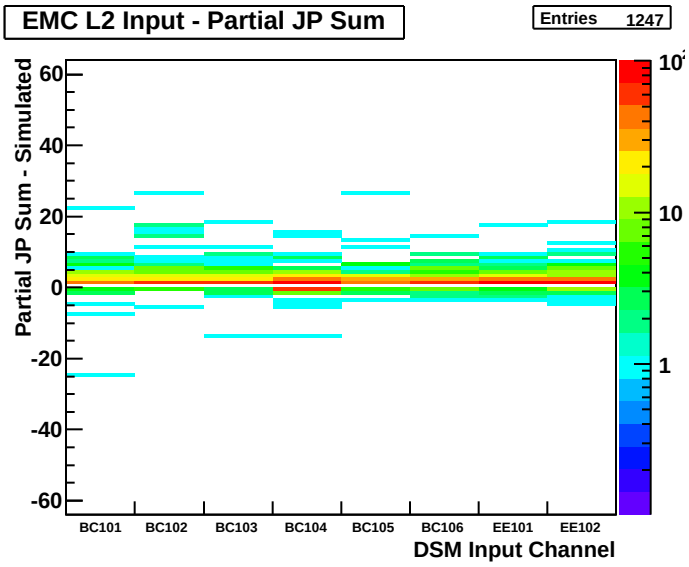

EMC L2 Input - JPY/JPB bits
Entries 1.235719e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

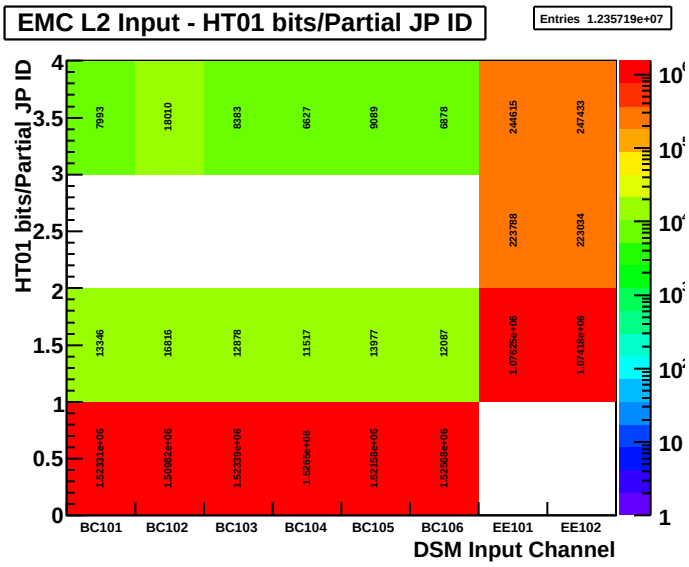

EMC L2 Input - JPY/JPB bits
Entries 1789
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

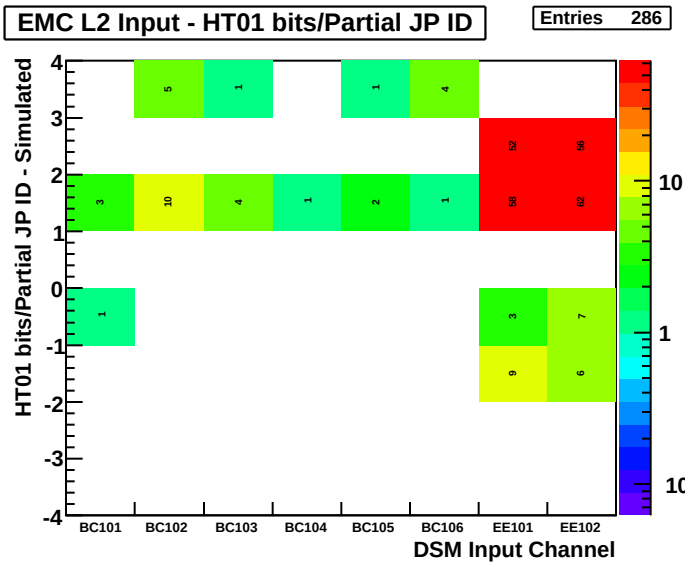

EMC L2 Input - JPZ/JPC bits
Entries 1.235719e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

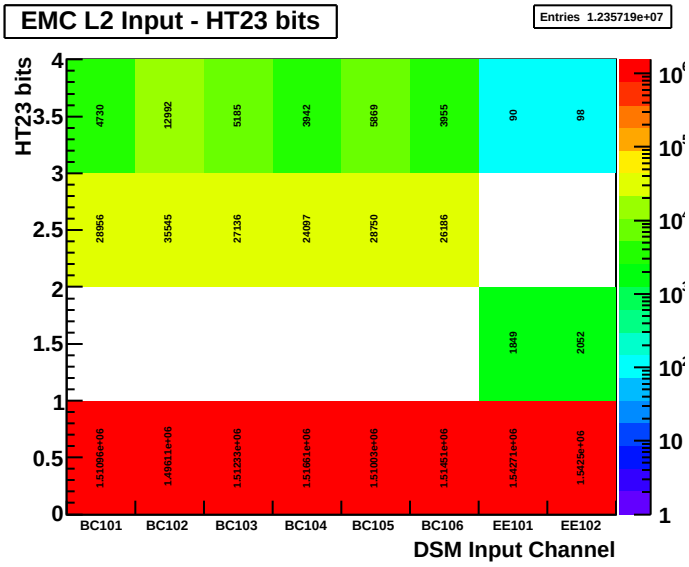

EMC L2 Input - JPZ/JPC bits
Entries 25
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

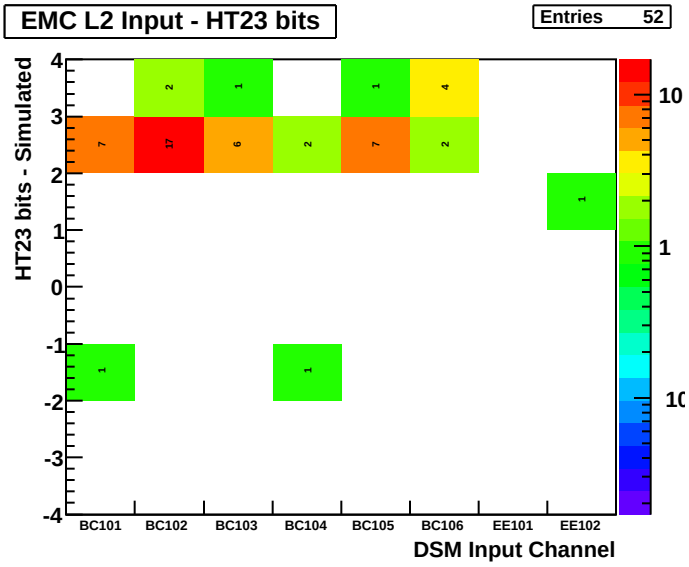

EMC L2 Input - Partial JP Sum
Entries 1.235719e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

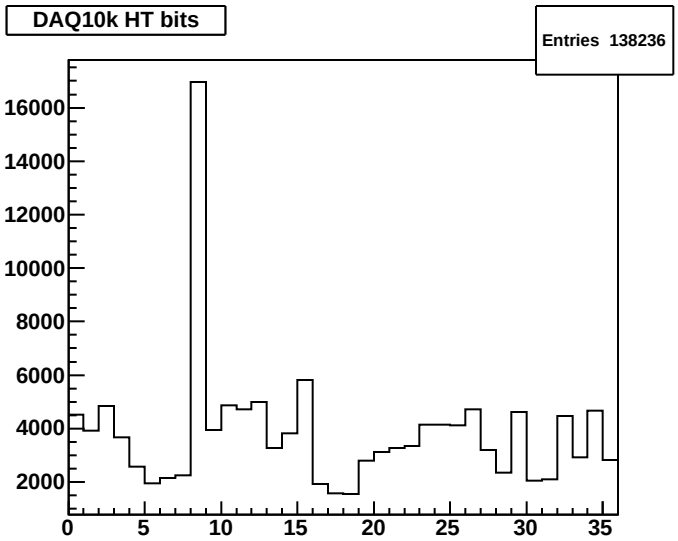

EMC L2 Input - Partial JP Sum
Entries 1247
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

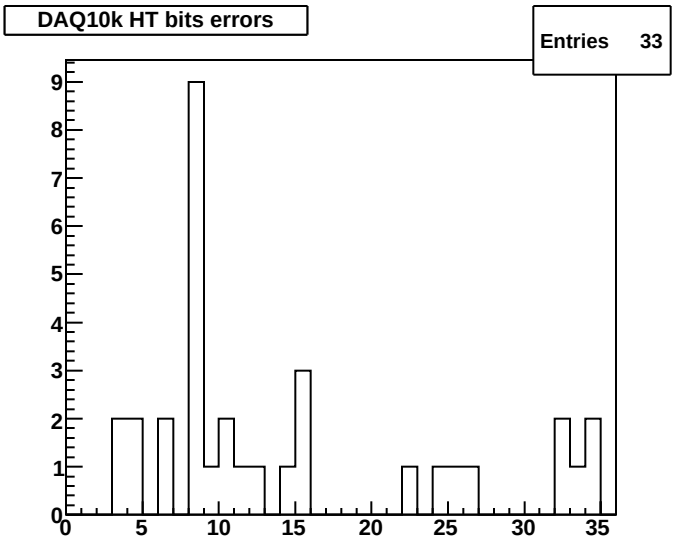

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.235719e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

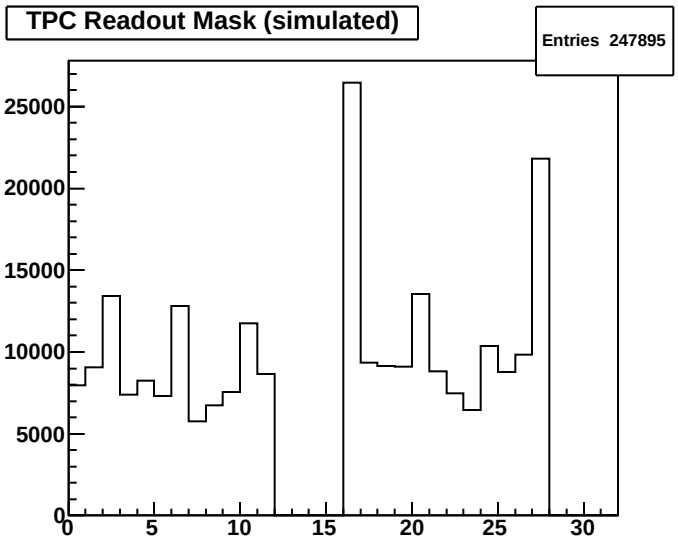

EMC L2 Input - HT01 bits/Partial JP ID
Entries 286
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

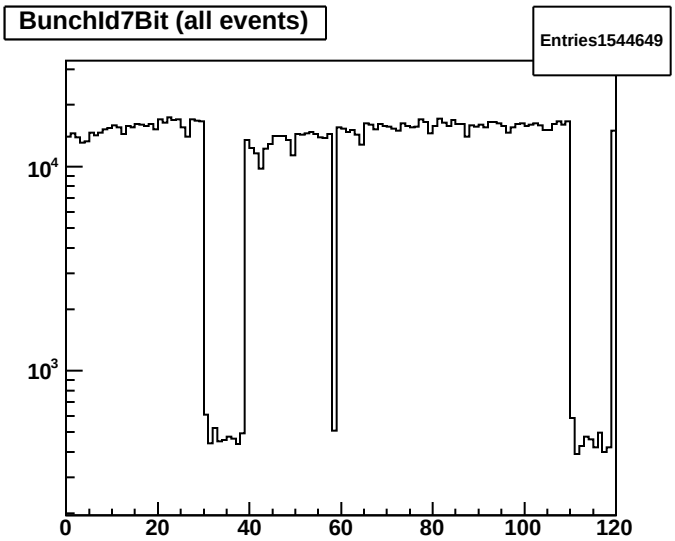

EMC L2 Input - HT23 bits
Entries 1.235719e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

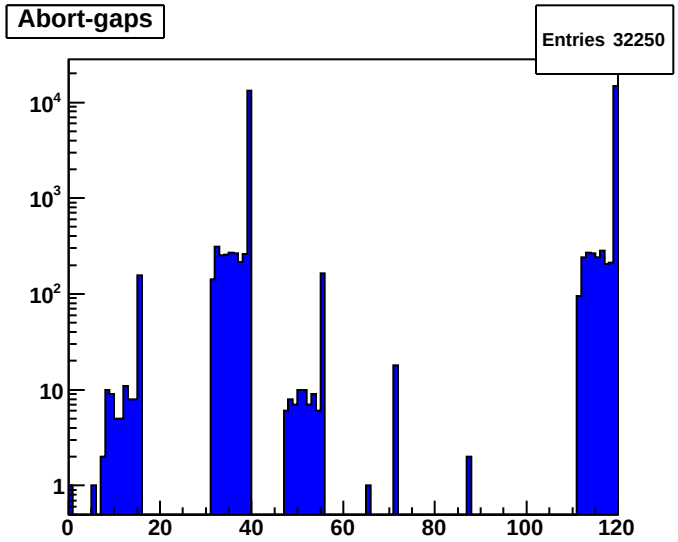

EMC L2 Input - HT23 bits
Entries 52
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

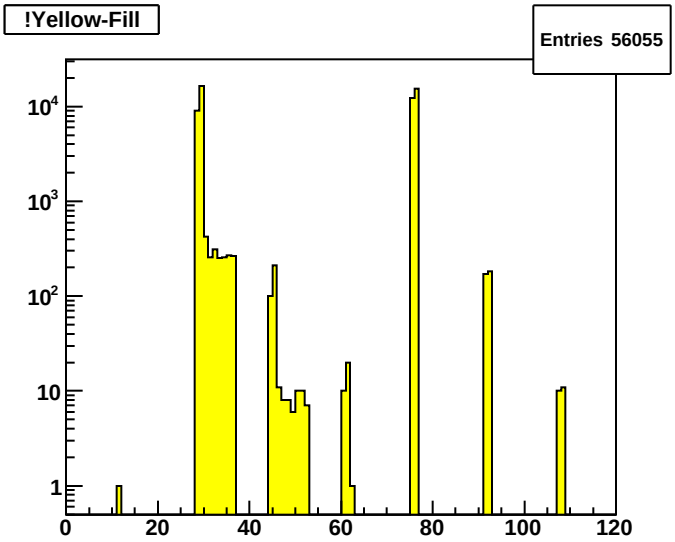

DAQ10k HT bits
Entries 138236

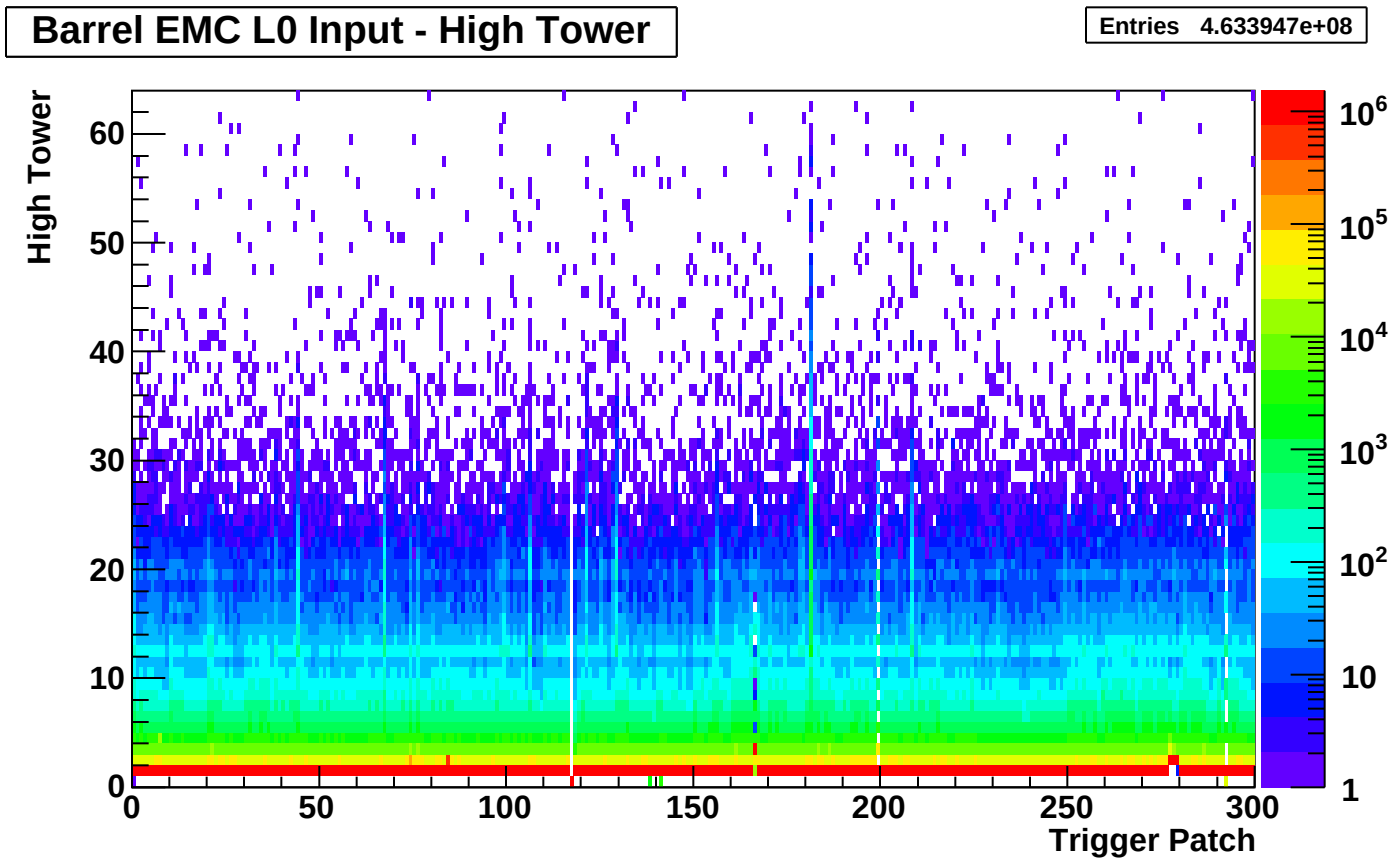

DAQ10k HT bits errors
Entries 33

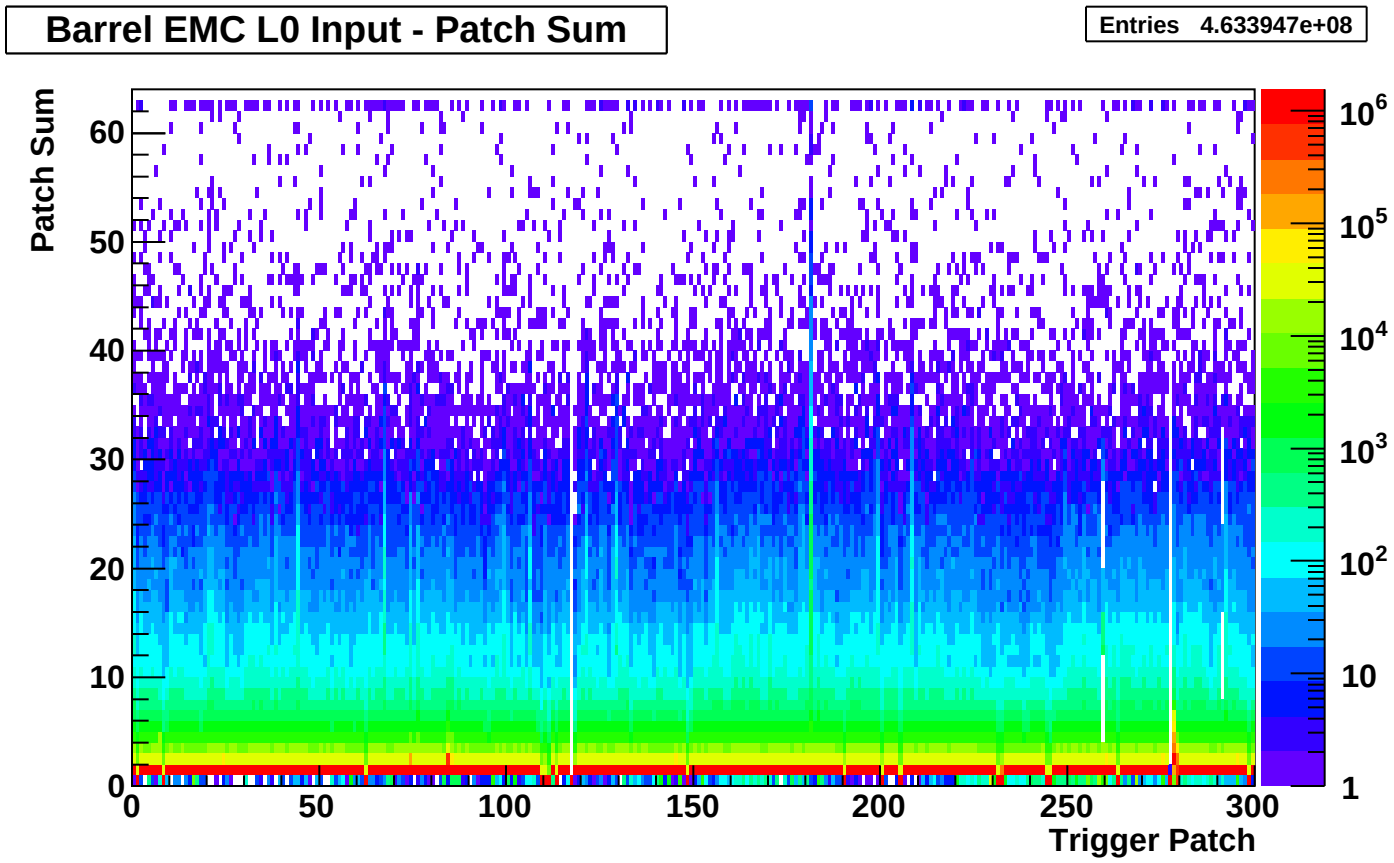

TPC Readout Mask (simulated)
Entries 247895

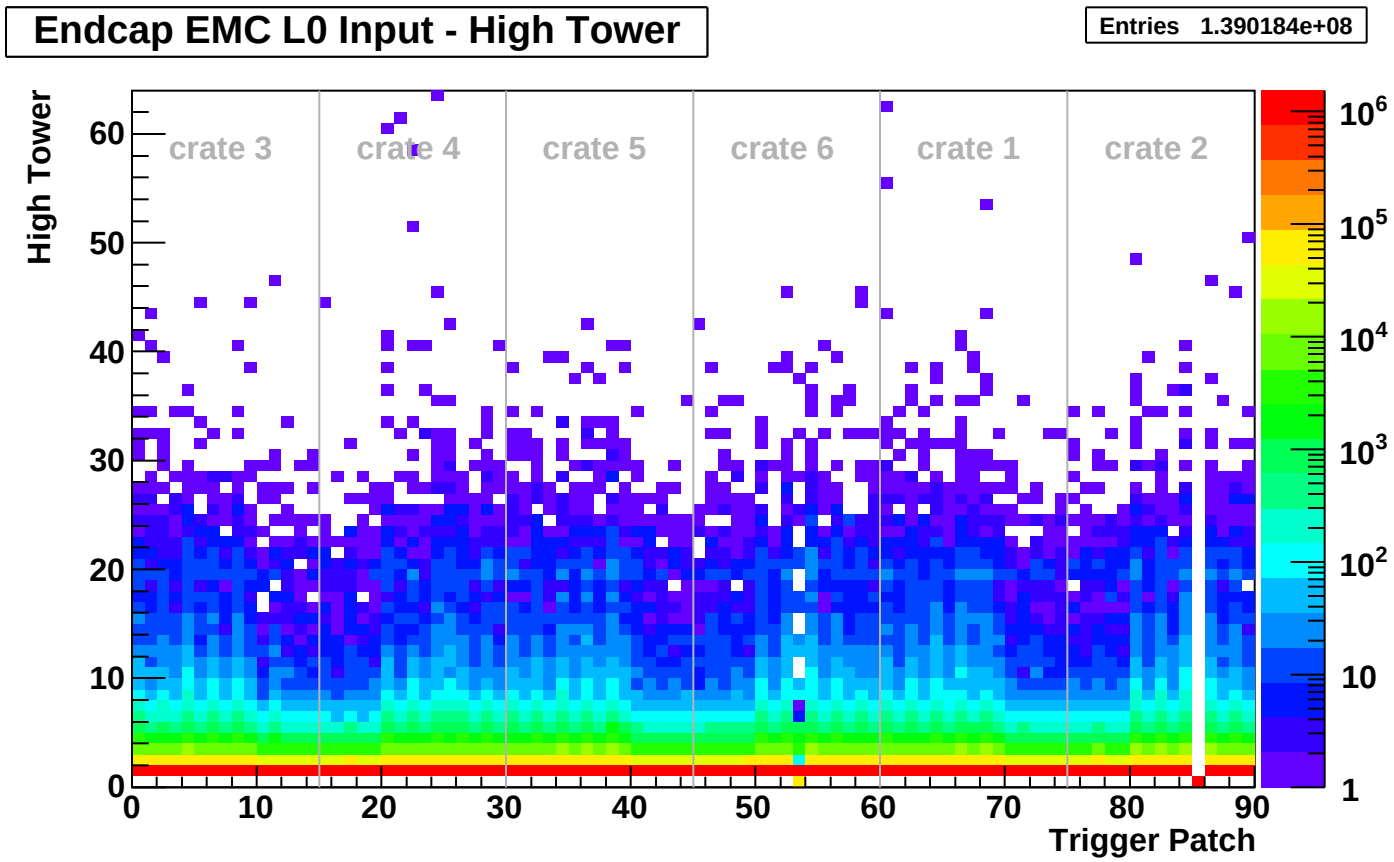

BunchId7Bit (all events)
Entries1544649

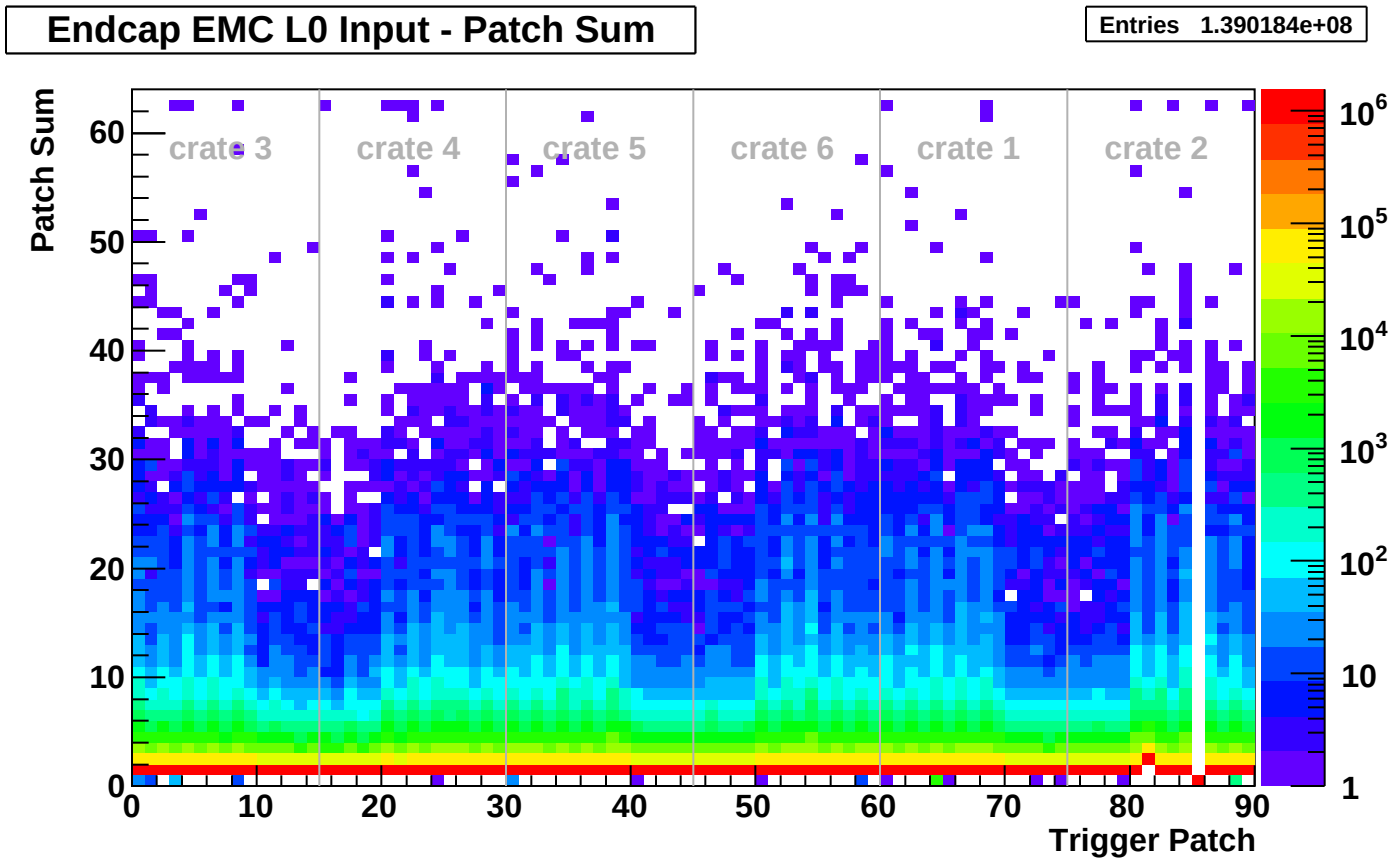

Abort-gaps
Entries 32250

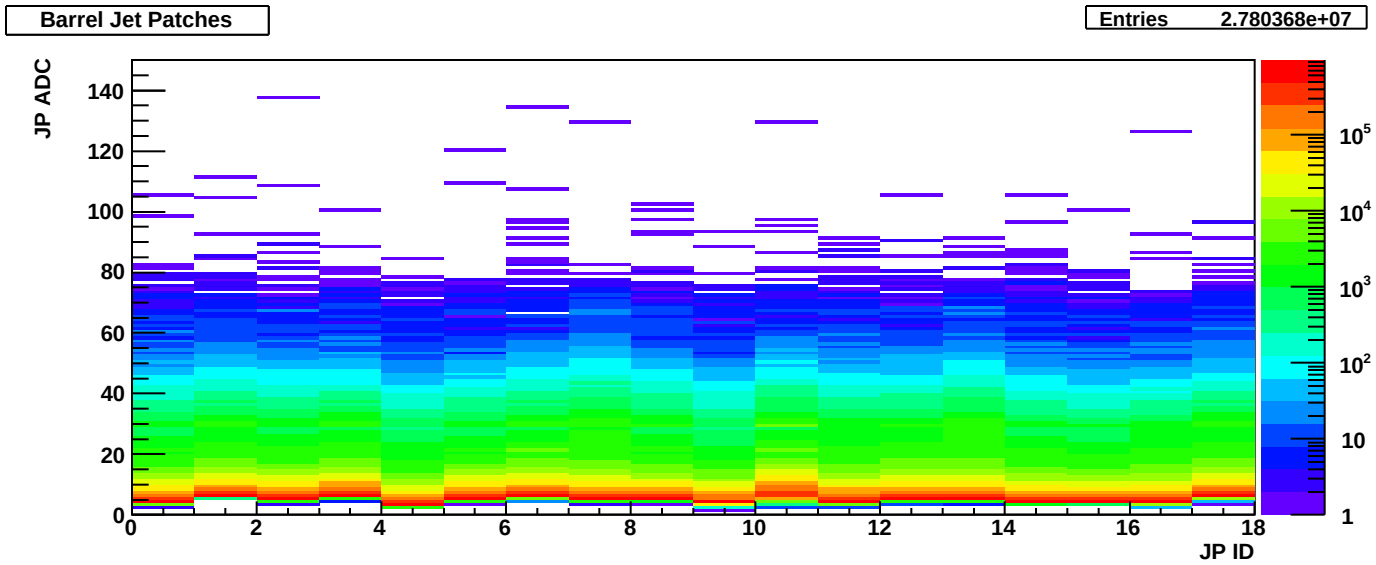

!Yellow-Fill
Entries 56055

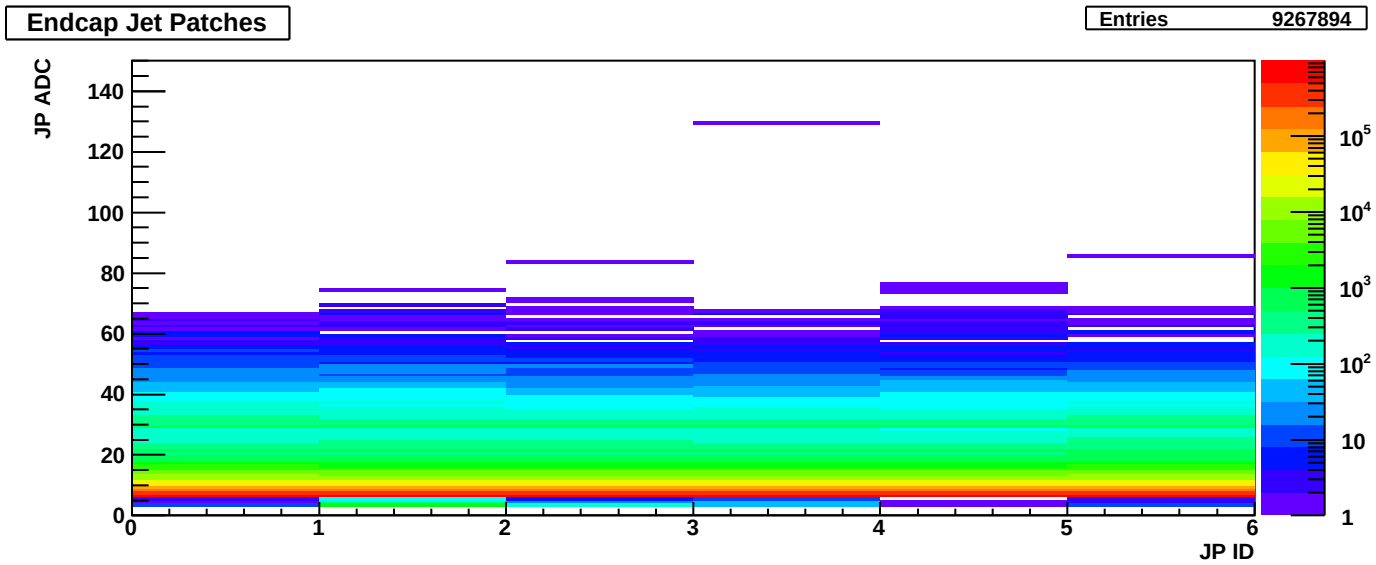

Barrel EMC L0 Input - High Tower
Entries 4.633947e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

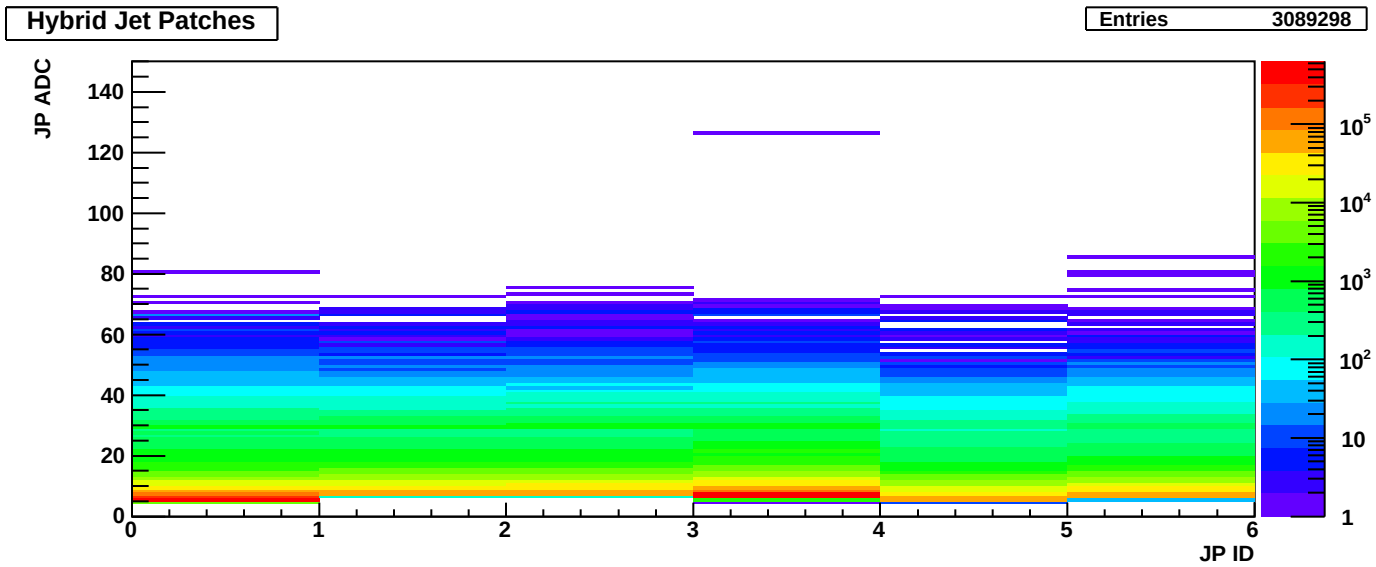

Barrel EMC L0 Input - Patch Sum
Entries 4.633947e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

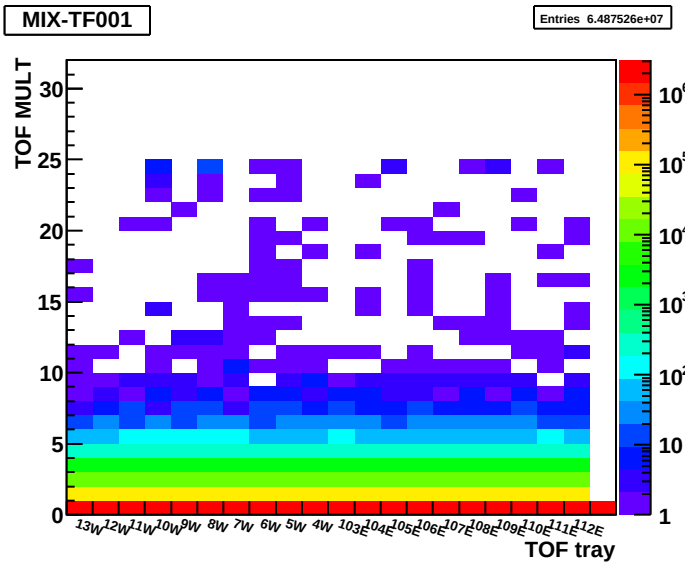

Endcap EMC L0 Input - High Tower
Entries 1.390184e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

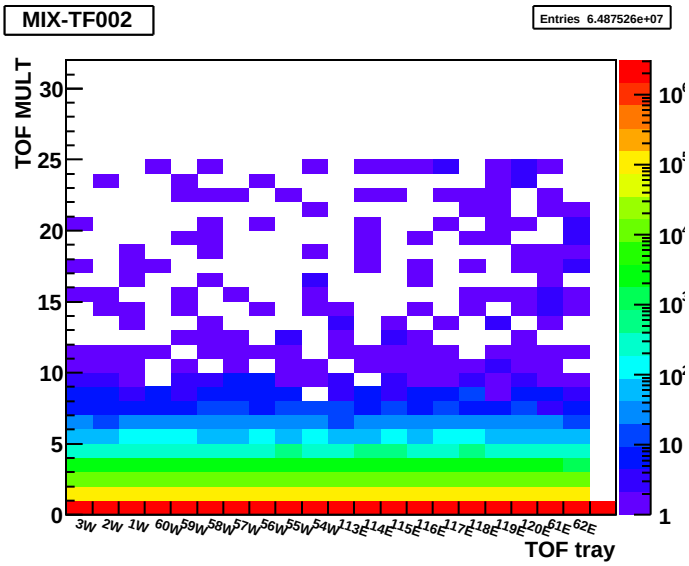

Endcap EMC L0 Input - Patch Sum
Entries 1.390184e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

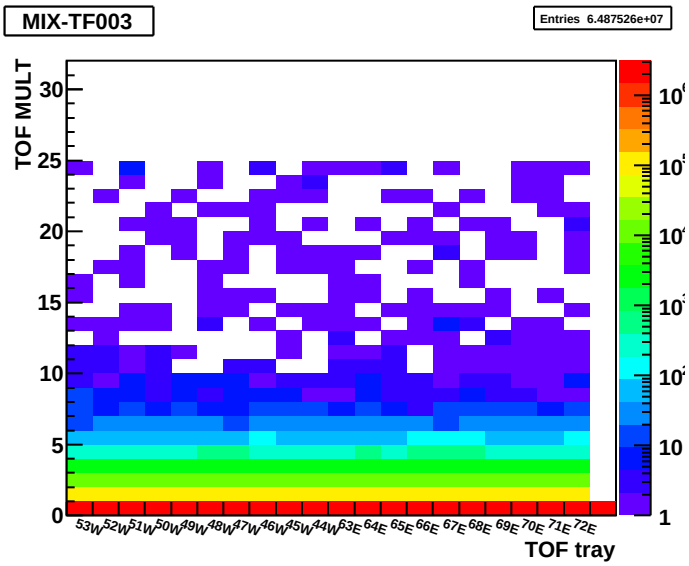

Barrel Jet Patches
Entries 2.780368e+07
JP ADC
JP ID

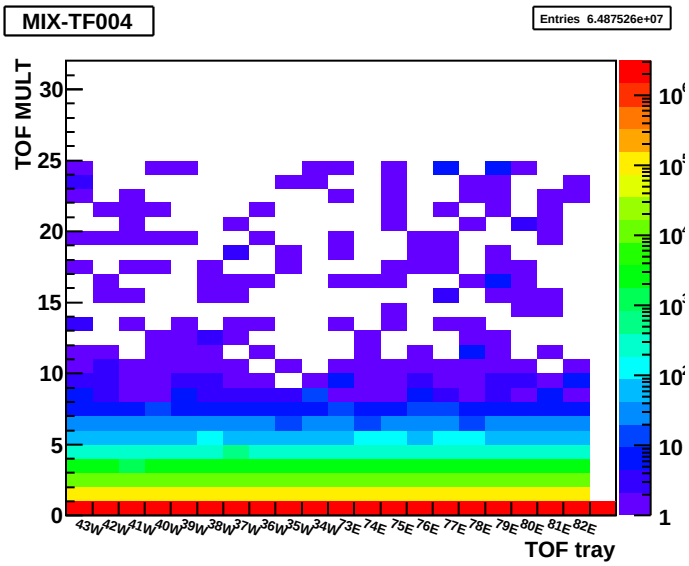

Endcap Jet Patches
Entries 9267894
JP ADC
JP ID

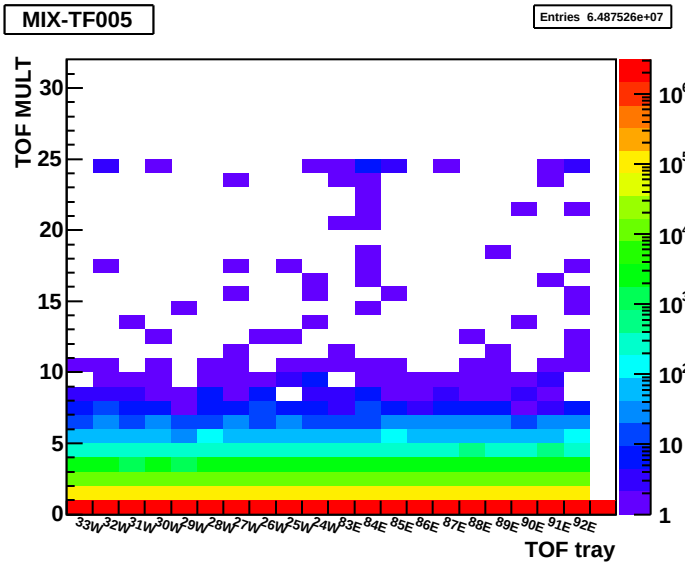

Hybrid Jet Patches
Entries 3089298
JP ADC
JP ID

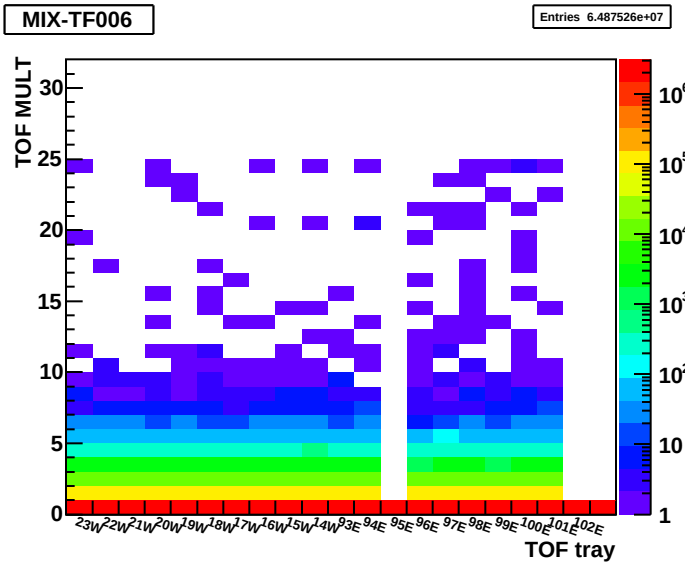

MIX-TF001
Entries 6.487526e+07
TOF MULT
TOF tray

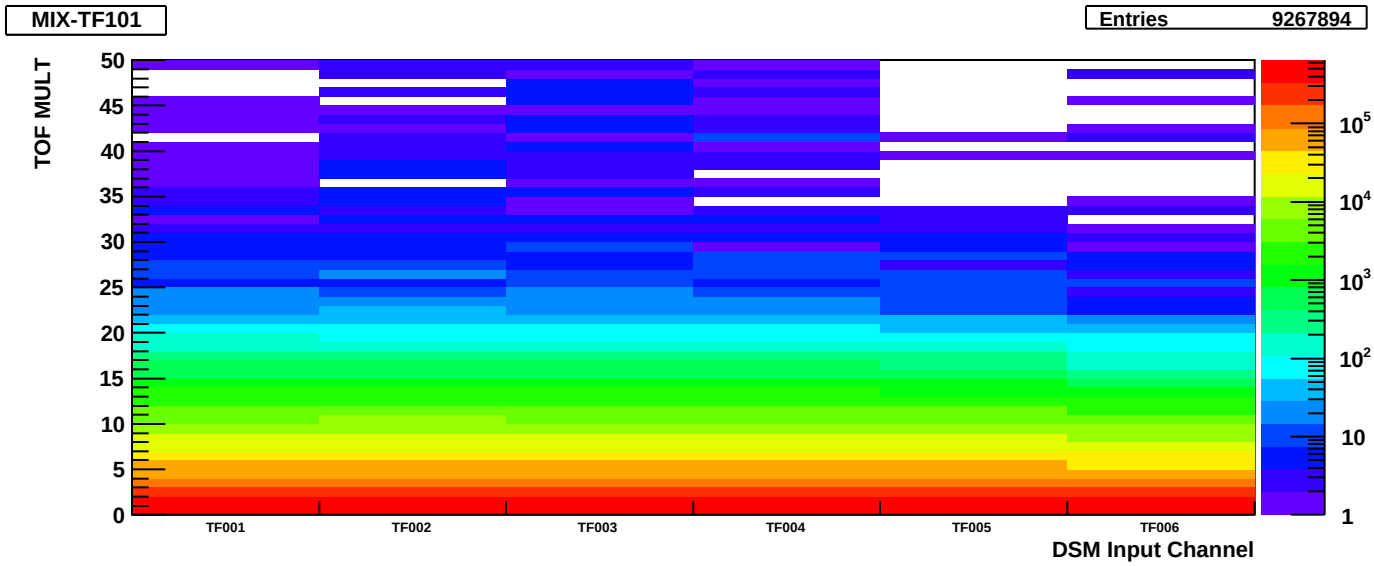

MIX-TF002
Entries 6.487526e+07
TOF MULT
TOF tray

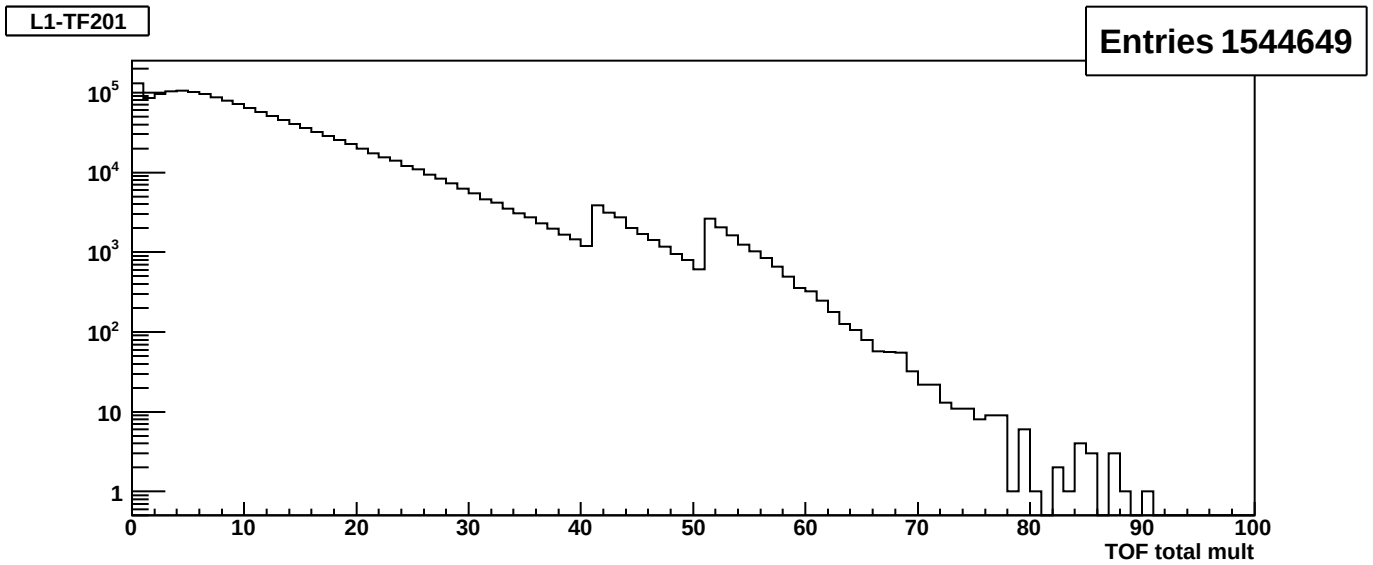

MIX-TF003
Entries 6.487526e+07
TOF MULT
TOF tray

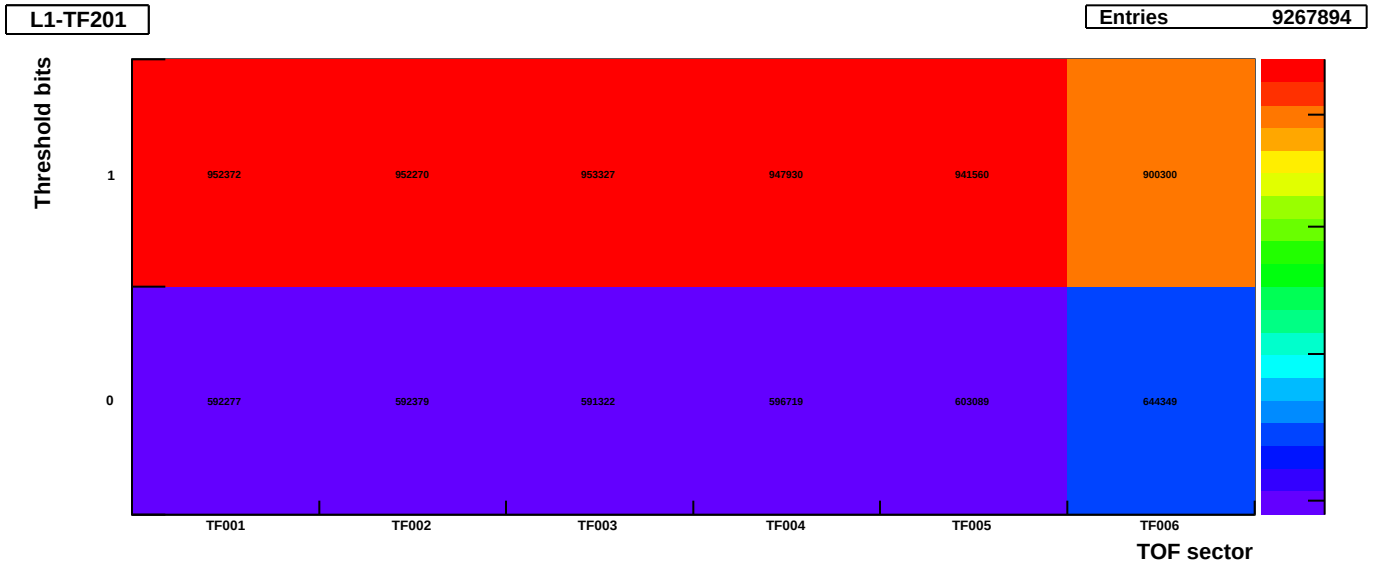

MIX-TF004
Entries 6.487526e+07
TOF MULT
TOF tray

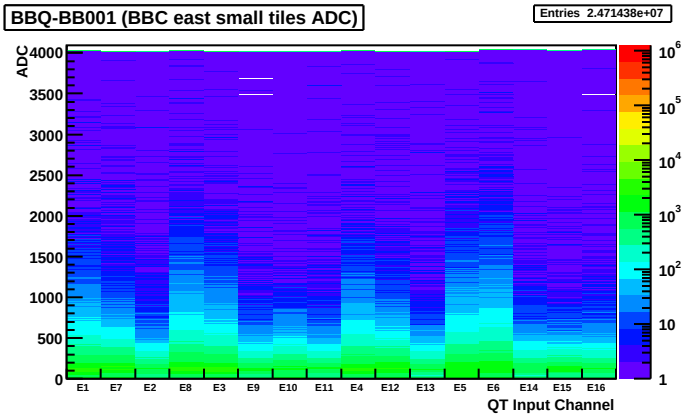

MIX-TF005
Entries 6.487526e+07
TOF MULT
TOF tray

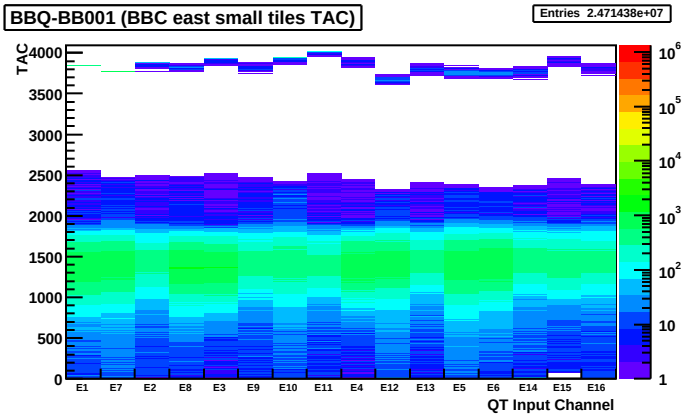

MIX-TF006
Entries 6.487526e+07
TOF MULT
TOF tray

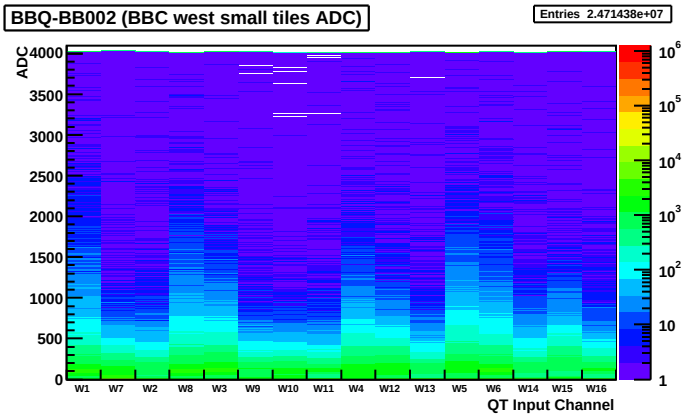

MIX-TF101
Entries 9267894
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

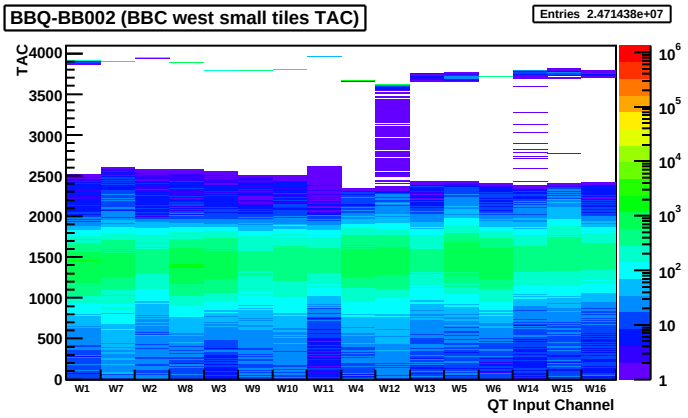

L1-TF201
Entries 1544649
TOF total mult

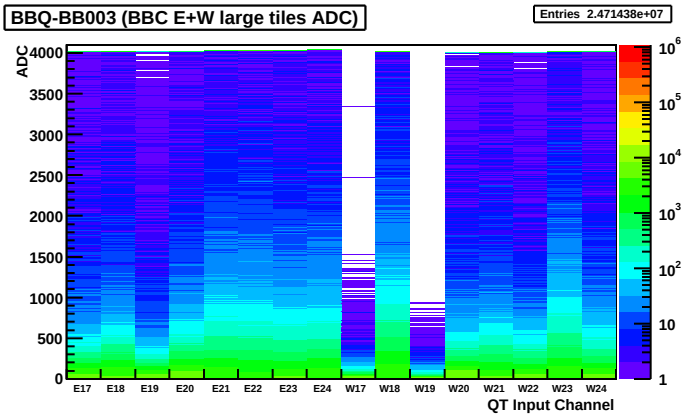

L1-TF201
Entries 9267894
Threshold bits
952372
952270
953327
947930
941560
900300
592277
592379
591322
596719
603089
644349
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

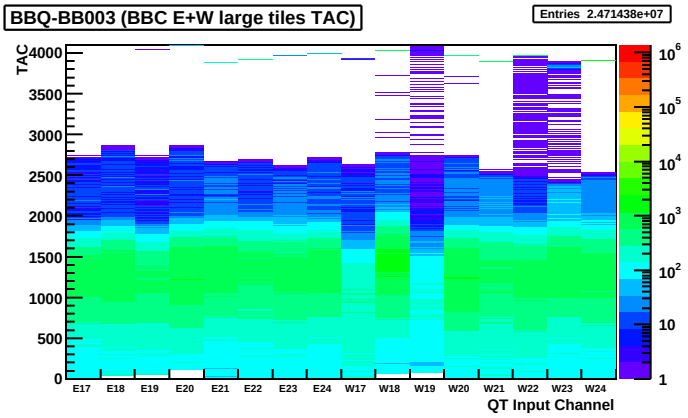

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.471438e+07
ADC
QT Input Channel

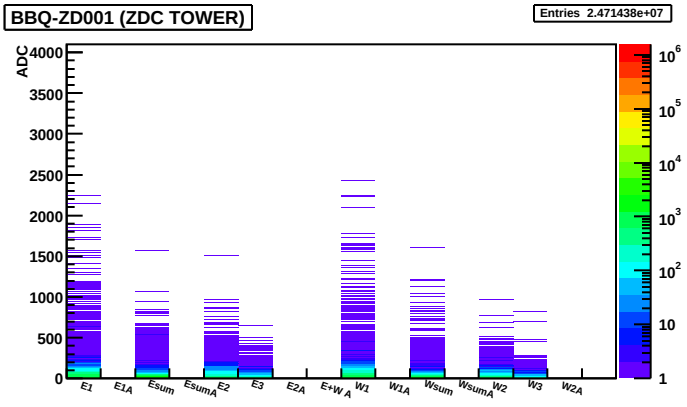

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.471438e+07
TAC
QT Input Channel

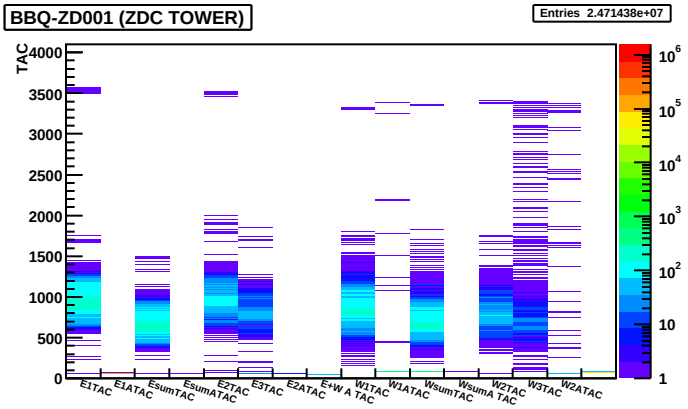

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.471438e+07
ADC
QT Input Channel

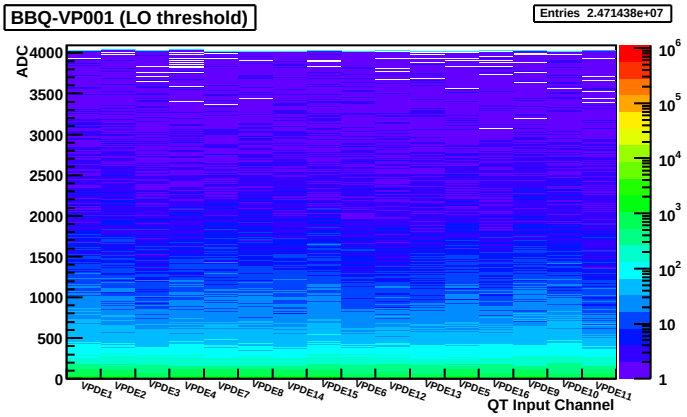

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.471438e+07
TAC
QT Input Channel

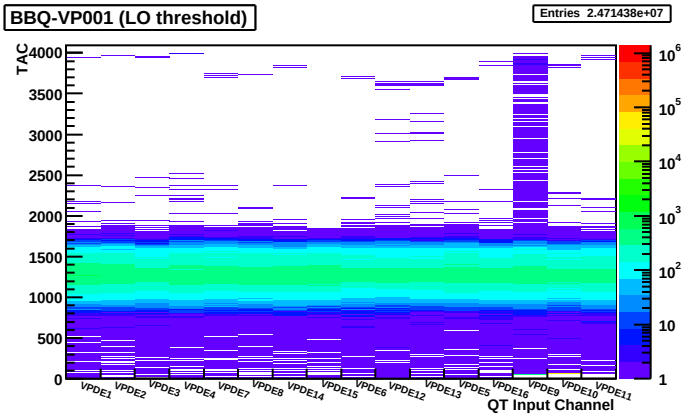

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.471438e+07
ADC
QT Input Channel

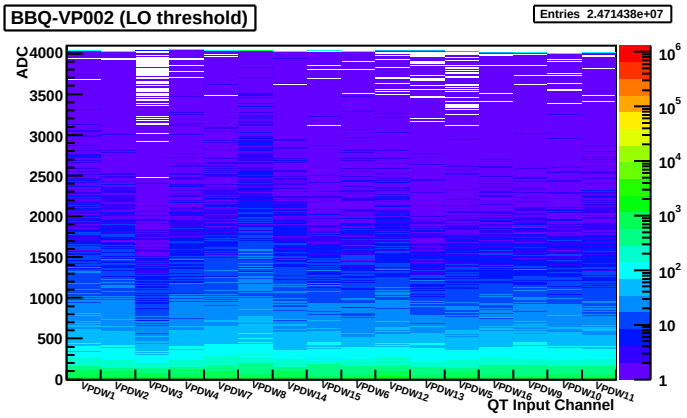

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.471438e+07
TAC
QT Input Channel

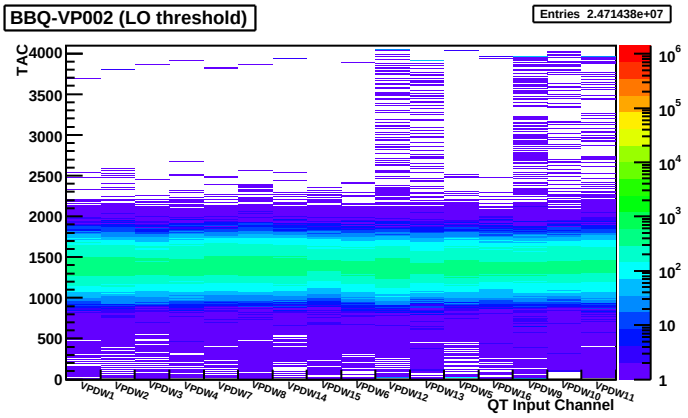

BBQ-ZD001 (ZDC TOWER)
Entries 2.471438e+07
ADC

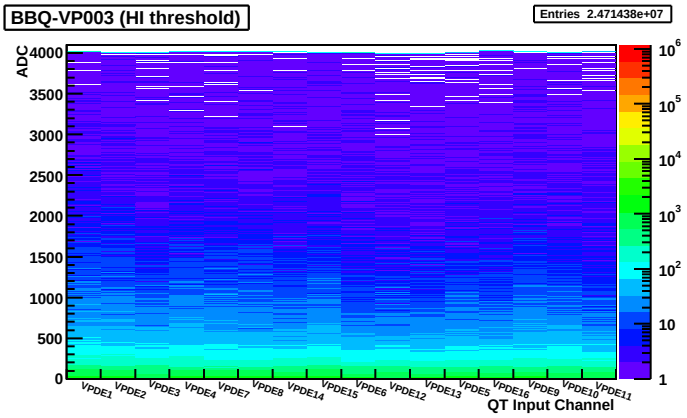

BBQ-ZD001 (ZDC TOWER)
Entries 2.471438e+07
TAC

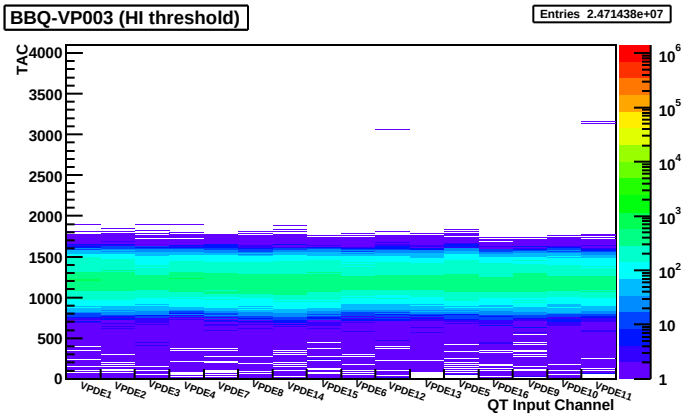

BBQ-VP001 (LO threshold)
Entries 2.471438e+07
ADC
QT Input Channel

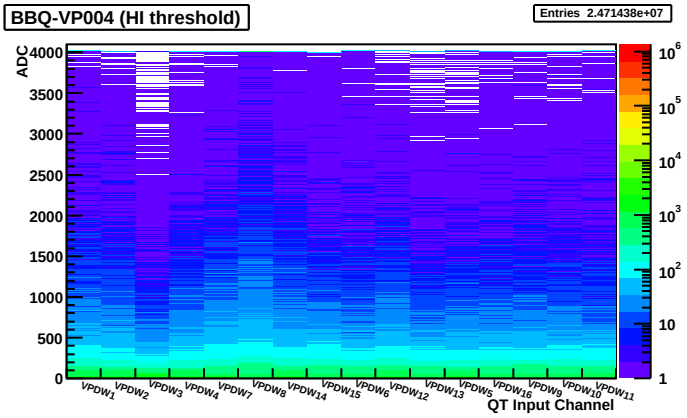

BBQ-VP001 (LO threshold)
Entries 2.471438e+07
TAC
QT Input Channel

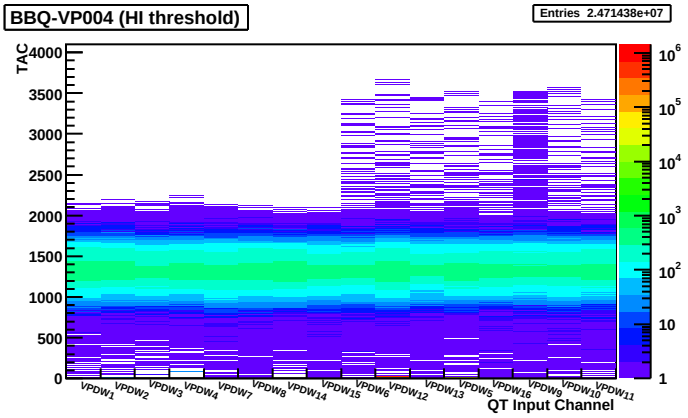

BBQ-VP002 (LO threshold)
Entries 2.471438e+07
ADC
QT Input Channel

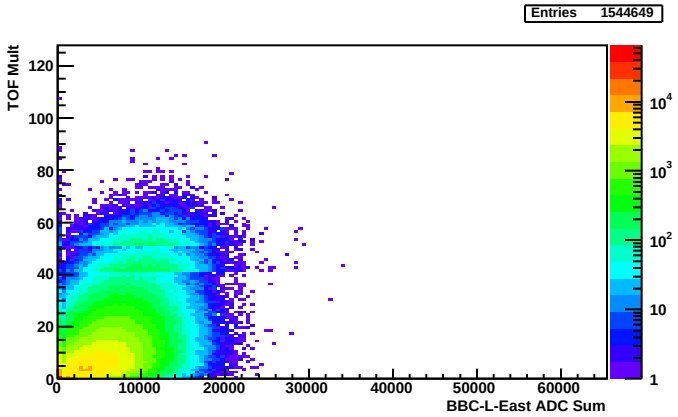

BBQ-VP002 (LO threshold)
Entries 2.471438e+07
TAC
QT Input Channel

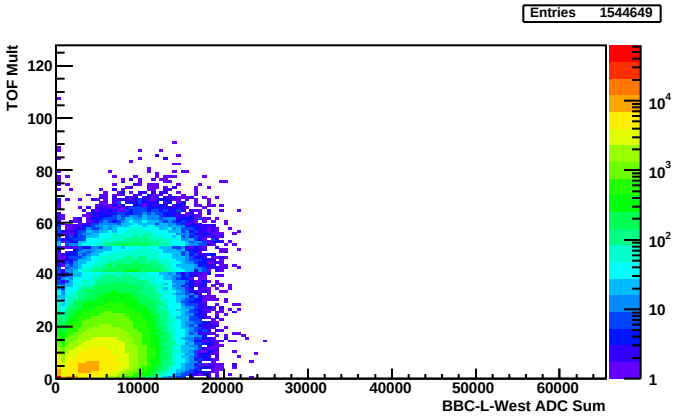

BBQ-VP003 (HI threshold)
Entries 2.471438e+07
ADC
QT Input Channel

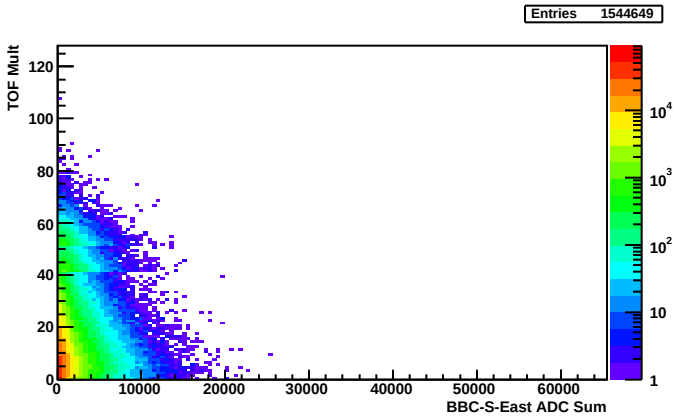

BBQ-VP003 (HI threshold)
Entries 2.471438e+07
TAC
QT Input Channel

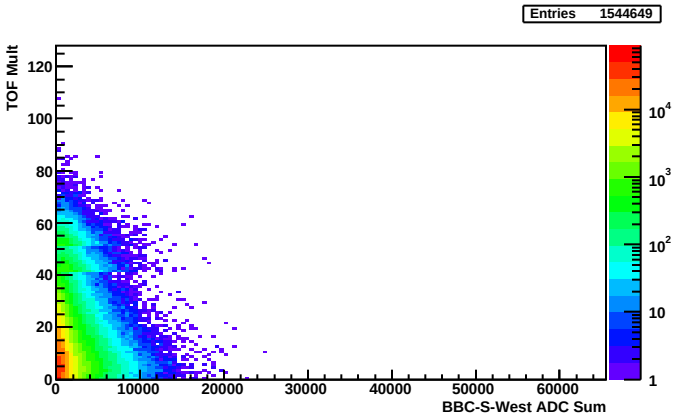

BBQ-VP004 (HI threshold)
Entries 2.471438e+07
ADC
QT Input Channel

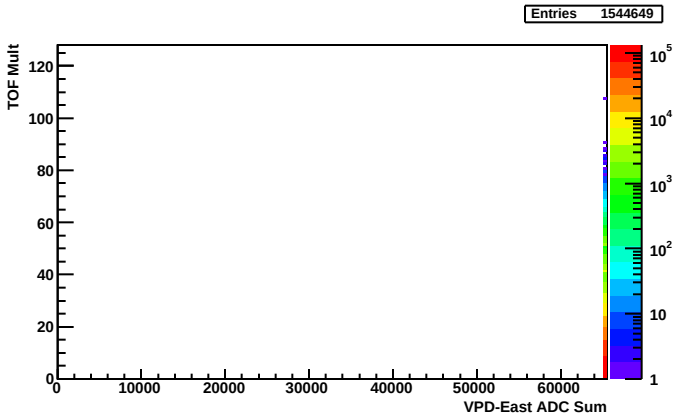

BBQ-VP004 (HI threshold)
Entries 2.471438e+07
TAC
QT Input Channel

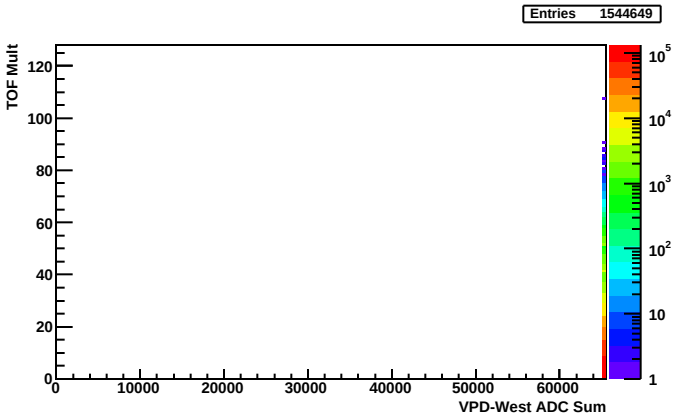

Entries 1544649
TOF Mult
BBC-L-East ADC Sum

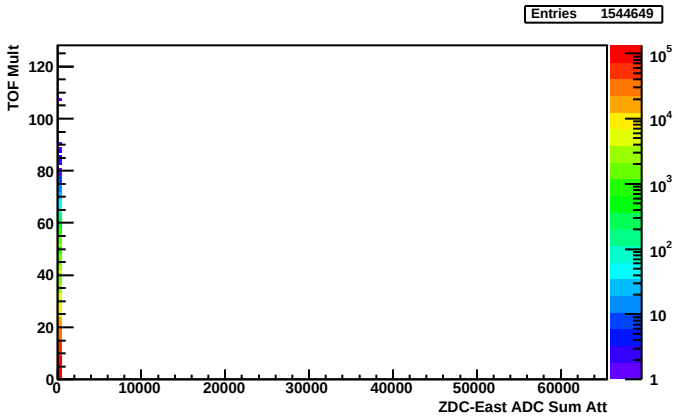

Entries 1544649
TOF Mult
BBC-L-West ADC Sum

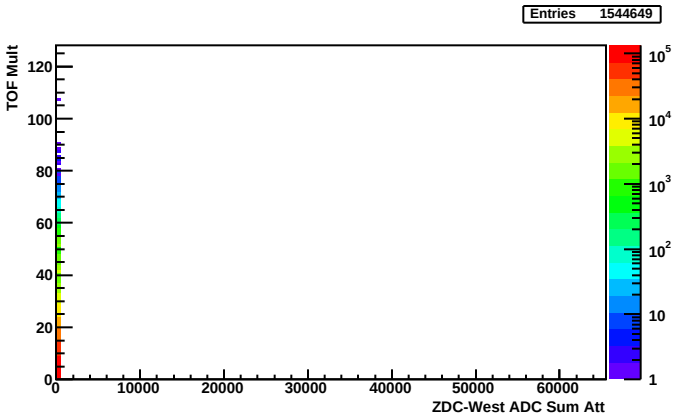

Entries 1544649
TOF Mult
BBC-S-East ADC Sum

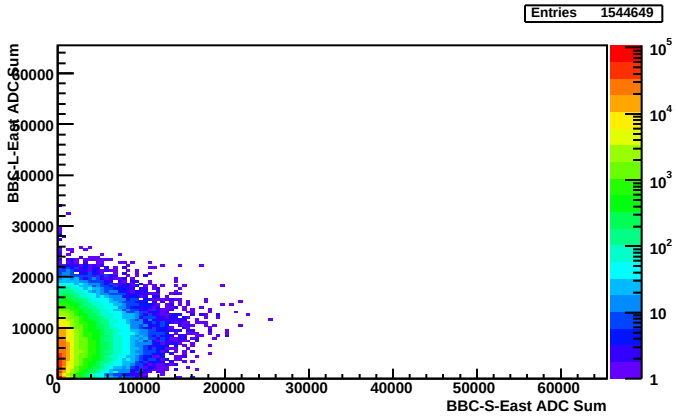

Entries 1544649
TOF Mult
BBC-S-West ADC Sum

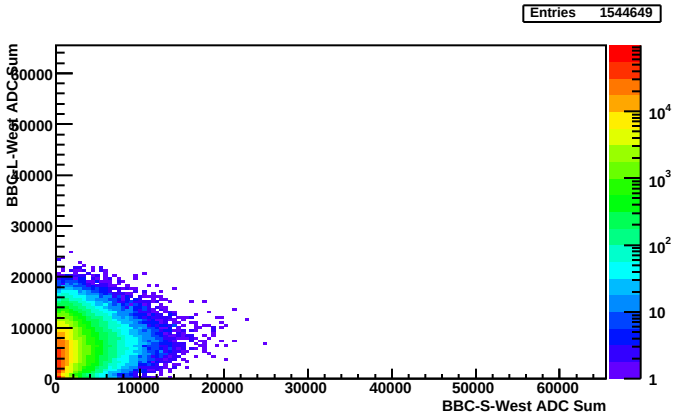

Entries 1544649
TOF Mult
VPD-East ADC Sum

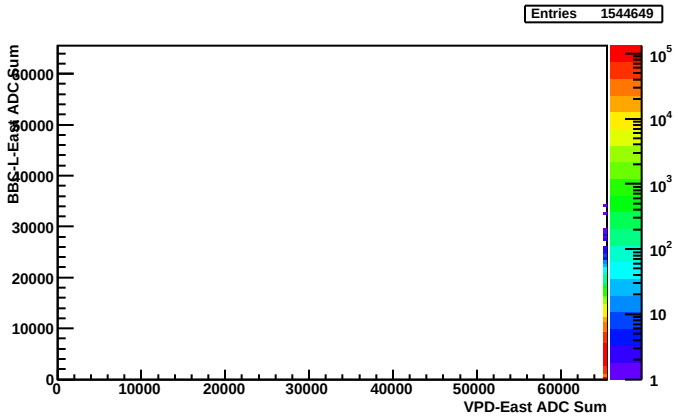

Entries 1544649
TOF Mult
VPD-West ADC Sum

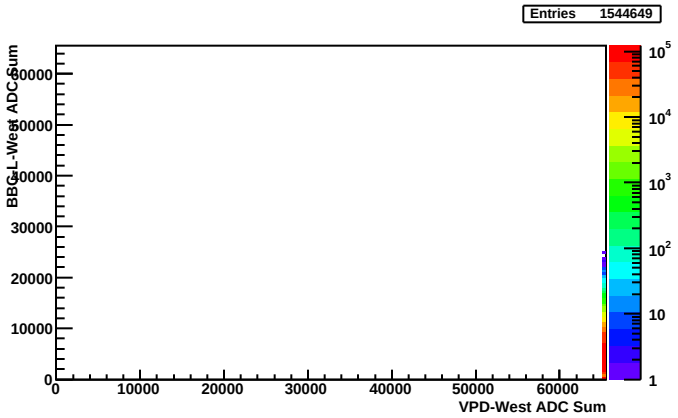

Entries 1544649
TOF Mult
ZDC-East ADC Sum Att

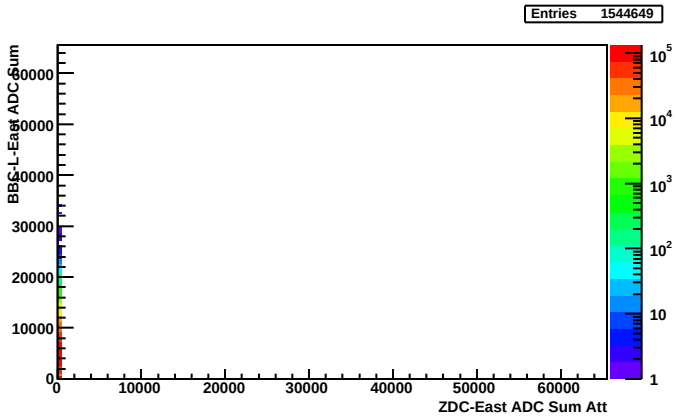

Entries 1544649
TOF Mult
ZDC-West ADC Sum Att


Entries 1544649
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1544649
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1544649
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 1544649
BBC-L-West ADC Sum
VPD-West ADC Sum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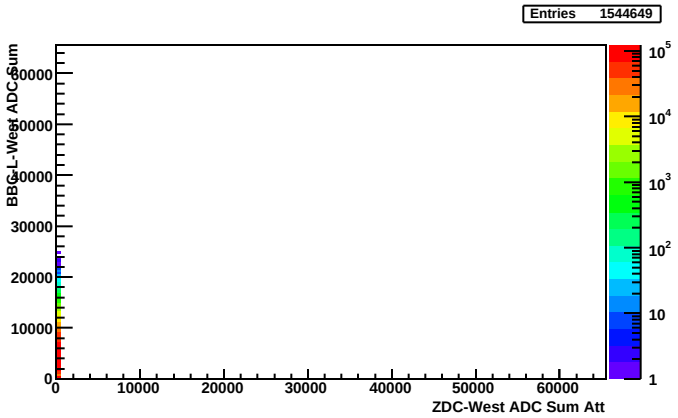
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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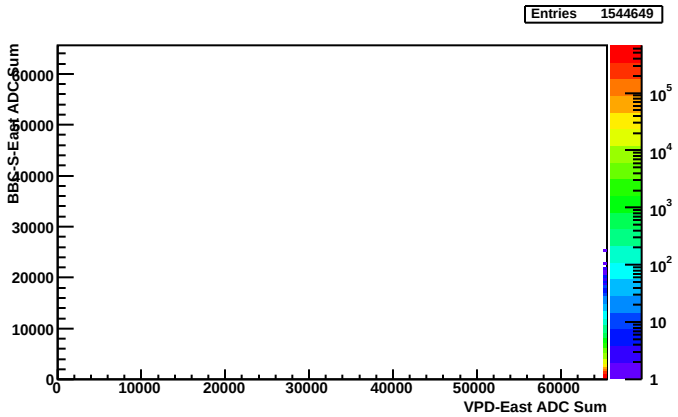
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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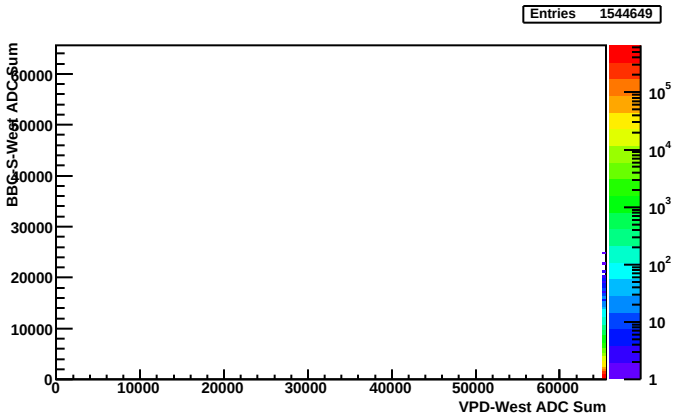
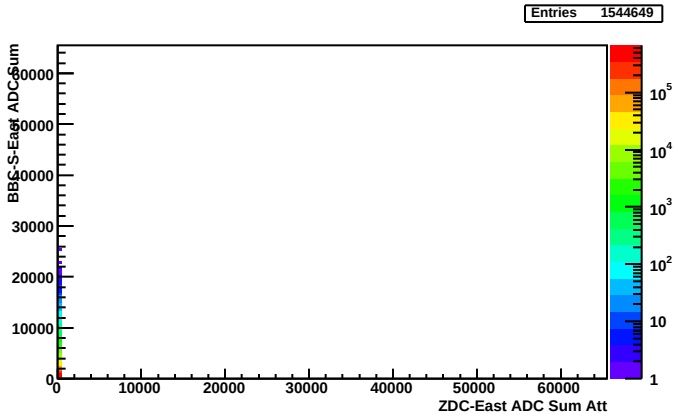
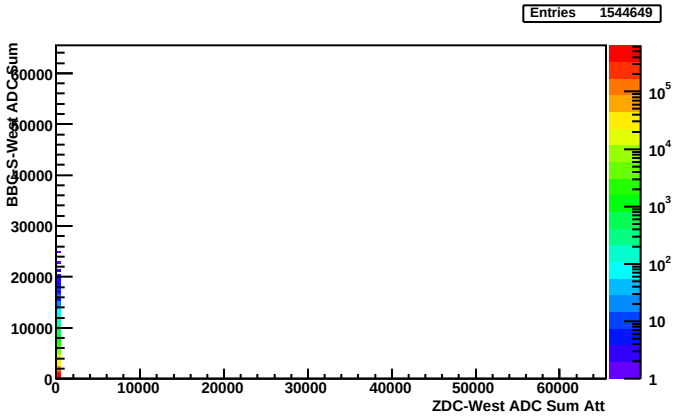
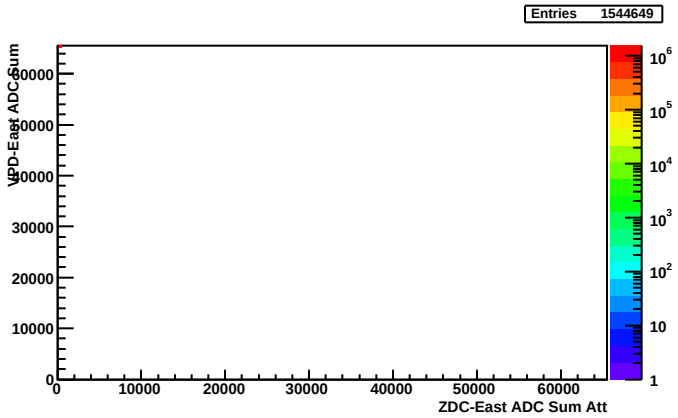
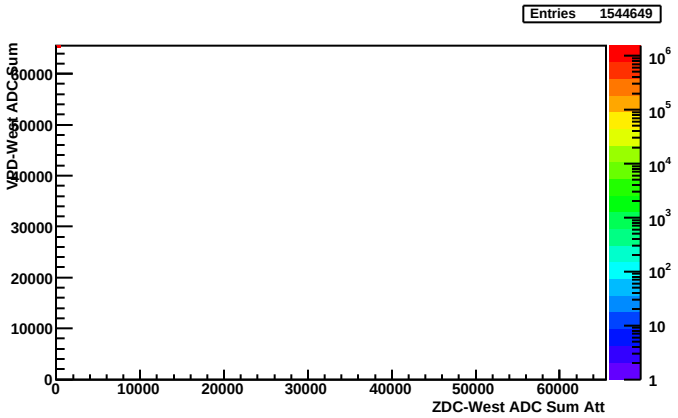
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1544649
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1544649
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1544649
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1544649
VPD-East ADC Sum
ZDC-East ADC Sum Att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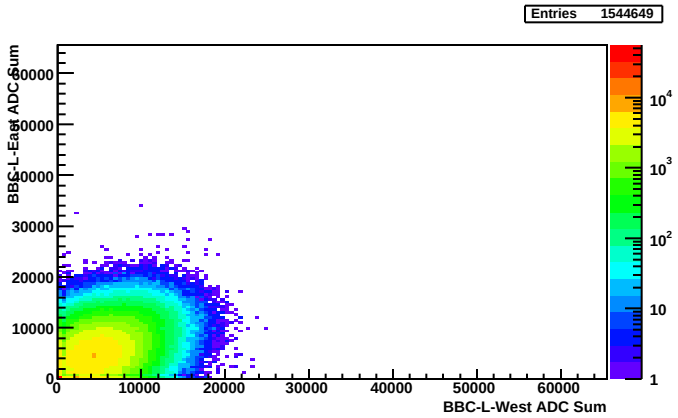
VPD-West ADC Sum
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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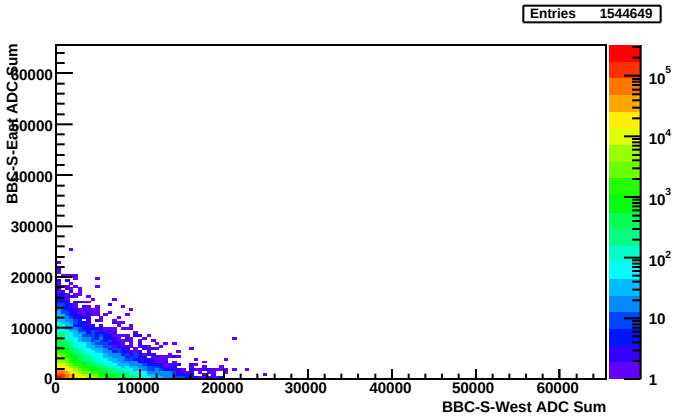
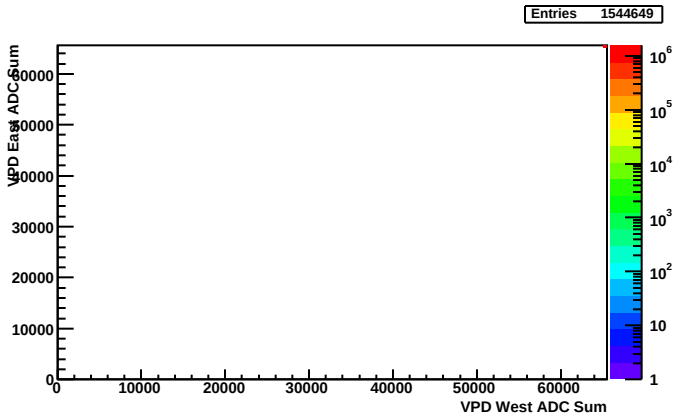
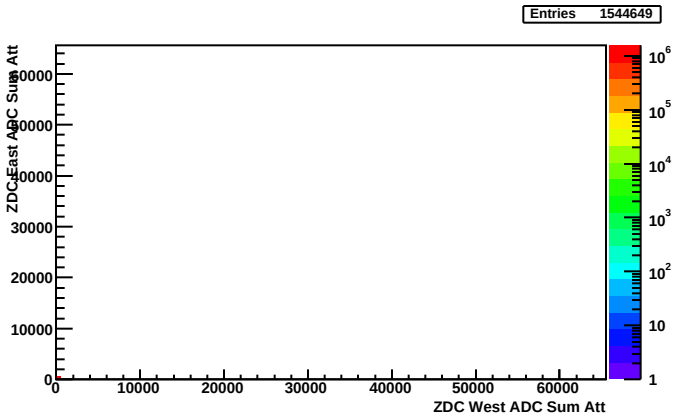
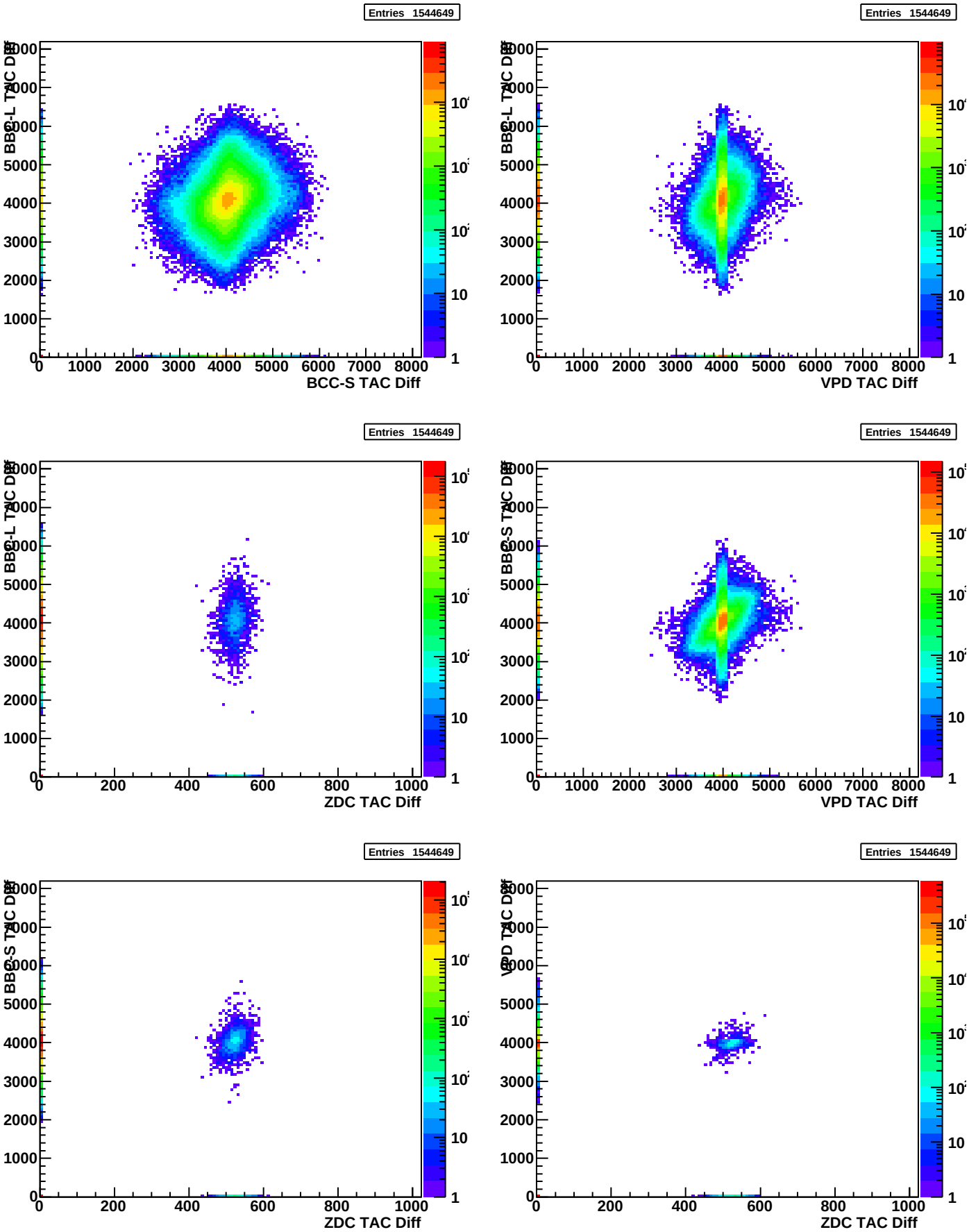
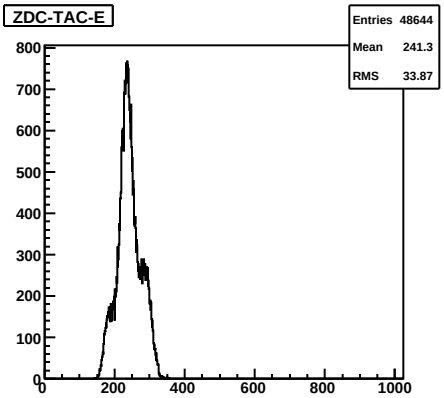
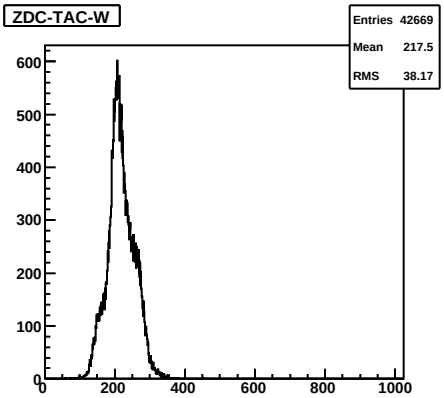

Entries 1544649
BBC-S-East ADC Sum
BBC-S-West ADC Sum

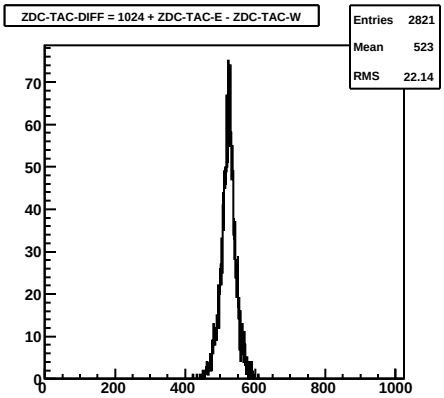

Entries 1544649
VPD East ADC Sum
VPD West ADC Sum

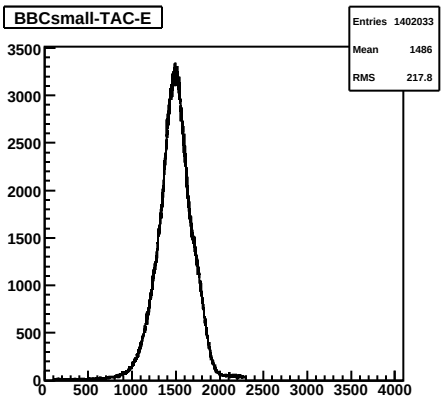

Entries 1544649
ZDC East ADC Sum Att
ZDC West ADC Sum Att

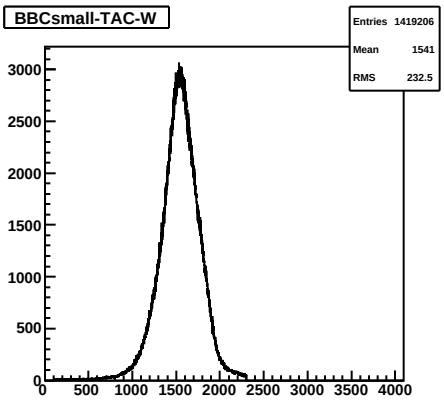

Entries 1544649
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1544649
BBC-L TAC Diff
VPD TAC Diff
Entries 1544649
BBC-L TAC Diff
ZDC TAC Diff
Entries 1544649
BBC-S TAC Diff
VPD TAC Diff
Entries 1544649
BBC-S TAC Diff
ZDC TAC Diff
Entries 1544649
VPD TAC Diff
ZDC TAC Diff

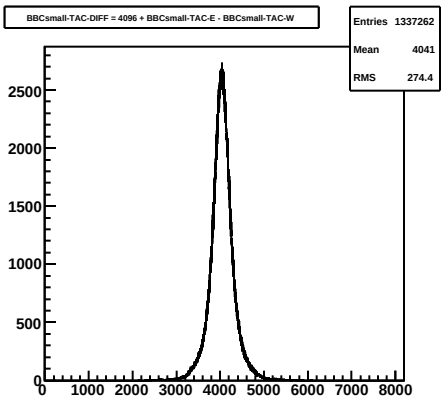

ZDC-TAC-E
Entries 48644
Mean 241.3
RMS 33.87

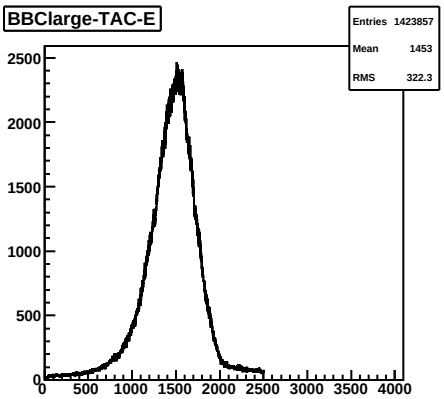

ZDC-TAC-W
Entries 42669
Mean 217.5
RMS 38.17

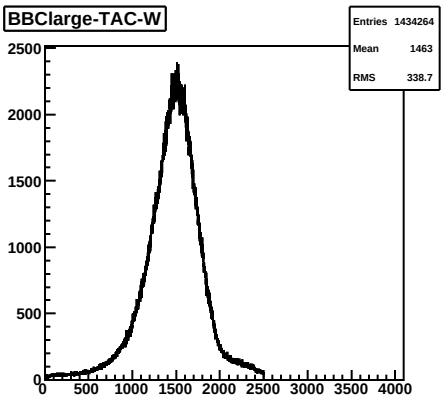

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2821
Mean 523
RMS 22.14

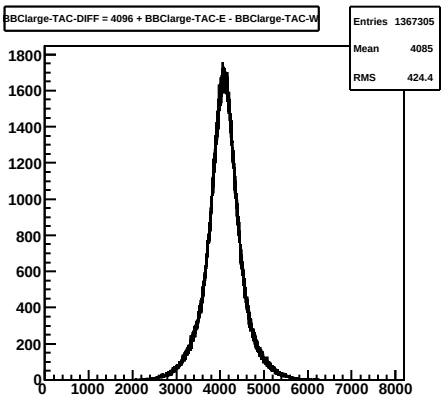

BBCsmall-TAC-E
Entries 1402033
Mean 1486
RMS 217.8

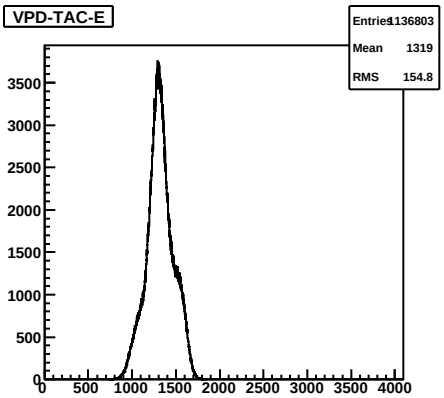

BBCsmall-TAC-W
Entries 1419206
Mean 1541
RMS 232.5

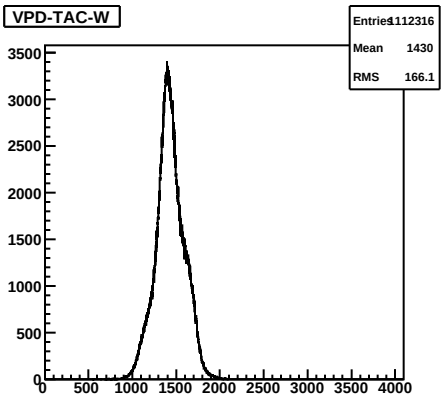

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1337262
Mean 4041
RMS 274.4

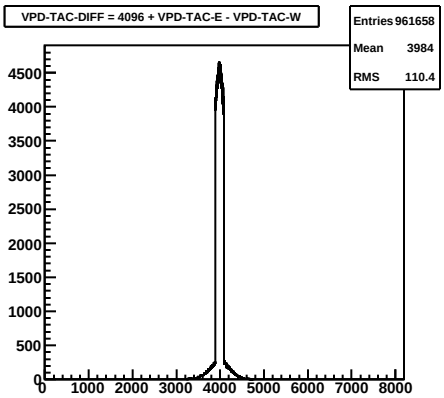

BBClarge-TAC-E
Entries 1423857
Mean 1453
RMS 322.3

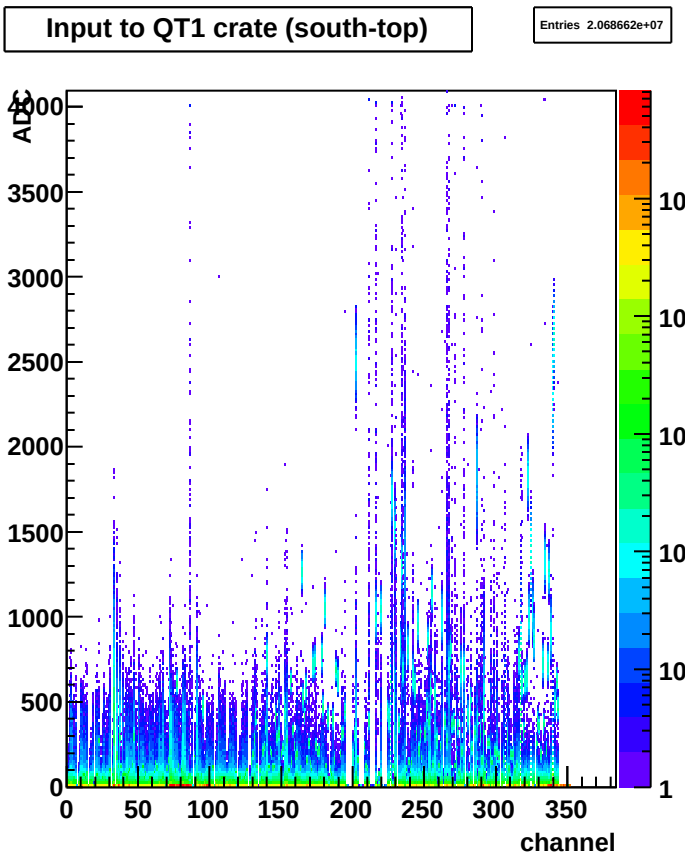

BBClarge-TAC-W
Entries 1434264
Mean 1463
RMS 338.7

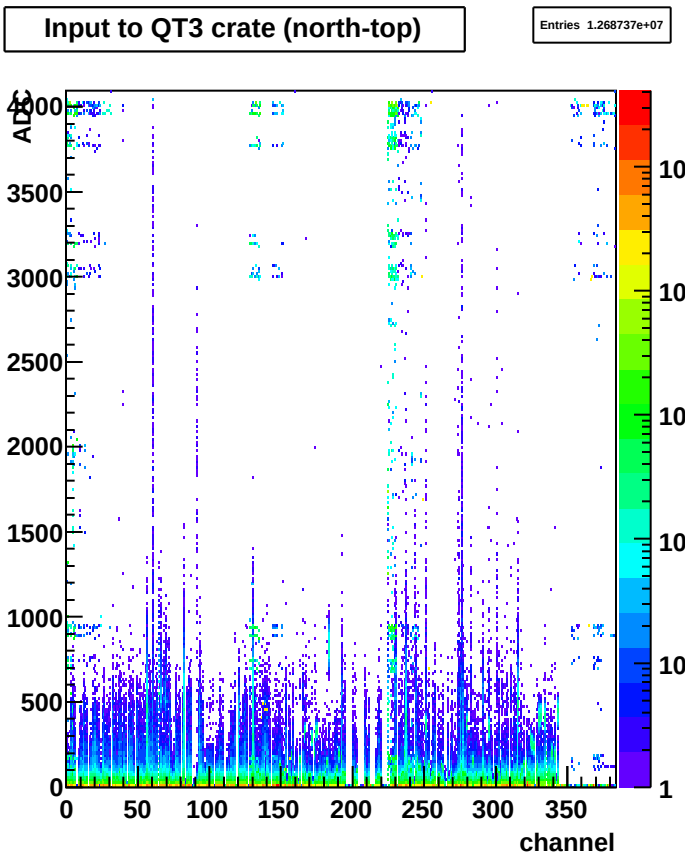

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1367305
Mean 4085
RMS 424.4

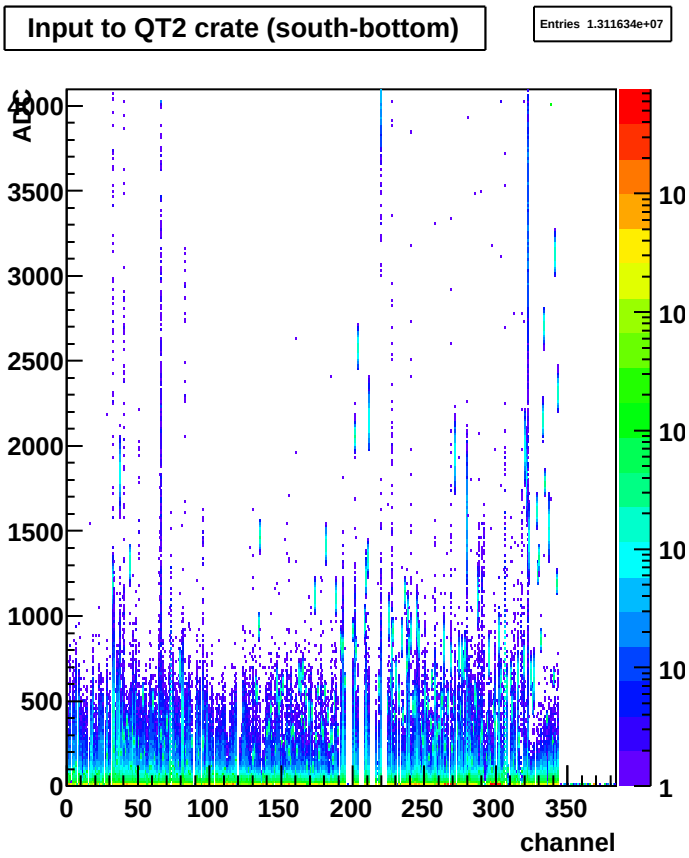

VPD-TAC-E
Entries 1136803
Mean 1319
RMS 154.8

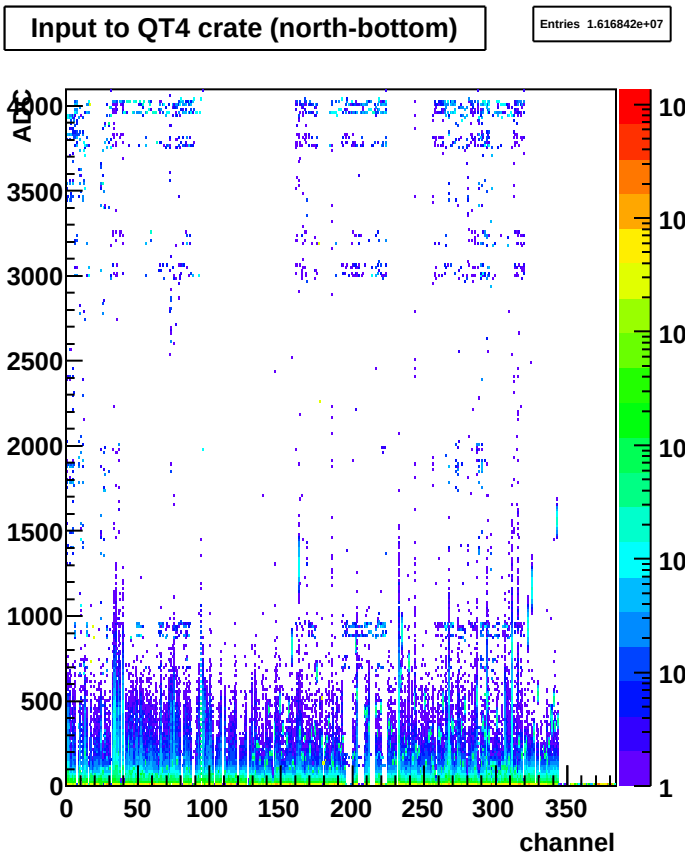

VPD-TAC-W
Entries 1112316
Mean 1430
RMS 166.1

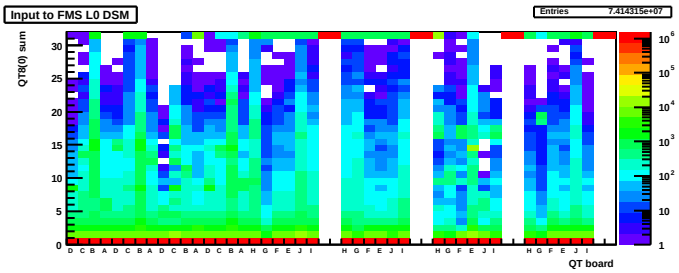

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 961658
Mean 3984
RMS 110.4

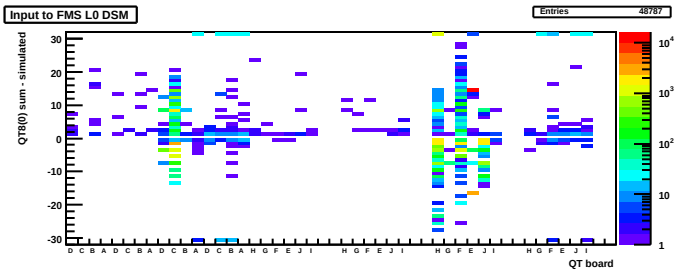

Input to QT1 crate (south-top)
Entries 2.068662e+07
ADC
channel

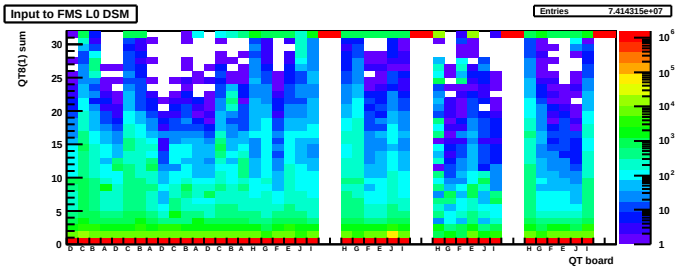

Input to QT3 crate (north-top)
Entries 1.268737e+07
ADC
channel

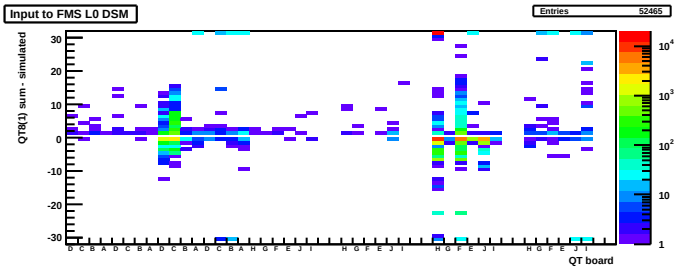

Input to QT2 crate (south-bottom)
Entries 1.311634e+07
ADC
channel

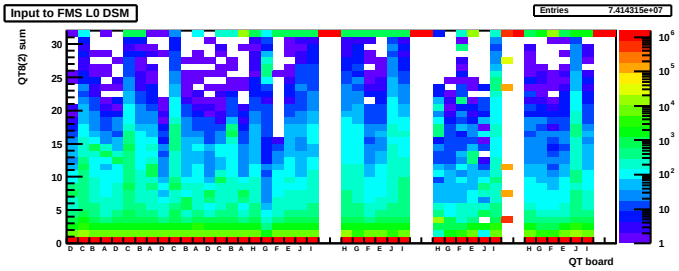

Input to QT4 crate (north-bottom)
Entries 1.616842e+07
ADC
channel

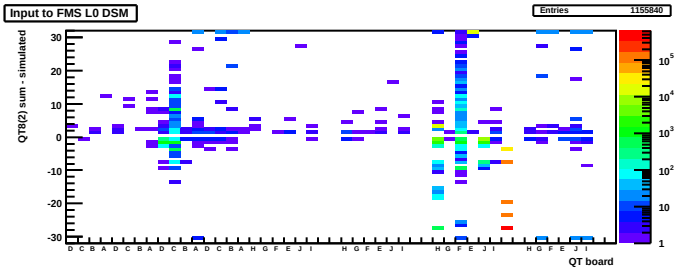

Input to FMS L0 DSM
Entries 7.414315e+07
QT8(0) sum
QT board

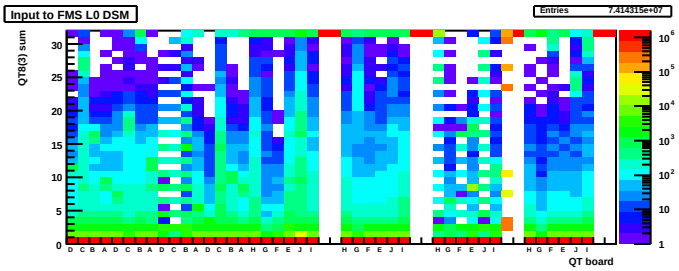

Input to FMS L0 DSM
Entries 48787
QT8(0) sum - simulated
QT board

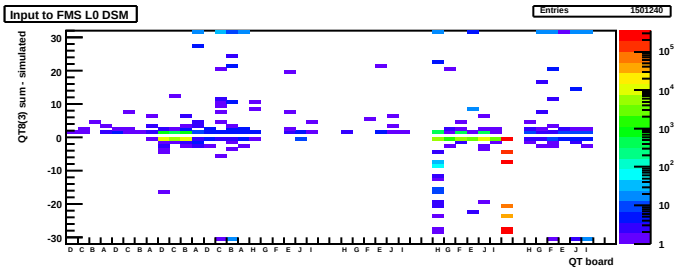

Input to FMS L0 DSM
Entries 7.414315e+07
QT8(1) sum
QT board

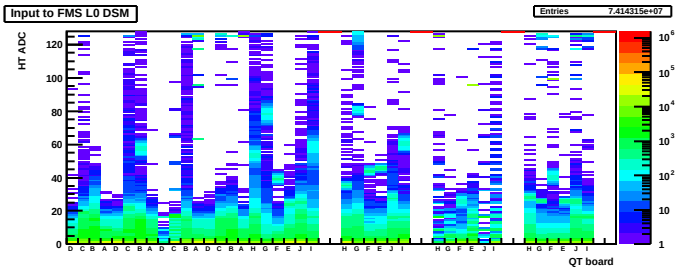

Input to FMS L0 DSM
Entries 52465
QT8(1) sum - simulated
QT board

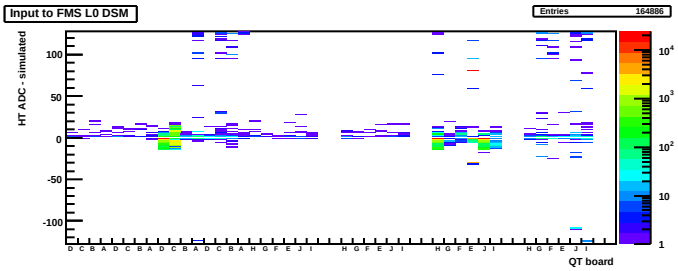

Input to FMS L0 DSM
Entries 7.414315e+07
QT8(2) sum
QT board

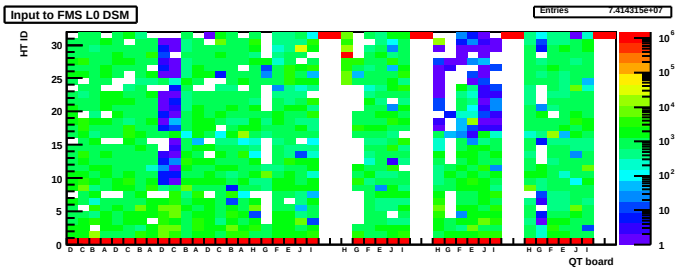

Input to FMS L0 DSM
Entries 1155840
QT8(2) sum - simulated
QT board

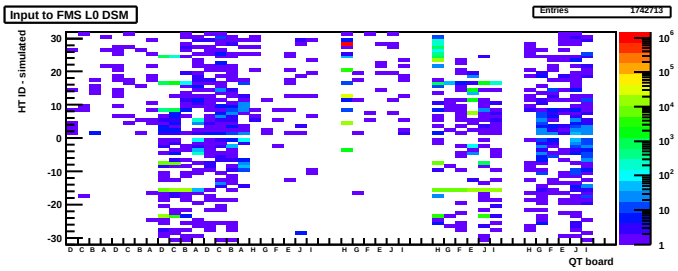

Input to FMS L0 DSM
Entries 7.414315e+07
QT8(3) sum
QT board

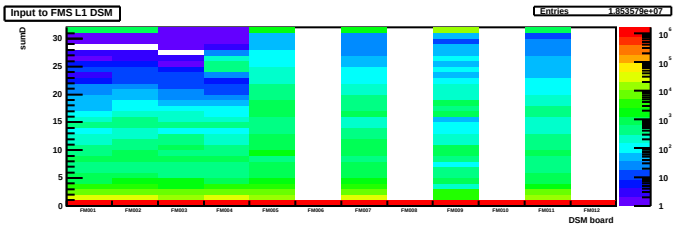

Input to FMS L0 DSM
Entries 1501240
QT8(3) sum - simulated
QT board

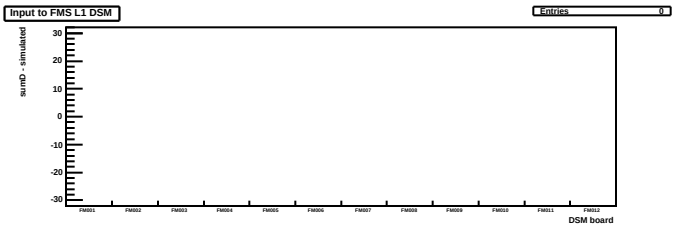

Input to FMS L0 DSM
Entries 7.414315e+07
HT ADC
QT board

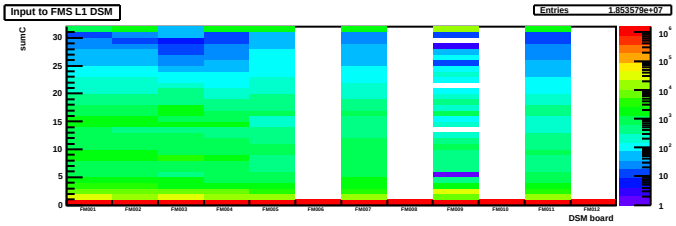

Input to FMS L0 DSM
Entries 164886
HT ADC - simulated
QT board

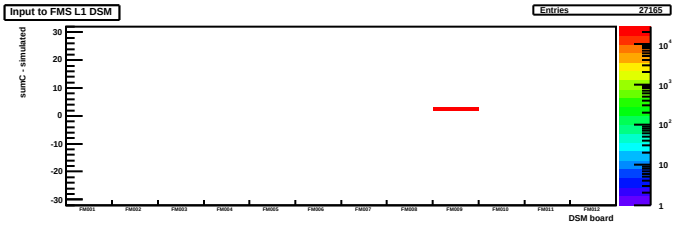

Input to FMS L0 DSM
Entries 7.414315e+07
HT ID
QT board

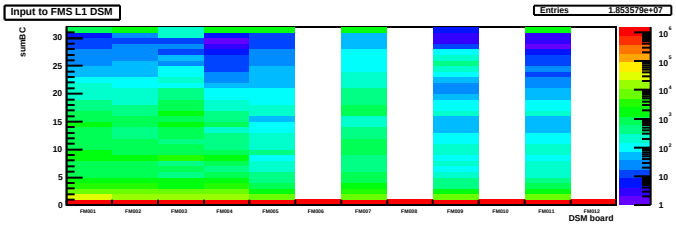

Input to FMS L0 DSM
Entries 1742713
HT ID - simulated
QT board

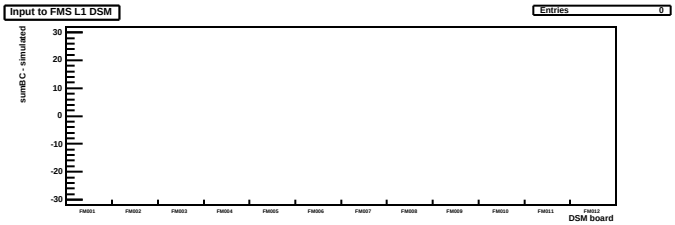

Input to FMS L1 DSM
Entries 1.853579e+07
sumD
DSM board

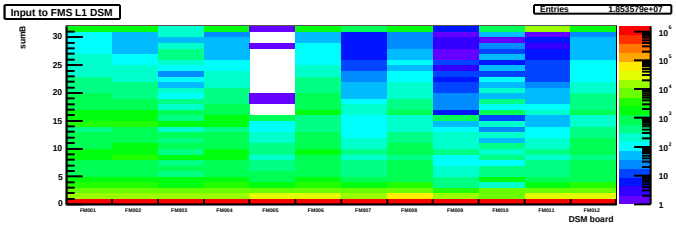

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

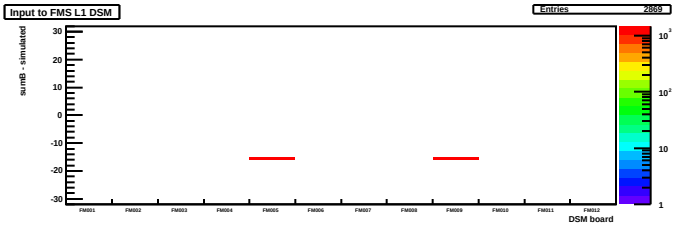

Input to FMS L1 DSM
Entries 1.853579e+07
sumC
DSM board

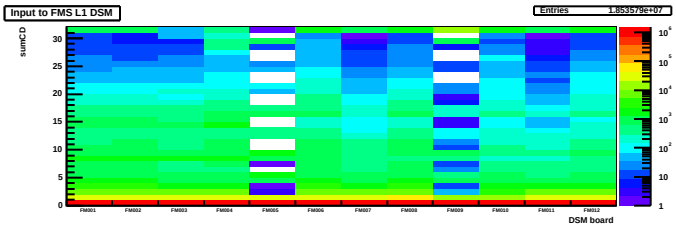

Input to FMS L1 DSM
Entries 27165
sumC - simulated
DSM board

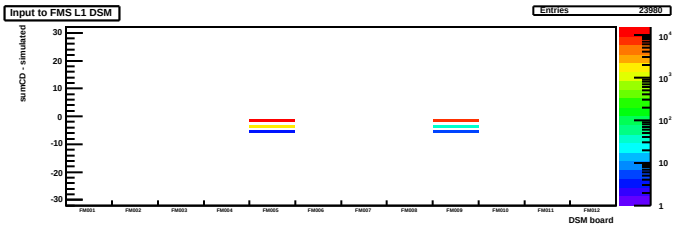

Input to FMS L1 DSM
Entries 1.853579e+07
sumBC
DSM board

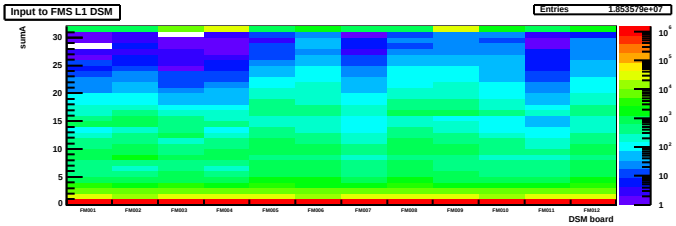

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

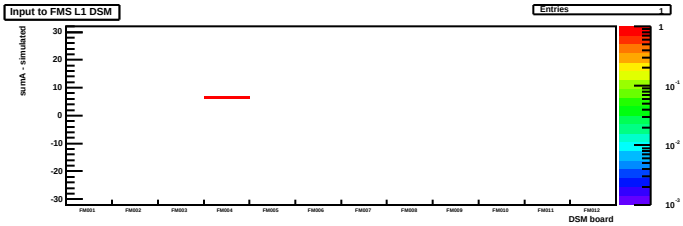

Input to FMS L1 DSM
Entries 1.853579e+07
sumB
DSM board

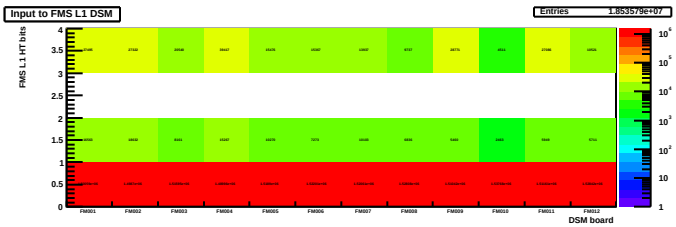

Input to FMS L1 DSM
Entries 2869
sumB - simulated
DSM board

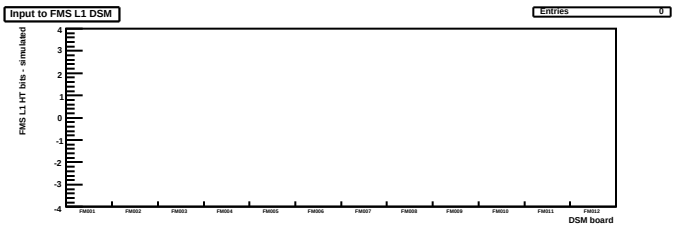

Input to FMS L1 DSM
Entries 1.853579e+07
sumCD
DSM board

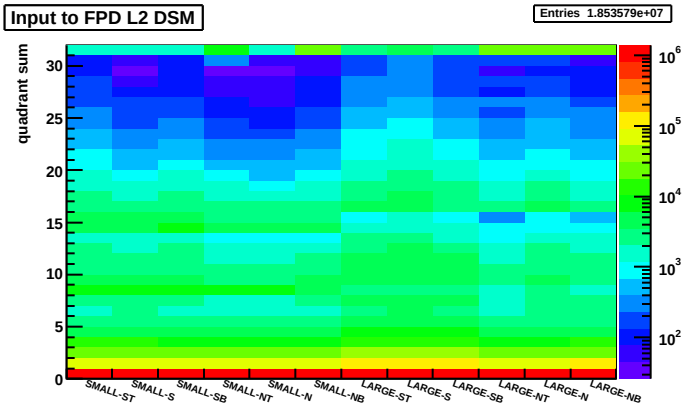

Input to FMS L1 DSM
Entries 23980
sumCD - simulated
DSM board

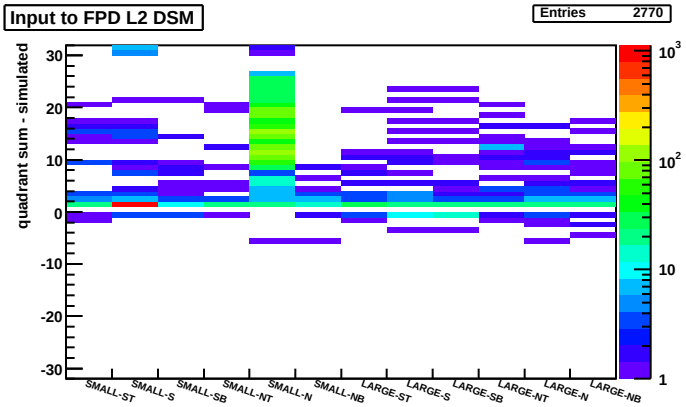

Input to FMS L1 DSM
Entries 1.853579e+07
sumA
DSM board

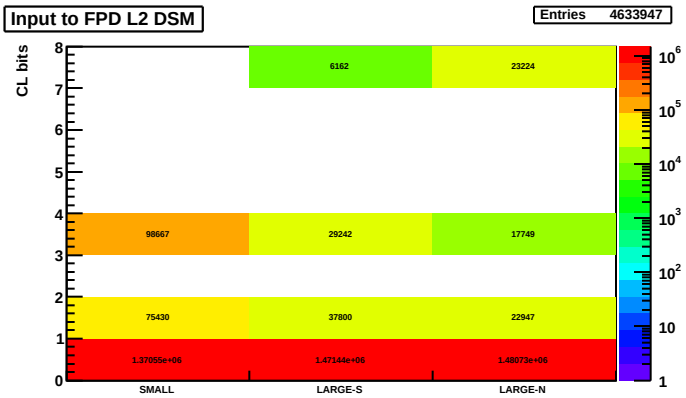

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

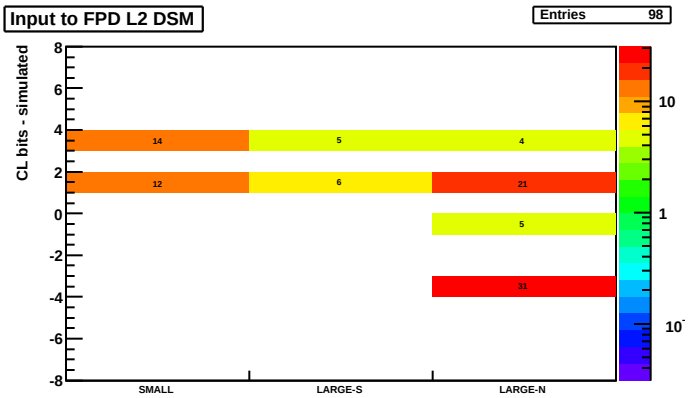

Input to FMS L1 DSM
Entries 1.853579e+07
FMS L1 HT bits
DSM board

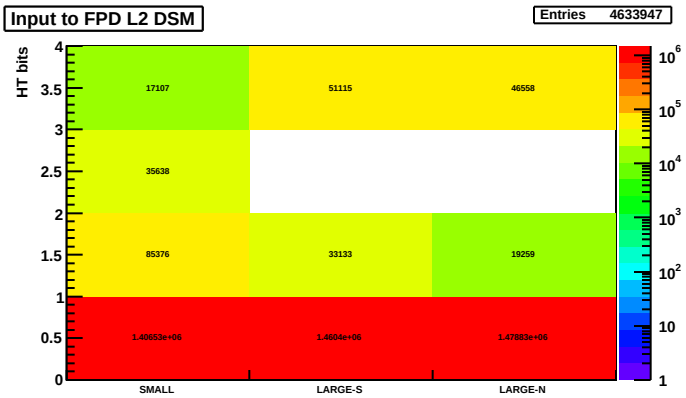

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

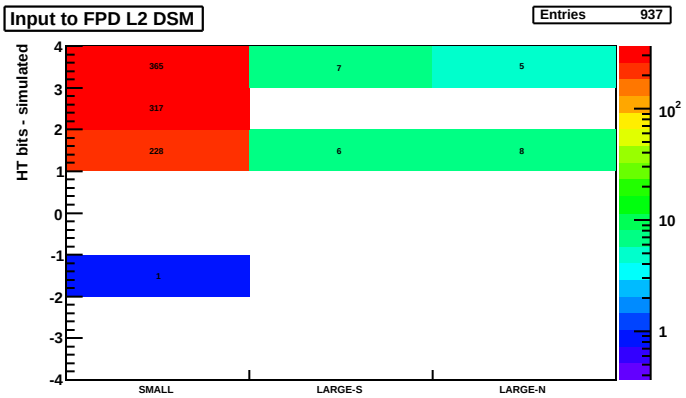

Input to FPD L2 DSM
Entries 1.853579e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

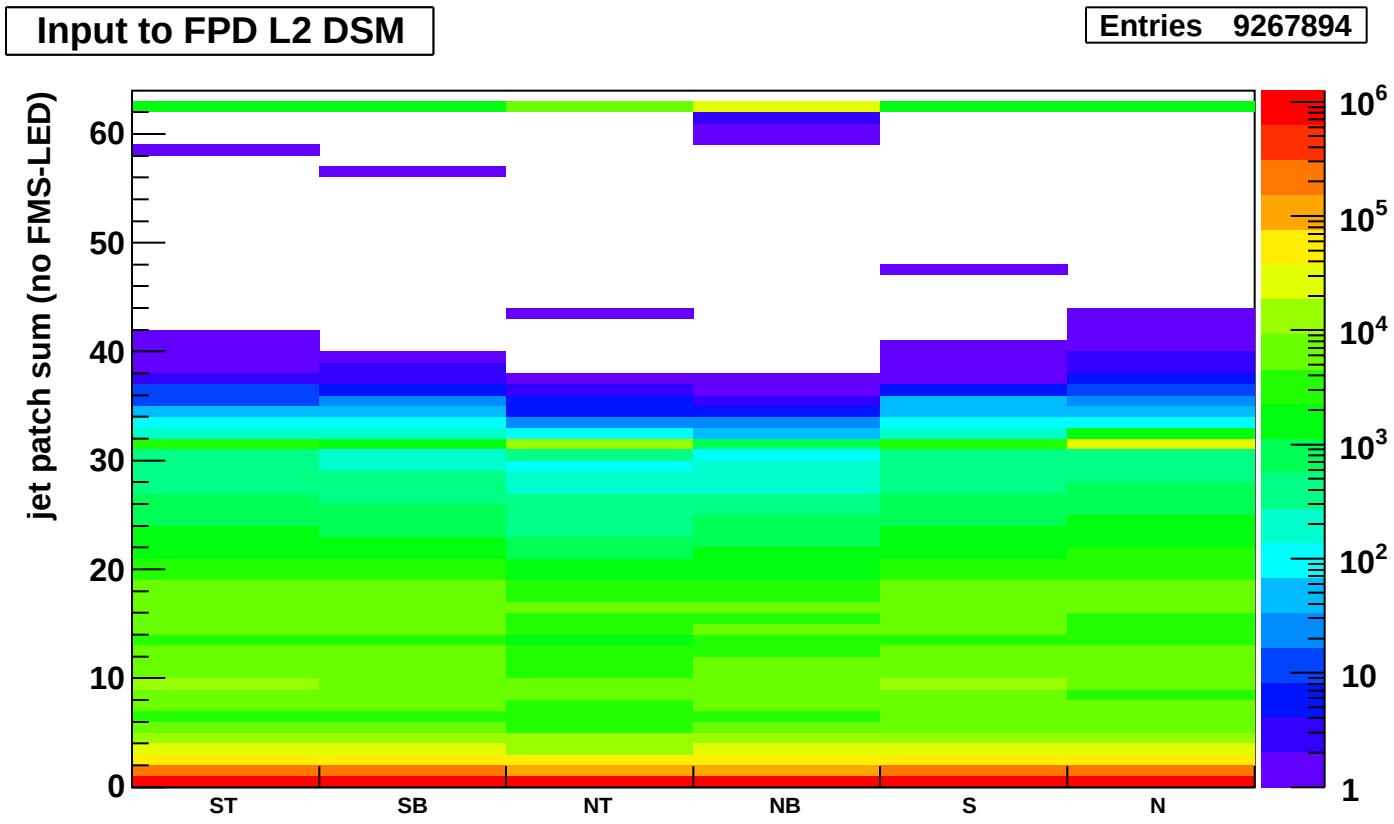

Input to FPD L2 DSM
Entries 2770
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

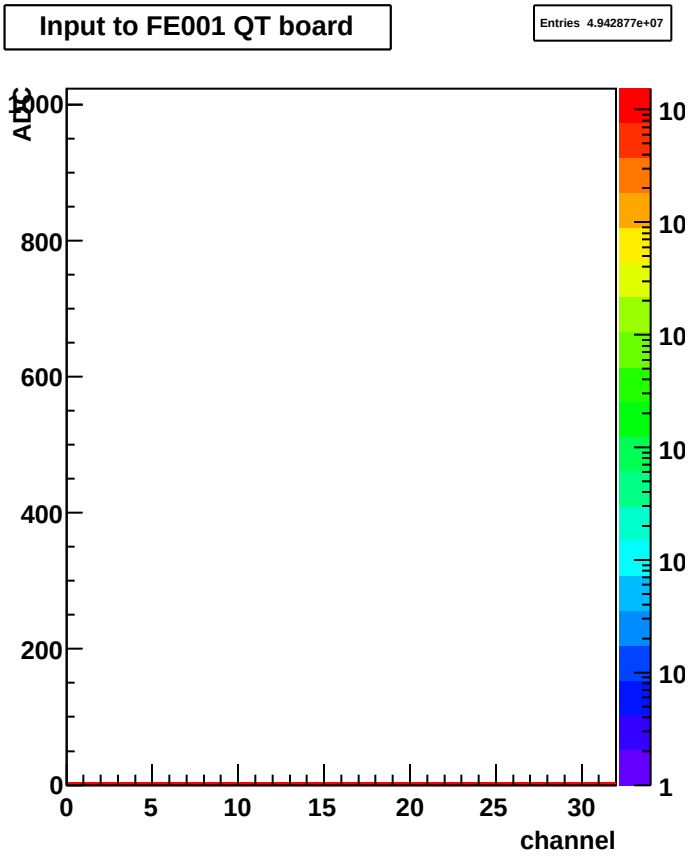

Input to FPD L2 DSM
Entries 4633947
CL bits
6162
23224
98667
29242
17749
75430
37800
22947
1.37055e+06
1.47144e+06
1.48073e+06
SMALL
LARGE-S
LARGE-N

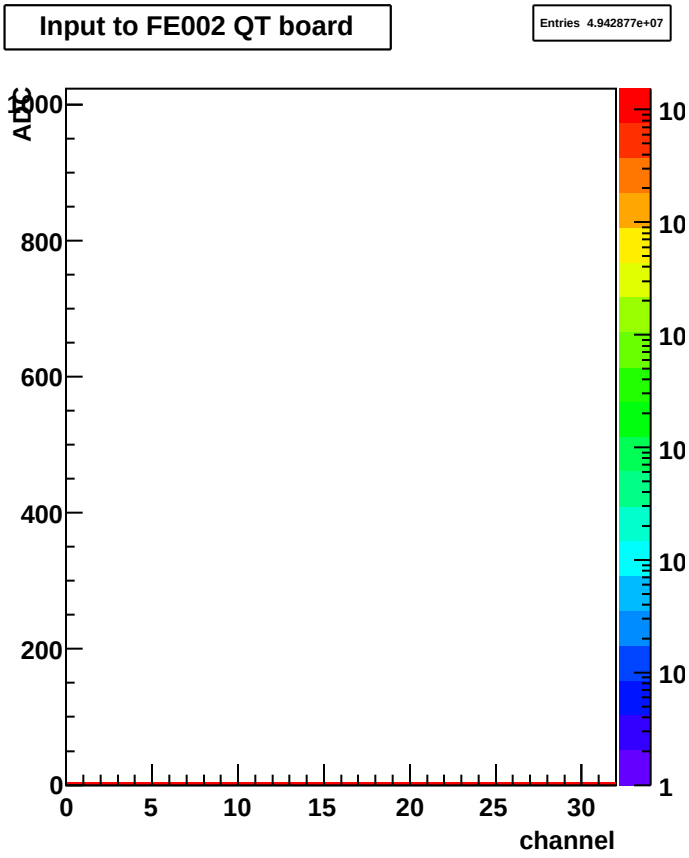

Input to FPD L2 DSM
Entries 98
CL bits - simulated
14
5
4
12
6
21
5
31
SMALL
LARGE-S
LARGE-N

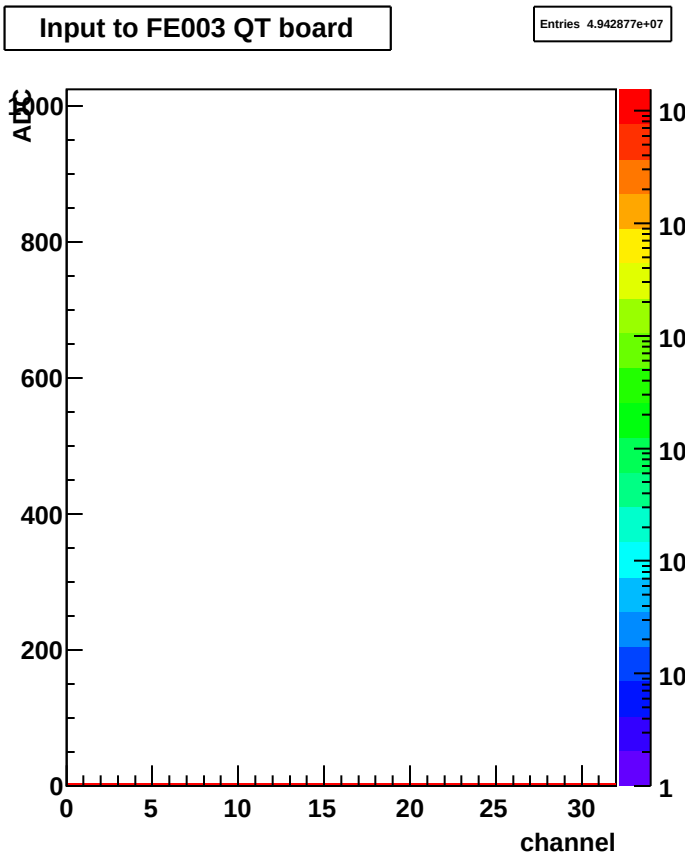

Input to FPD L2 DSM
Entries 4633947
HT bits
17107
51115
46558
35638
85376
33133
19259
1.40653e+06
1.4604e+06
1.47883e+06
SMALL
LARGE-S
LARGE-N

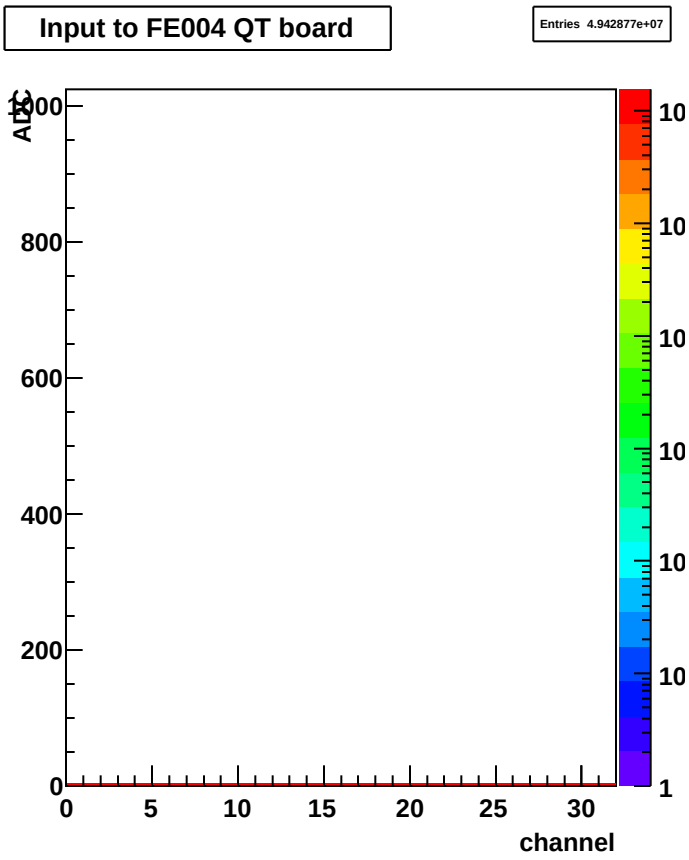

Input to FPD L2 DSM
Entries 937
HT bits - simulated
365
7
5
317
228
6
8
1
SMALL
LARGE-S
LARGE-N

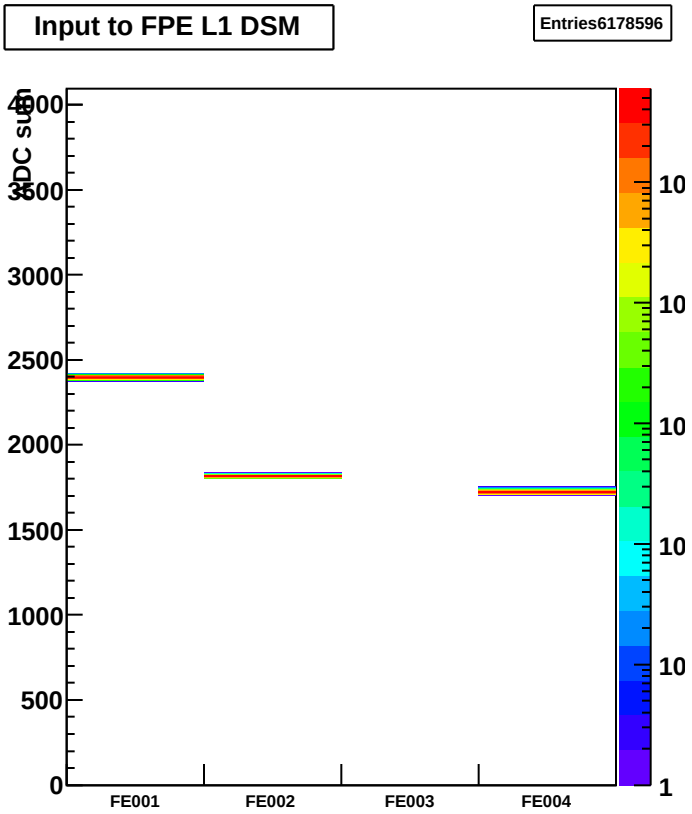

Input to FPD L2 DSM
Entries 9267894
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 4.942877e+07
ADC
channel

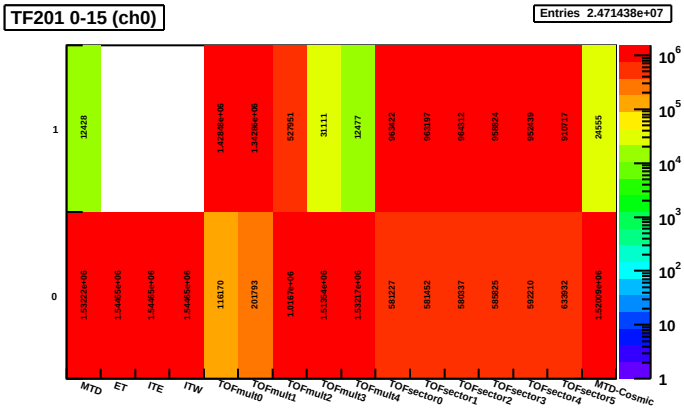

Input to FE002 QT board
Entries 4.942877e+07
ADC
channel


Input to FE003 QT board
Entries 4.942877e+07
ADC
channel

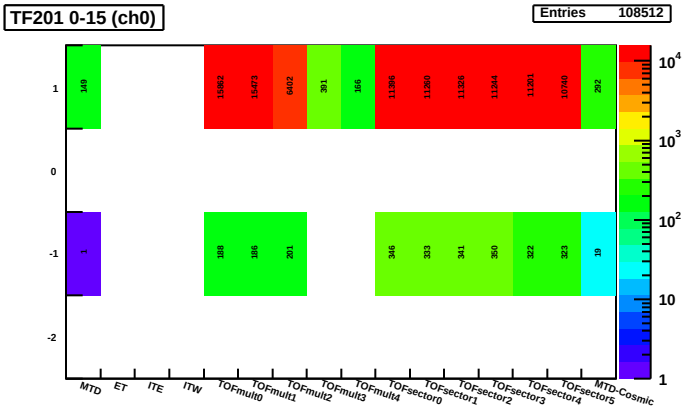

Input to FE004 QT board
Entries 4.942877e+07
ADC
channel


Input to FPE L1 DSM
Entries6178596
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

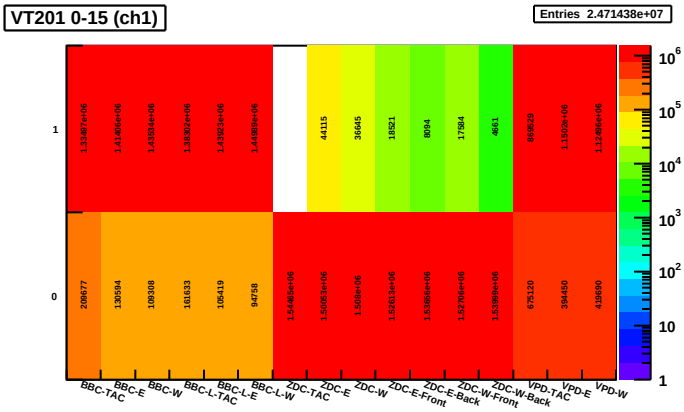
TF201 0-15 (ch0)



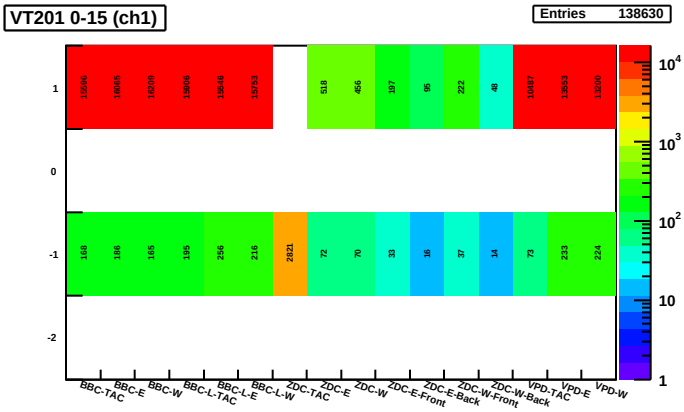
TF201 0-15 (ch0)



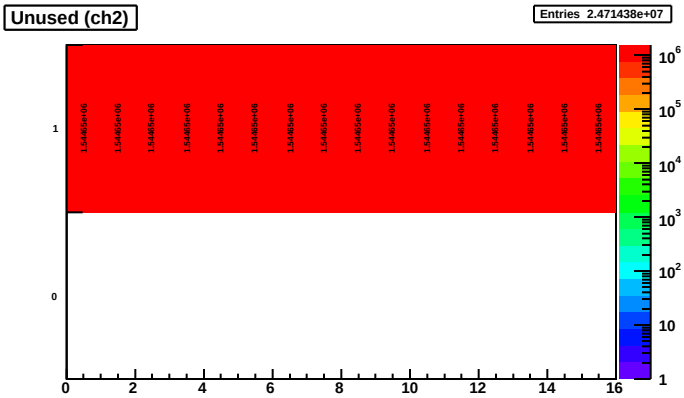
VT201 0-15 (ch1)

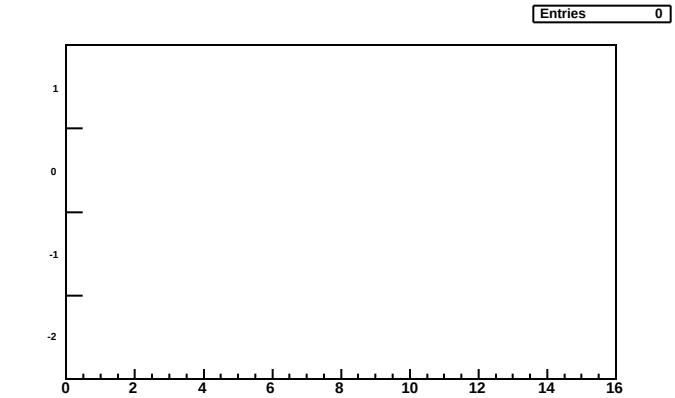


VT201 0-15 (ch1)

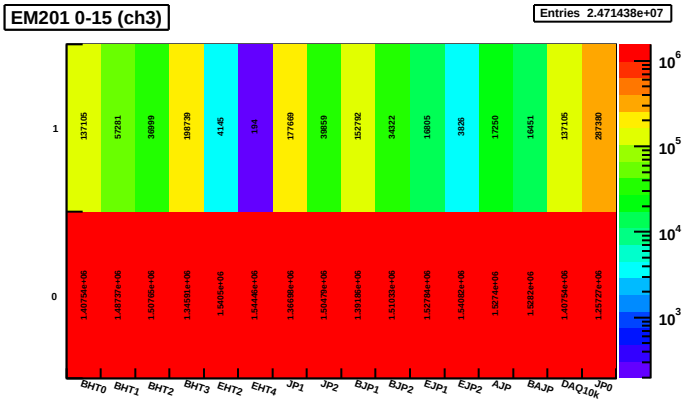


Unused (ch2)





EM201 0-15 (ch3)



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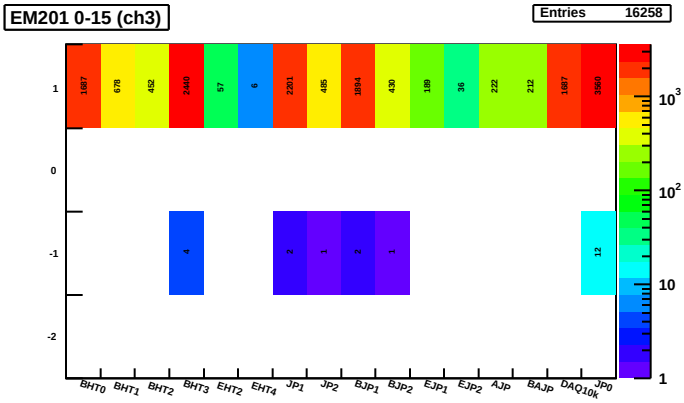
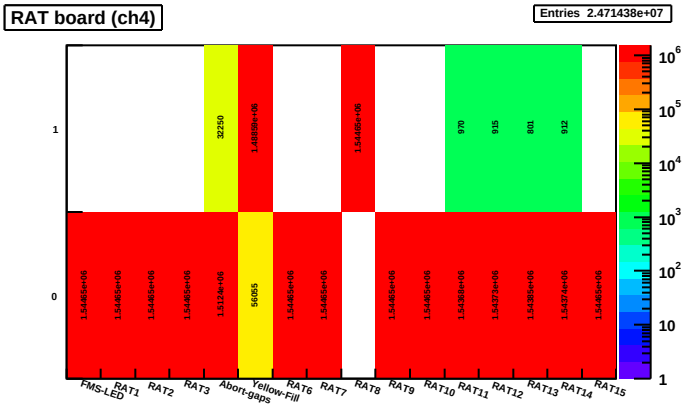
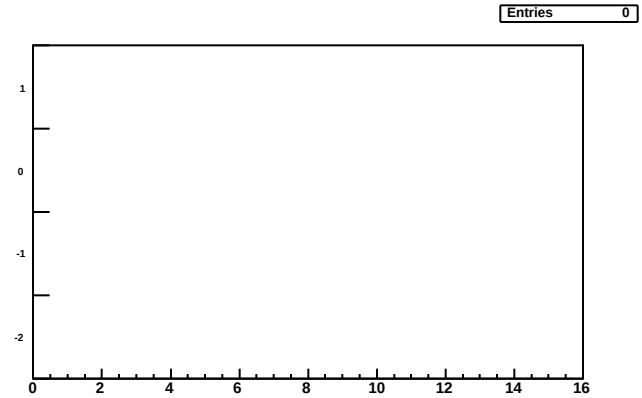


RAT board (ch4)





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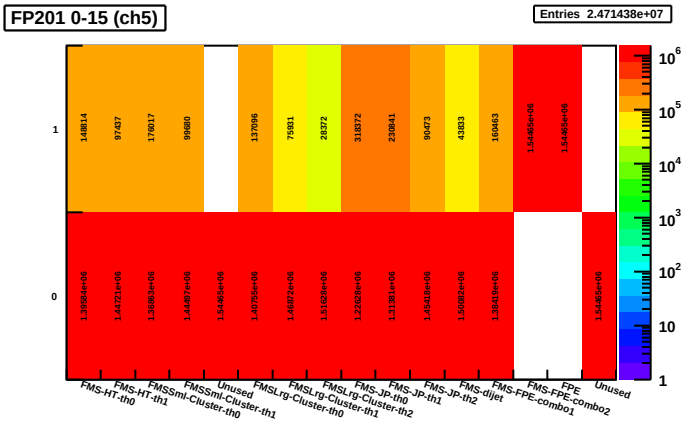

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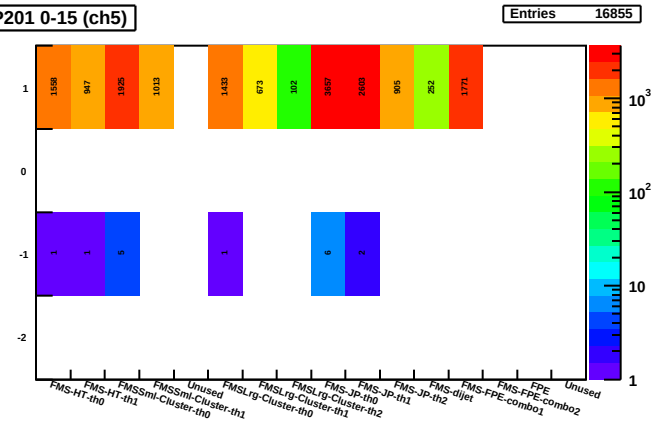

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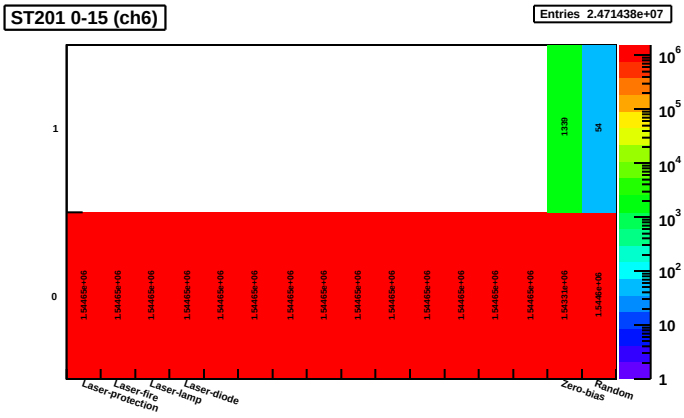

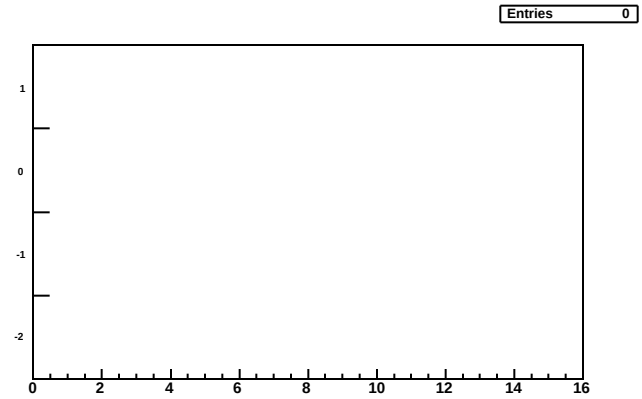
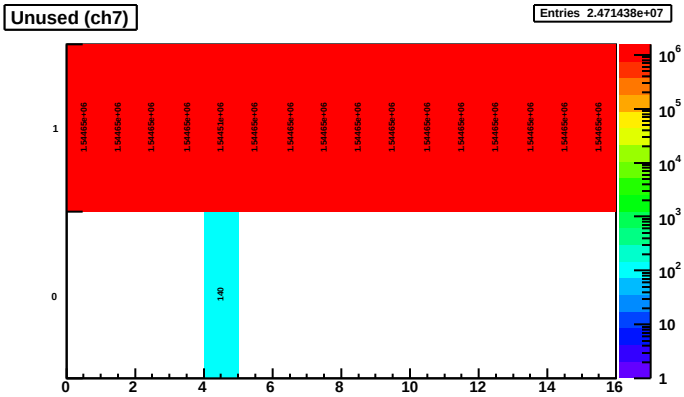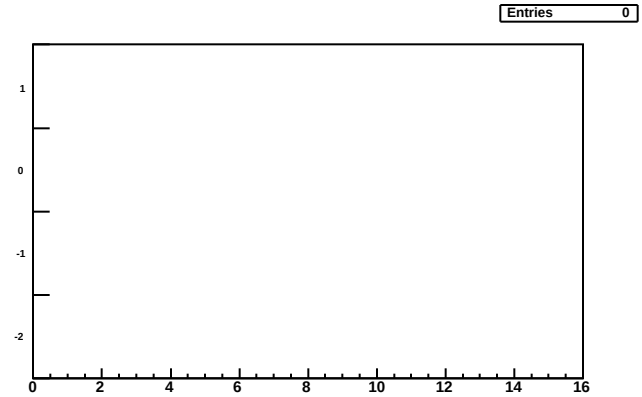
Unused (ch7)