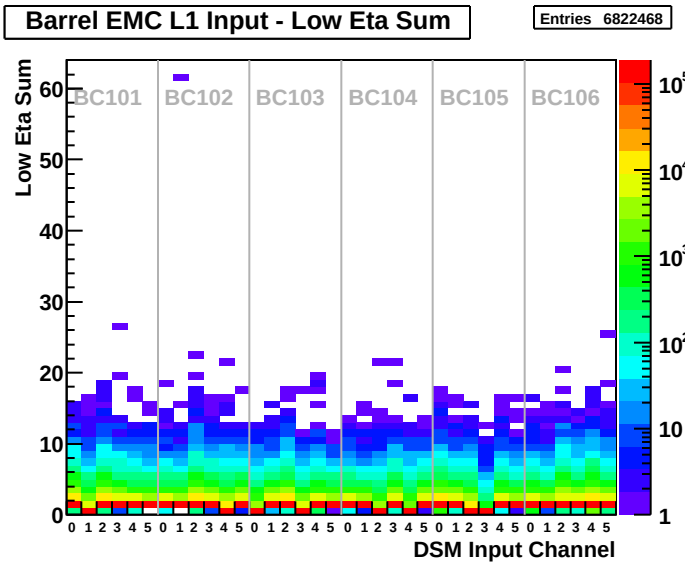
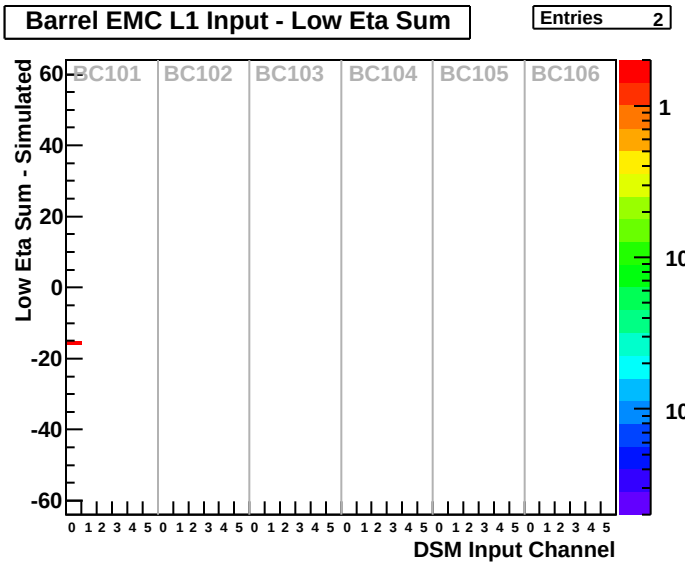
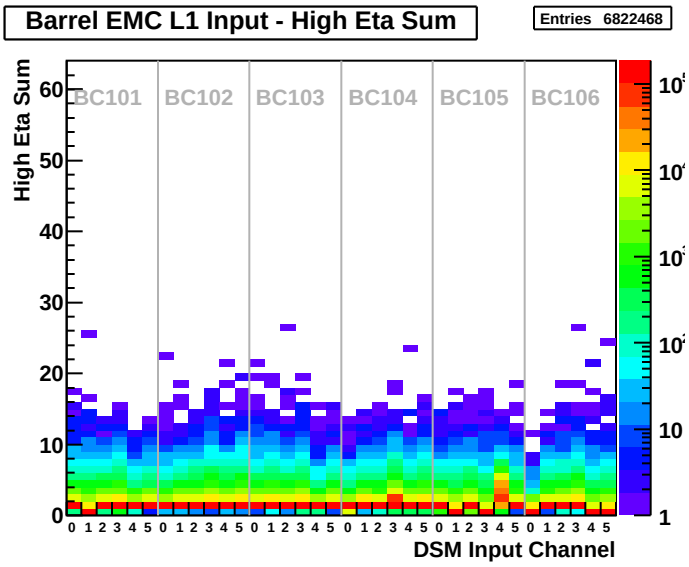
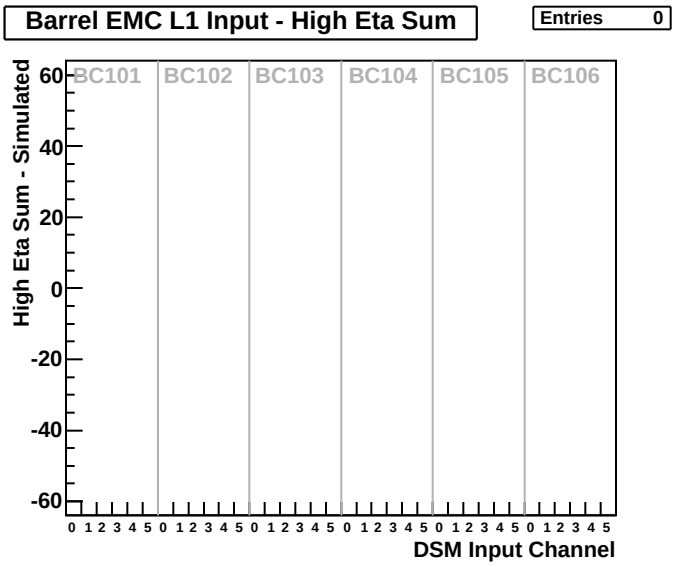
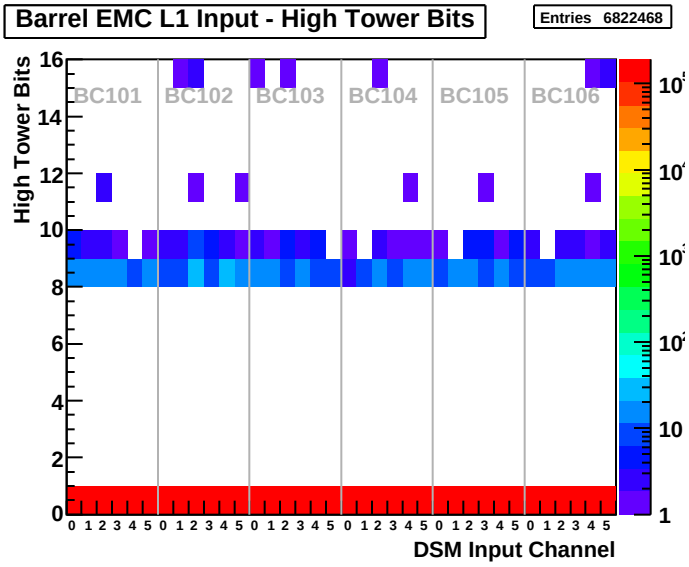
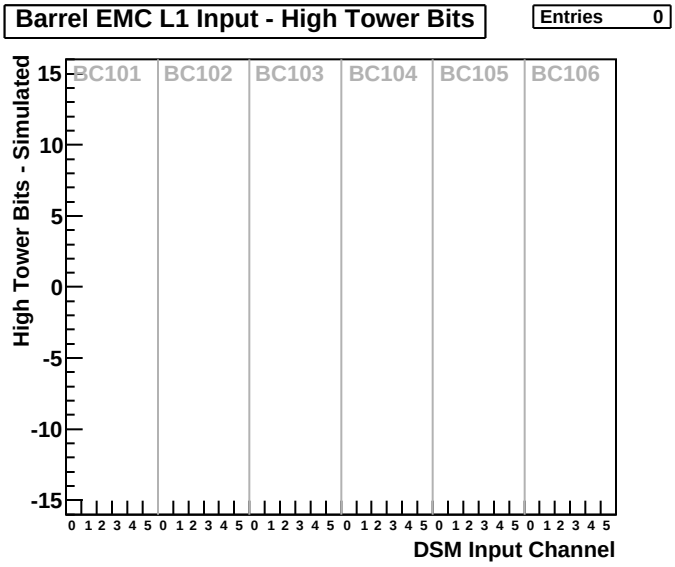

Barrel EMC L1 Input - Low Eta Sum
Entries 6822468
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

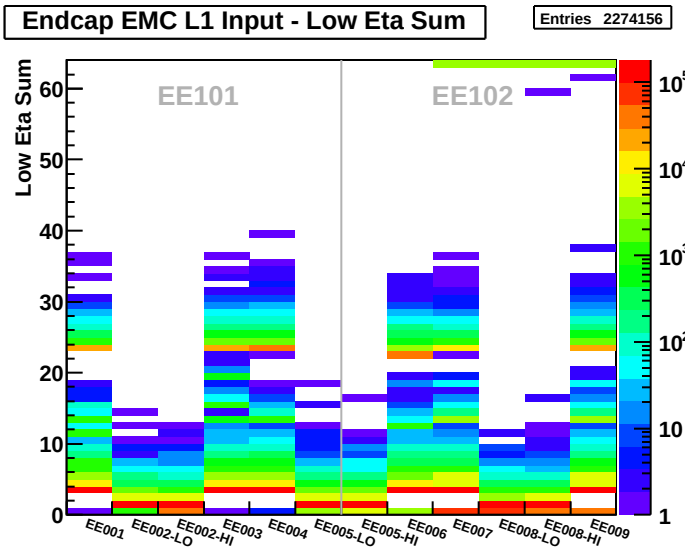

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

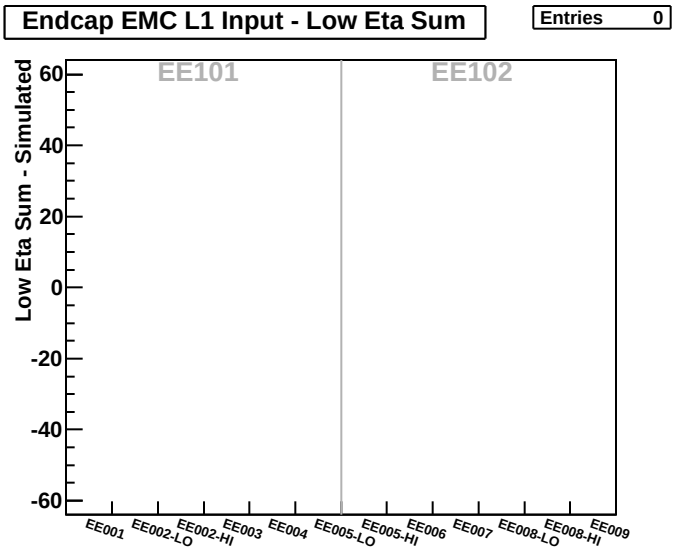

Barrel EMC L1 Input - High Eta Sum
Entries 6822468
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

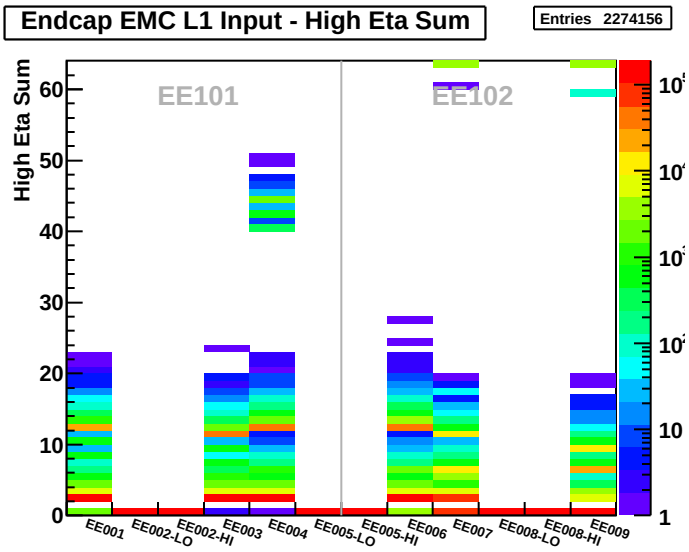

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

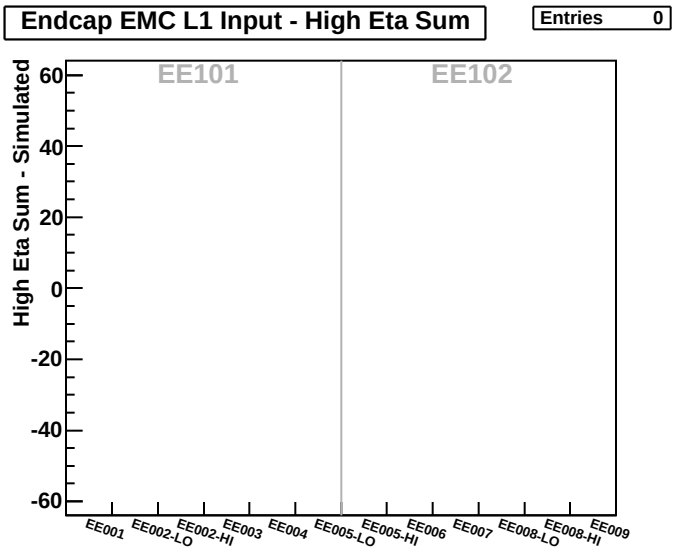

Barrel EMC L1 Input - High Tower Bits
Entries 6822468
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

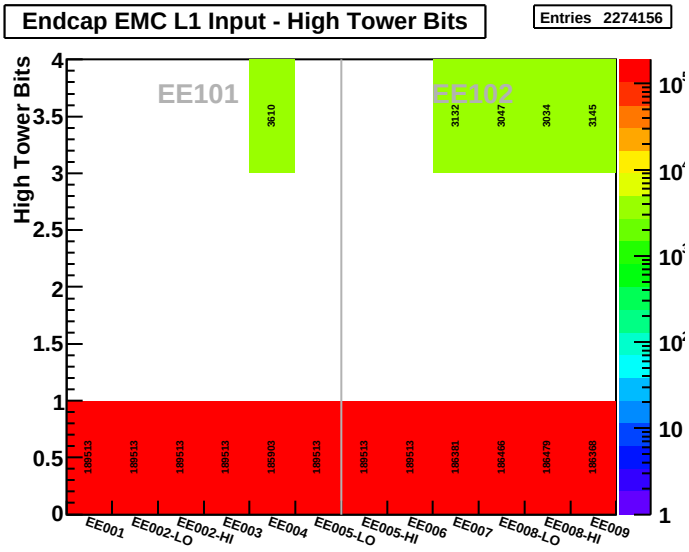

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

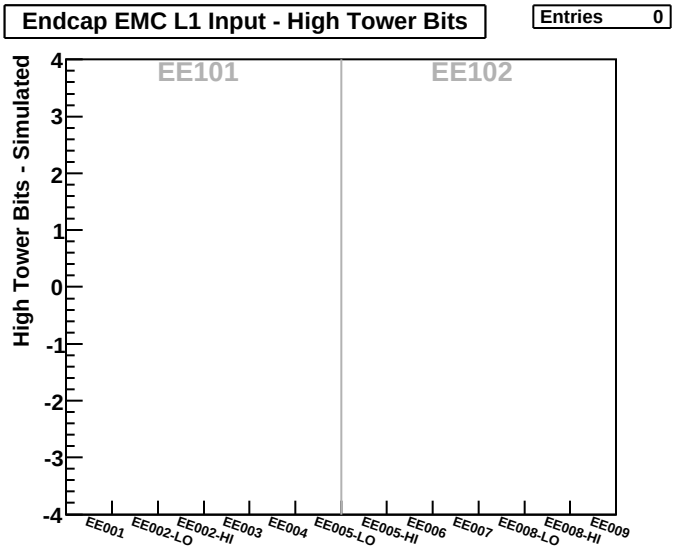

Endcap EMC L1 Input - Low Eta Sum
Entries 2274156
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

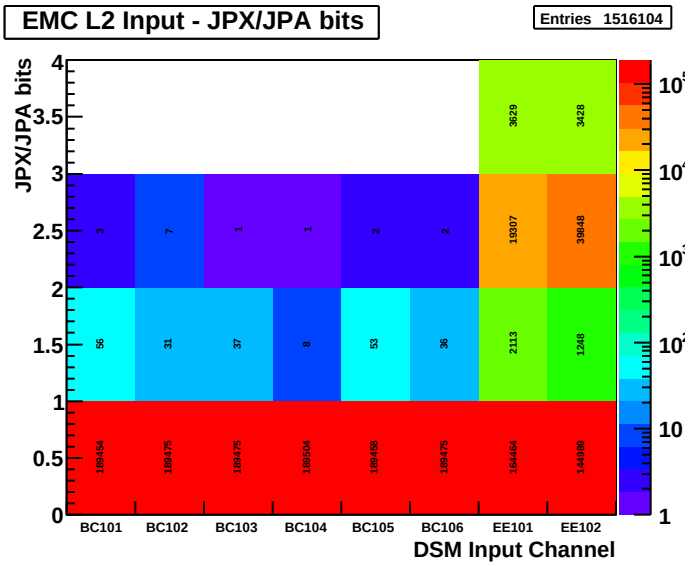

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

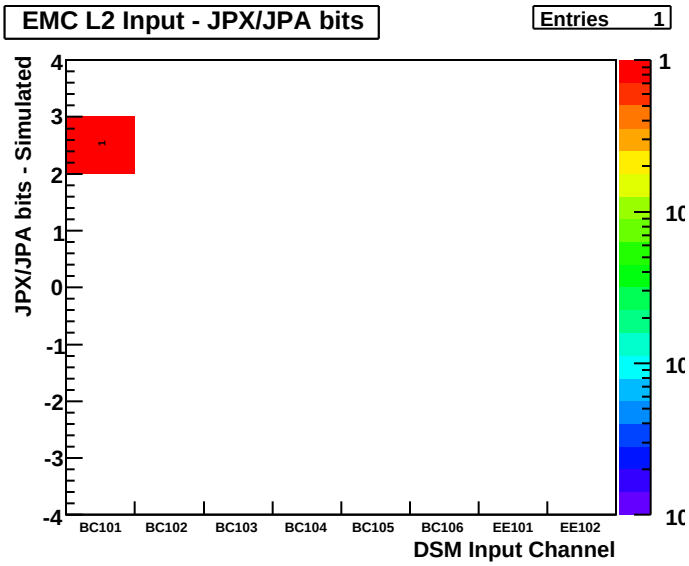

Endcap EMC L1 Input - High Eta Sum
Entries 2274156
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

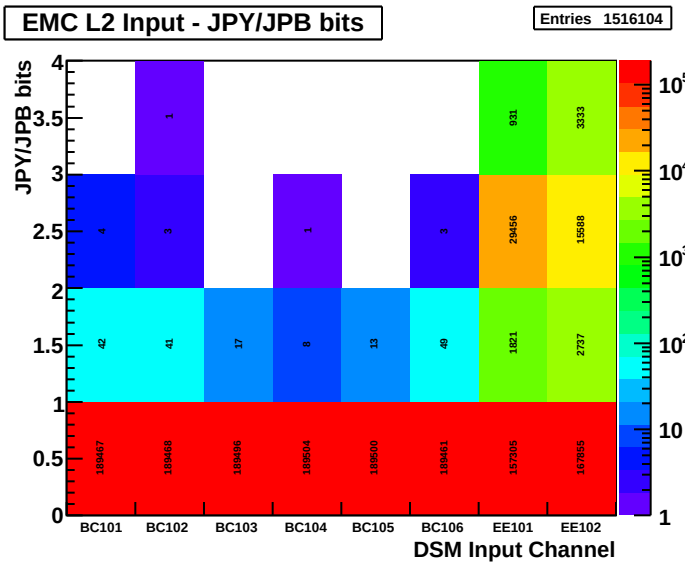

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

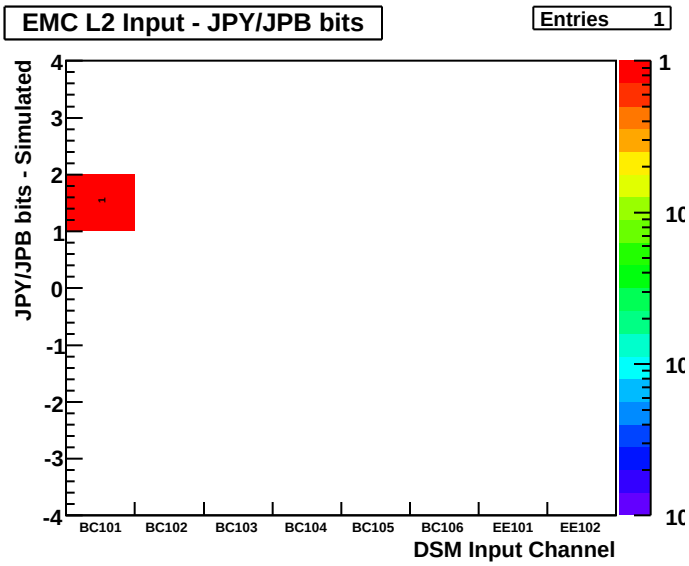

Endcap EMC L1 Input - High Tower Bits
Entries 2274156
High Tower Bits
EE101
EE102
3610
3132
3047
3034
3145
189513
189513
189513
189513
185903
189513
189513
189513
186381
186466
186479
186388
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

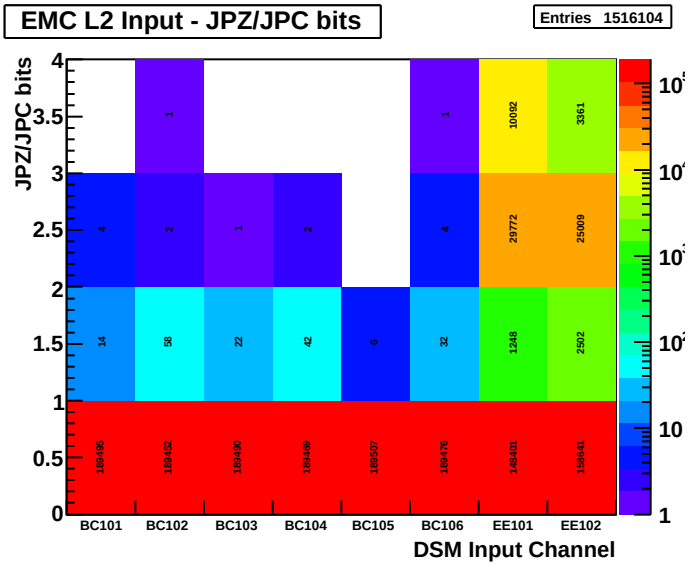

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

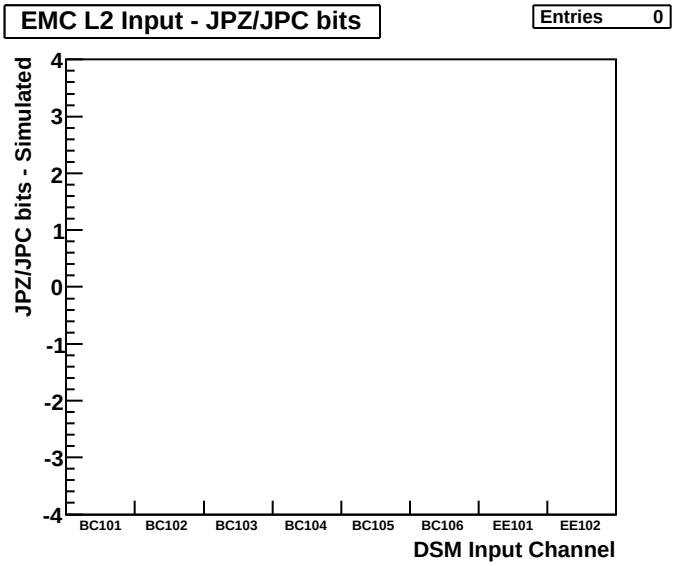

EMC L2 Input - JPX/JPA bits
Entries 1516104
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

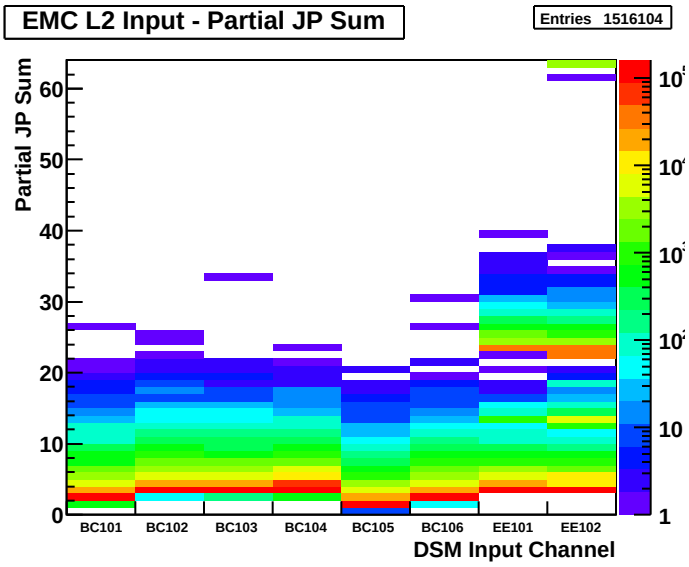

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

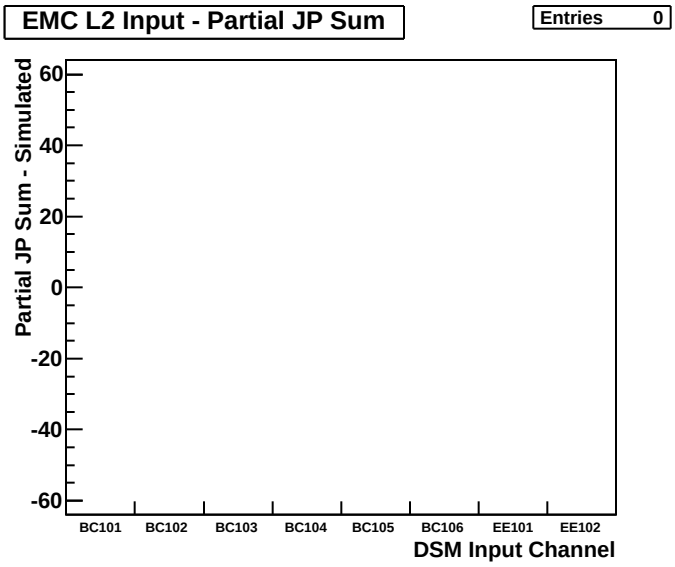

EMC L2 Input - JPY/JPB bits
Entries 1516104
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

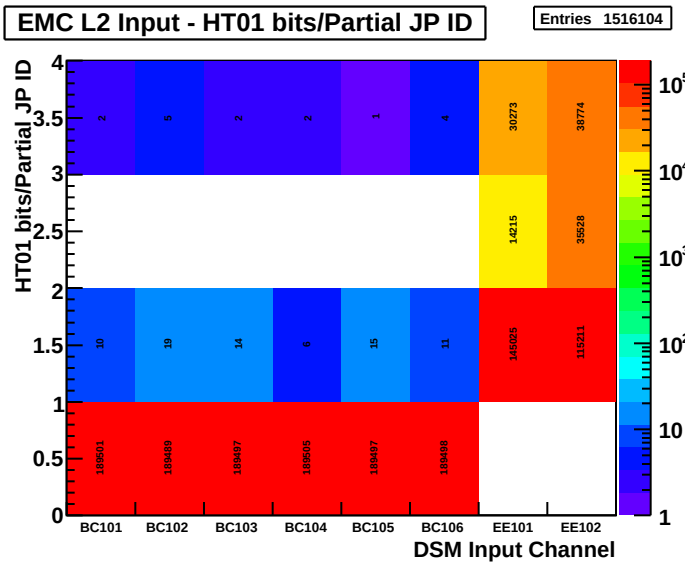

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

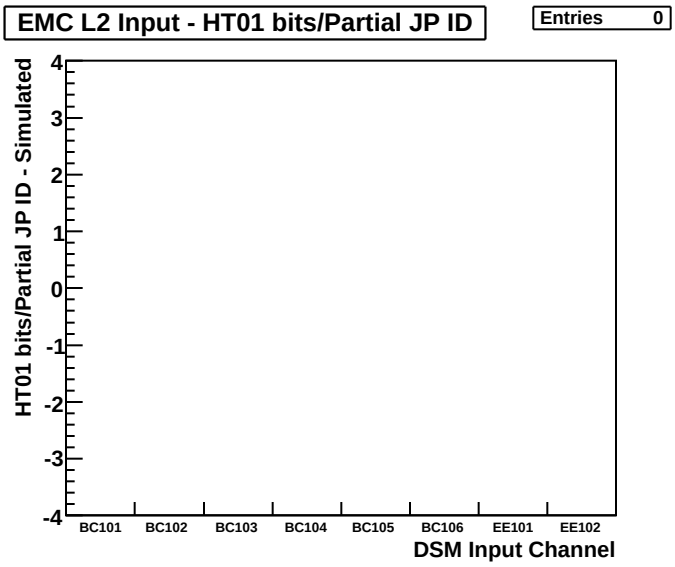

EMC L2 Input - JPZ/JPC bits
Entries 1516104
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

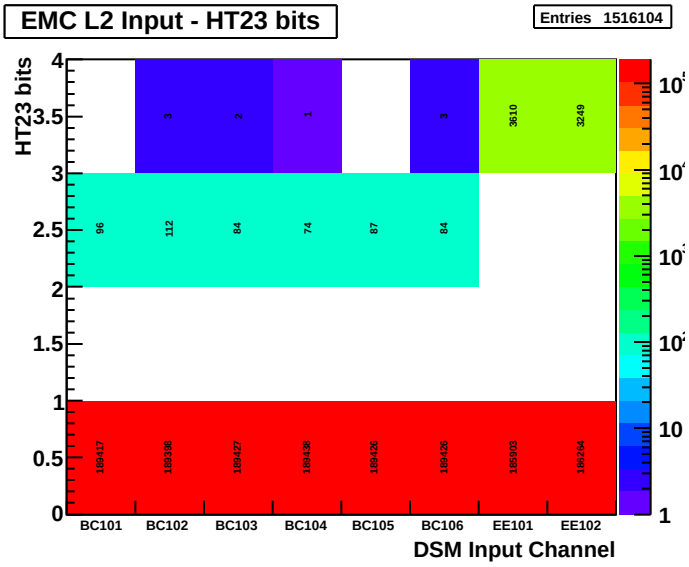

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

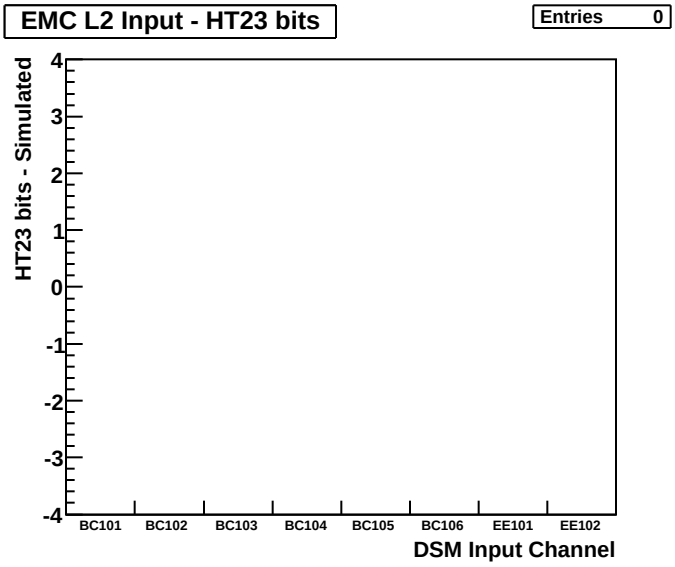

EMC L2 Input - Partial JP Sum
Entries 1516104
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

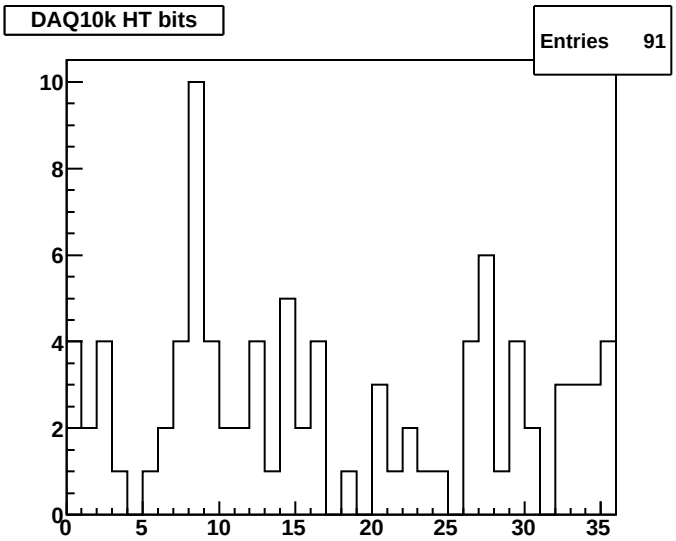

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

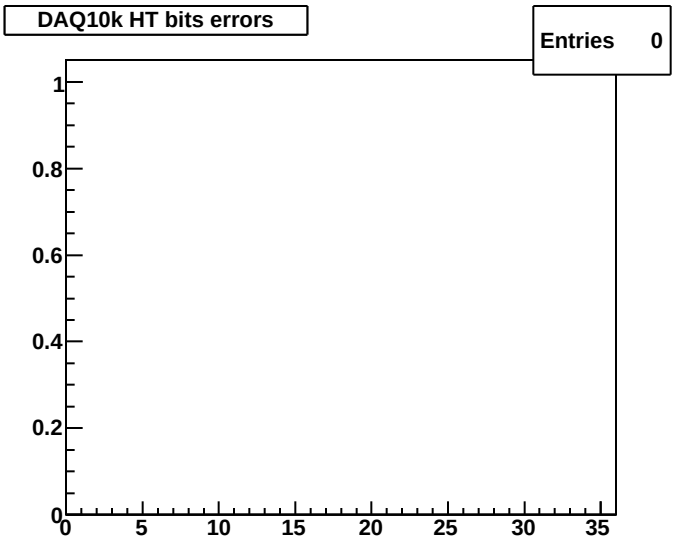

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1516104
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

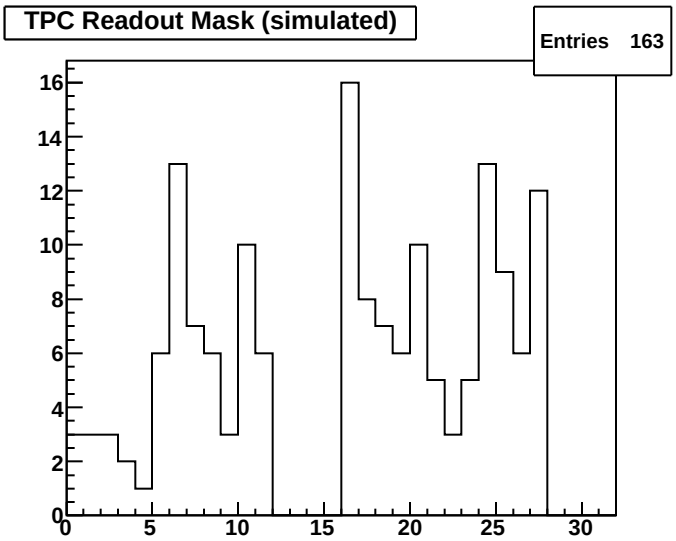

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

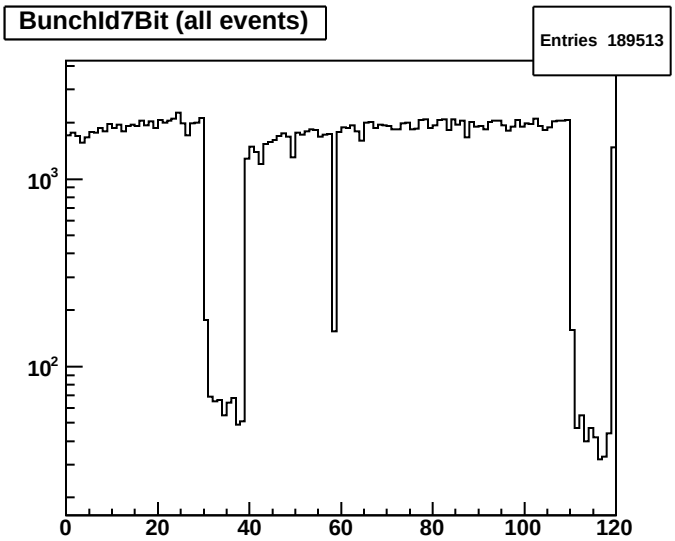

EMC L2 Input - HT23 bits
Entries 1516104
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

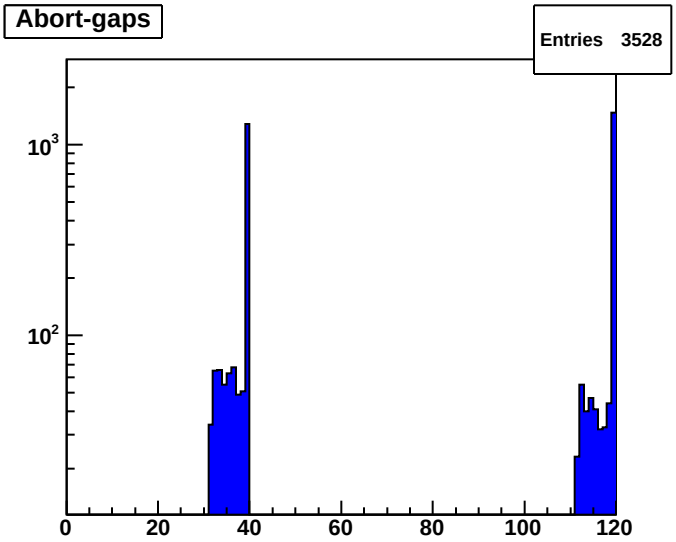

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

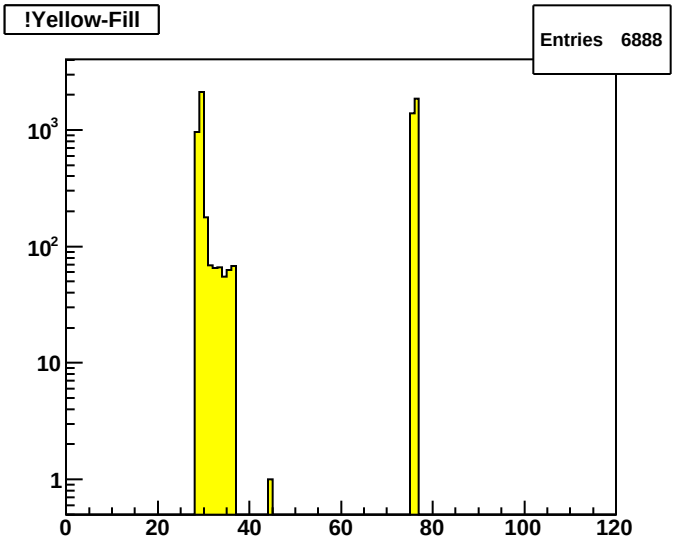

DAQ10k HT bits
Entries 91

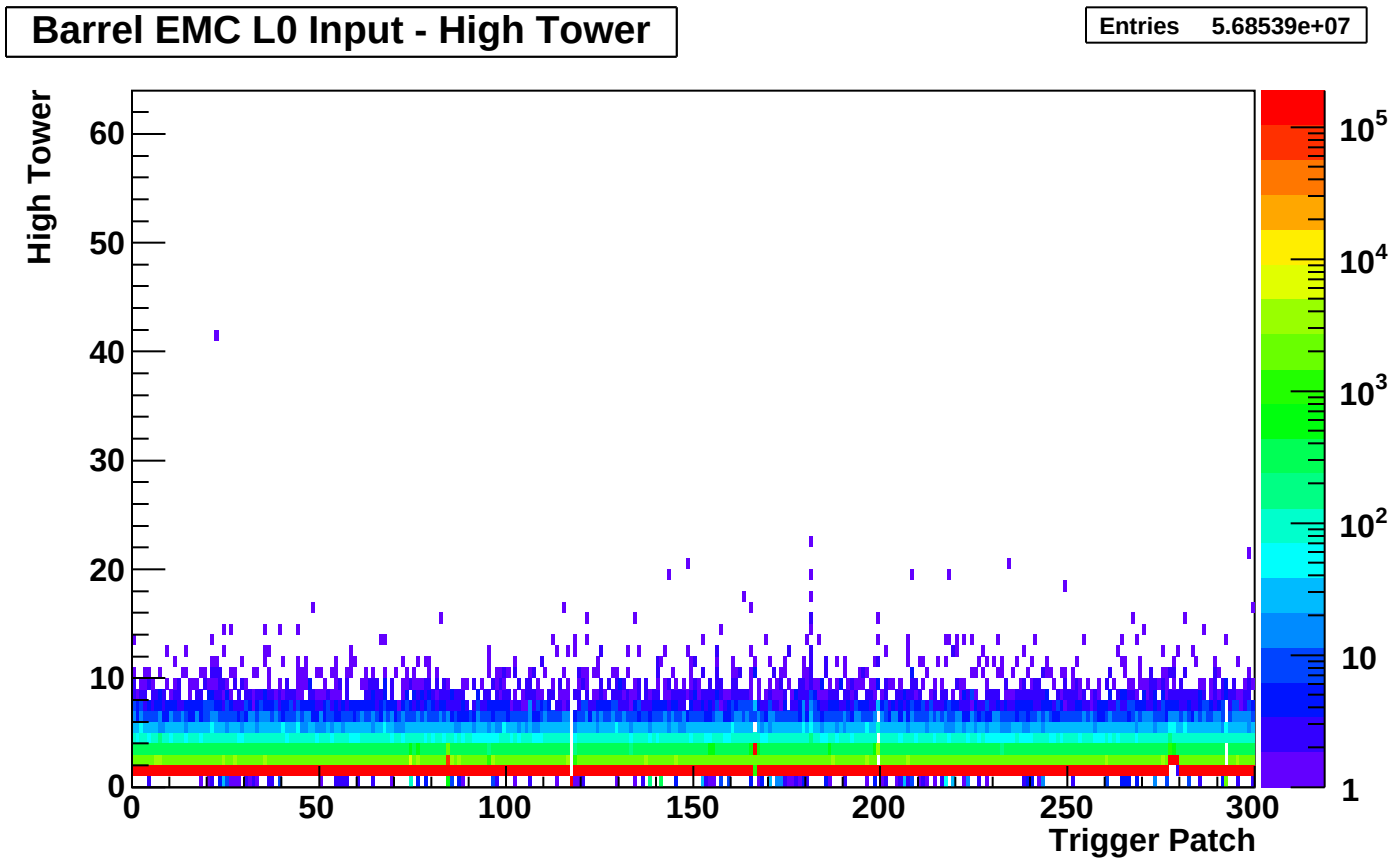

DAQ10k HT bits errors
Entries 0

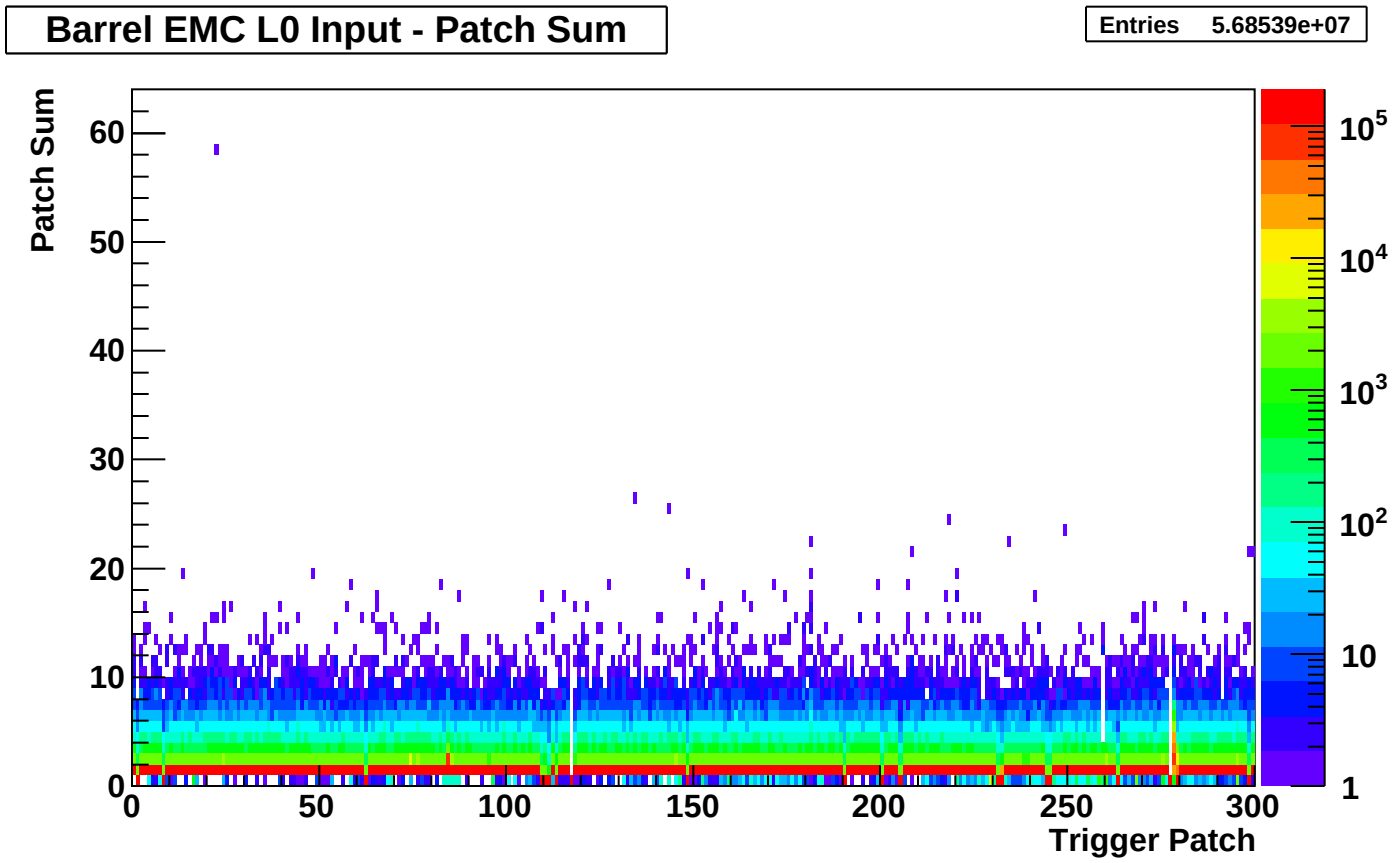

TPC Readout Mask (simulated)
Entries 163

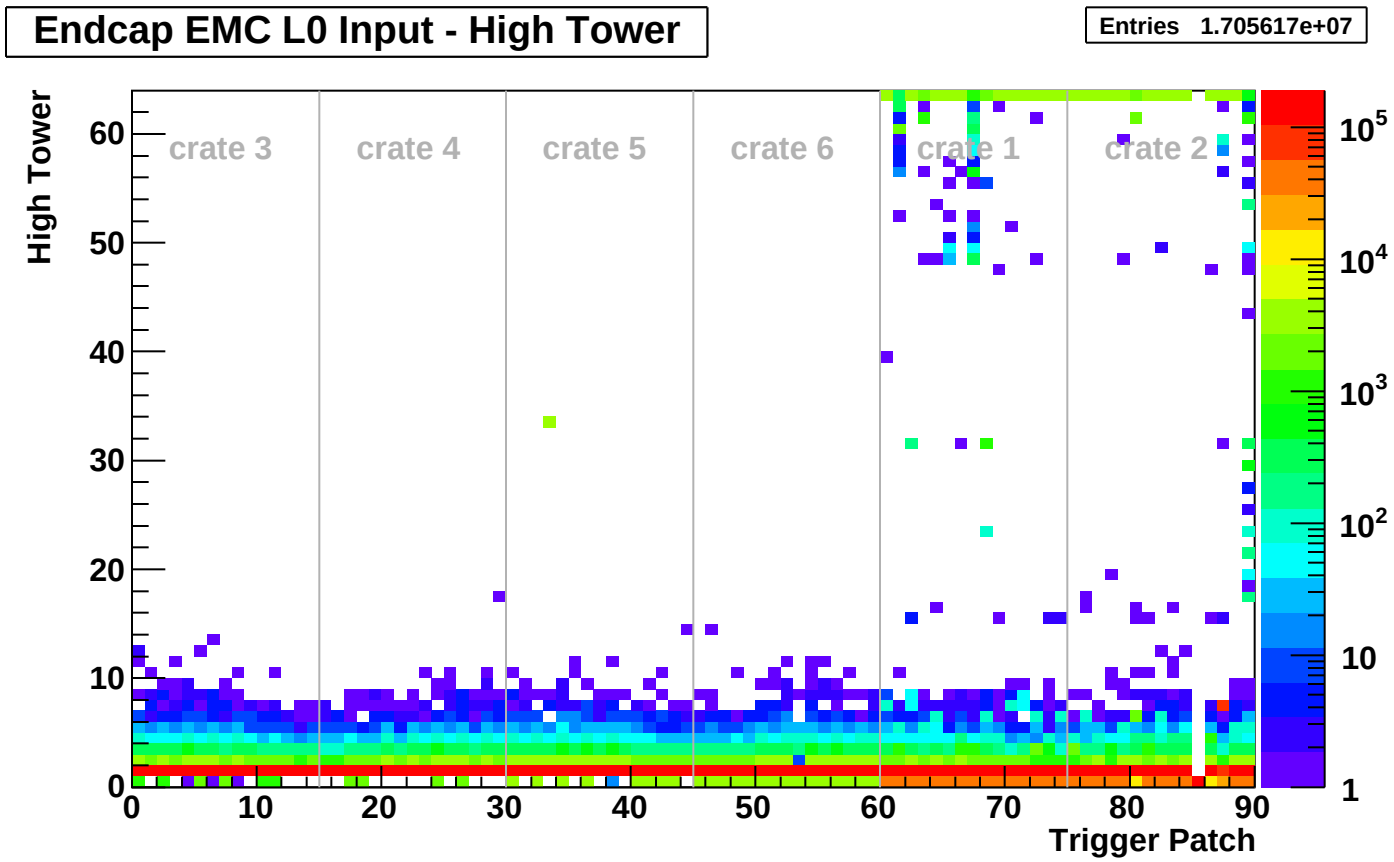

BunchId7Bit (all events)
Entries 189513

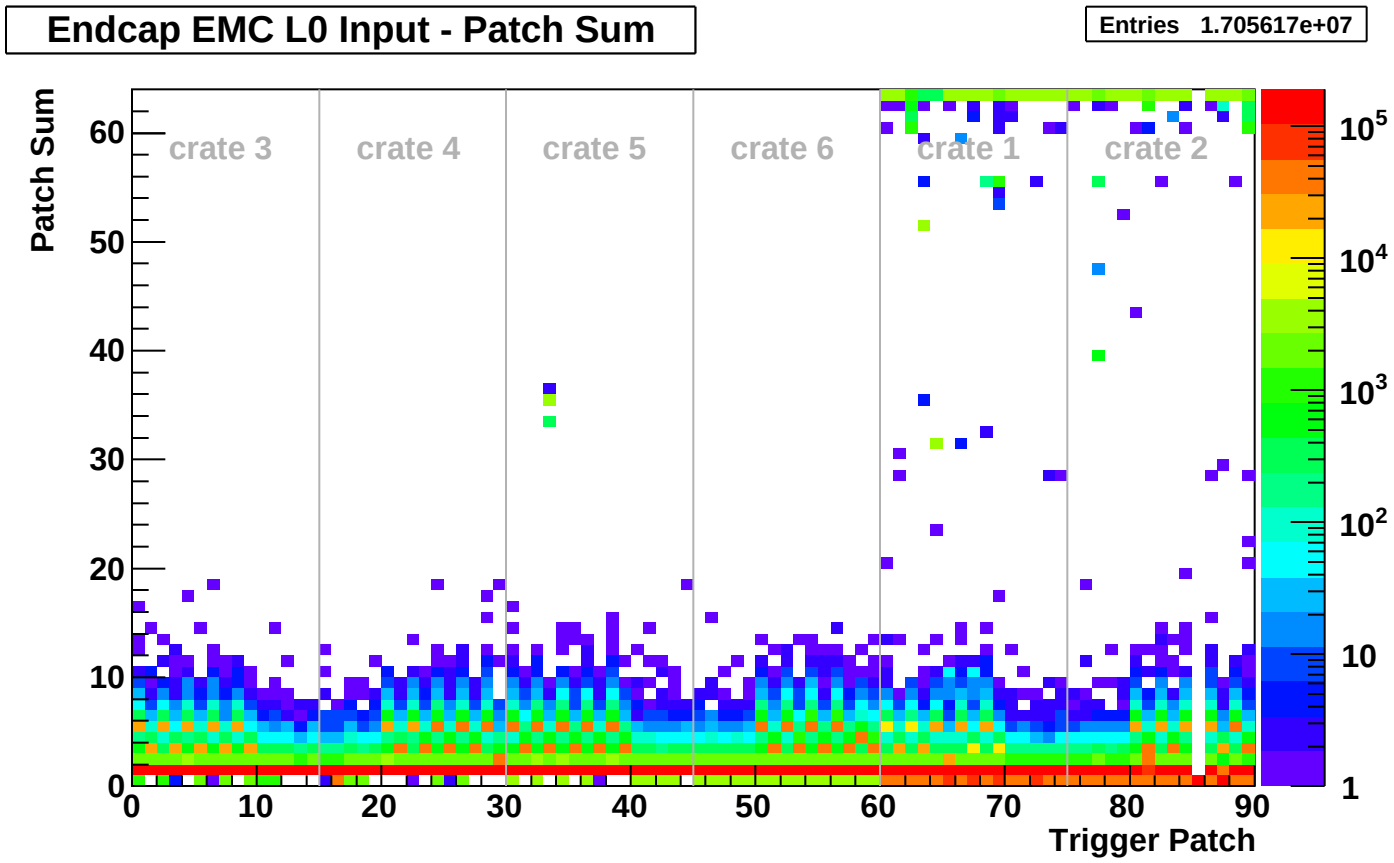

Abort-gaps
Entries 3528

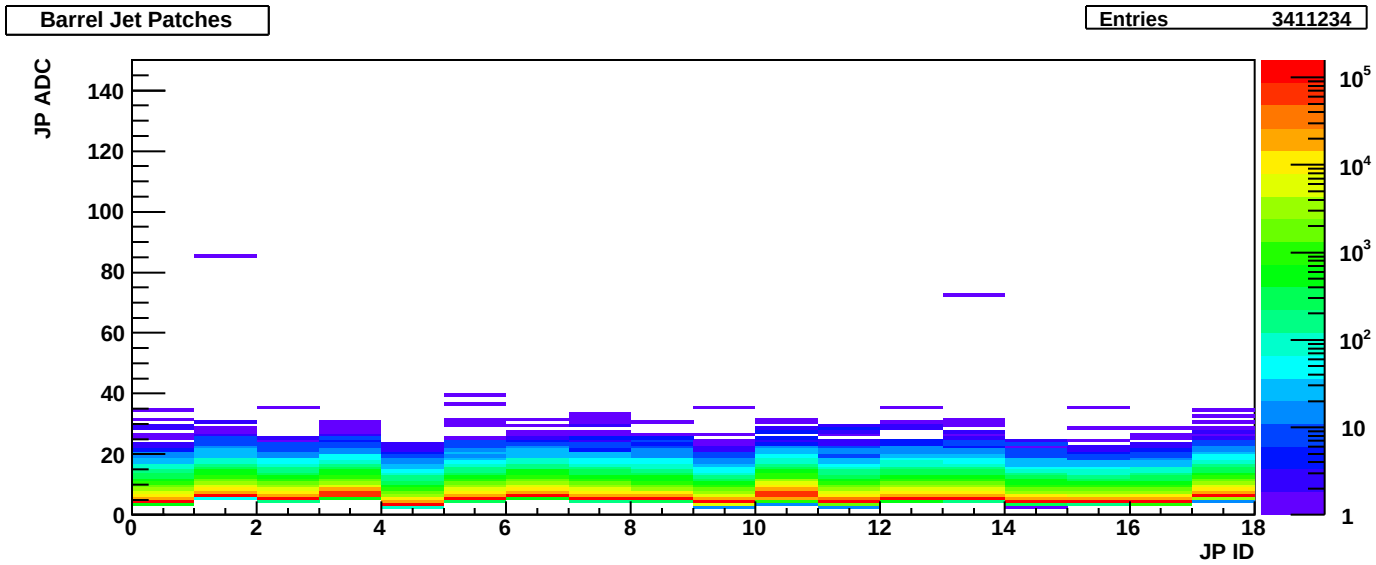

!Yellow-Fill
Entries 6888

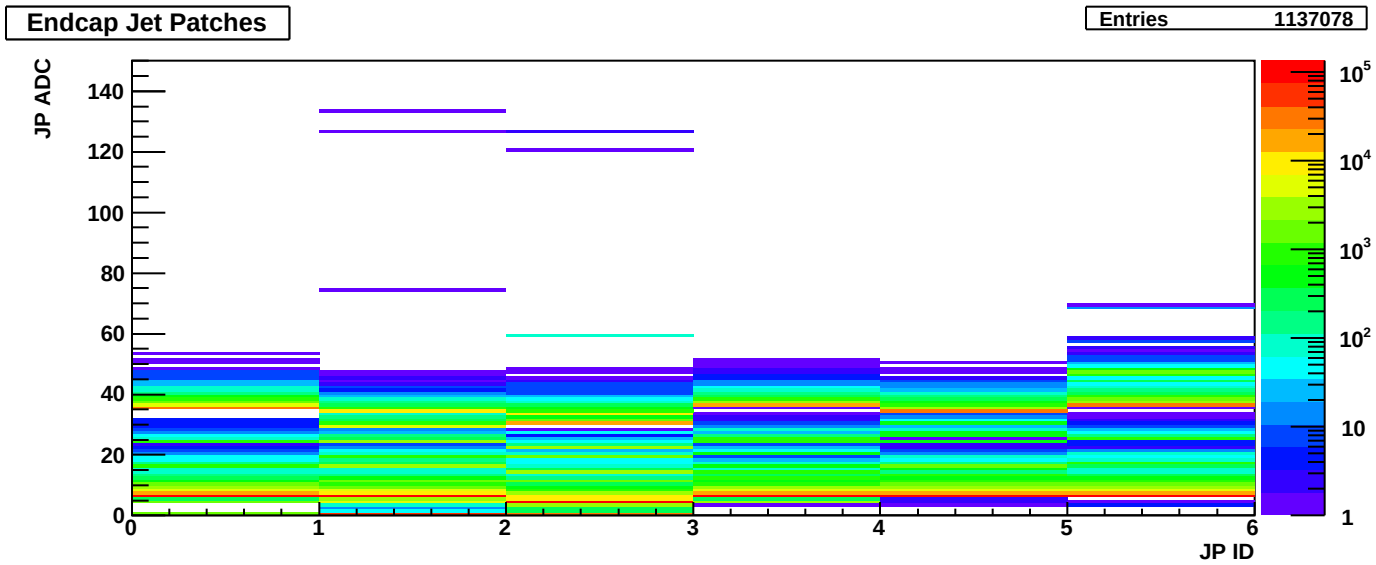

Barrel EMC L0 Input - High Tower
Entries 5.68539e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

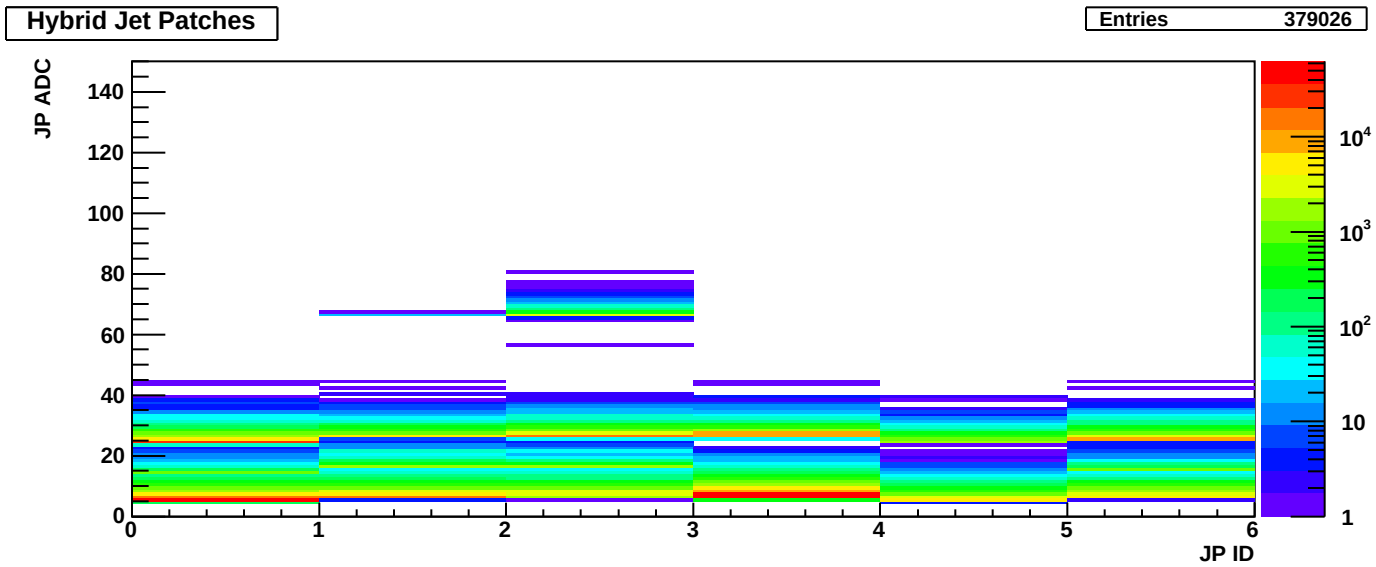

Barrel EMC L0 Input - Patch Sum
Entries 5.68539e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

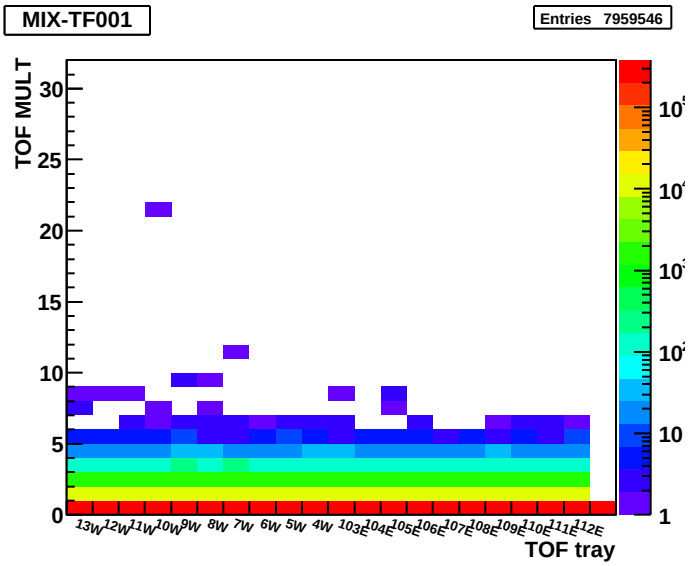

Endcap EMC L0 Input - High Tower
Entries 1.705617e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

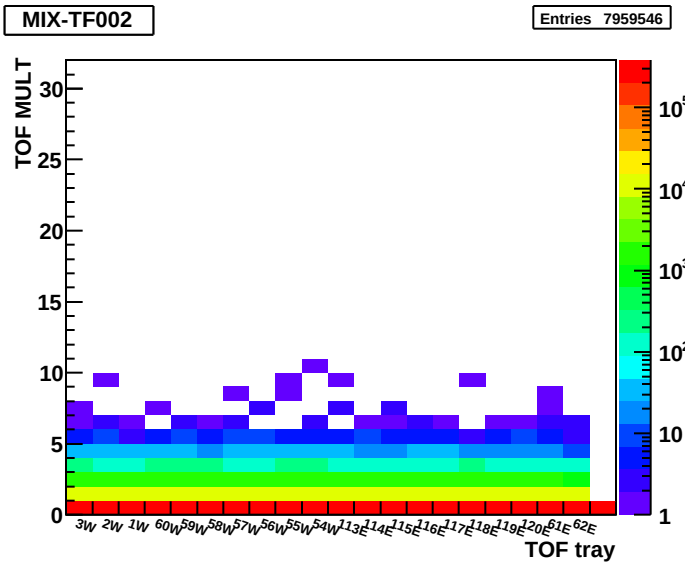

Endcap EMC L0 Input - Patch Sum
Entries 1.705617e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

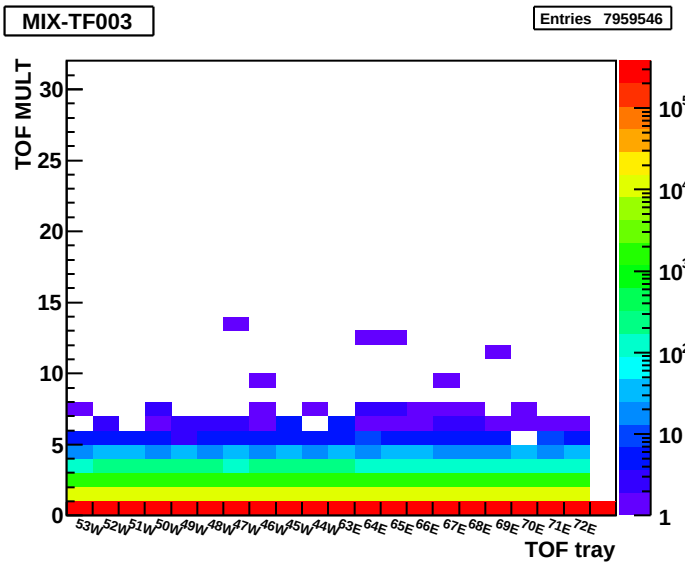

Barrel Jet Patches
Entries 3411234
JP ADC
JP ID

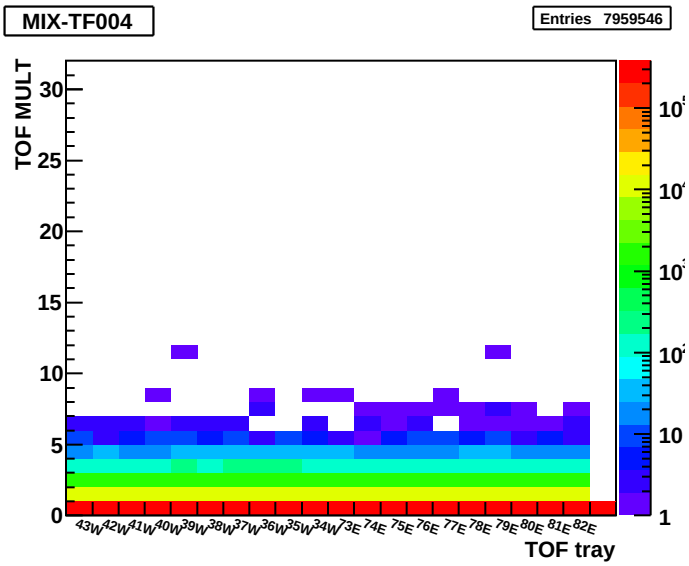

Endcap Jet Patches
Entries 1137078
JP ADC
JP ID

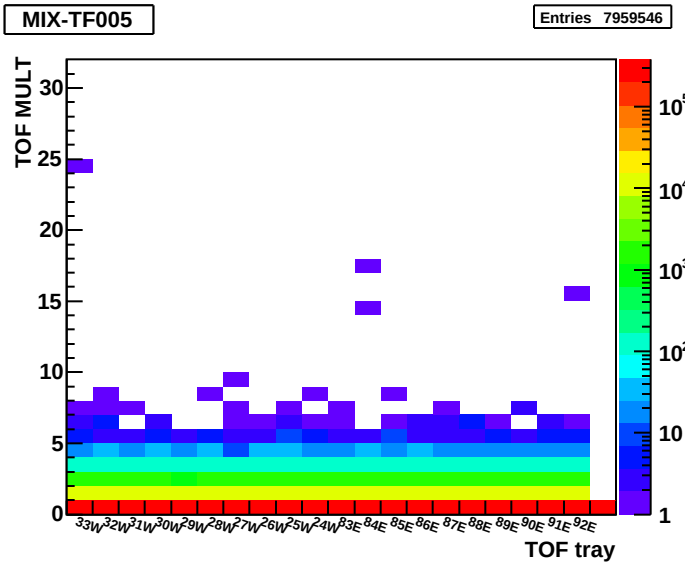

Hybrid Jet Patches
Entries 379026
JP ADC
JP ID

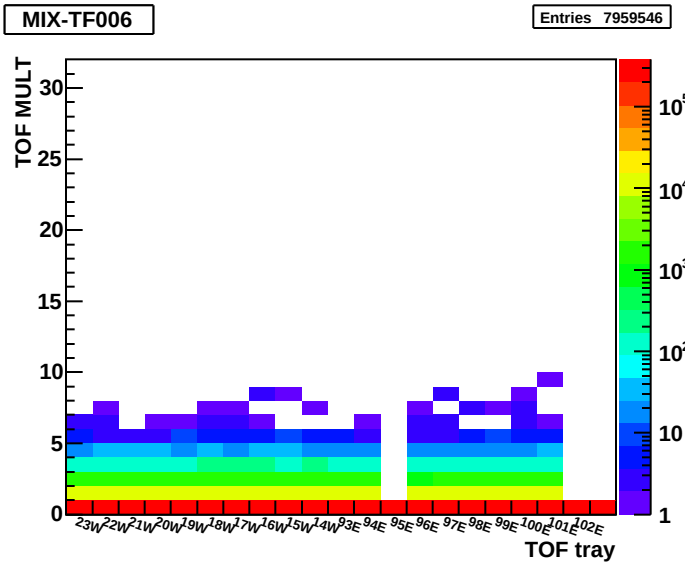

MIX-TF001
Entries 7959546
TOF MULT
TOF tray

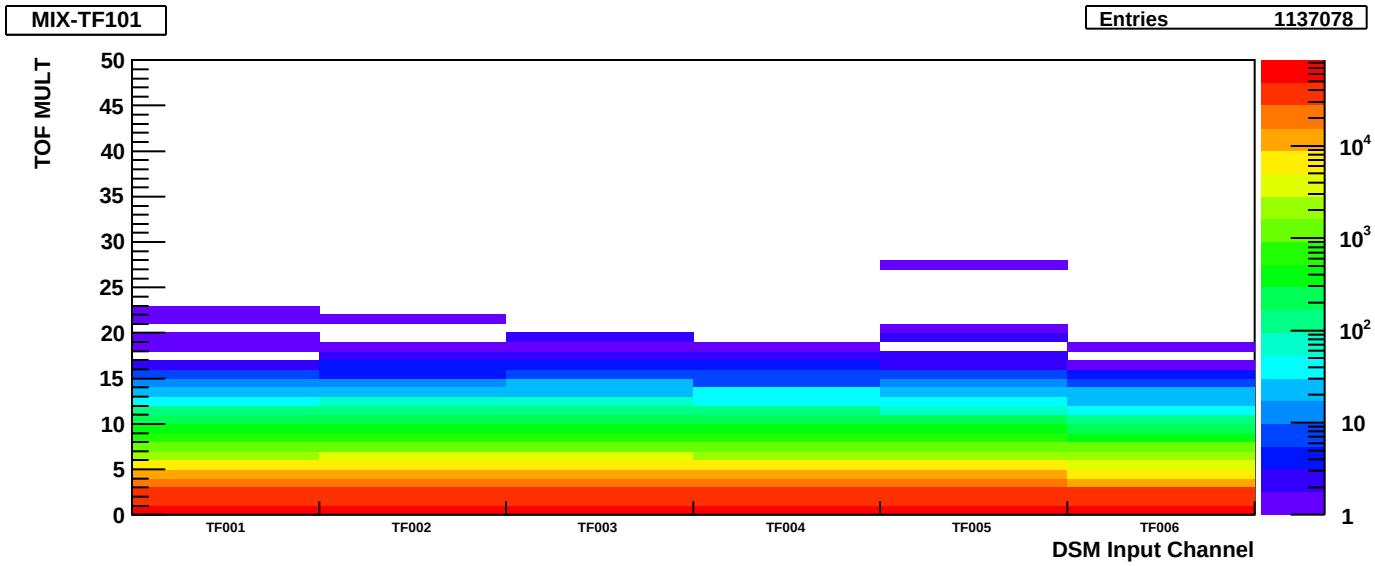

MIX-TF002
Entries 7959546
TOF MULT
TOF tray

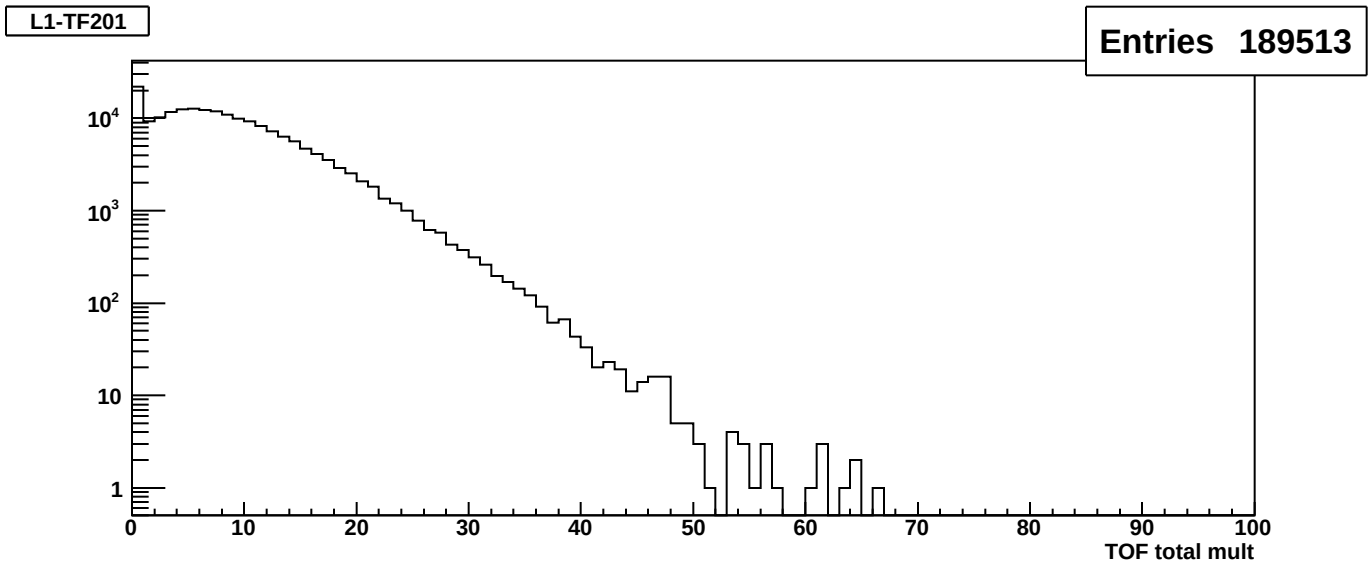

MIX-TF003
Entries 7959546
TOF MULT
TOF tray

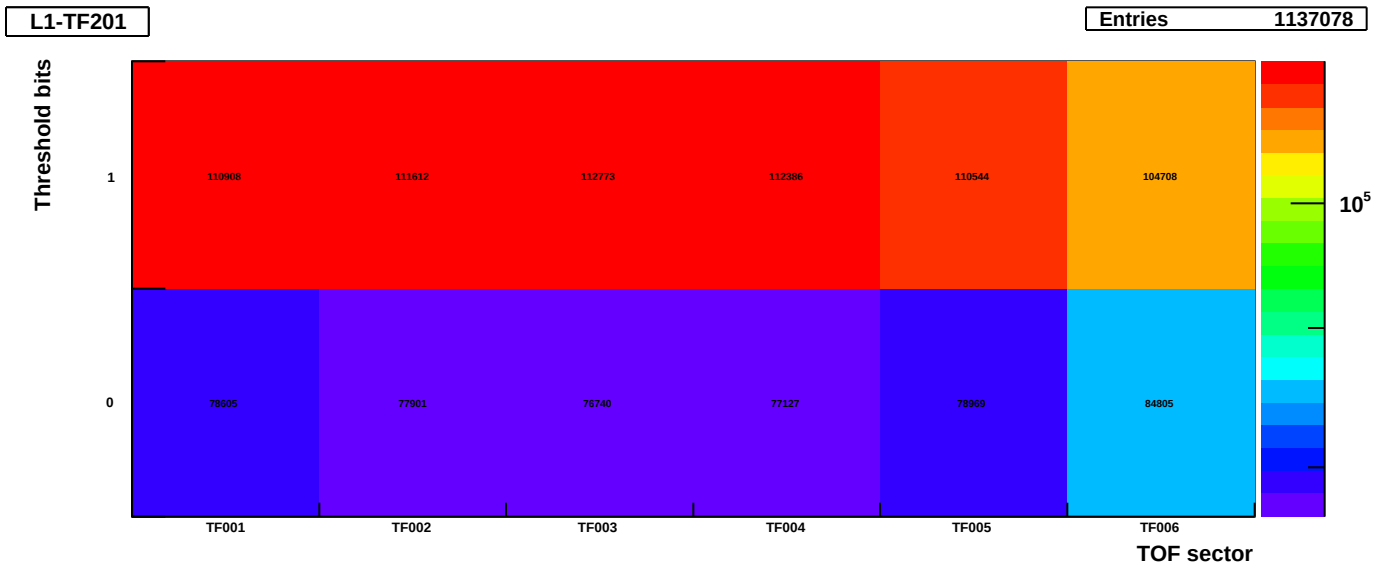

MIX-TF004
Entries 7959546
TOF MULT
TOF tray

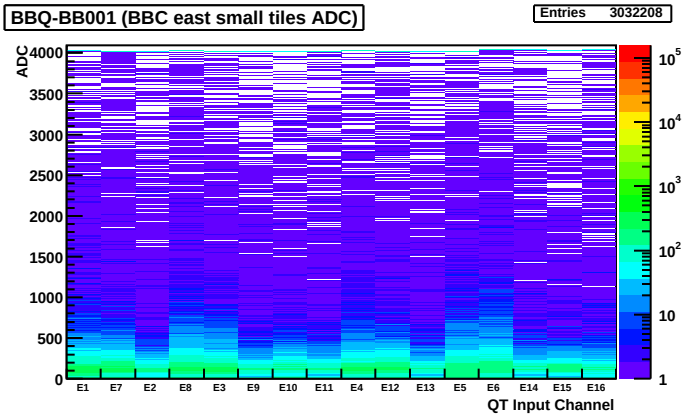

MIX-TF005
Entries 7959546
TOF MULT
TOF tray

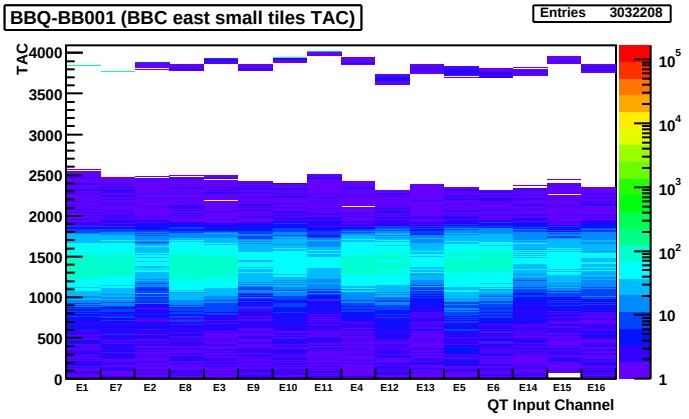

MIX-TF006
Entries 7959546
TOF MULT
TOF tray

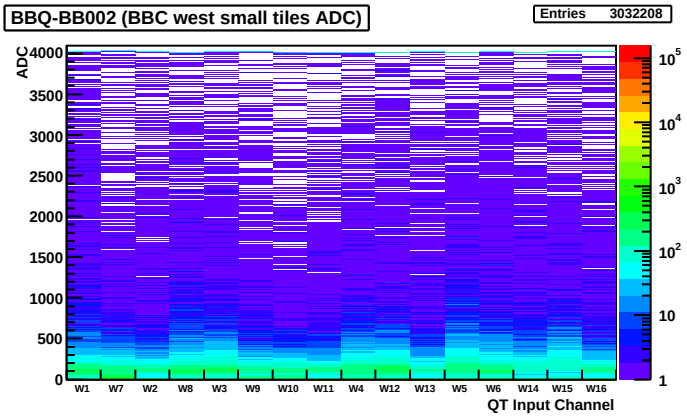

MIX-TF101
Entries 1137078
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

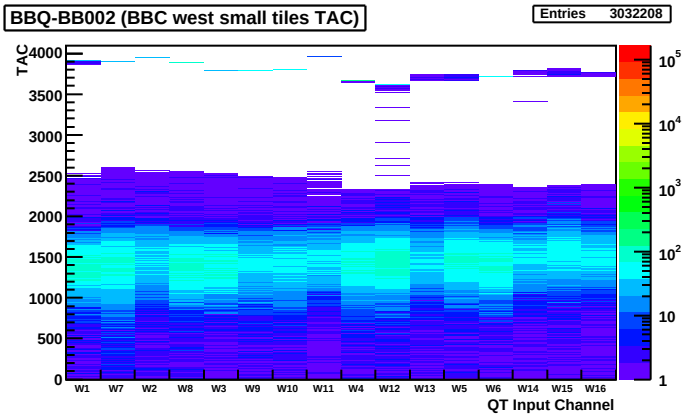

L1-TF201
Entries 189513
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

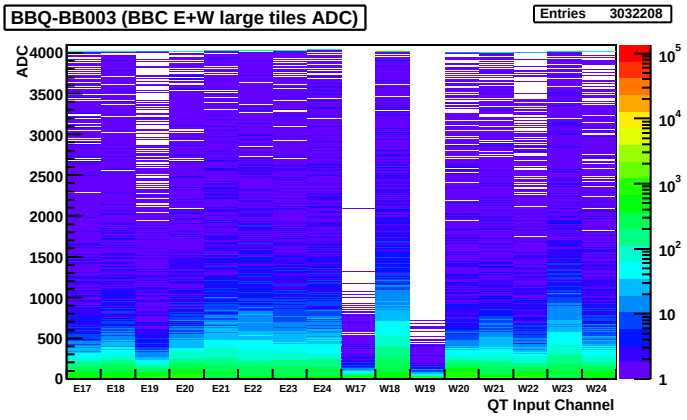

L1-TF201
Entries 1137078
Threshold bits
1
0
110908
111612
112773
112386
110544
104708
78605
77901
76740
77127
78969
84805
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

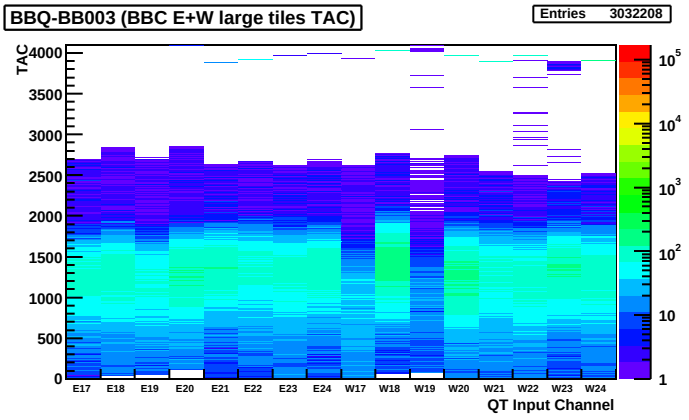

BBQ-BB001 (BBC east small tiles ADC)
Entries 3032208
ADC
QT Input Channel

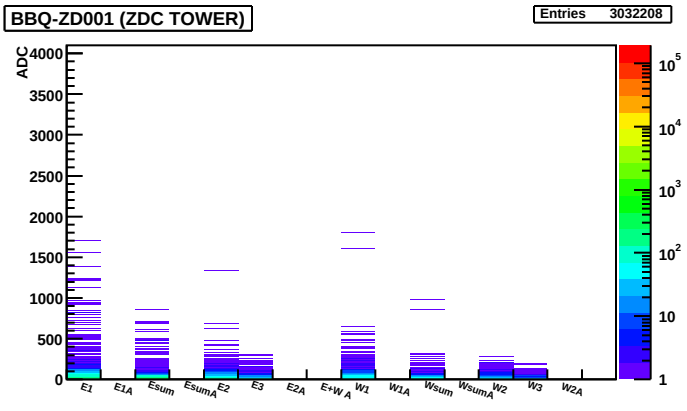

BBQ-BB001 (BBC east small tiles TAC)
Entries 3032208
TAC
QT Input Channel

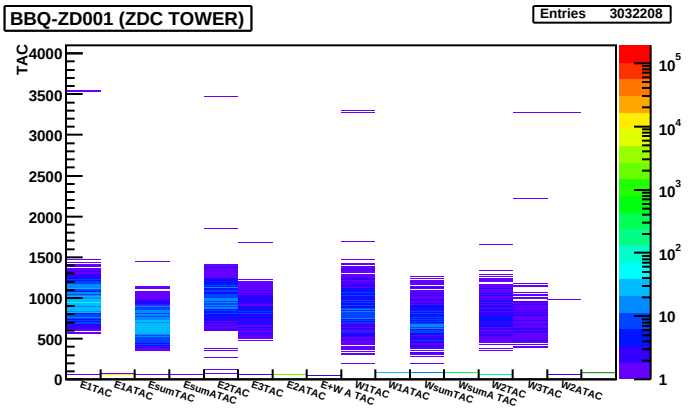

BBQ-BB002 (BBC west small tiles ADC)
Entries 3032208
ADC
QT Input Channel

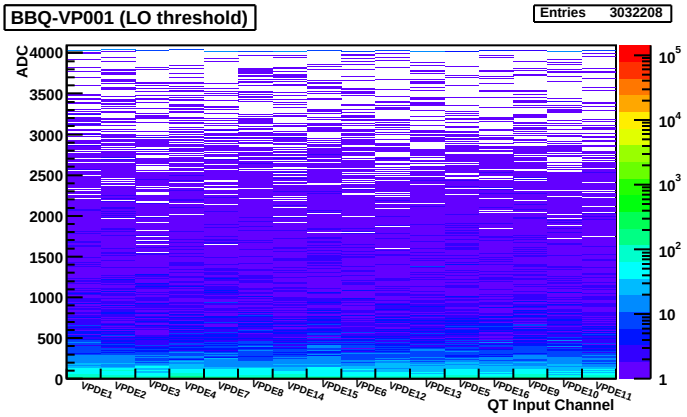

BBQ-BB002 (BBC west small tiles TAC)
Entries 3032208
TAC
QT Input Channel

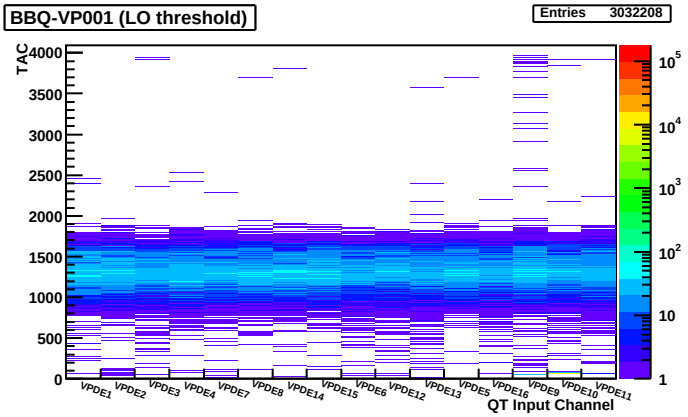

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3032208
ADC
QT Input Channel

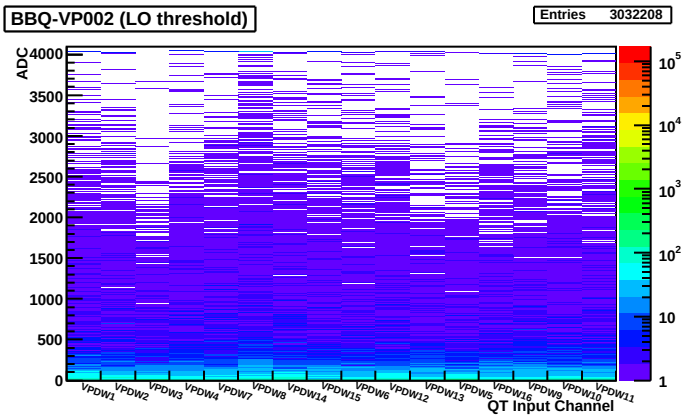

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3032208
TAC
QT Input Channel

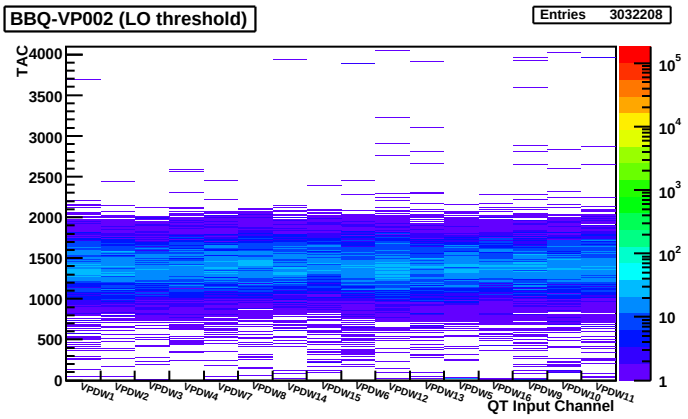

BBQ-ZD001 (ZDC TOWER)
Entries 3032208
ADC

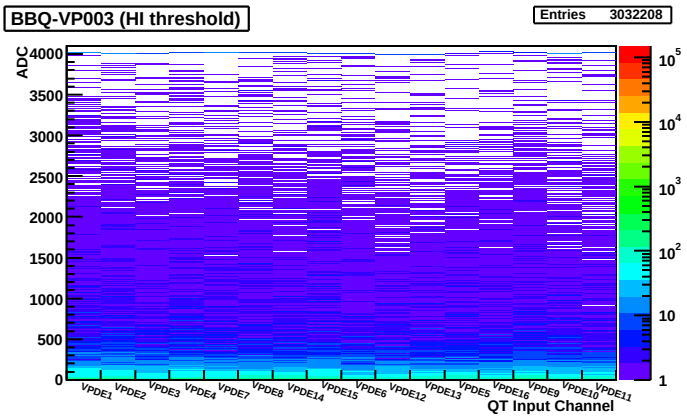

BBQ-ZD001 (ZDC TOWER)
Entries 3032208
TAC

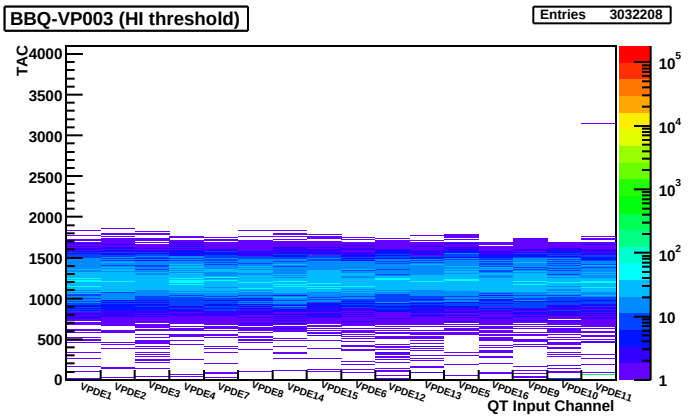

BBQ-VP001 (LO threshold)
Entries 3032208
ADC
QT Input Channel

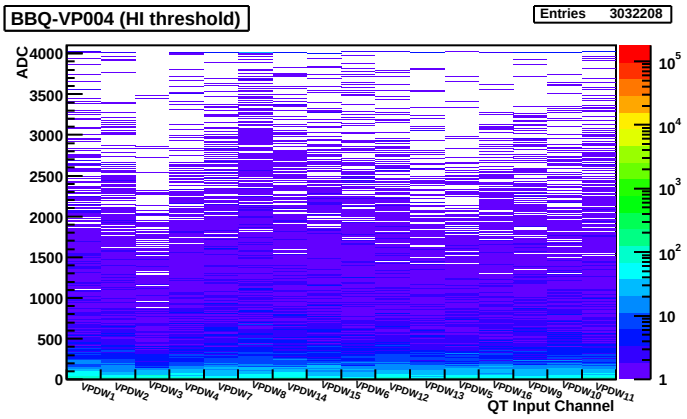

BBQ-VP001 (LO threshold)
Entries 3032208
TAC
QT Input Channel

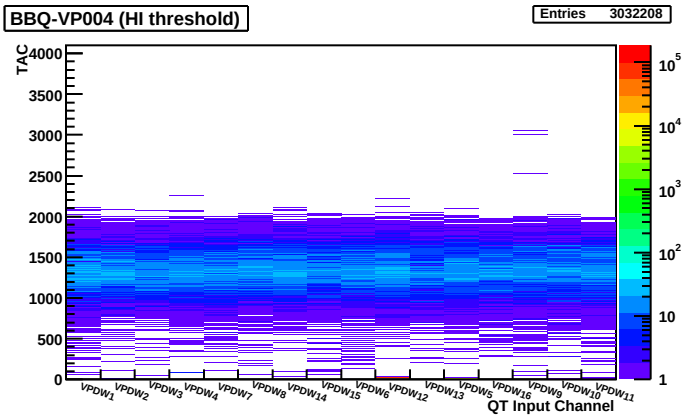

BBQ-VP002 (LO threshold)
Entries 3032208
ADC
QT Input Channel

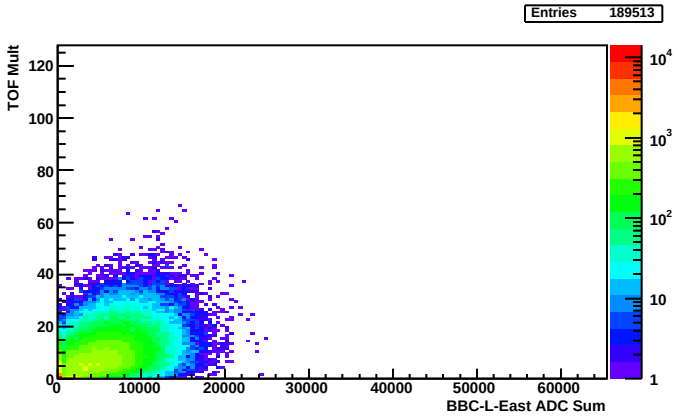

BBQ-VP002 (LO threshold)
Entries 3032208
TAC
QT Input Channel

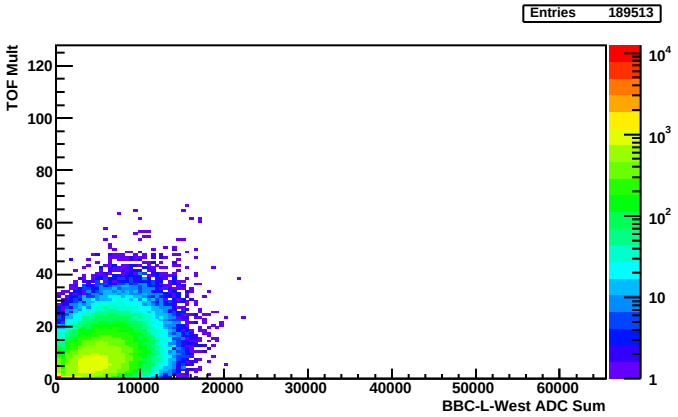

BBQ-VP003 (HI threshold)
Entries 3032208
ADC
QT Input Channel

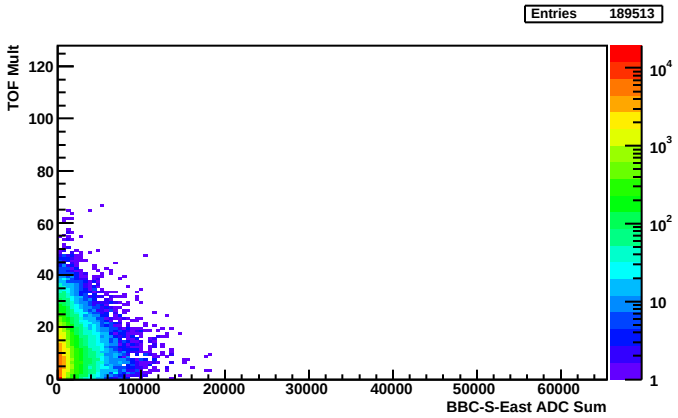

BBQ-VP003 (HI threshold)
Entries 3032208
TAC
QT Input Channel

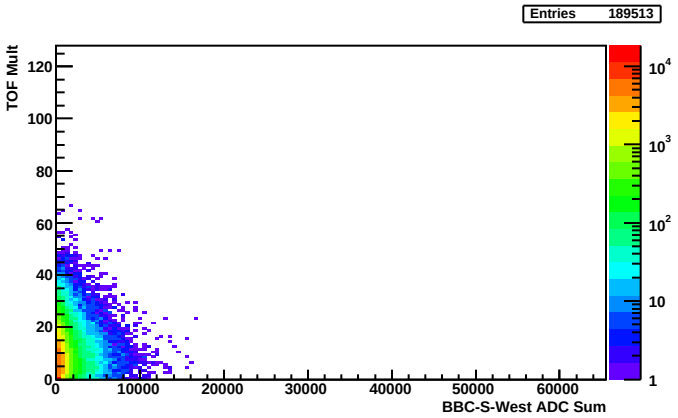

BBQ-VP004 (HI threshold)
Entries 3032208
ADC
QT Input Channel

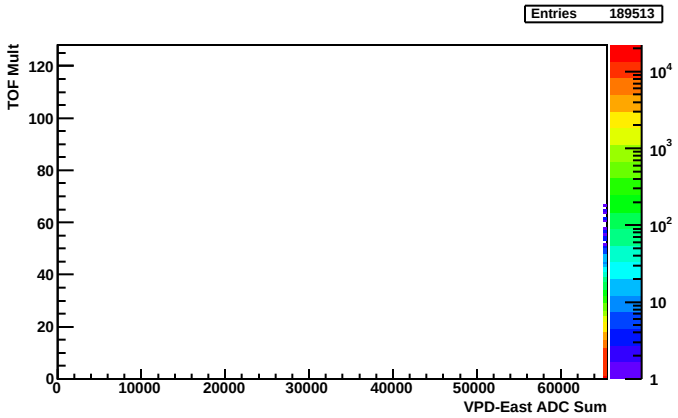

BBQ-VP004 (HI threshold)
Entries 3032208
TAC
QT Input Channel

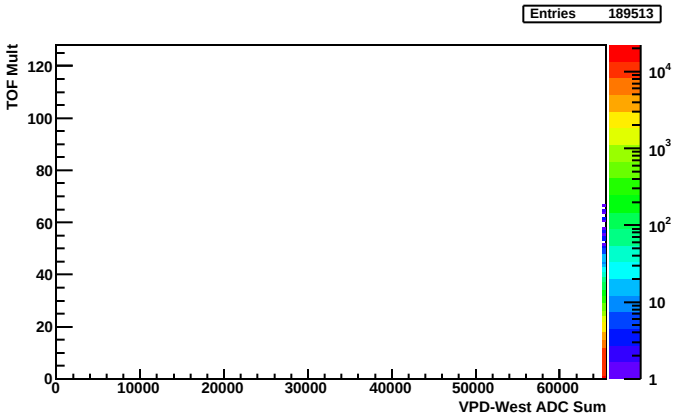

Entries 189513
TOF Mult
BBC-L-East ADC Sum

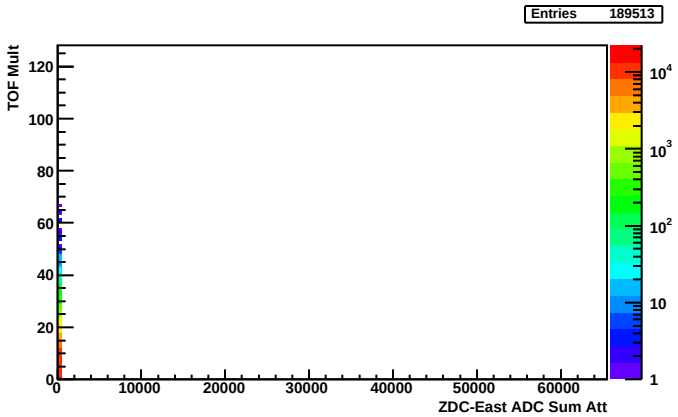

Entries 189513
TOF Mult
BBC-L-West ADC Sum

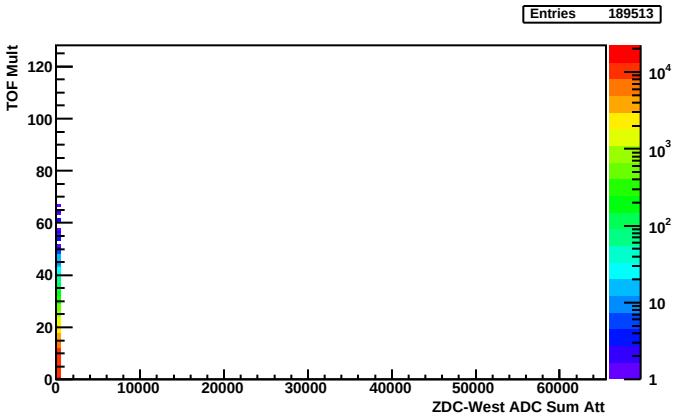

Entries 189513
TOF Mult
BBC-S-East ADC Sum

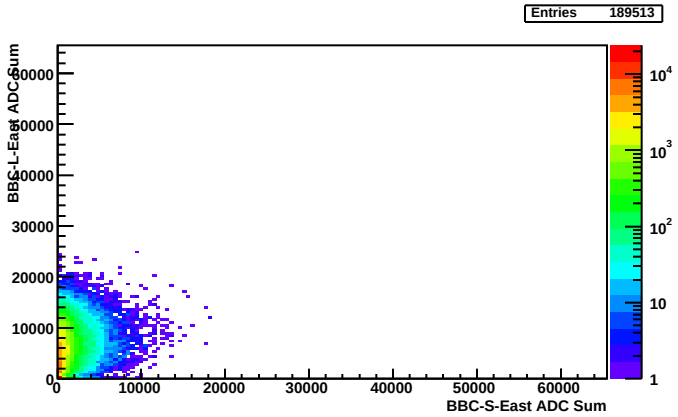

Entries 189513
TOF Mult
BBC-S-West ADC Sum

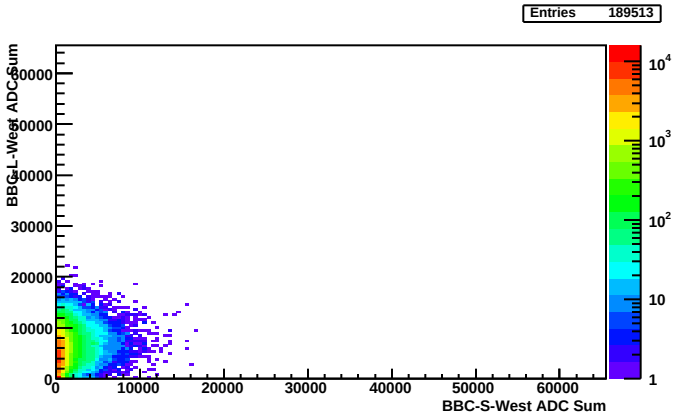

Entries 189513
TOF Mult
VPD-East ADC Sum

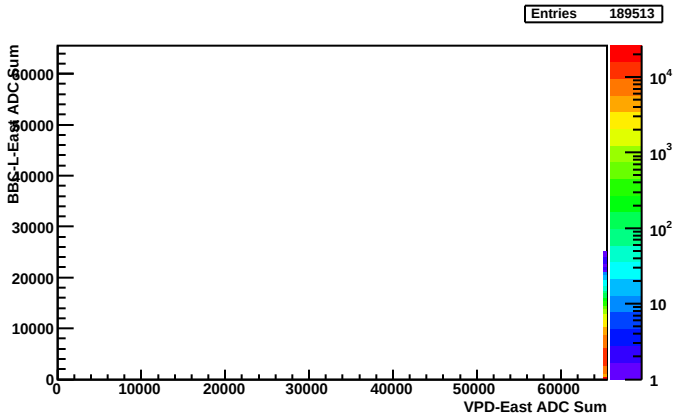

Entries 189513
TOF Mult
VPD-West ADC Sum

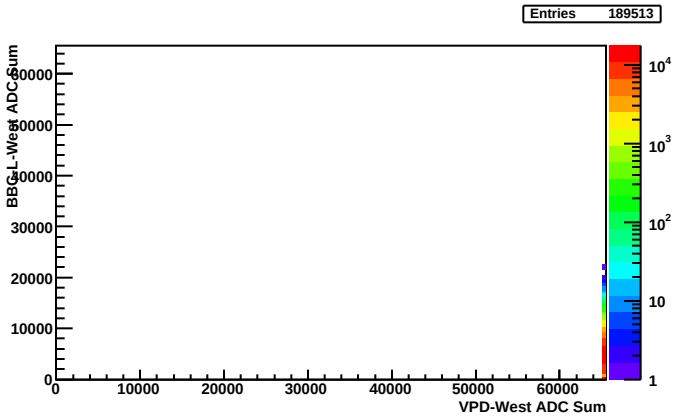

Entries 189513
TOF Mult
ZDC-East ADC Sum Att

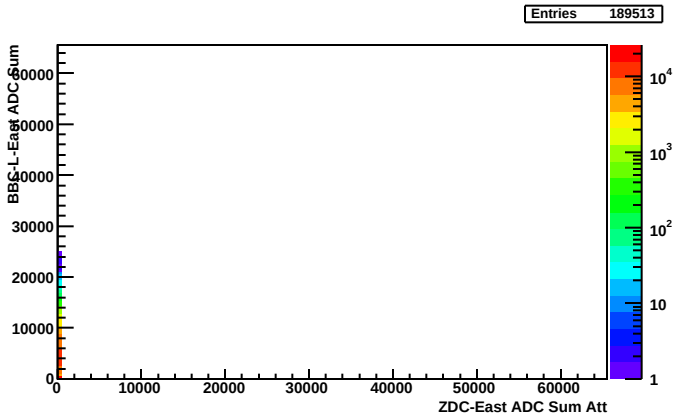

Entries 189513
TOF Mult
ZDC-West ADC Sum Att

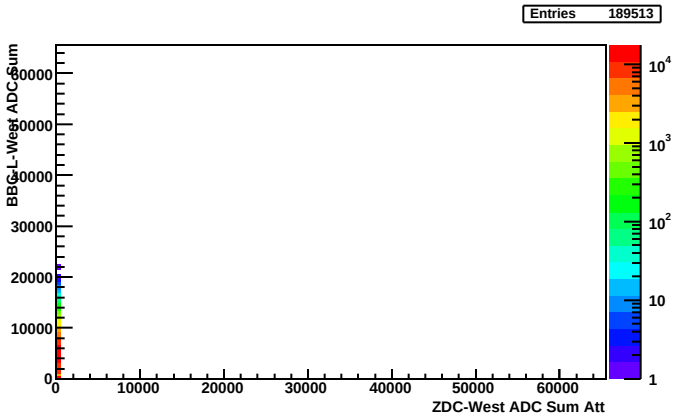

Entries 189513
BBC-L-East ADC Sum
BBC-S-East ADC Sum

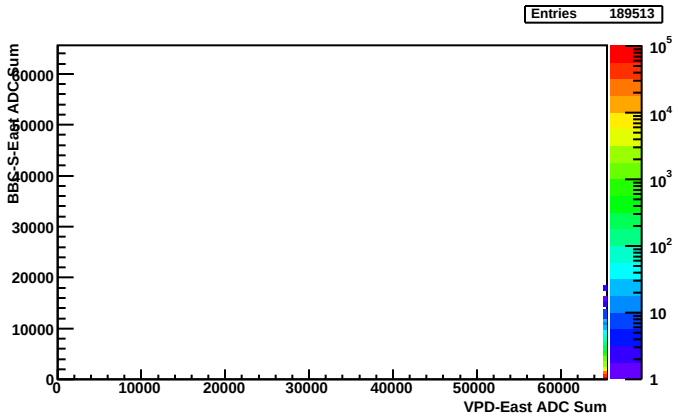

Entries 189513
BBC-L-West ADC Sum
BBC-S-West ADC Sum

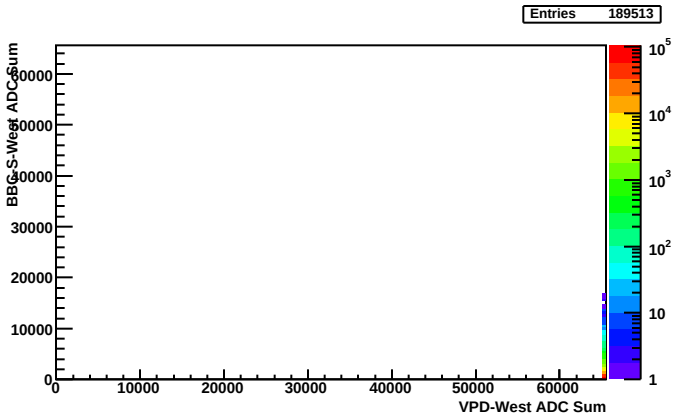

Entries 189513
BBC-L-East ADC Sum
VPD-East ADC Sum

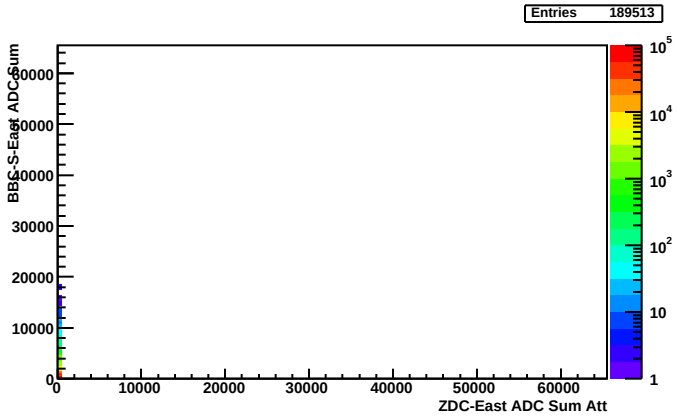

Entries 189513
BBC-L-West ADC Sum
VPD-West ADC Sum

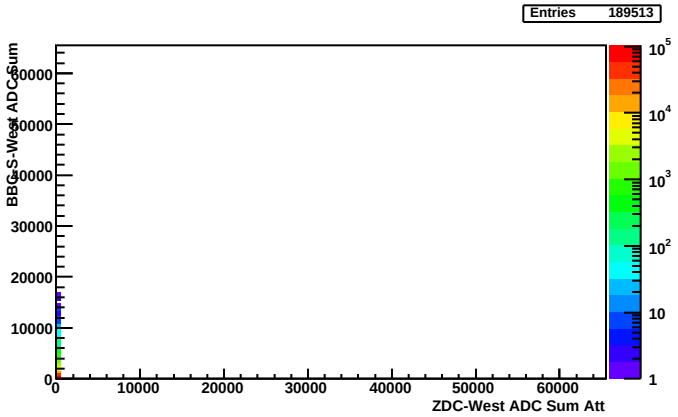

Entries 189513
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

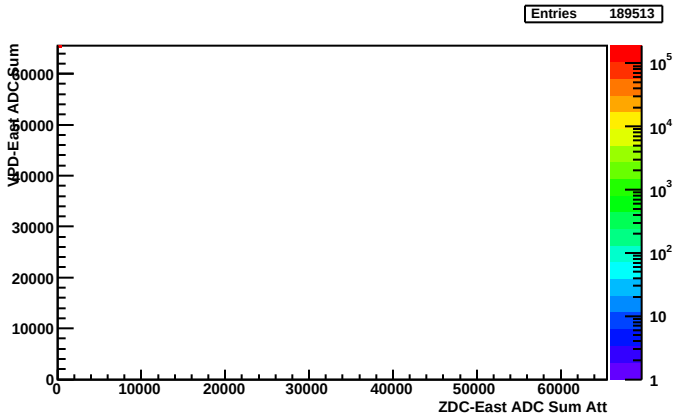

Entries 189513
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

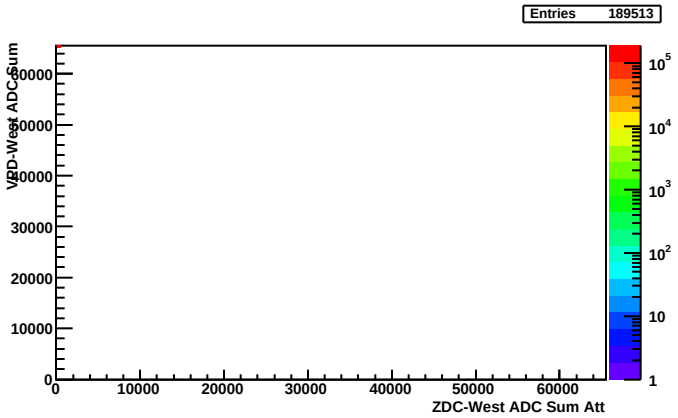

Entries 189513
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 189513
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 189513
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 189513
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 189513
VPD-East ADC Sum
ZDC-East ADC Sum Att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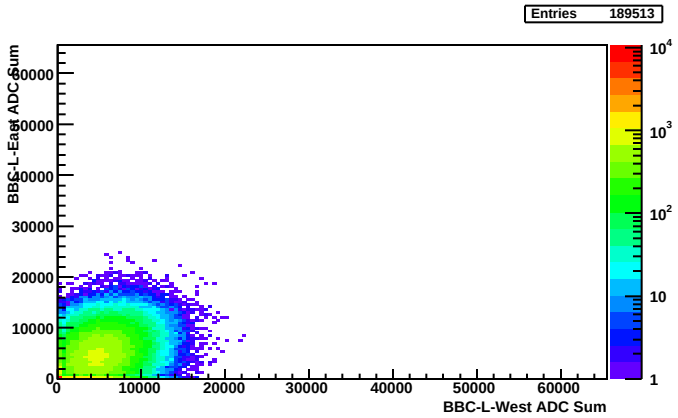
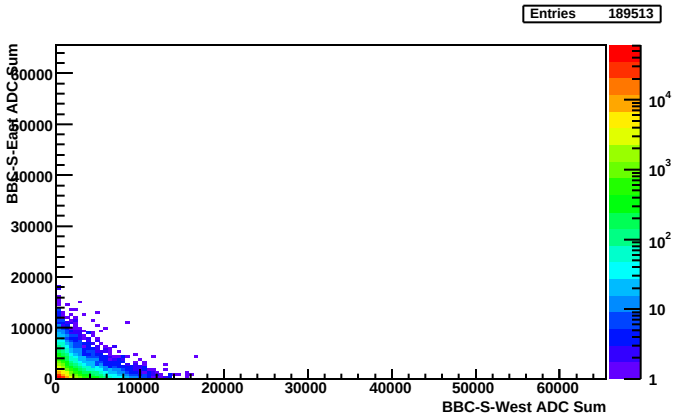
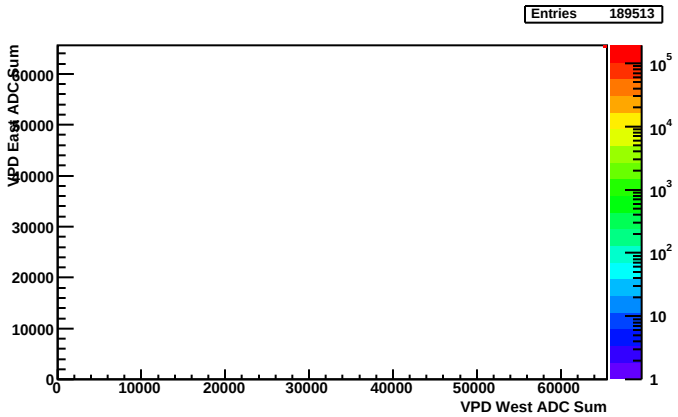
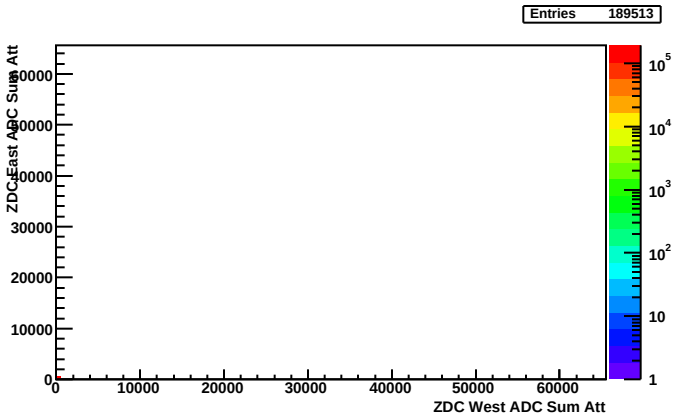
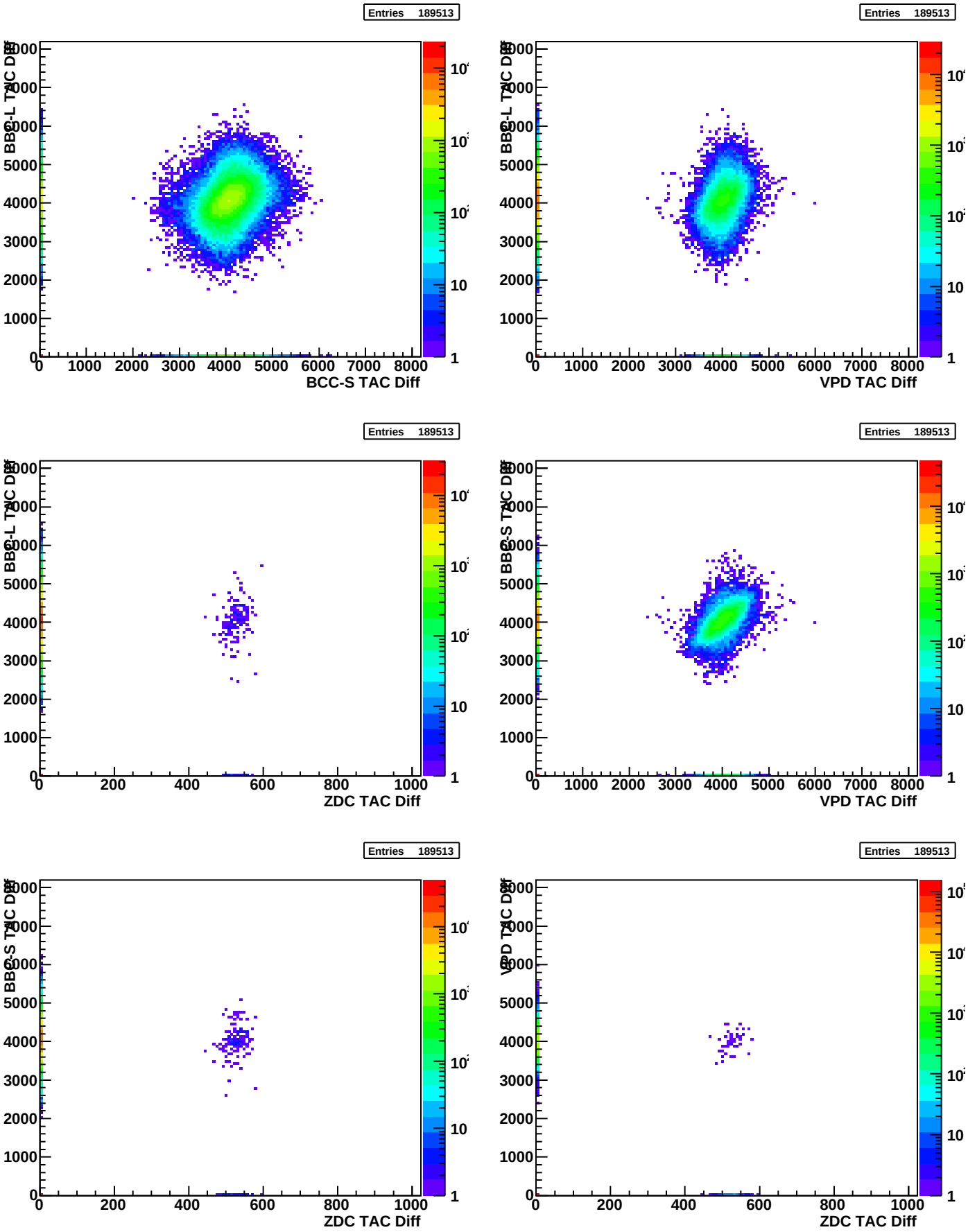
VPD-West ADC Sum
ZDC-West ADC Sum Att

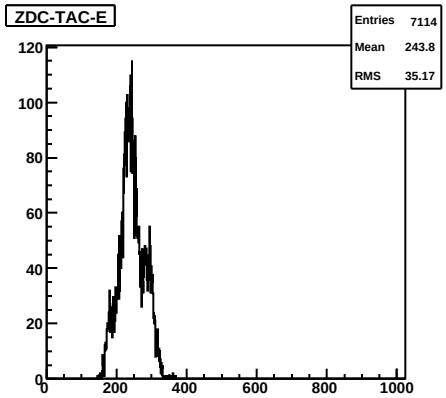

Entries 189513
BBC-L-East ADC Sum
BBC-L-West ADC Sum

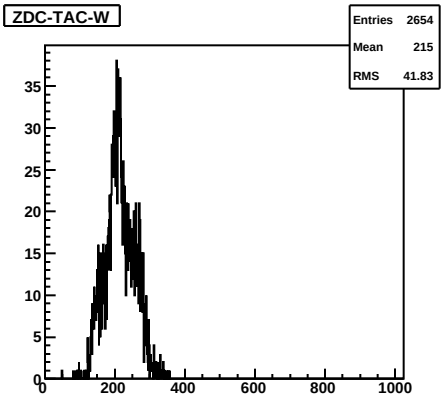

Entries 189513
BBC-S-East ADC Sum
BBC-S-West ADC Sum

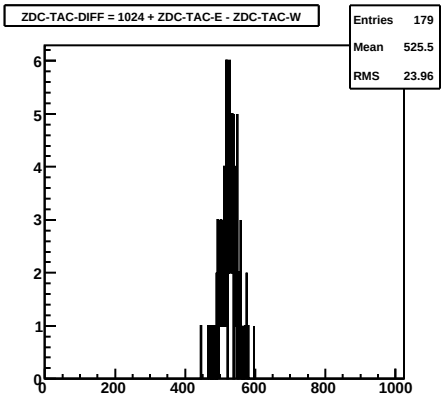

Entries 189513
VPD East ADC Sum
VPD West ADC Sum

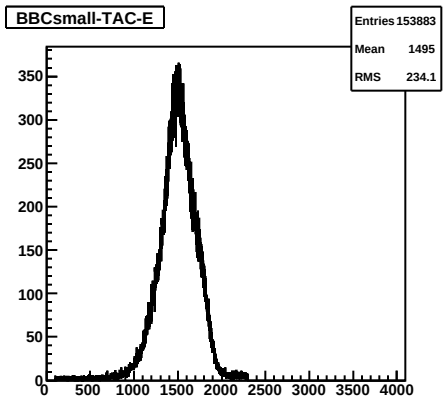

Entries 189513
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

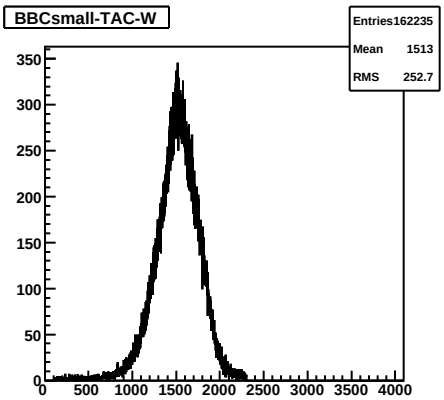

Entries 189513
BBC-L TAC Diff
BCC-S TAC Diff
Entries 189513
BBC-L TAC Diff
VPD TAC Diff
Entries 189513
BBC-L TAC Diff
ZDC TAC Diff
Entries 189513
BBC-S TAC Diff
VPD TAC Diff
Entries 189513
BBC-S TAC Diff
ZDC TAC Diff
Entries 189513
VPD TAC Diff
ZDC TAC Diff

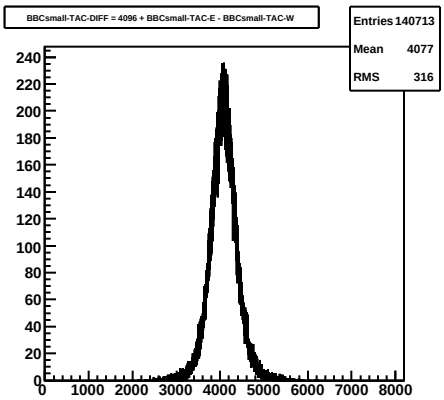

ZDC-TAC-E
Entries 7114
Mean 243.8
RMS 35.17

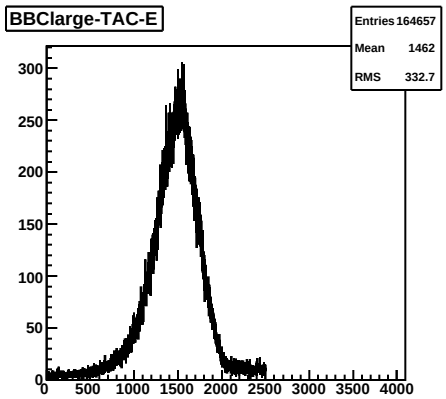

ZDC-TAC-W
Entries 2654
Mean 215
RMS 41.83

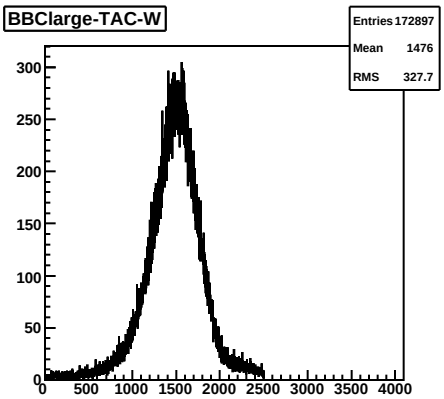

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 179
Mean 525.5
RMS 23.96

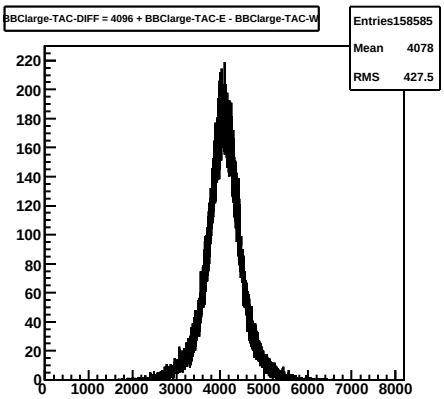

BBCsmall-TAC-E
Entries 153883
Mean 1495
RMS 234.1

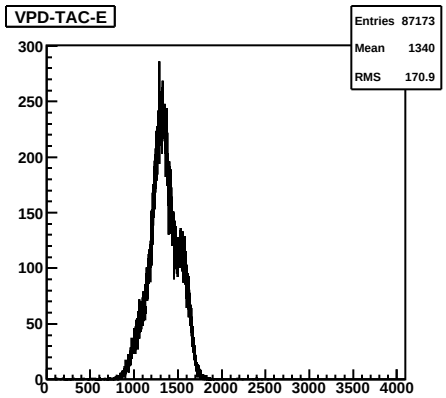

BBCsmall-TAC-W
Entries 162235
Mean 1513
RMS 252.7

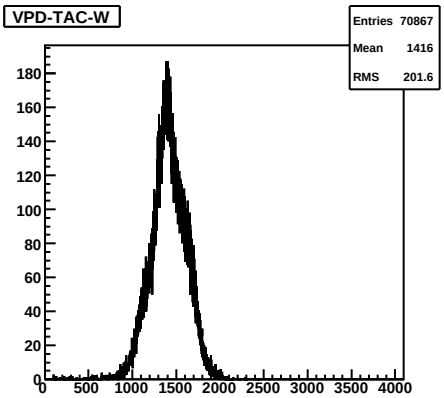

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 140713
Mean 4077
RMS 316

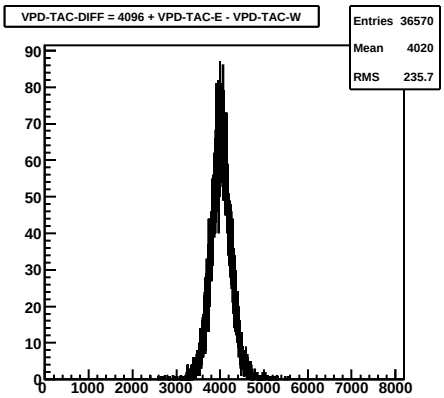

BBClarge-TAC-E
Entries 164657
Mean 1462
RMS 332.7

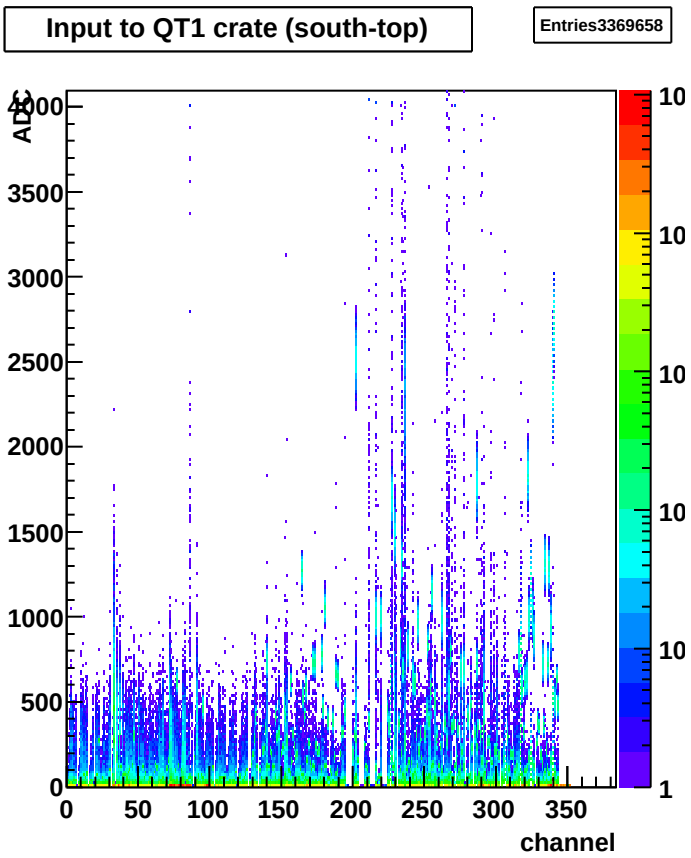

BBClarge-TAC-W
Entries 172897
Mean 1476
RMS 327.7

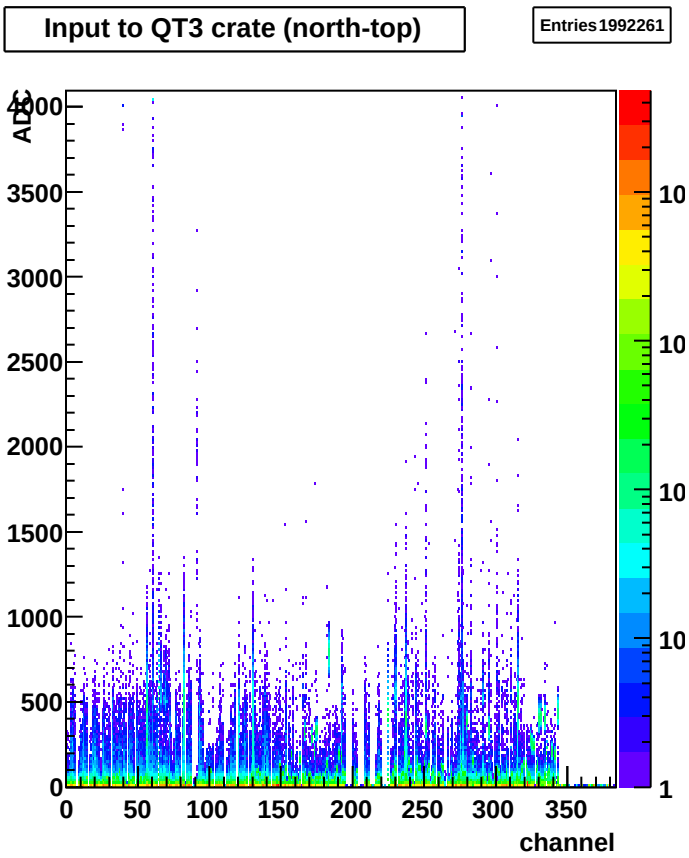

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 158585
Mean 4078
RMS 427.5

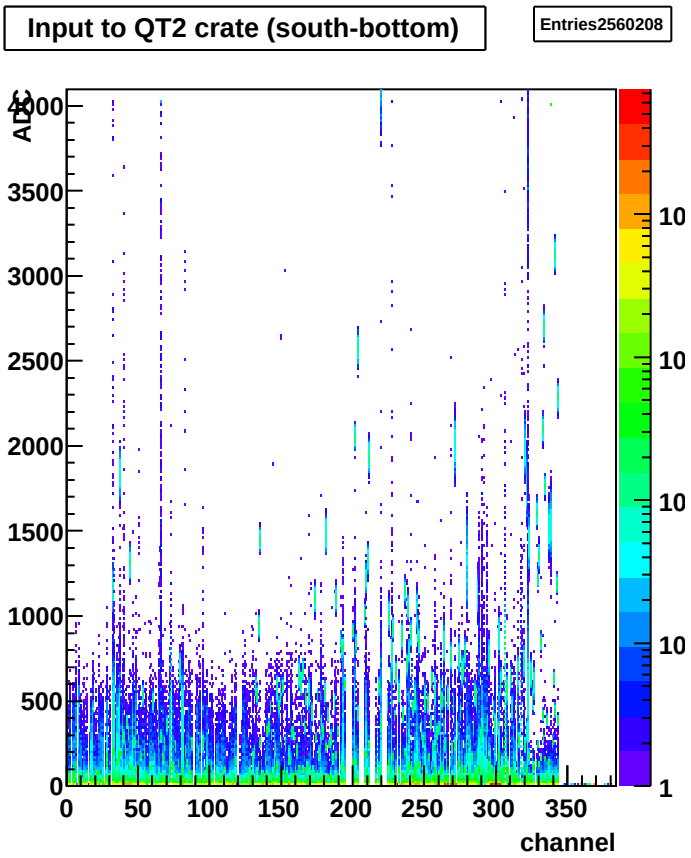

VPD-TAC-E
Entries 87173
Mean 1340
RMS 170.9

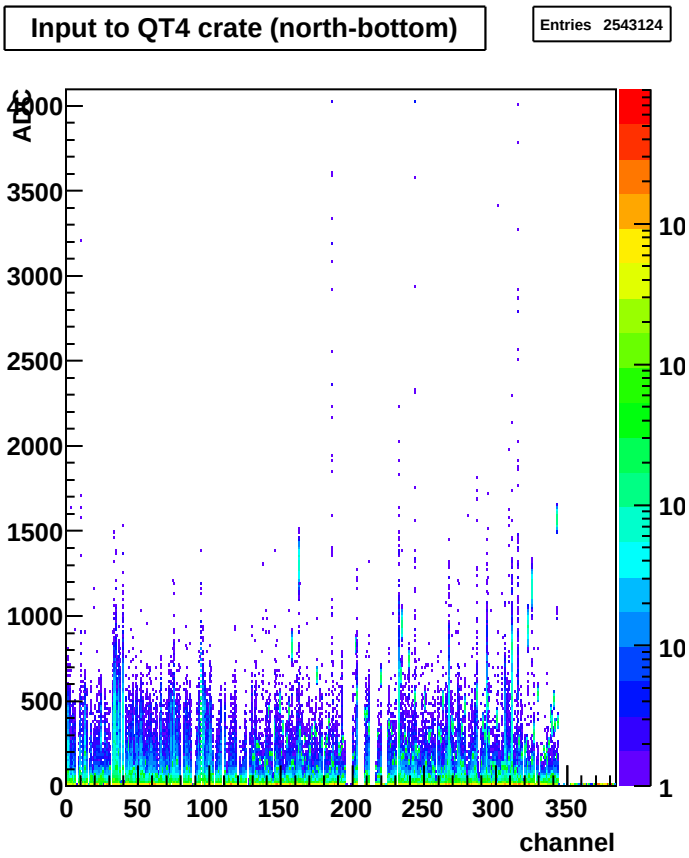

VPD-TAC-W
Entries 70867
Mean 1416
RMS 201.6

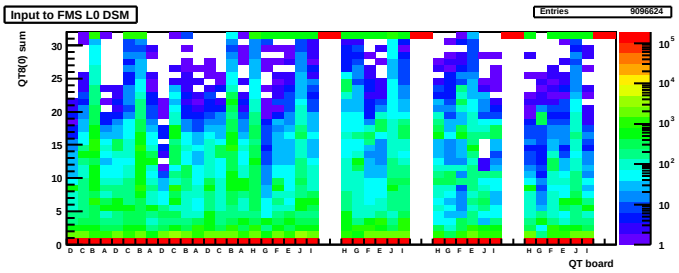

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 36570
Mean 4020
RMS 235.7

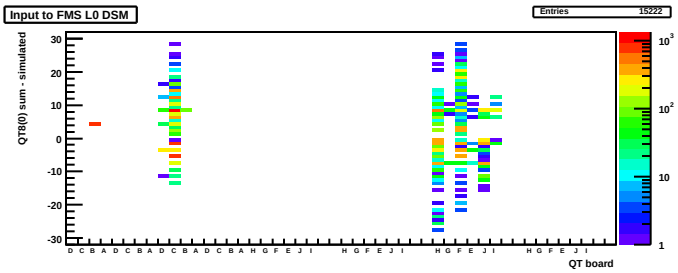

Input to QT1 crate (south-top)
Entries3369658
ADC
channel

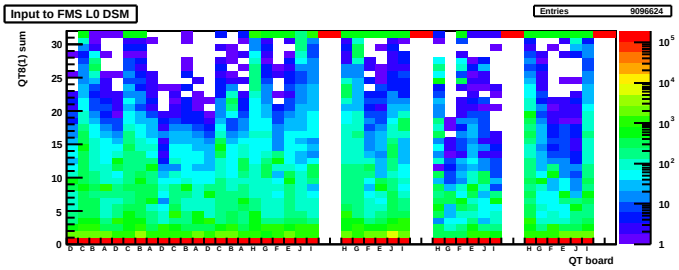

Input to QT3 crate (north-top)
Entries1992261
ADC
channel

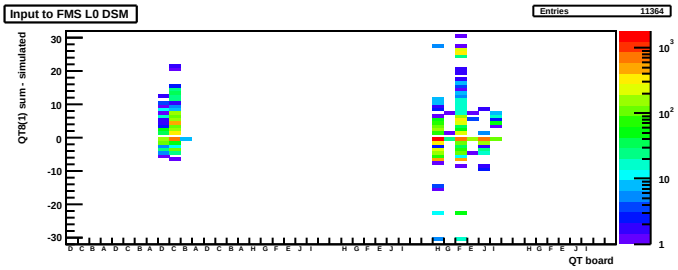

Input to QT2 crate (south-bottom)
Entries2560208
ADC
channel

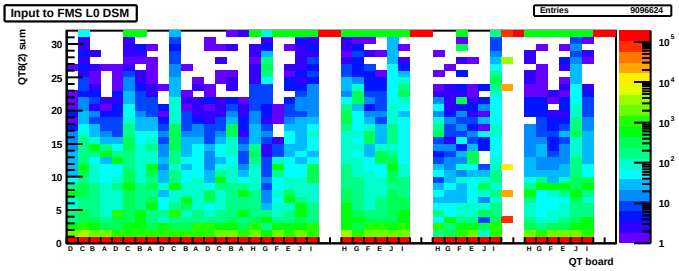

Input to QT4 crate (north-bottom)
Entries 2543124
ADC
channel

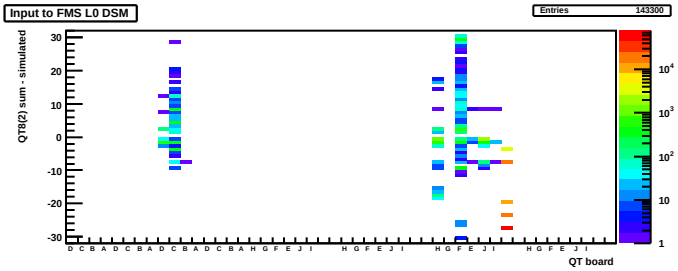

Input to FMS L0 DSM
Entries 9096624
QT8(0) sum
QT board

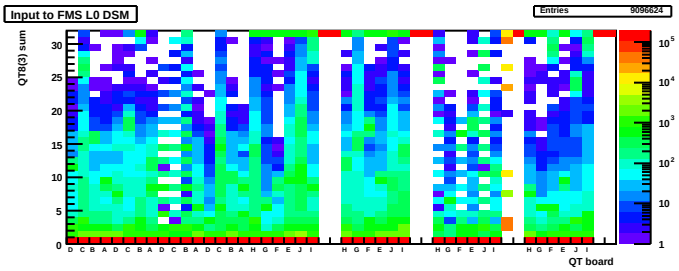

Input to FMS L0 DSM
Entries 15222
QT8(0) sum - simulated
QT board

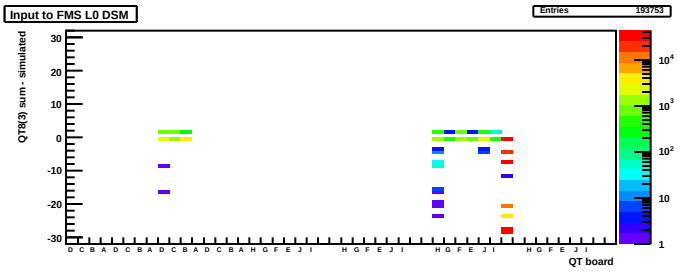

Input to FMS L0 DSM
Entries 9096624
QT8(1) sum
QT board

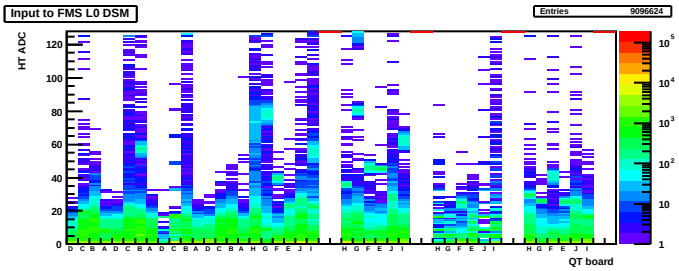

Input to FMS L0 DSM
Entries 11364
QT8(1) sum - simulated
QT board

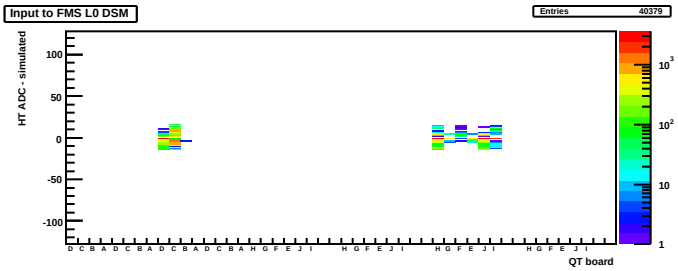

Input to FMS L0 DSM
Entries 9096624
QT8(2) sum
QT board

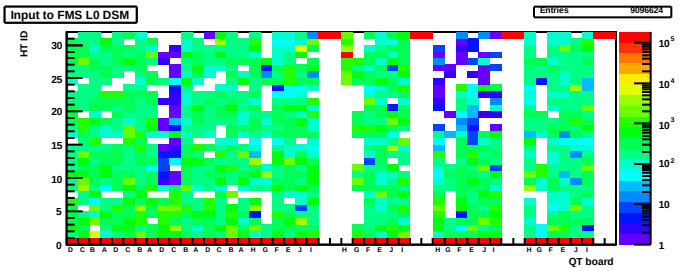

Input to FMS L0 DSM
Entries 143300
QT8(2) sum - simulated
QT board

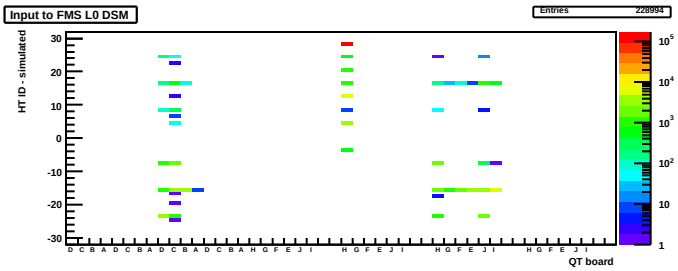

Input to FMS L0 DSM
Entries 9096624
QT8(3) sum
QT board

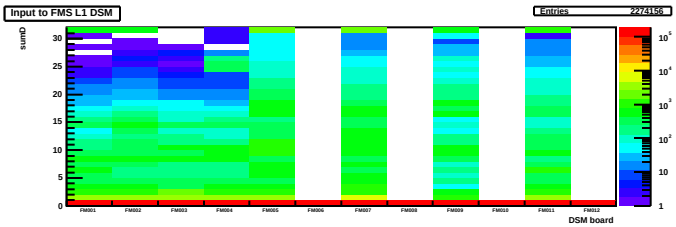

Input to FMS L0 DSM
Entries 193753
QT8(3) sum - simulated
QT board

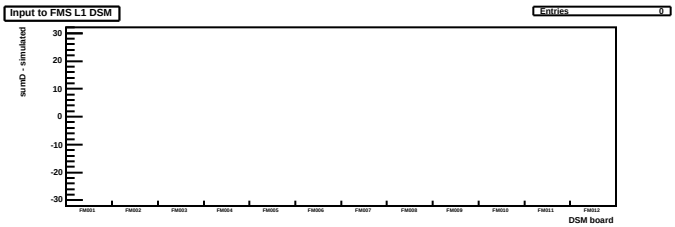

Input to FMS L0 DSM
Entries 9096624
HT ADC
QT board

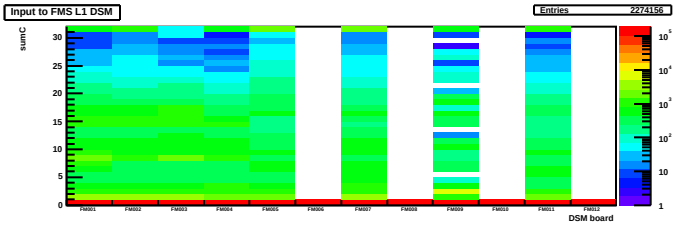

Input to FMS L0 DSM
Entries 40379
HT ADC - simulated
QT board

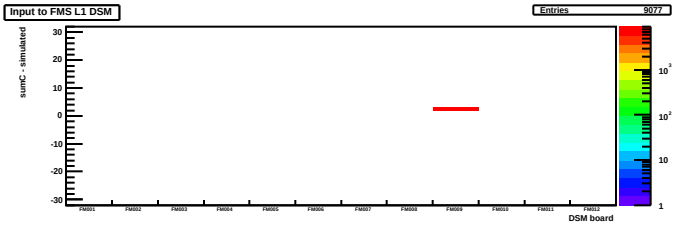

Input to FMS L0 DSM
Entries 9096624
HT ID
QT board

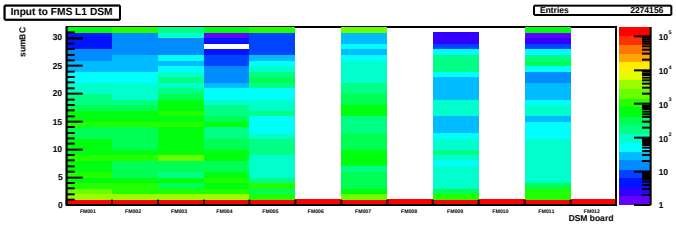

Input to FMS L0 DSM
Entries 228994
HT ID - simulated
QT board

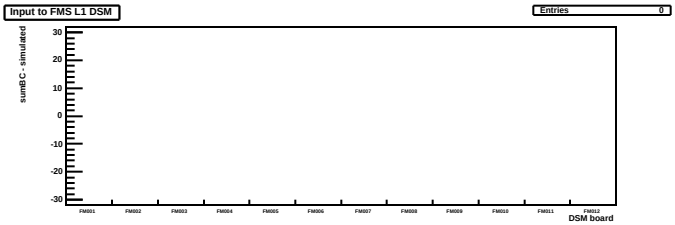

Input to FMS L1 DSM
Entries 2274156
sumD
DSM board

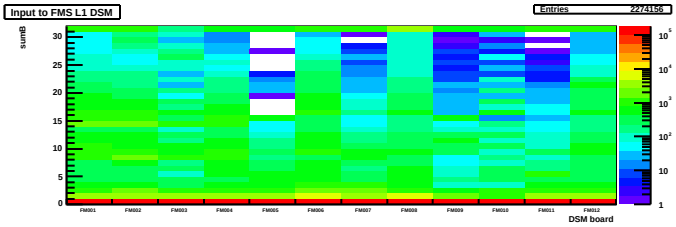

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

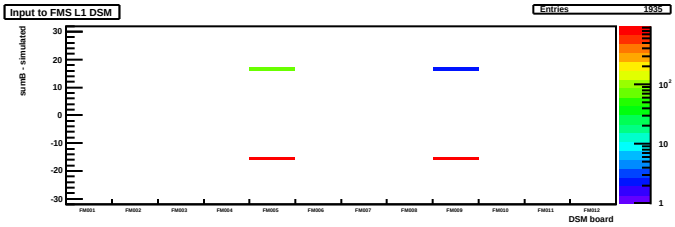

Input to FMS L1 DSM
Entries 2274156
sumC
DSM board

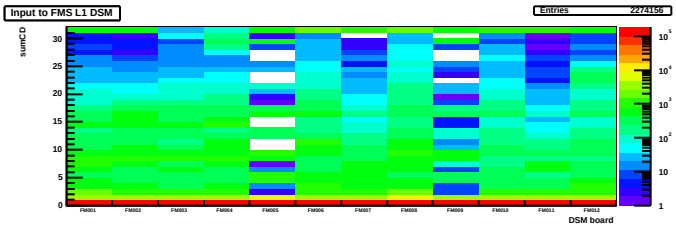

Input to FMS L1 DSM
Entries 9077
sumC - simulated
DSM board

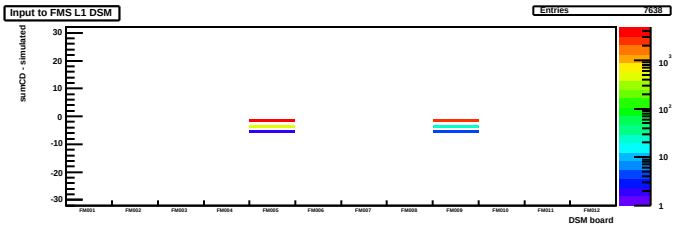

Input to FMS L1 DSM
Entries 2274156
sumBC
DSM board

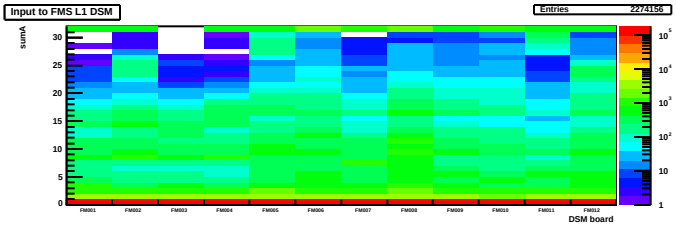

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

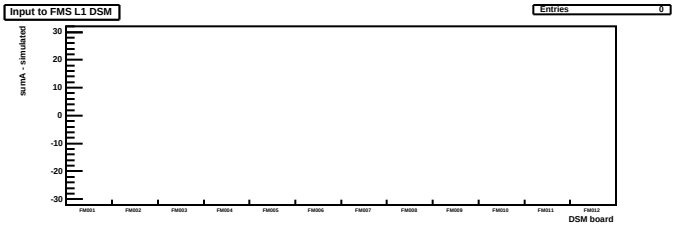

Input to FMS L1 DSM
Entries 2274156
sumB
DSM board

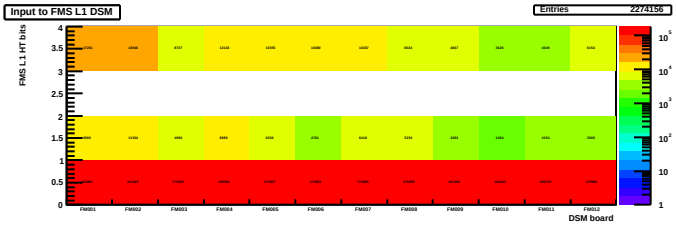

Input to FMS L1 DSM
Entries 1935
sumB - simulated
DSM board

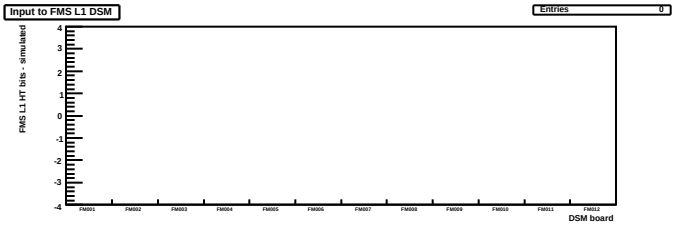

Input to FMS L1 DSM
Entries 2274156
sumCD
DSM board

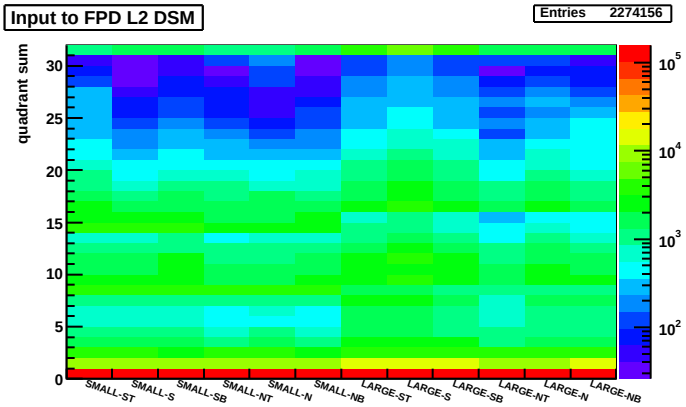

Input to FMS L1 DSM
Entries 7638
sumCD - simulated
DSM board

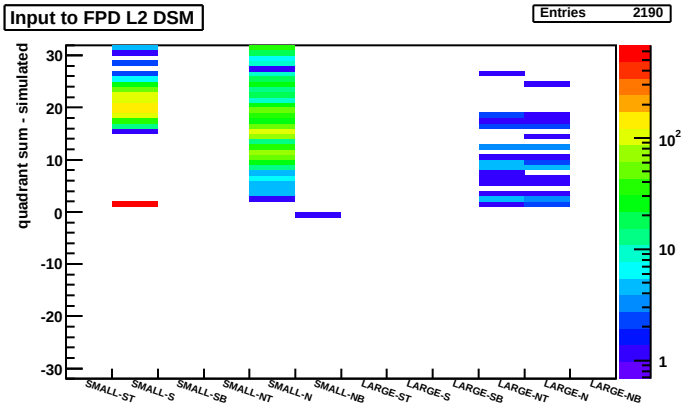

Input to FMS L1 DSM
Entries 2274156
sumA
DSM board

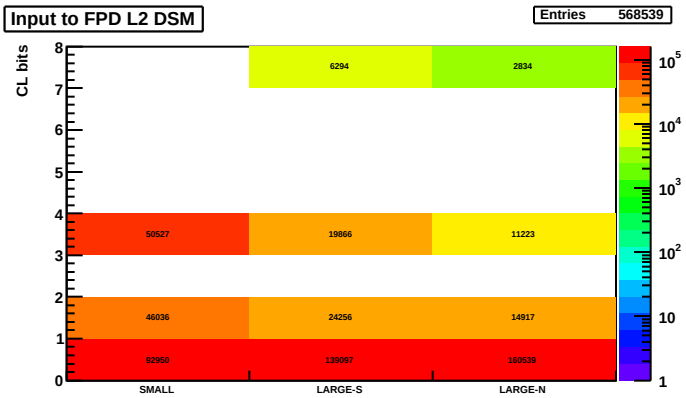

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

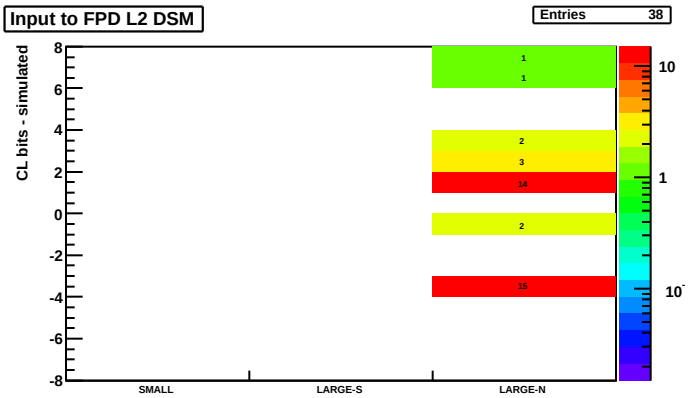

Input to FMS L1 DSM
Entries 2274156
FMS L1 HT bits
DSM board

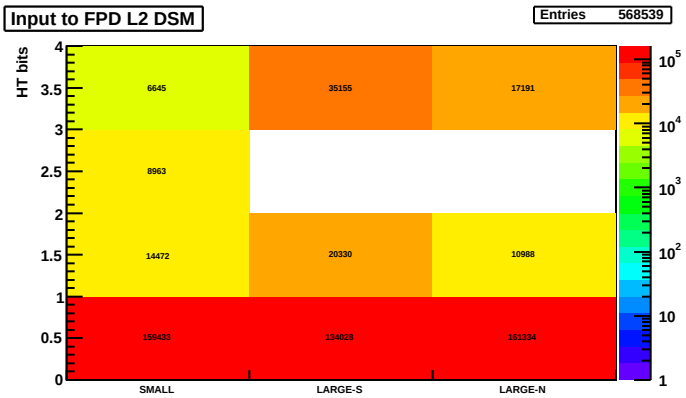

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

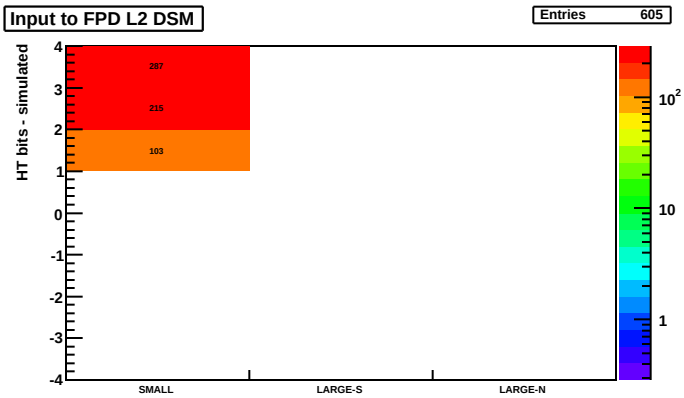

Input to FPD L2 DSM
Entries 2274156
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

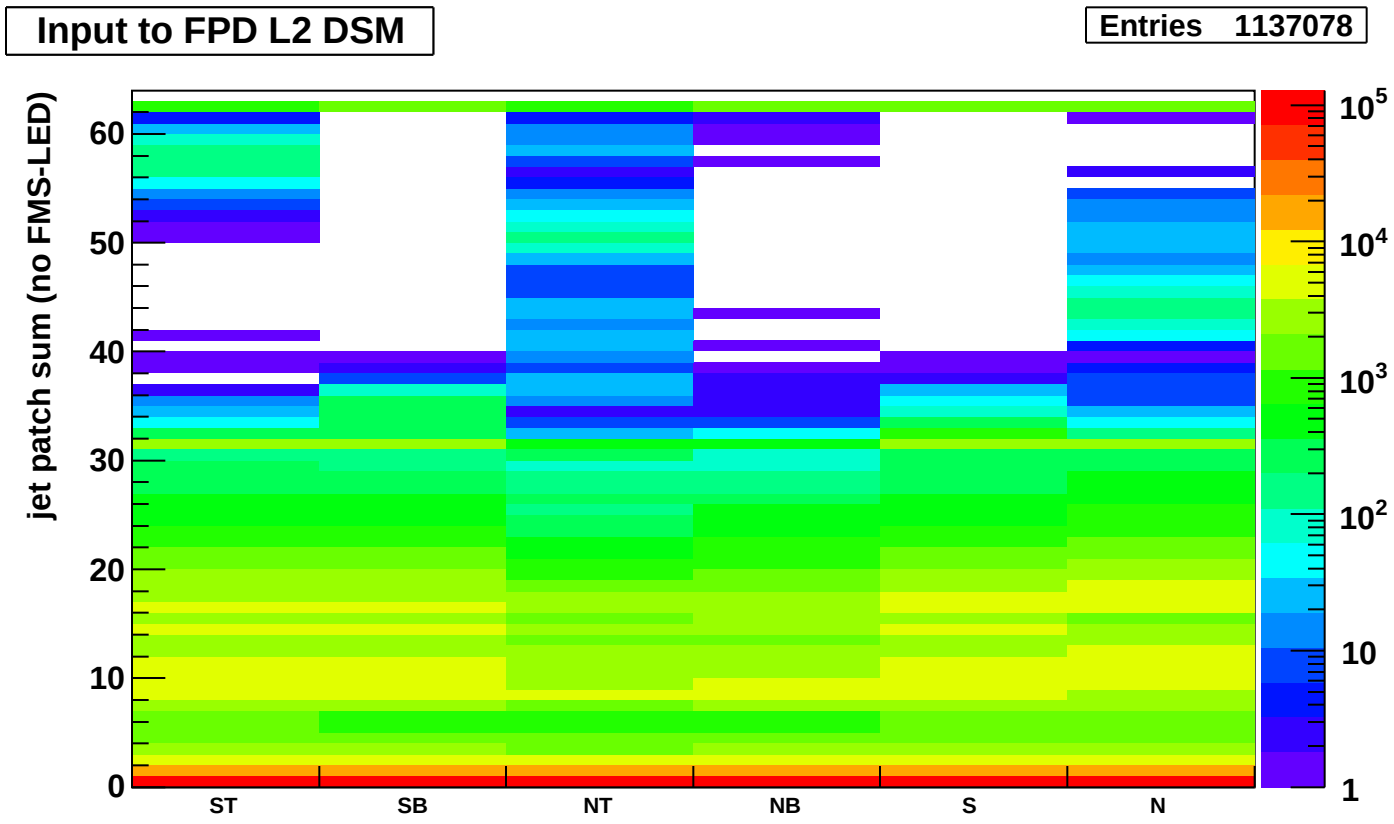

Input to FPD L2 DSM
Entries 2190
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

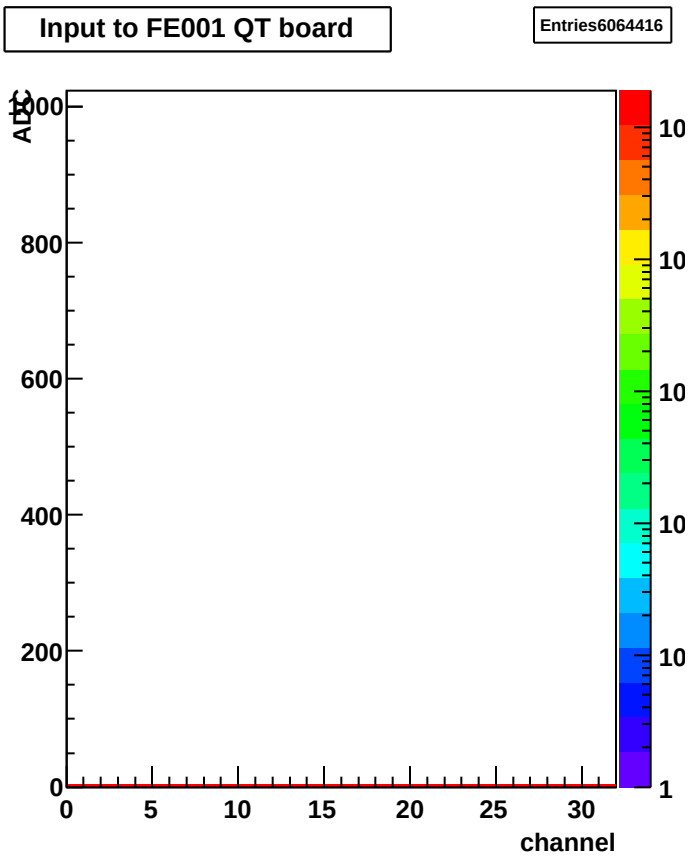

Input to FPD L2 DSM
Entries 568539
CL bits
6294
2834
50527
19866
11223
46036
24256
14917
92950
139097
160539
SMALL
LARGE-S
LARGE-N

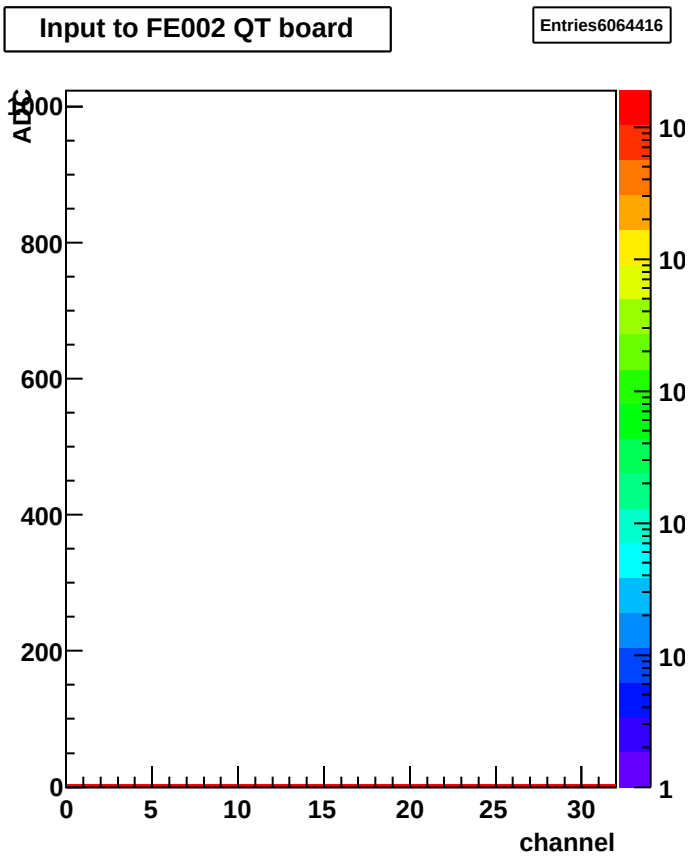

Input to FPD L2 DSM
Entries 38
CL bits - simulated
1
1
2
3
14
2
15
SMALL
LARGE-S
LARGE-N

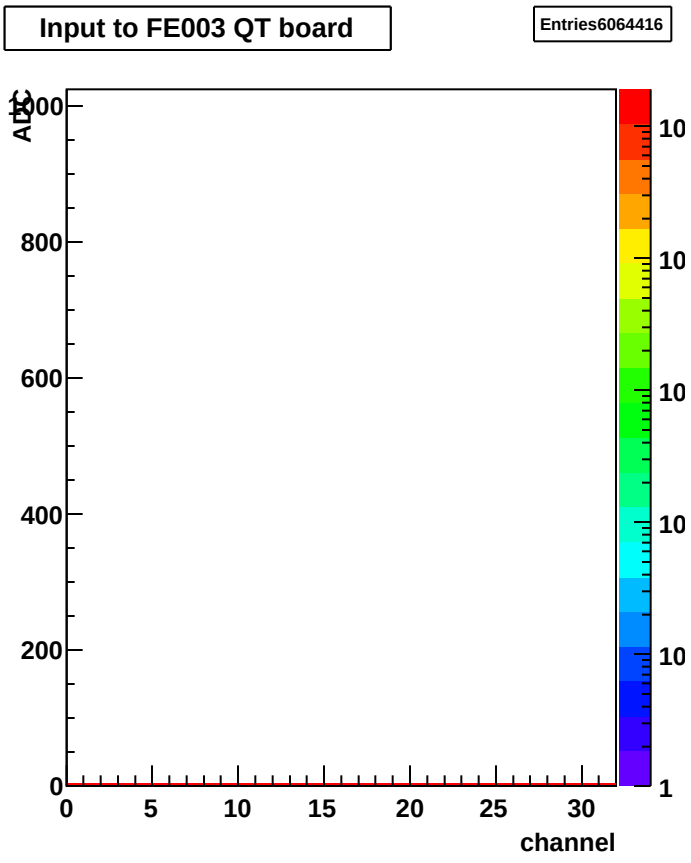

Input to FPD L2 DSM
Entries 568539
HT bits
6645
35155
17191
8963
14472
20330
10988
159433
134028
161334
SMALL
LARGE-S
LARGE-N

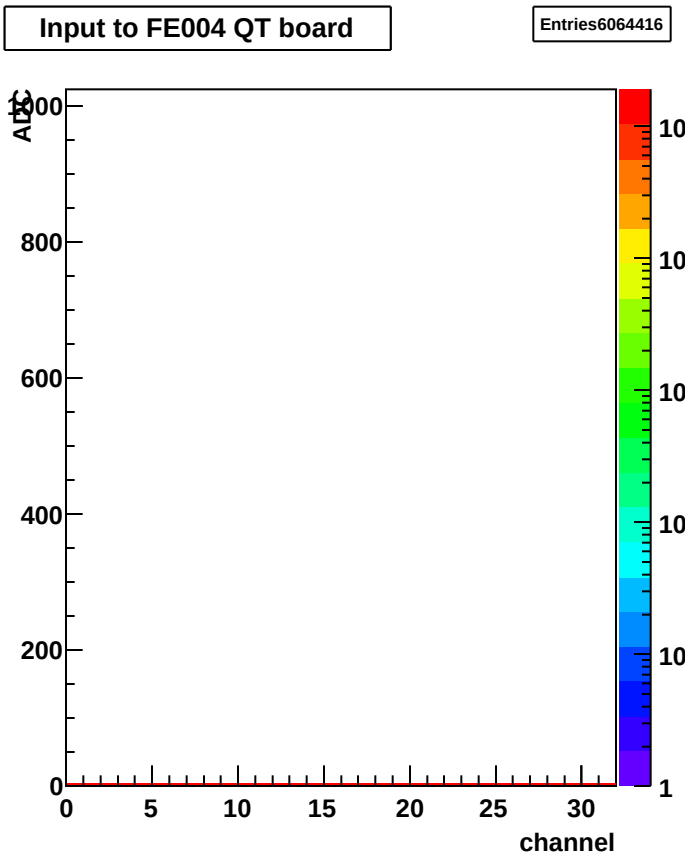

Input to FPD L2 DSM
Entries 605
HT bits - simulated
287
215
103
SMALL
LARGE-S
LARGE-N

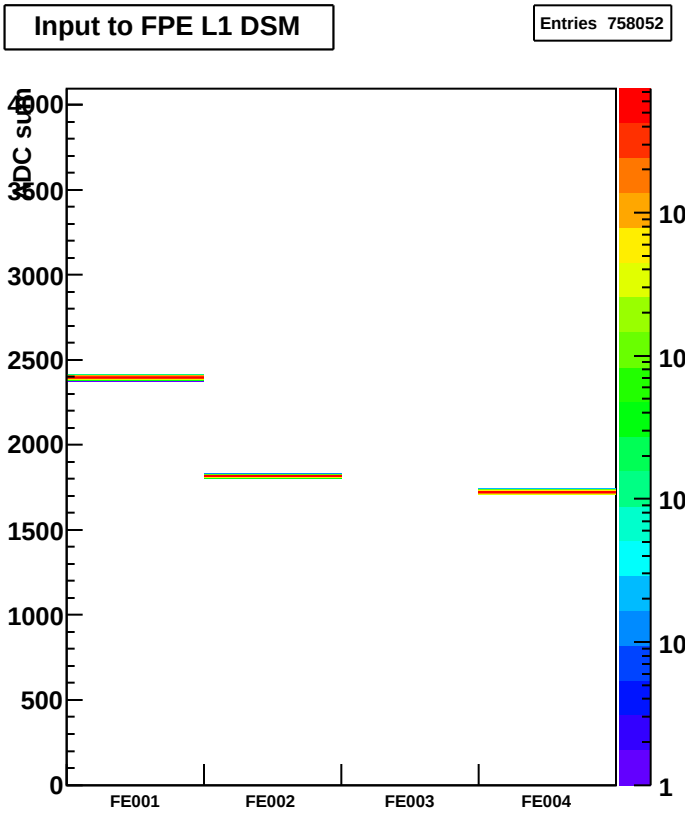

Input to FPD L2 DSM
Entries 1137078
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

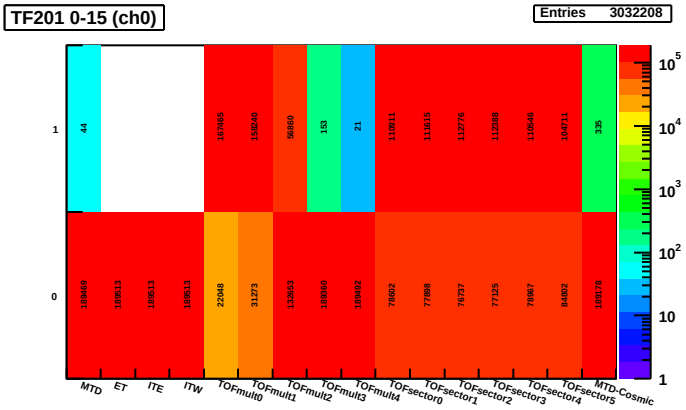

Input to FE001 QT board
Entries6064416
ADC
channel

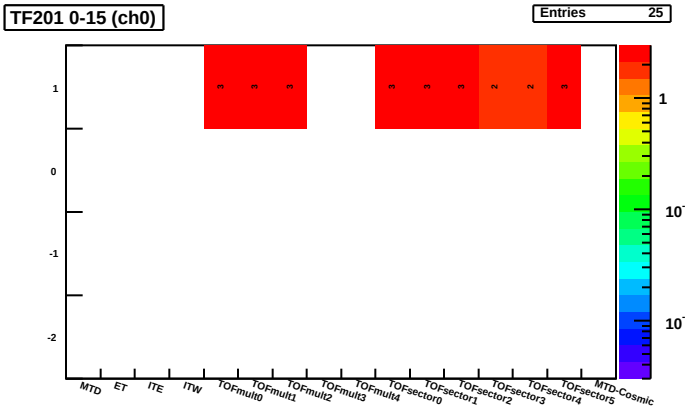

Input to FE002 QT board
Entries6064416
ADC
channel

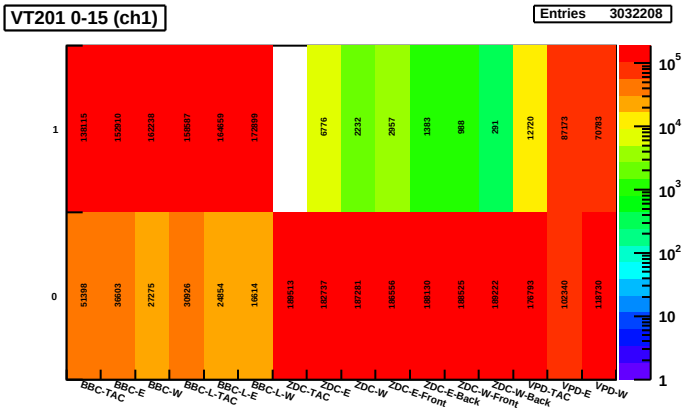

Input to FE003 QT board
Entries6064416
ADC
channel

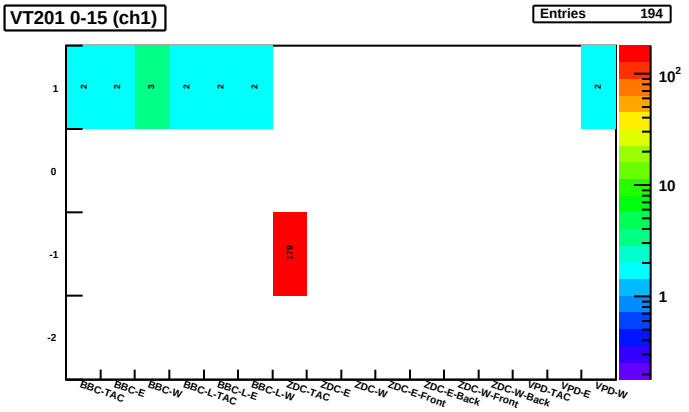

Input to FE004 QT board
Entries6064416
ADC
channel

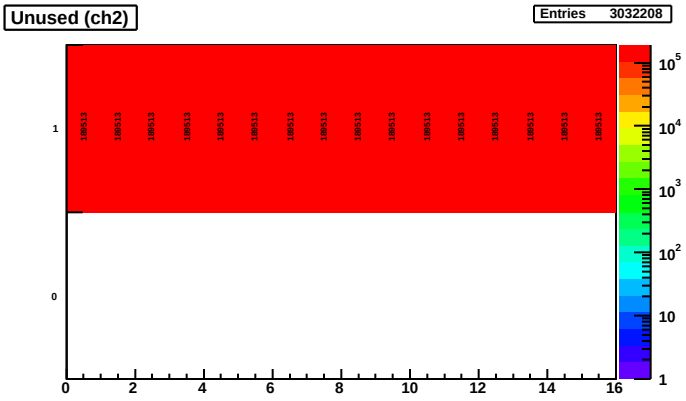

Input to FPE L1 DSM
Entries 758052
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

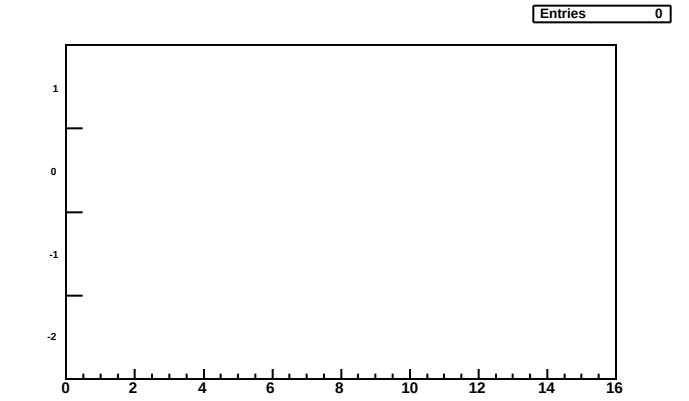

TF201 0-15 (ch0)
Entries 3032208
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 25
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3032208
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 194
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3032208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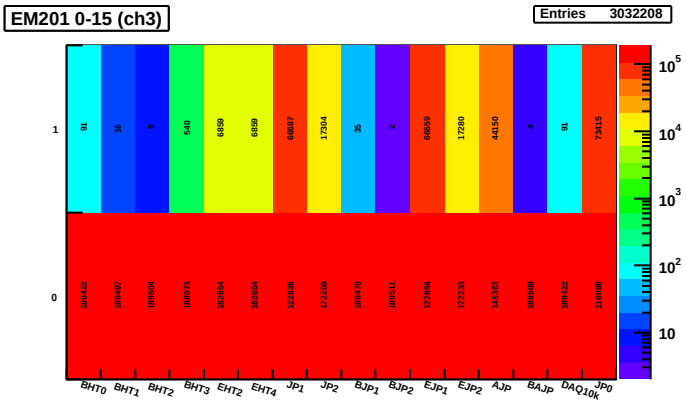
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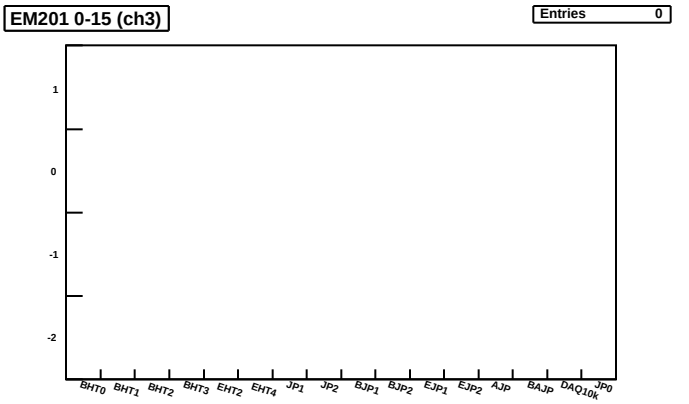
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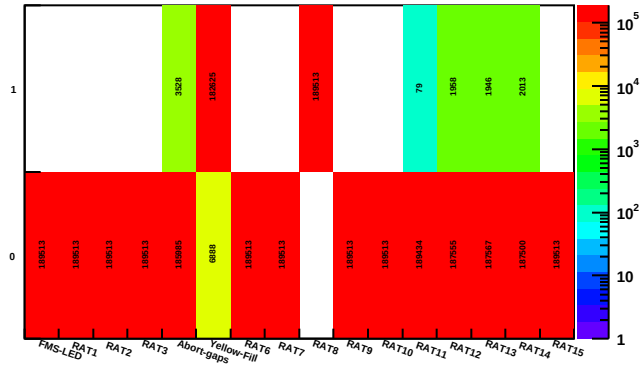
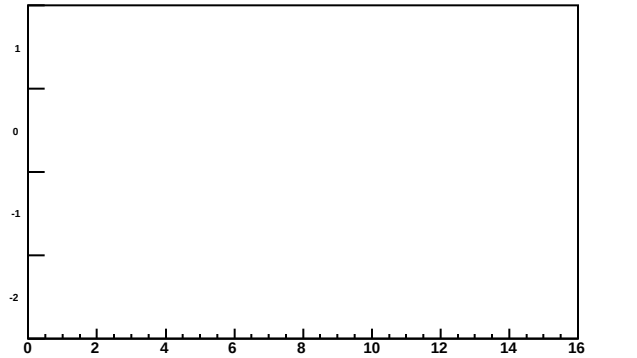
RAT board (ch4)





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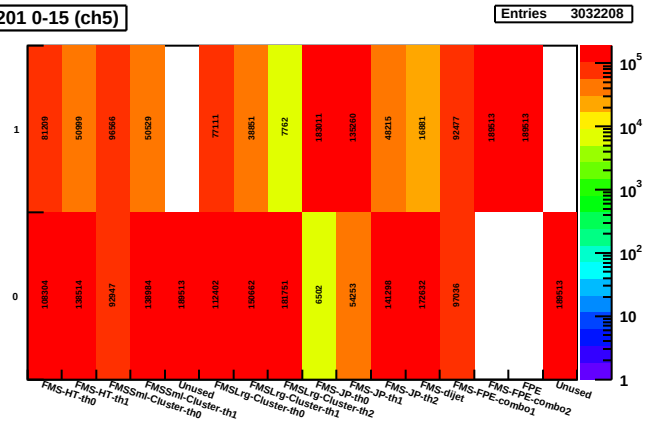

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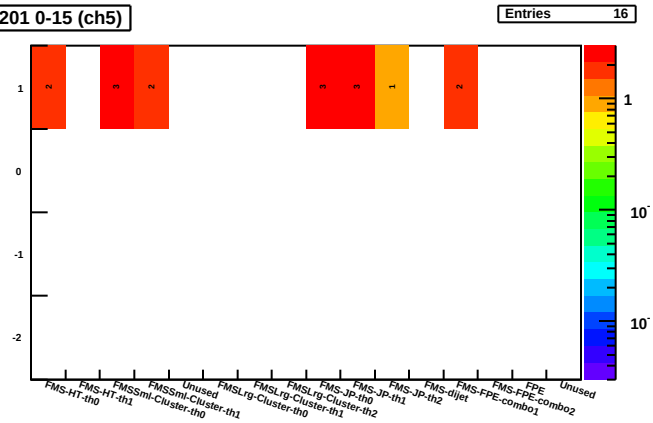

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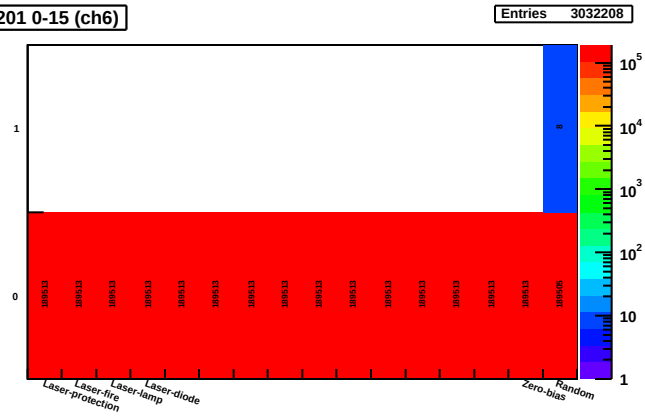

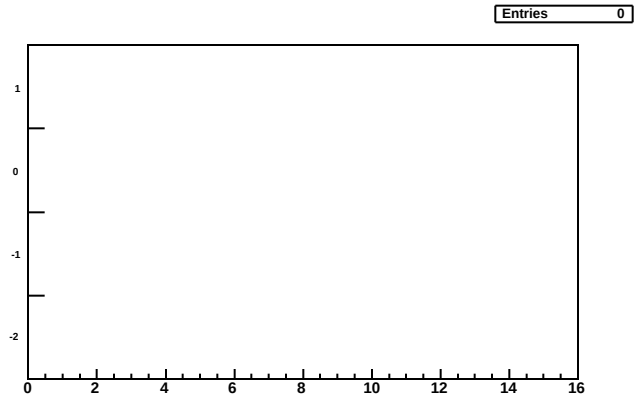
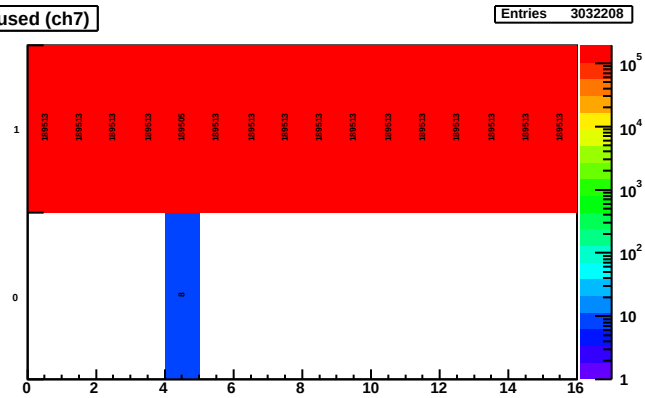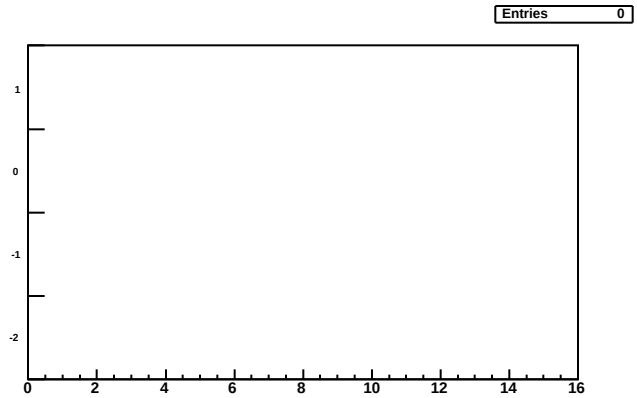
Unused (ch7)