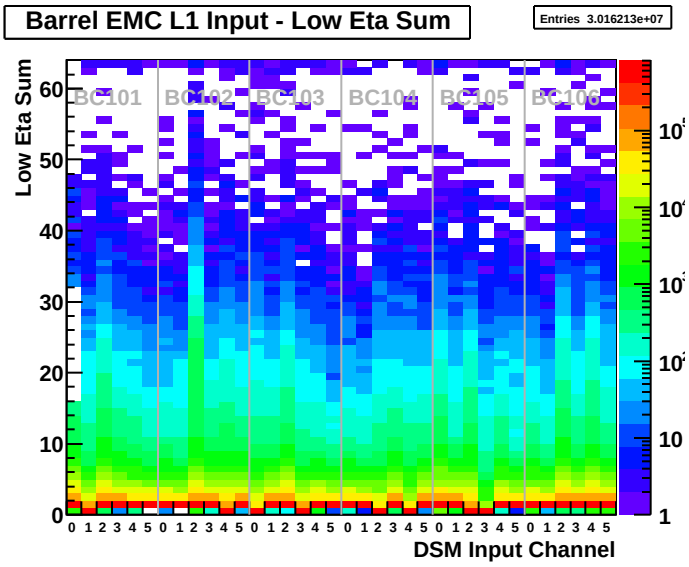
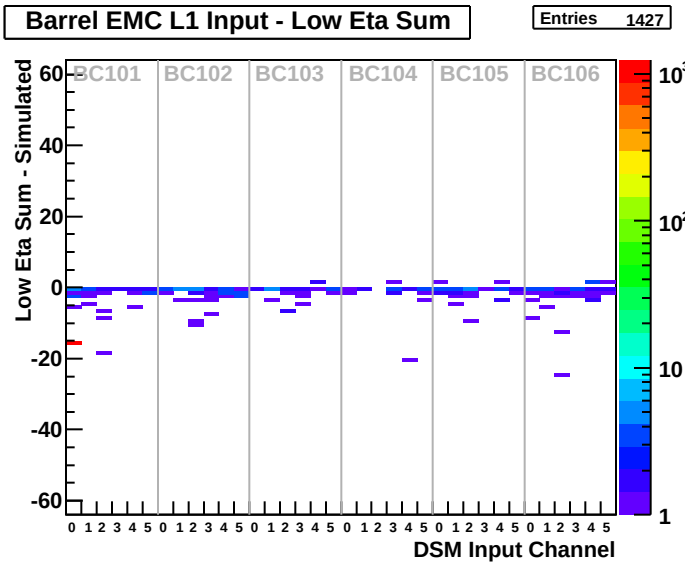
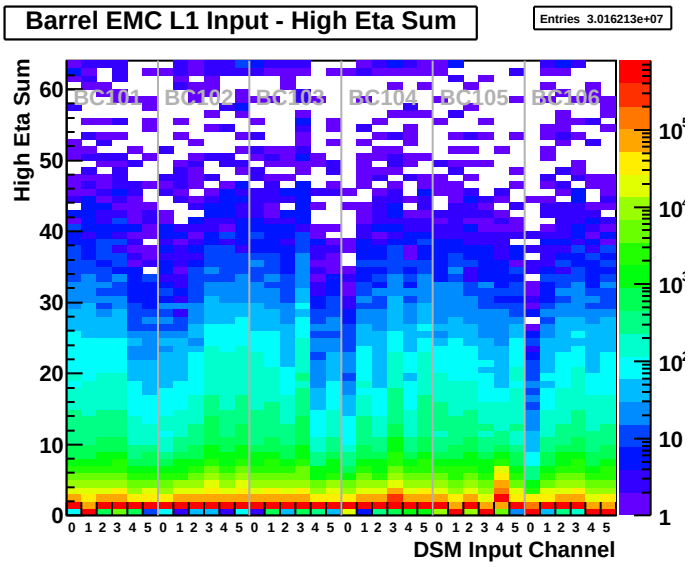
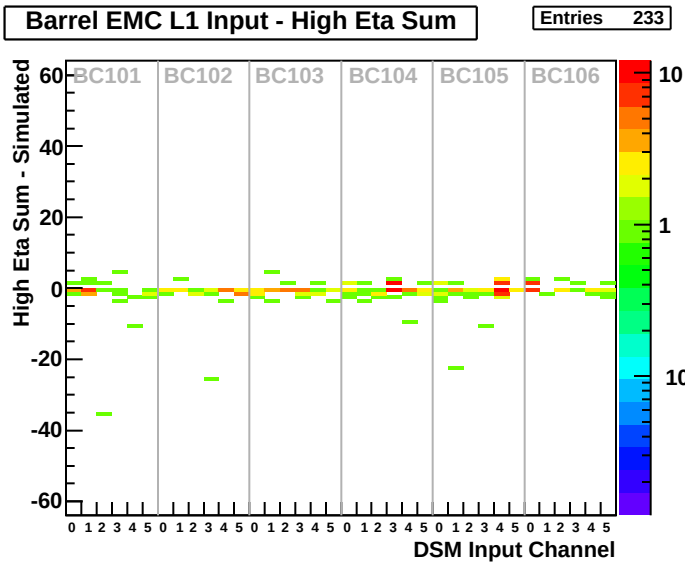
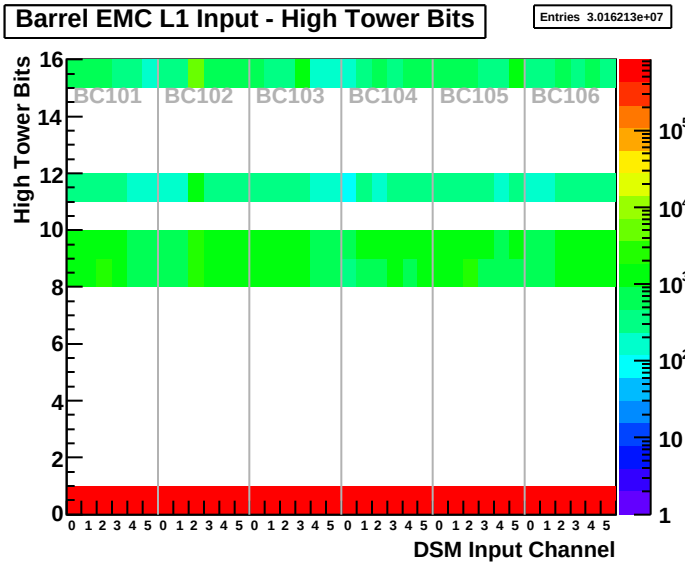
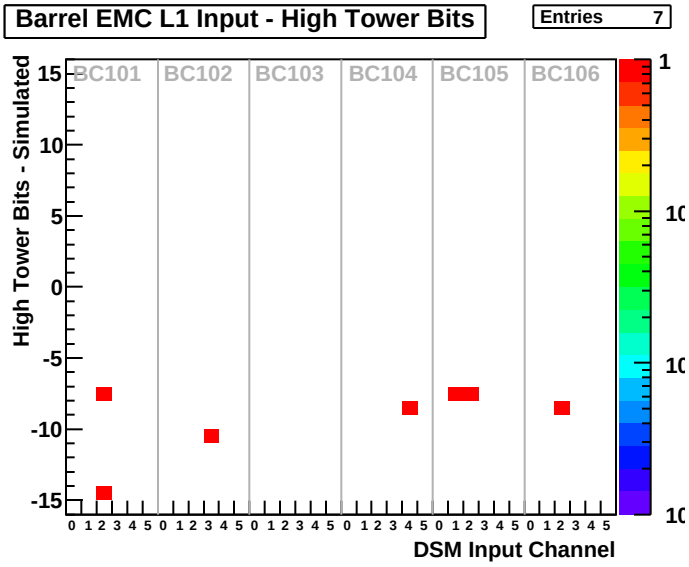

Barrel EMC L1 Input - Low Eta Sum
Entries 3.016213e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

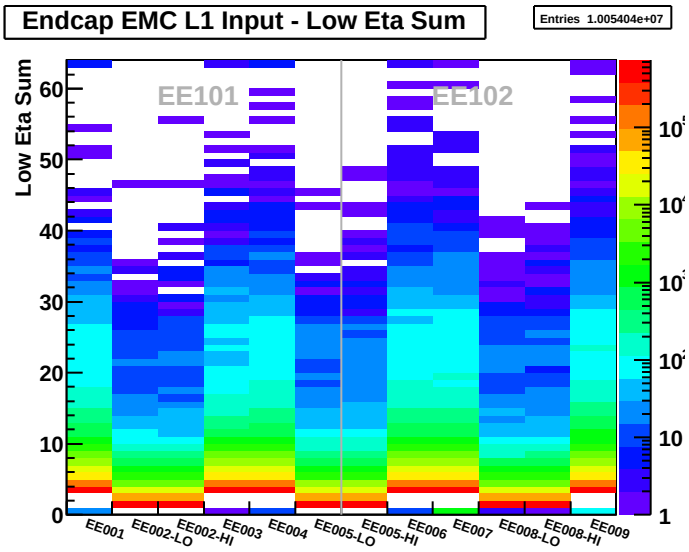

Barrel EMC L1 Input - Low Eta Sum
Entries 1427
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

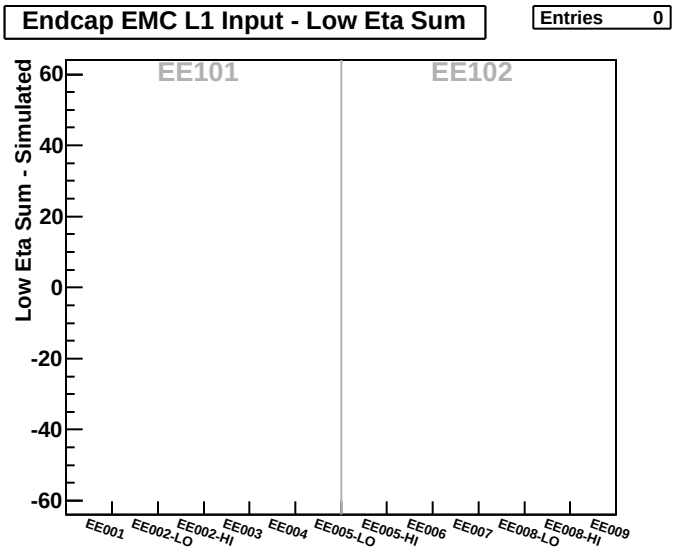

Barrel EMC L1 Input - High Eta Sum
Entries 3.016213e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

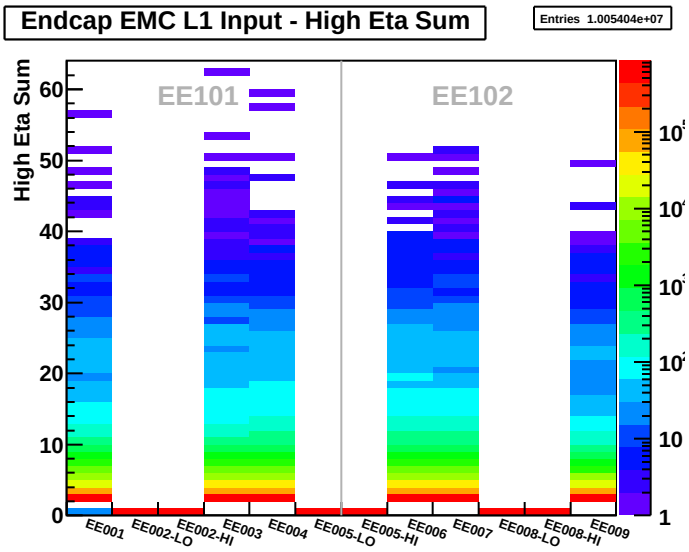

Barrel EMC L1 Input - High Eta Sum
Entries 233
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

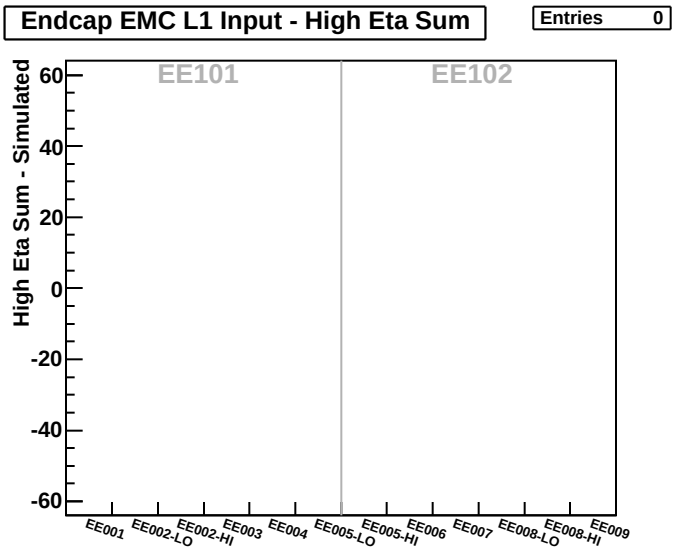

Barrel EMC L1 Input - High Tower Bits
Entries 3.016213e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

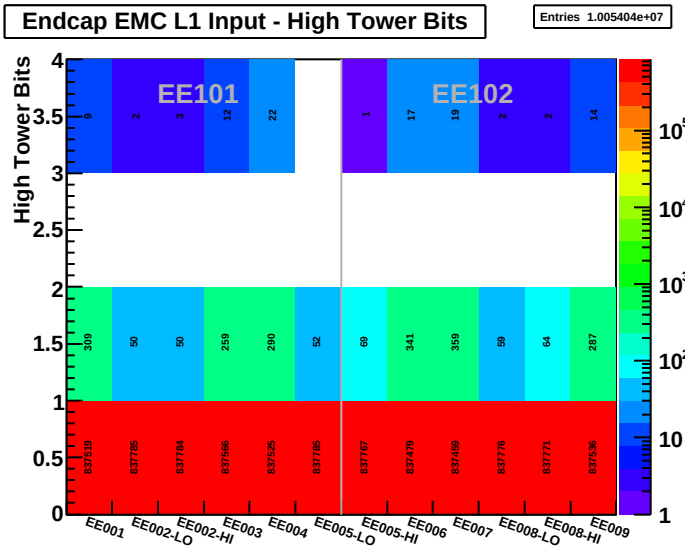

Barrel EMC L1 Input - High Tower Bits
Entries 7
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

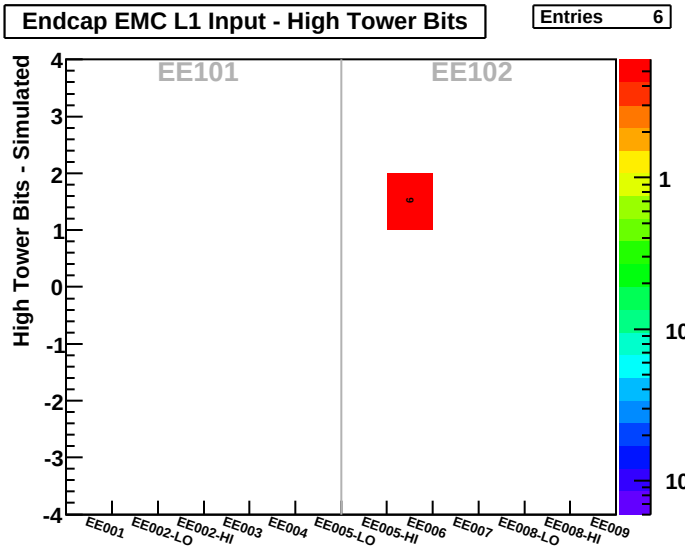

Endcap EMC L1 Input - Low Eta Sum
Entries 1.005404e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

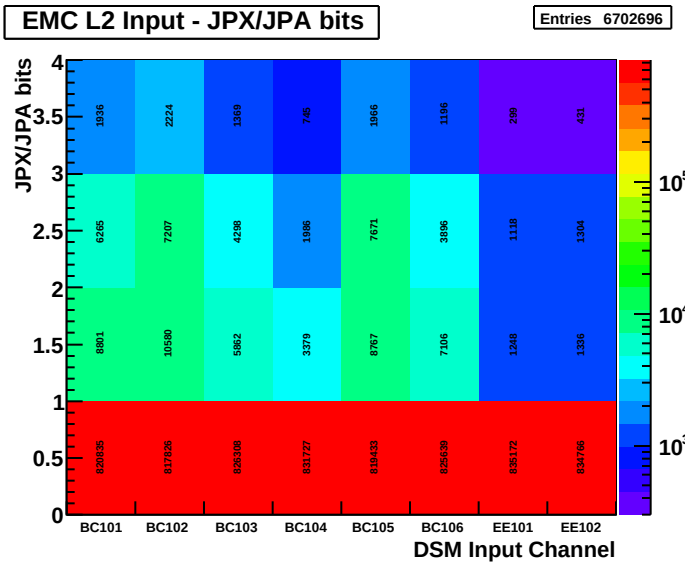

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

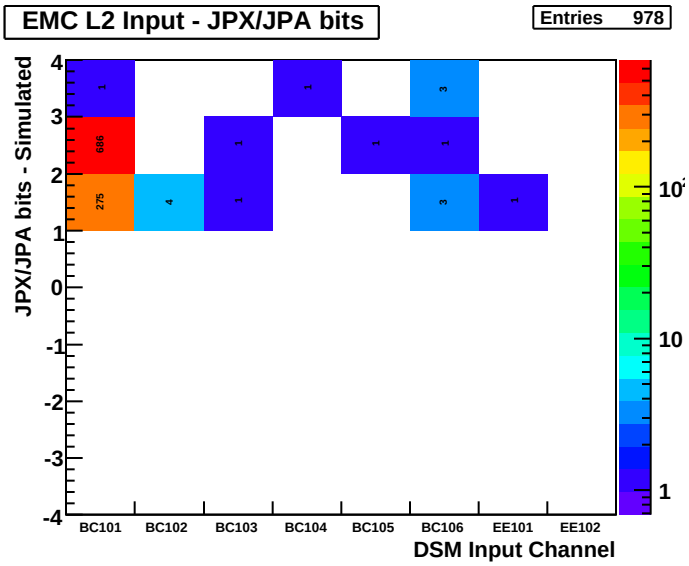

Endcap EMC L1 Input - High Eta Sum
Entries 1.005404e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

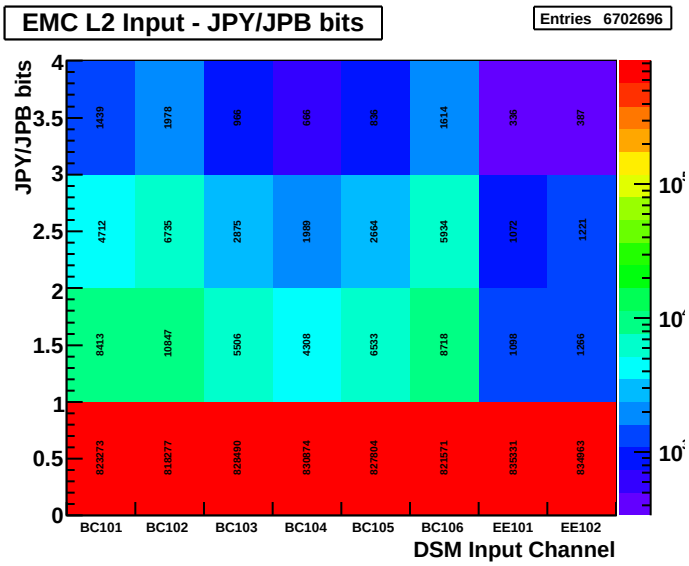

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

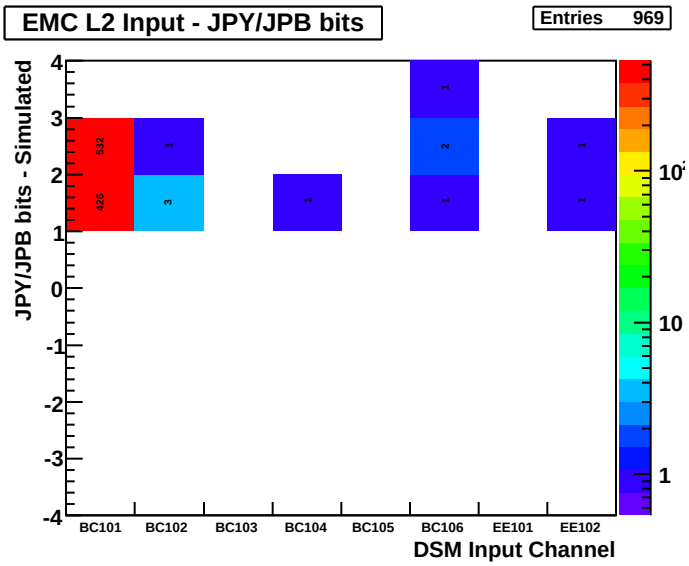

Endcap EMC L1 Input - High Tower Bits
Entries 1.005404e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

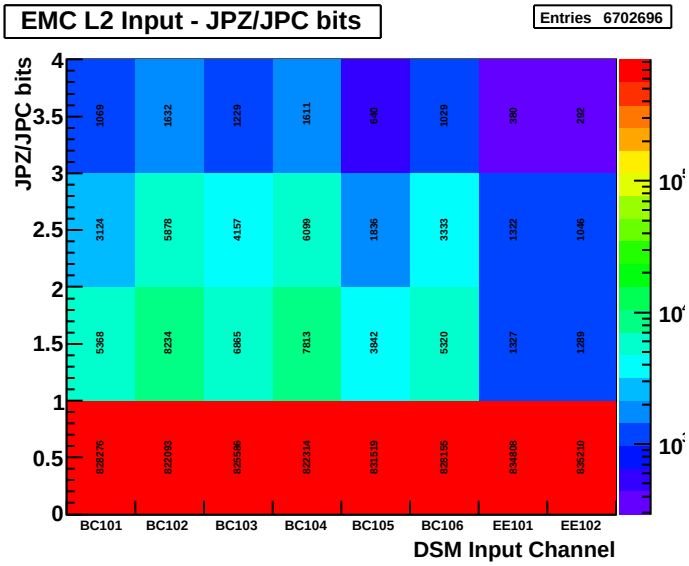

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

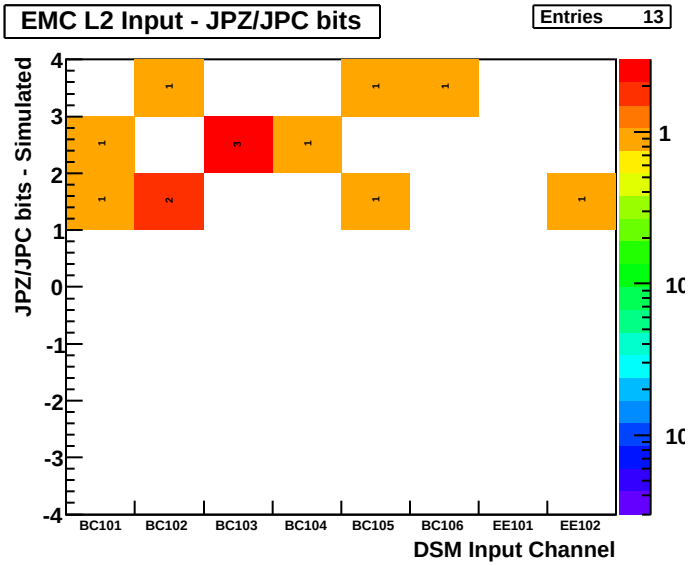

EMC L2 Input - JPX/JPA bits
Entries 6702696
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

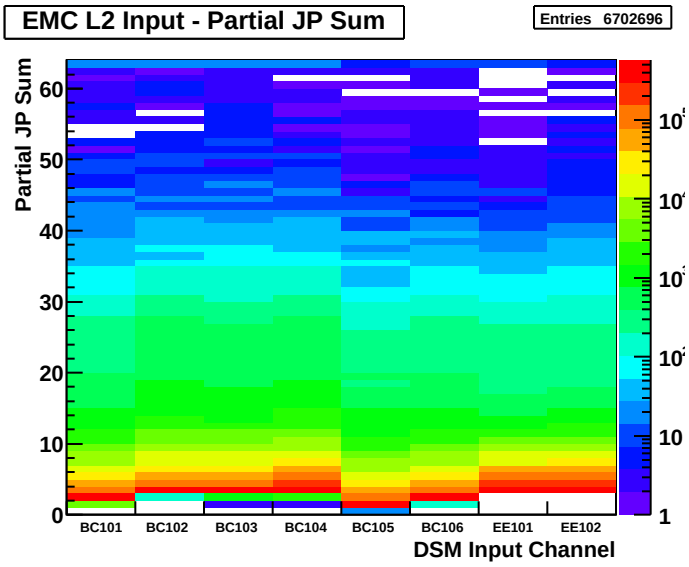

EMC L2 Input - JPX/JPA bits
Entries 978
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

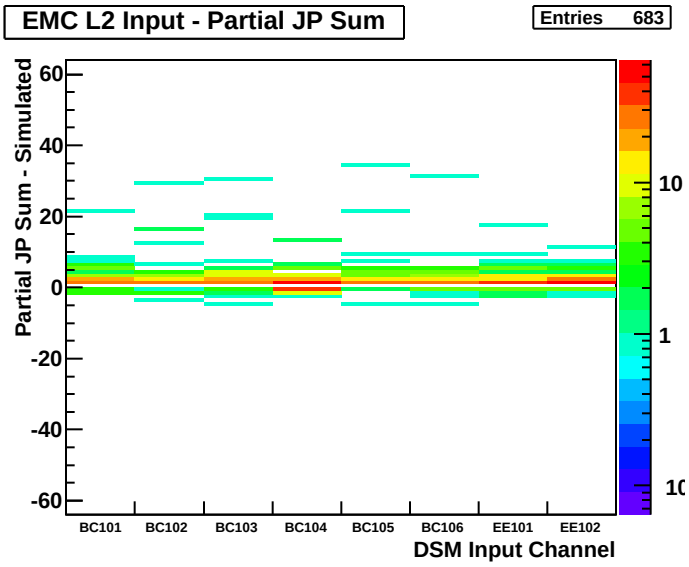

EMC L2 Input - JPY/JPB bits
Entries 6702696
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

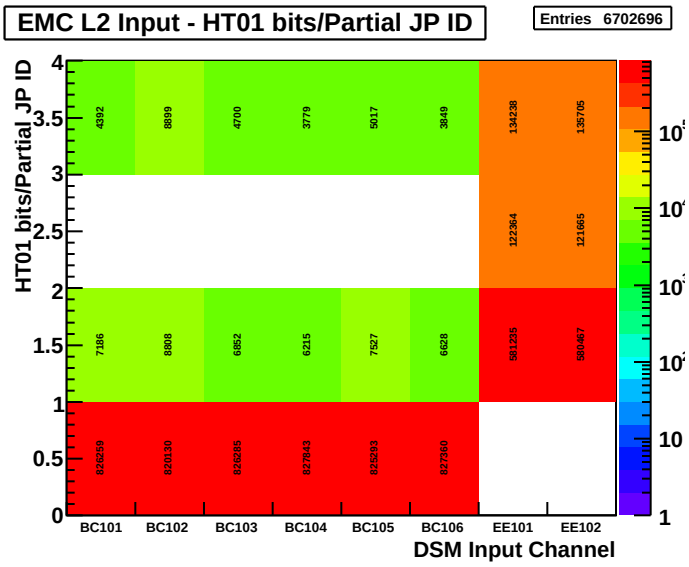

EMC L2 Input - JPY/JPB bits
Entries 969
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

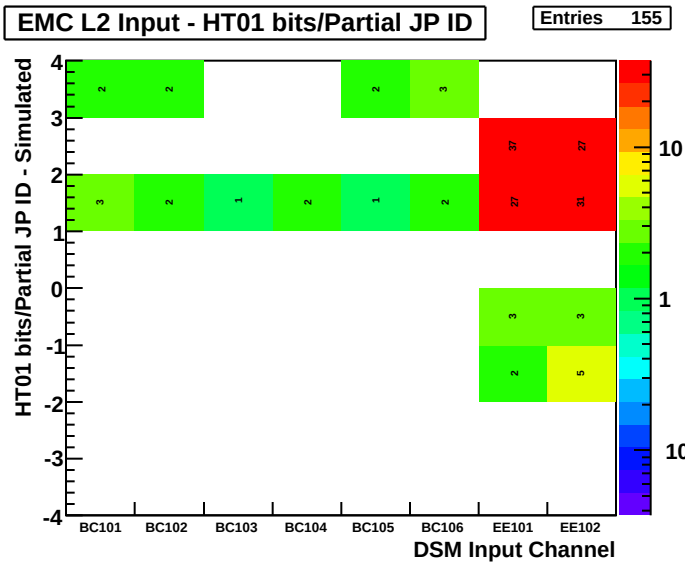

EMC L2 Input - JPZ/JPC bits
Entries 6702696
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

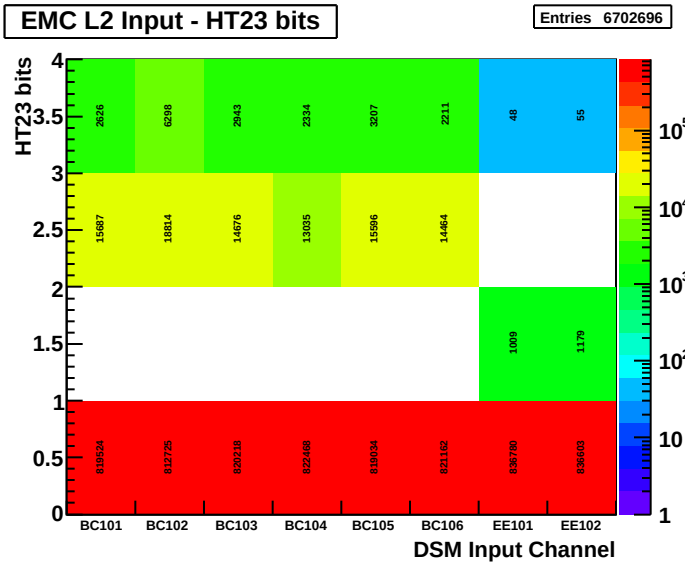

EMC L2 Input - JPZ/JPC bits
Entries 13
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

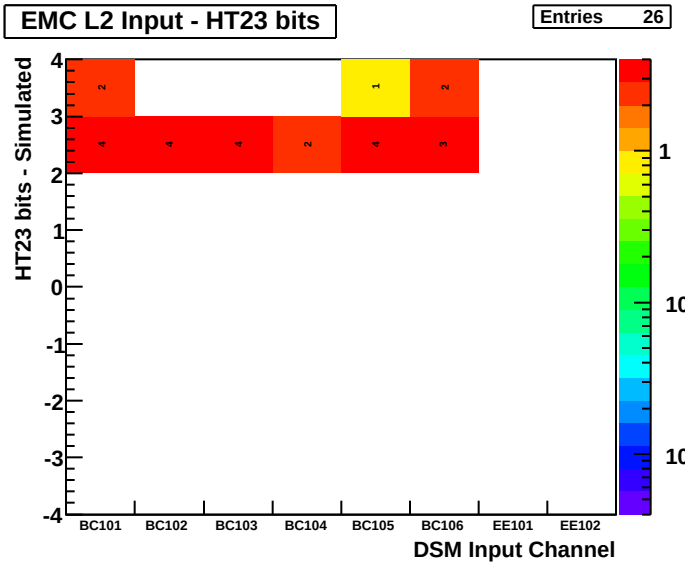

EMC L2 Input - Partial JP Sum
Entries 6702696
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

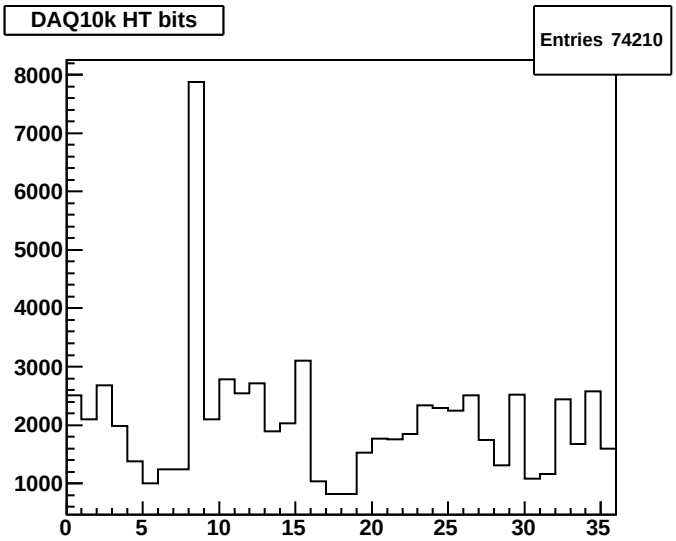

EMC L2 Input - Partial JP Sum
Entries 683
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

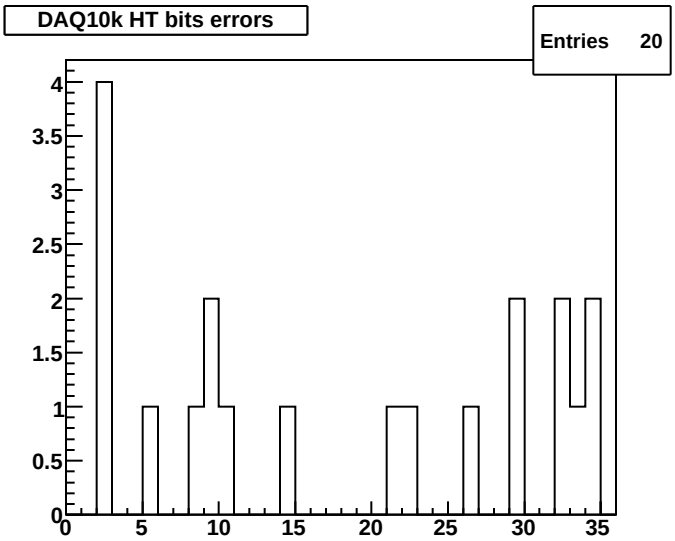

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6702696
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

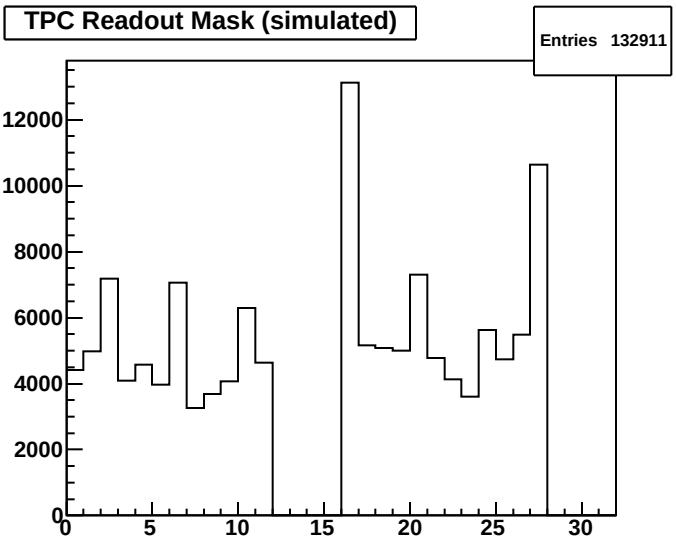

EMC L2 Input - HT01 bits/Partial JP ID
Entries 155
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

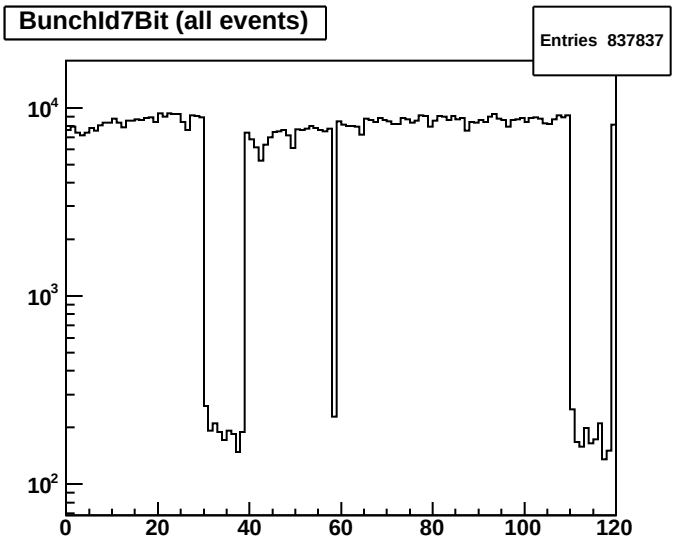

EMC L2 Input - HT23 bits
Entries 6702696
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

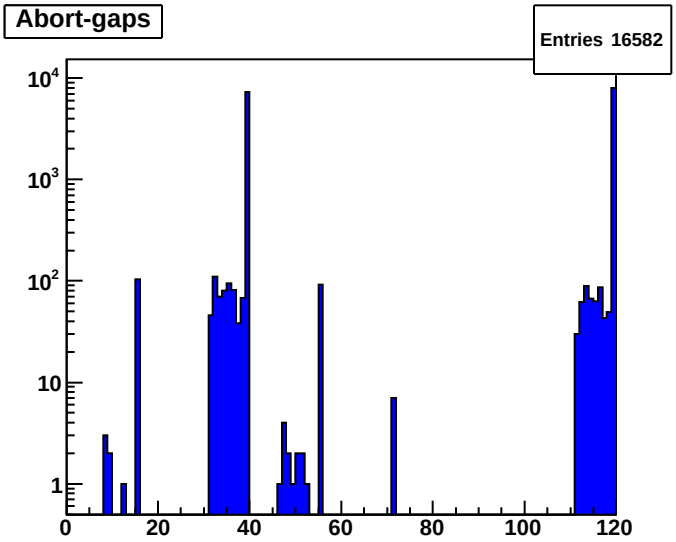

EMC L2 Input - HT23 bits
Entries 26
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

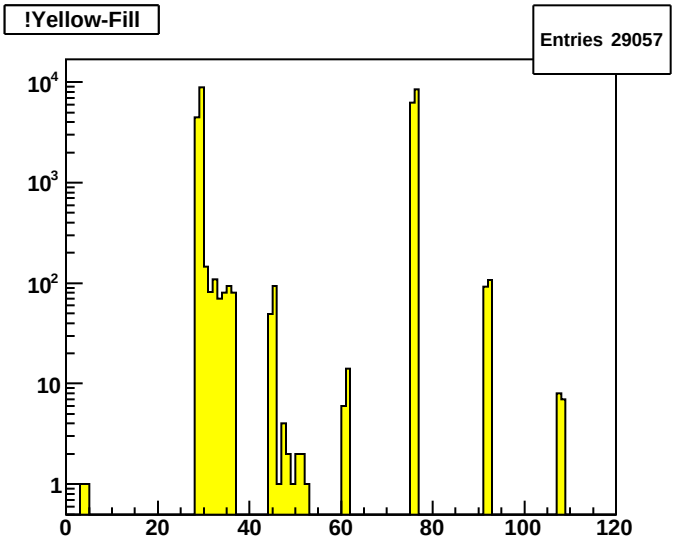

DAQ10k HT bits
Entries 74210

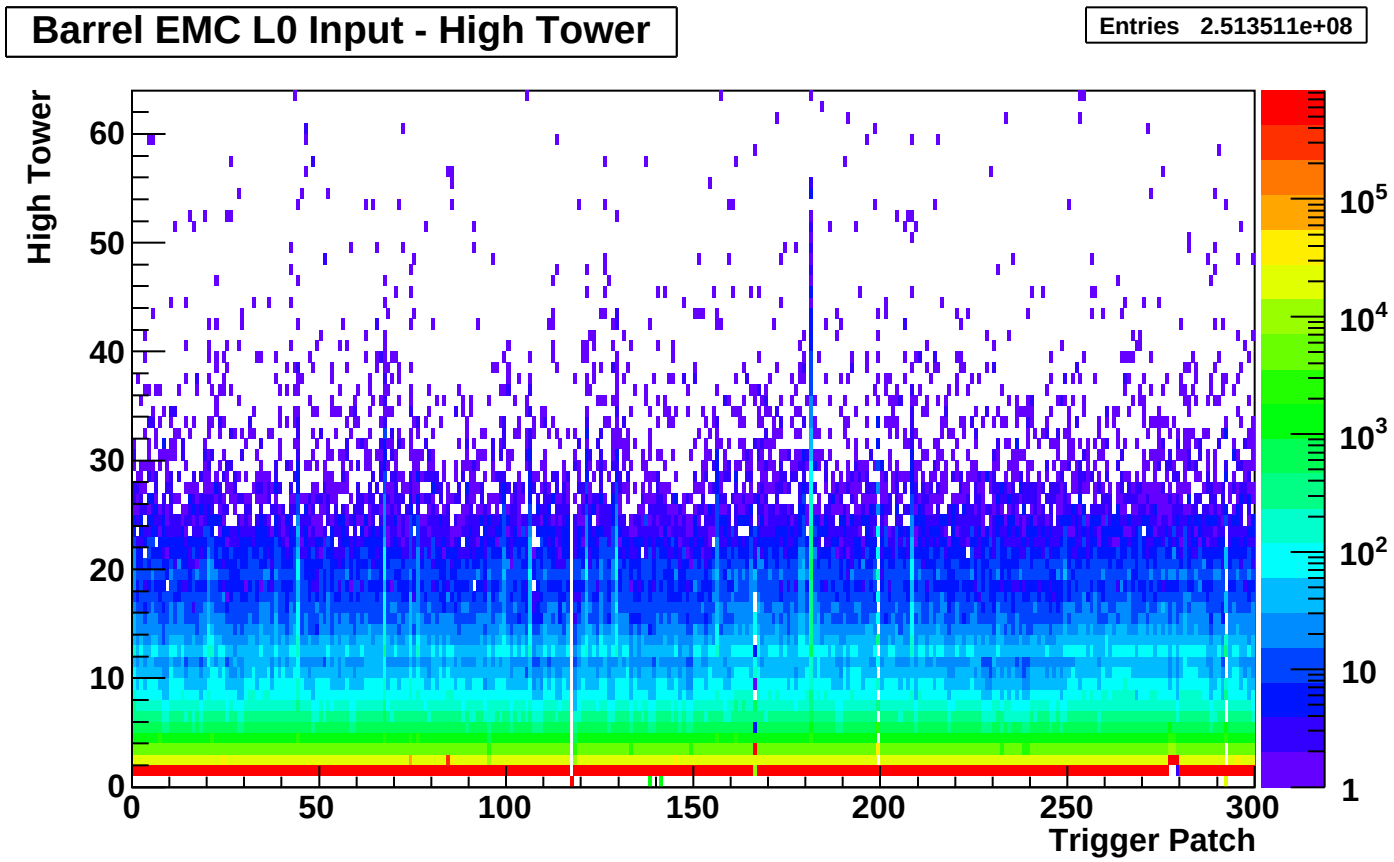

DAQ10k HT bits errors
Entries 20

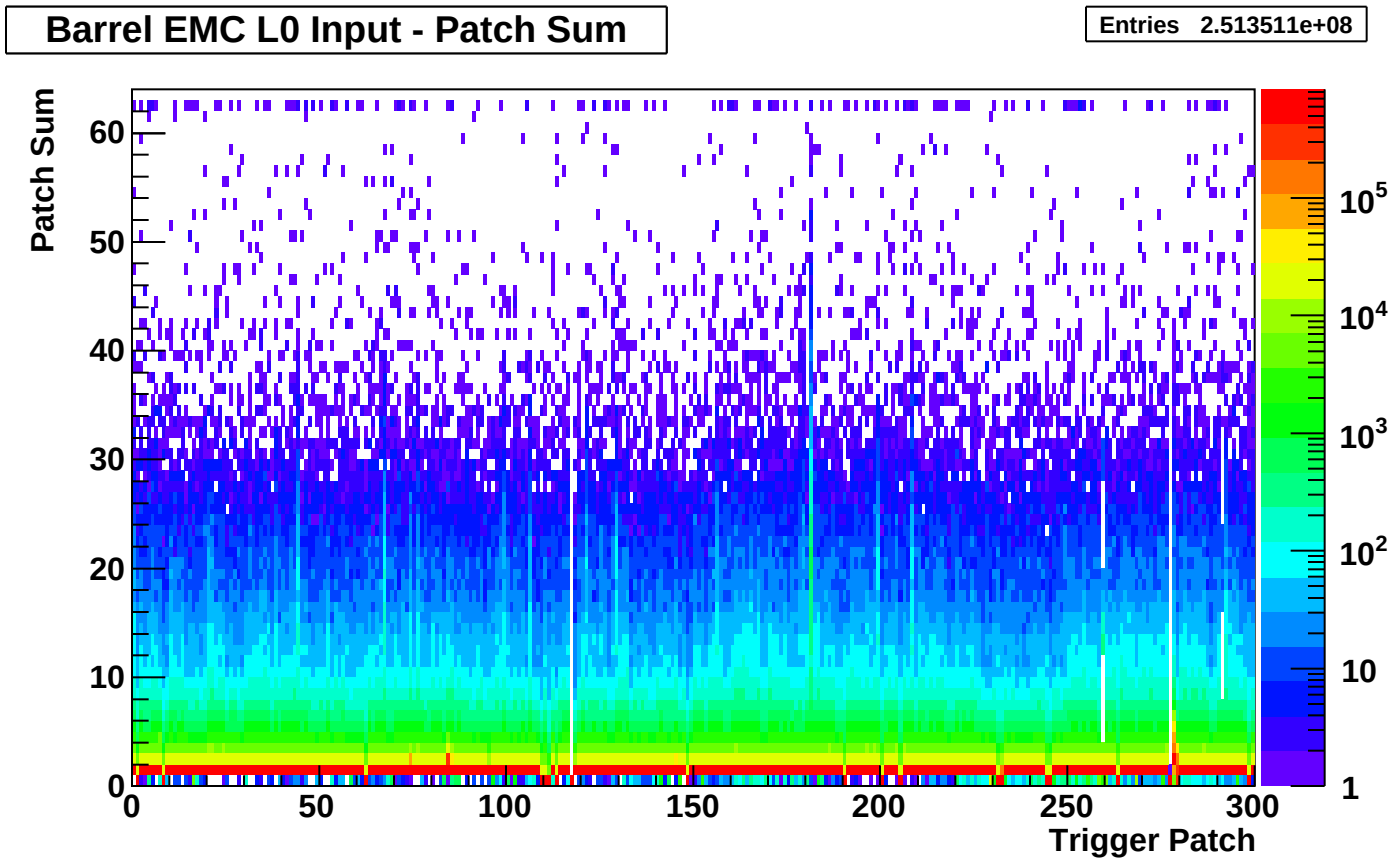

TPC Readout Mask (simulated)
Entries 132911

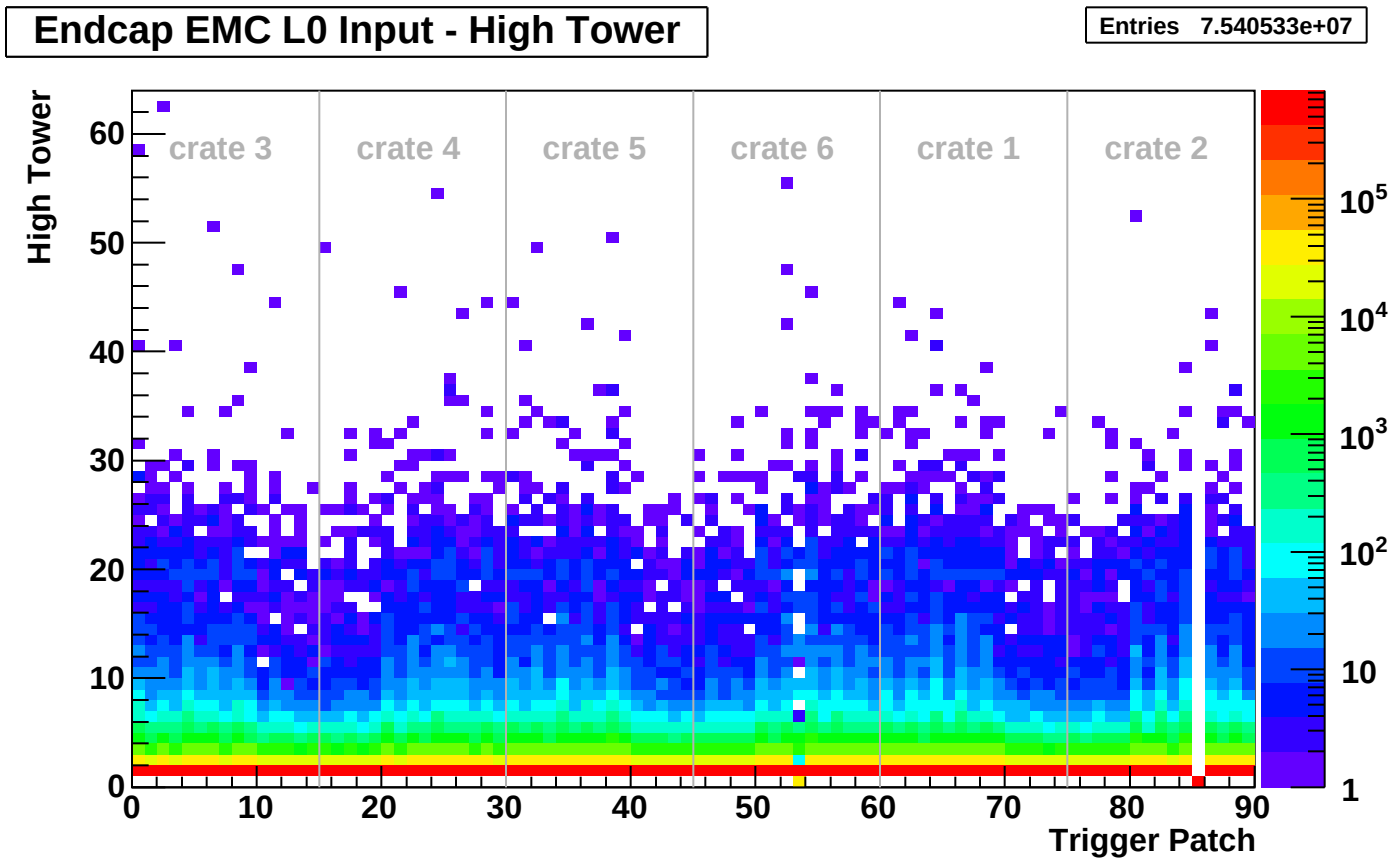

BunchId7Bit (all events)
Entries 837837

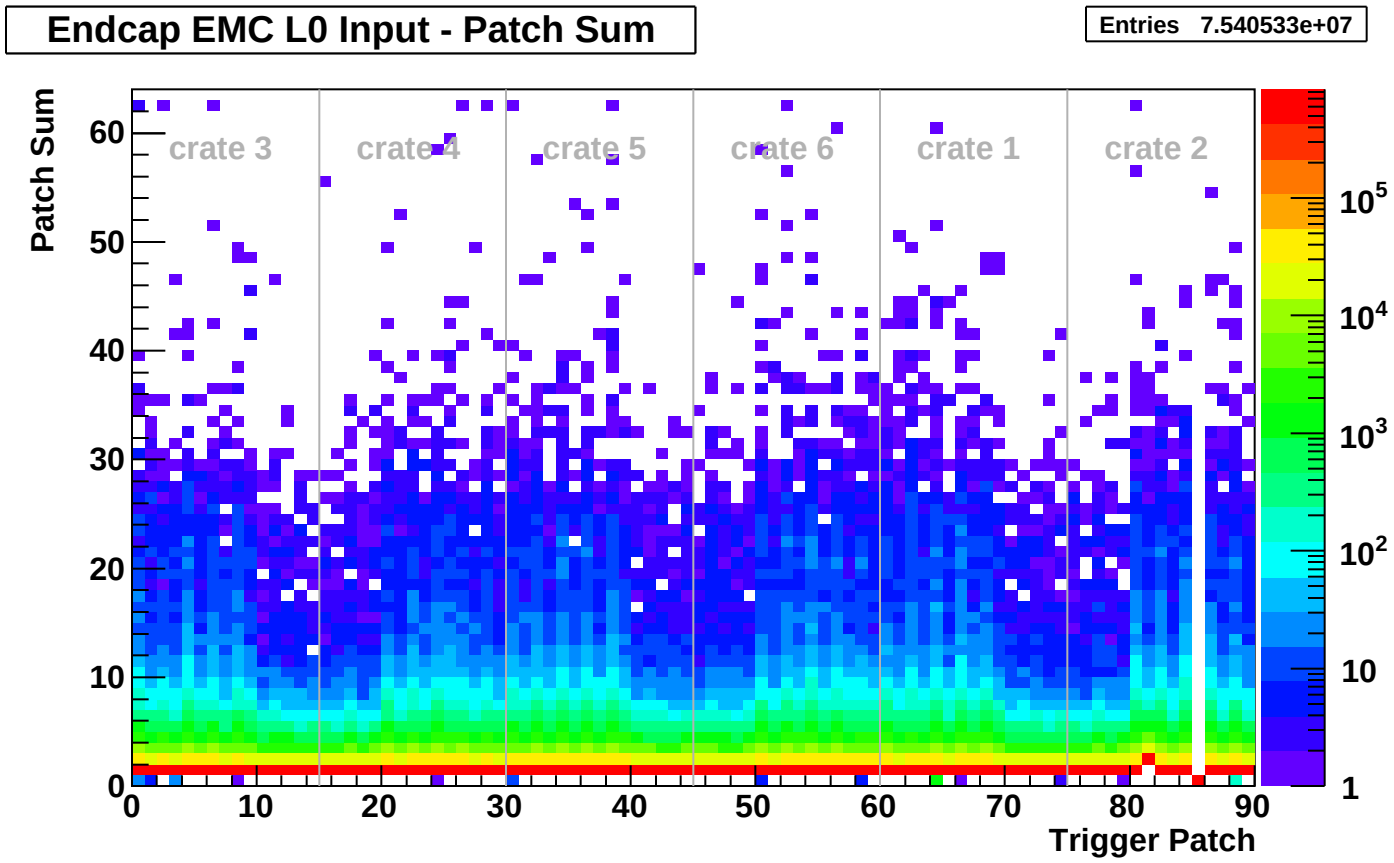

Abort-gaps
Entries 16582

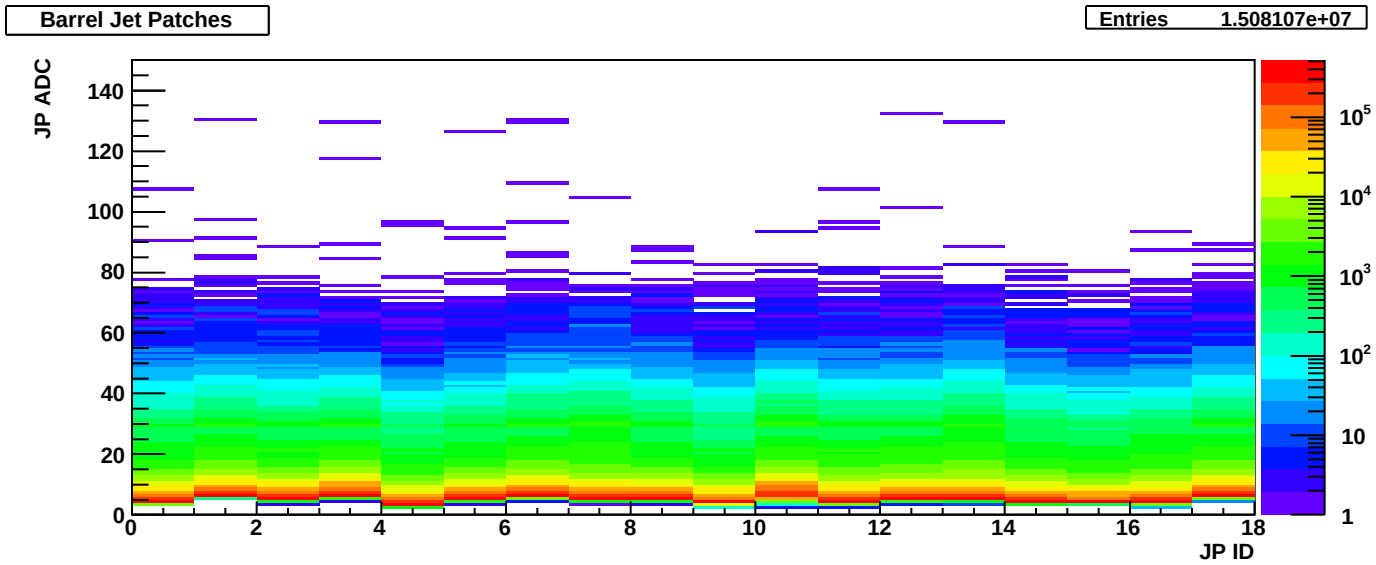

!Yellow-Fill
Entries 29057

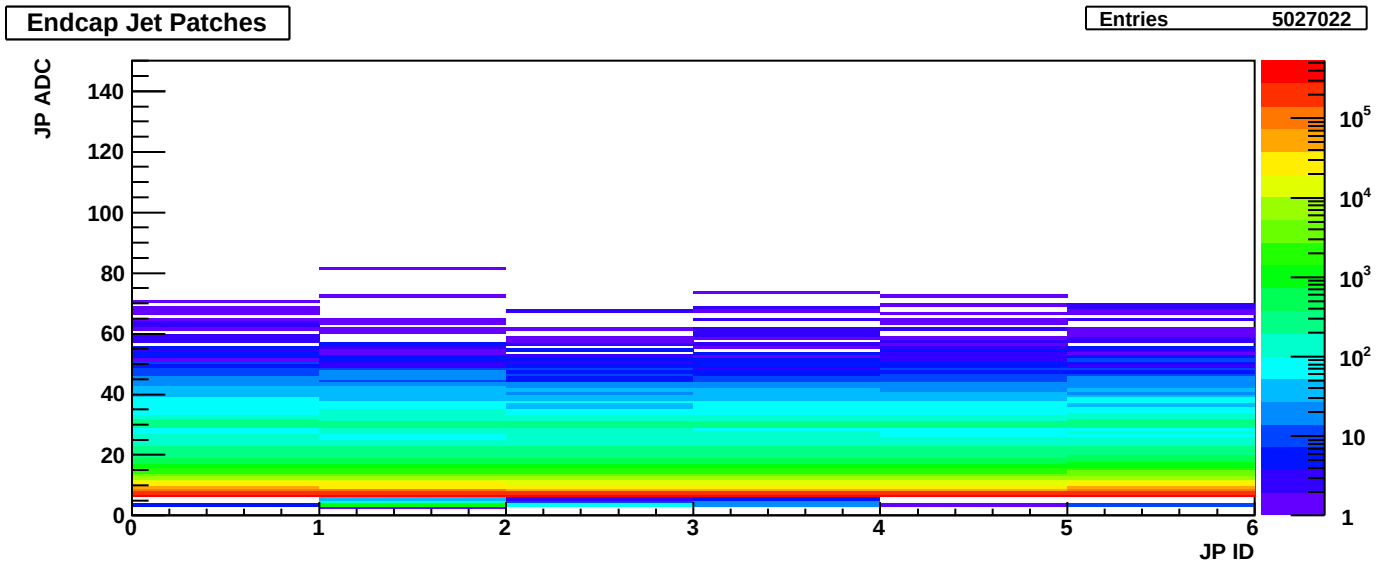

Barrel EMC L0 Input - High Tower
Entries 2.513511e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

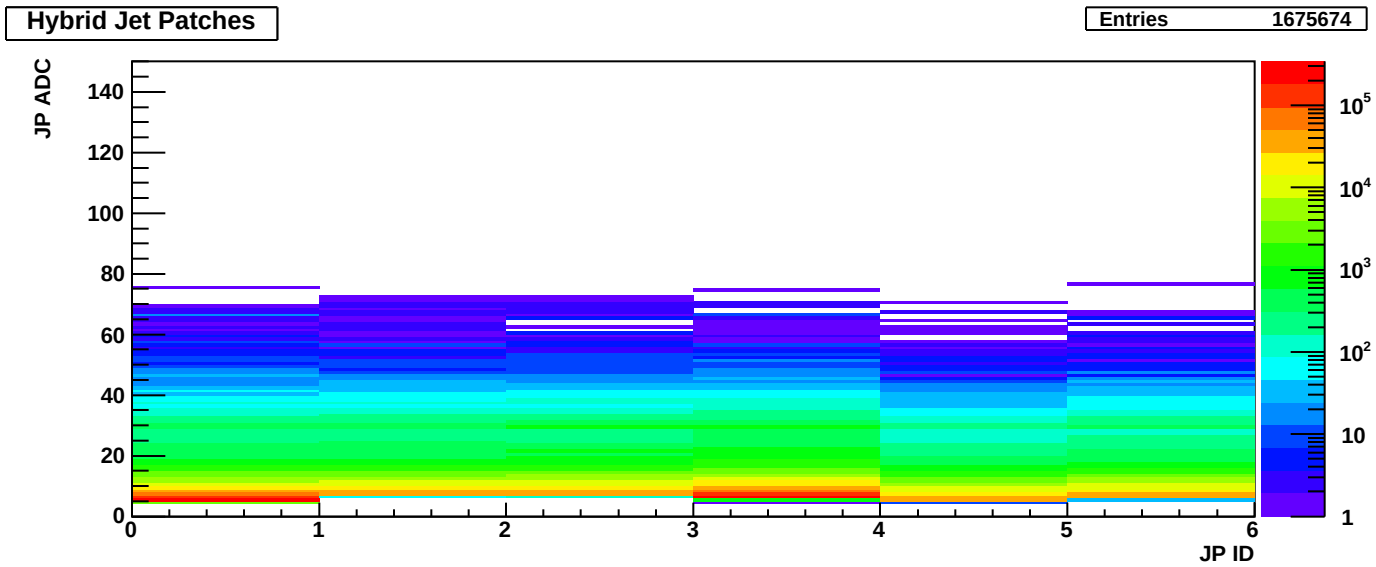

Barrel EMC L0 Input - Patch Sum
Entries 2.513511e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

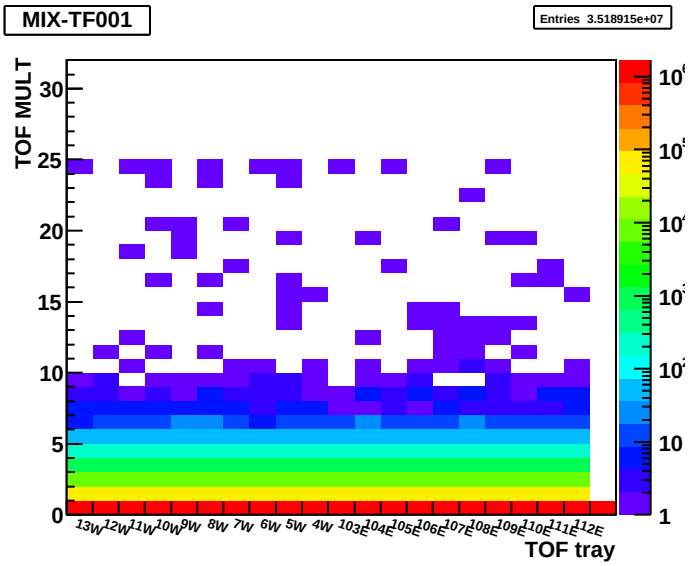

Endcap EMC L0 Input - High Tower
Entries 7.540533e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

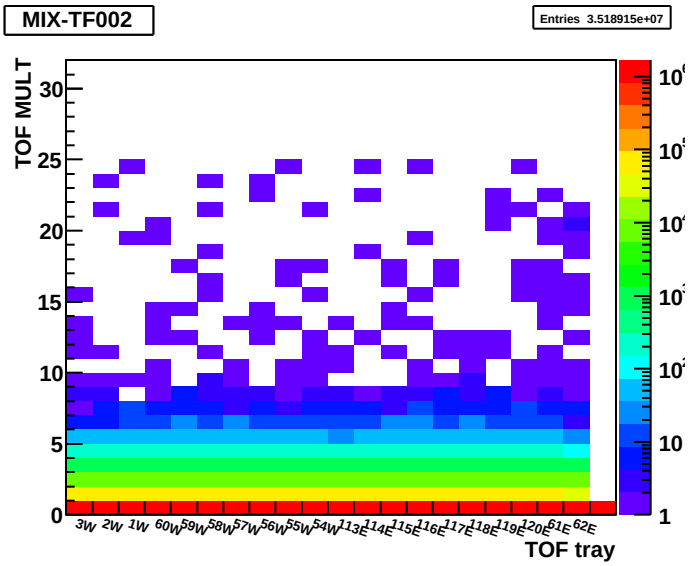

Endcap EMC L0 Input - Patch Sum
Entries 7.540533e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

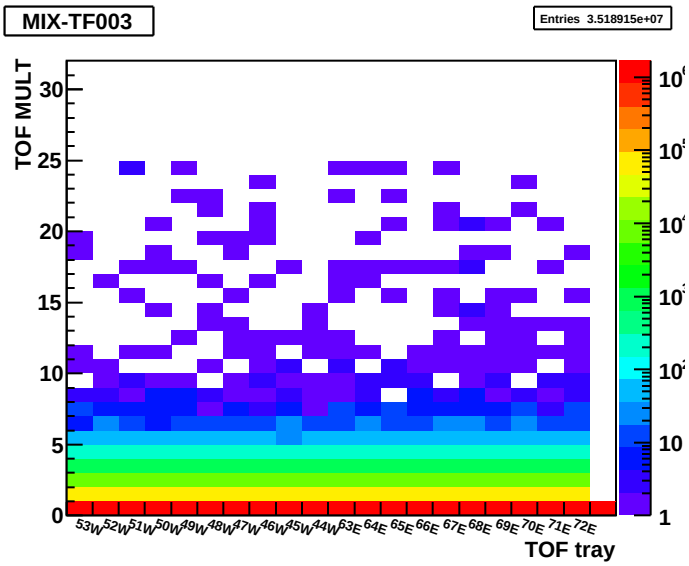

Barrel Jet Patches
Entries 1.508107e+07
JP ADC
JP ID

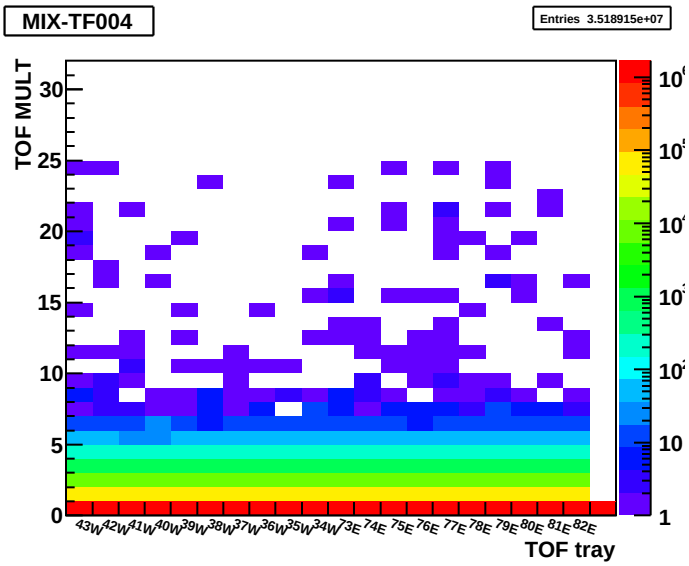

Endcap Jet Patches
Entries 5027022
JP ADC
JP ID

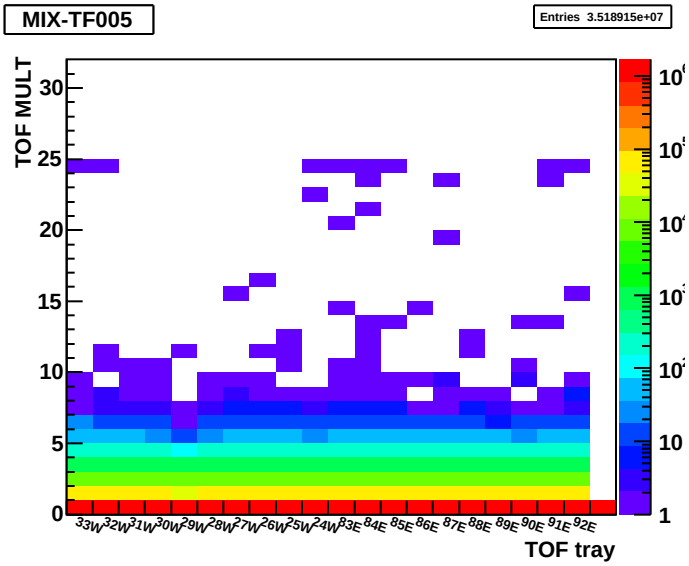

Hybrid Jet Patches
Entries 1675674
JP ADC
JP ID

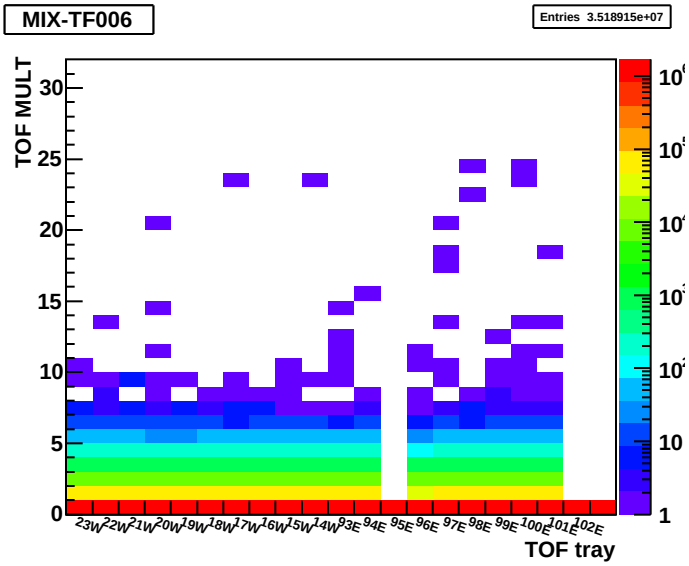

MIX-TF001
Entries 3.518915e+07
TOF MULT
TOF tray

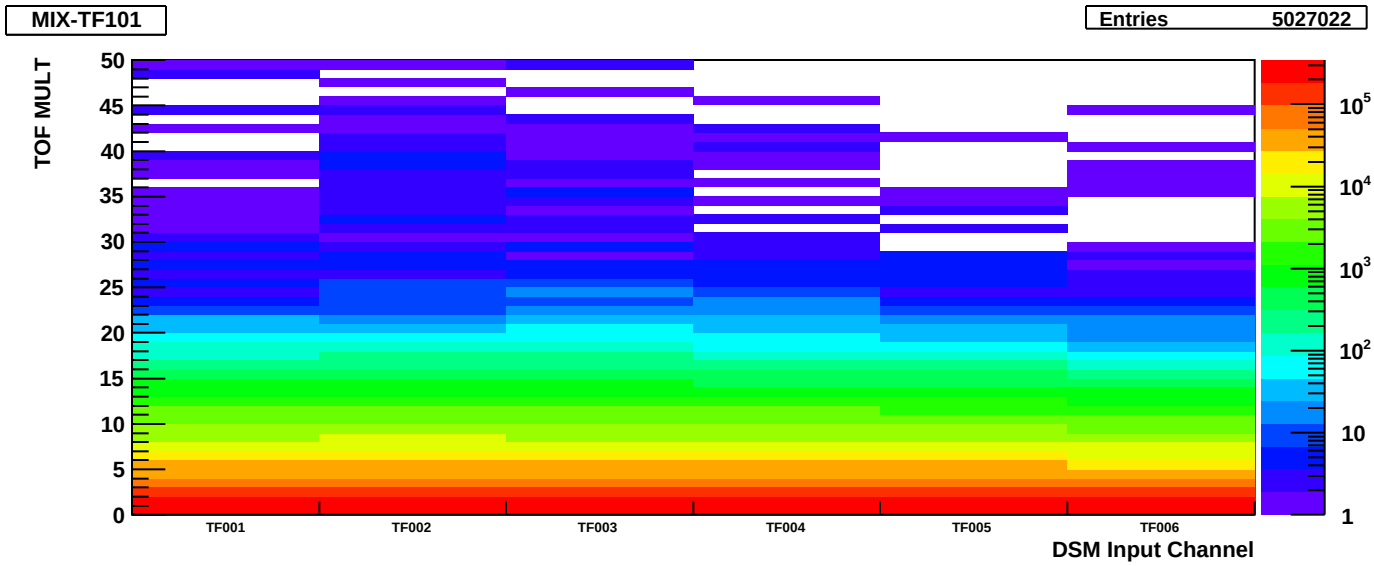

MIX-TF002
Entries 3.518915e+07
TOF MULT
TOF tray

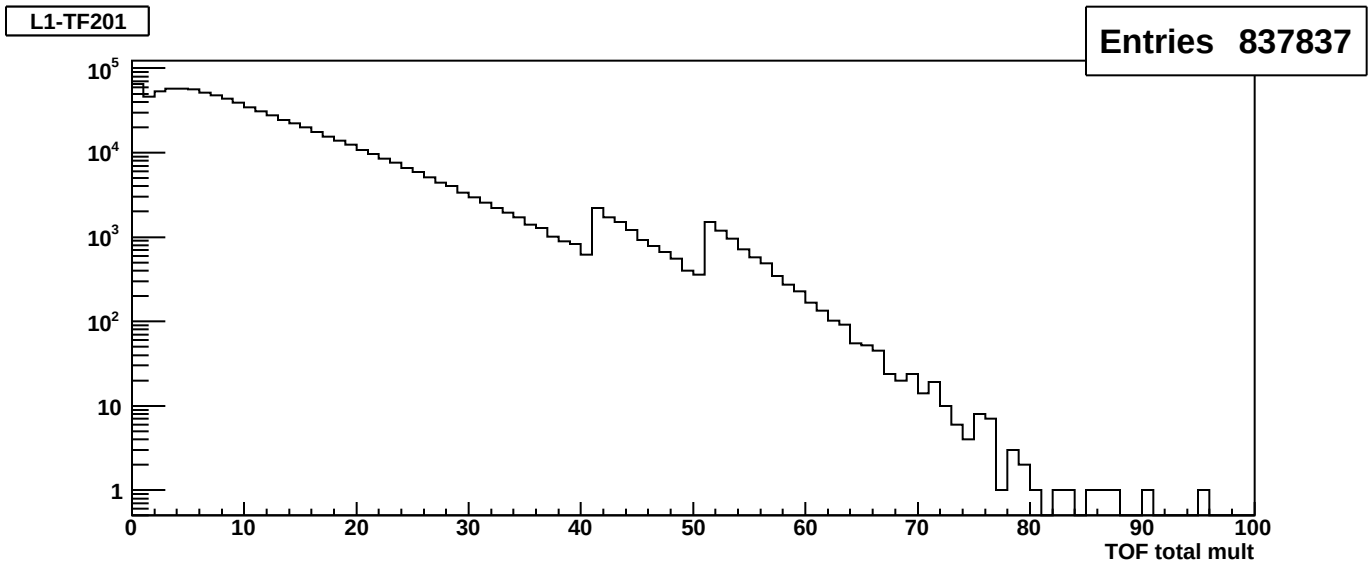

MIX-TF003
Entries 3.518915e+07
TOF MULT
TOF tray

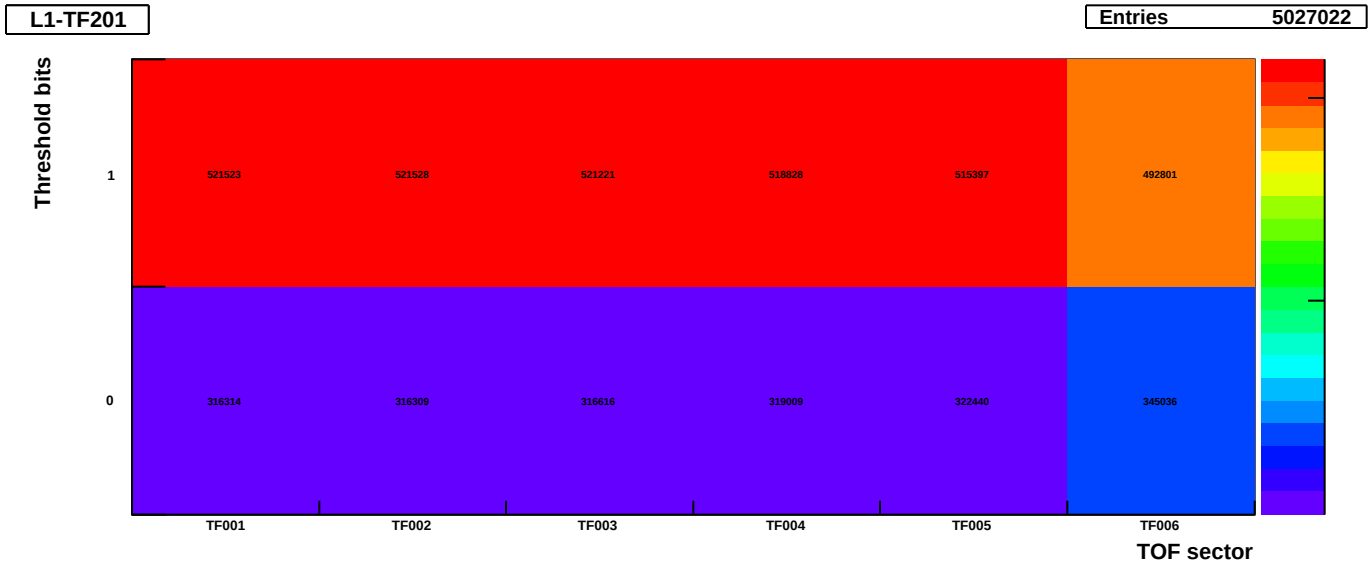

MIX-TF004
Entries 3.518915e+07
TOF MULT
TOF tray

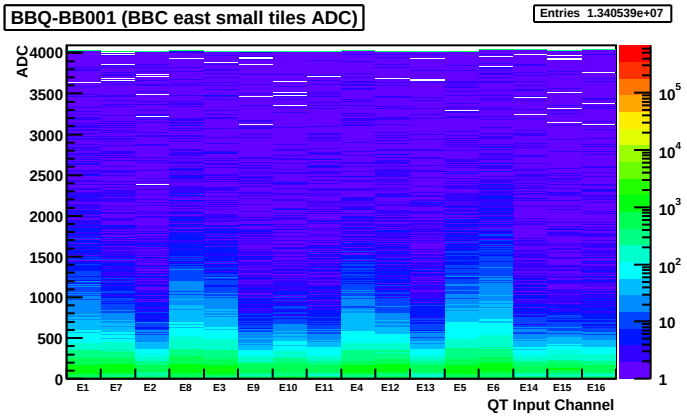

MIX-TF005
Entries 3.518915e+07
TOF MULT
TOF tray

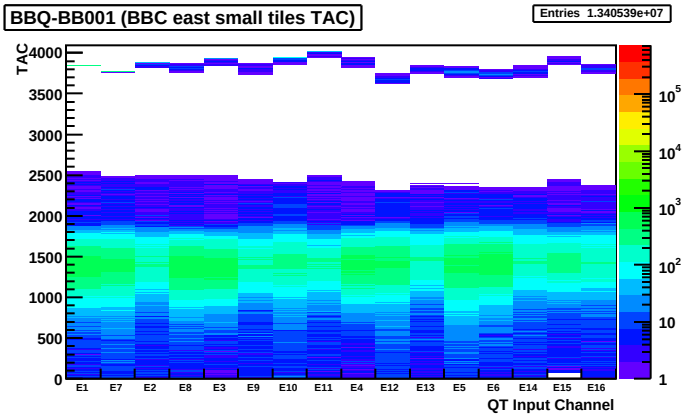

MIX-TF006
Entries 3.518915e+07
TOF MULT
TOF tray

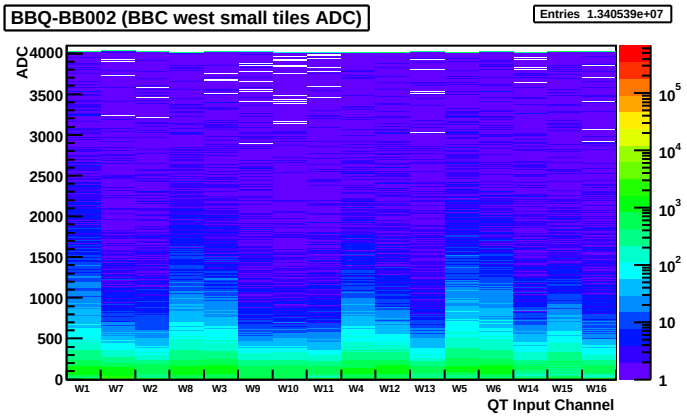

MIX-TF101
Entries 5027022
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

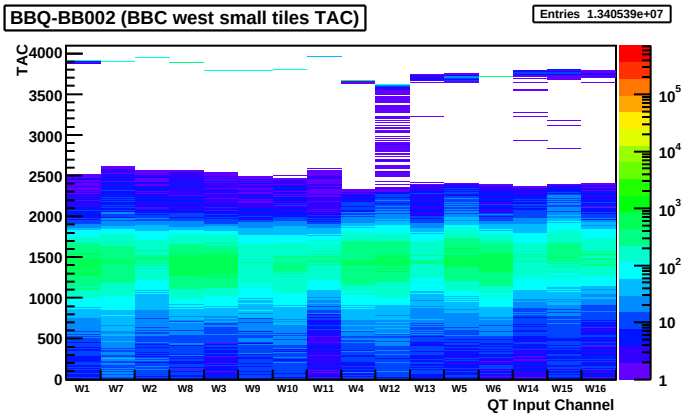

L1-TF201
Entries 837837
TOF total mult

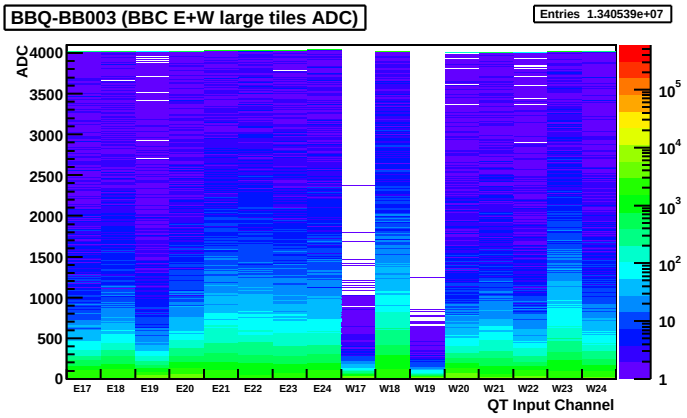

L1-TF201
Entries 5027022
Threshold bits
521523
521528
521221
518828
515397
492801
316314
316309
316616
319009
322440
345036
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

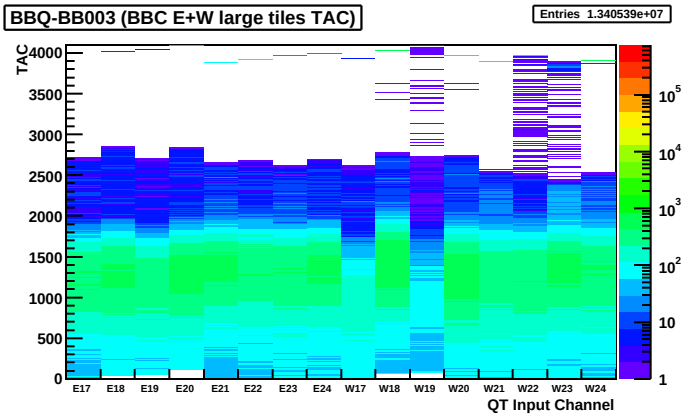

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.340539e+07
ADC
QT Input Channel

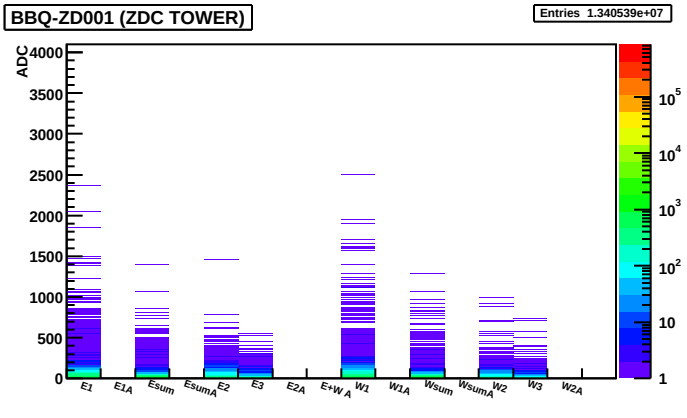

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.340539e+07
TAC
QT Input Channel

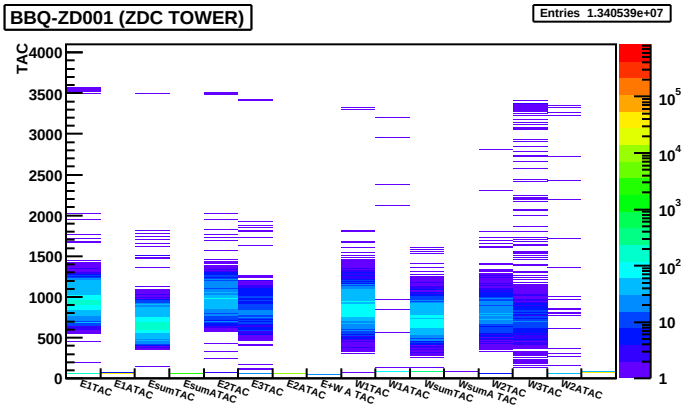

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.340539e+07
ADC
QT Input Channel

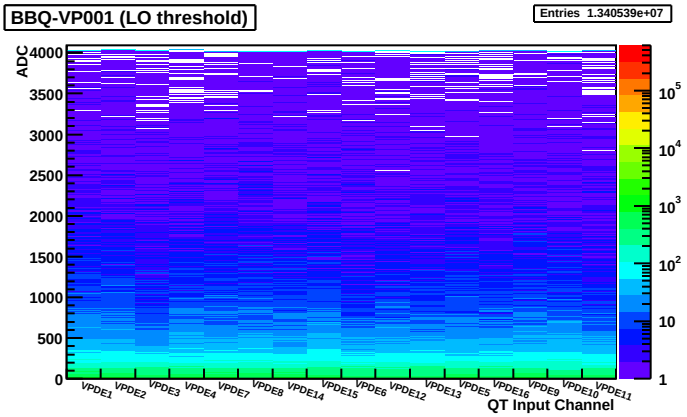

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.340539e+07
TAC
QT Input Channel

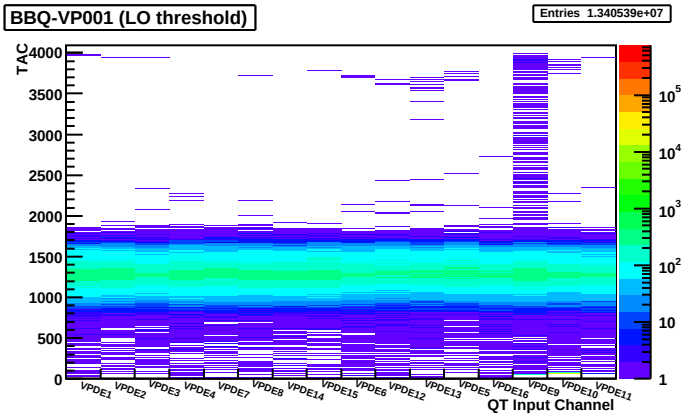

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.340539e+07
ADC
QT Input Channel

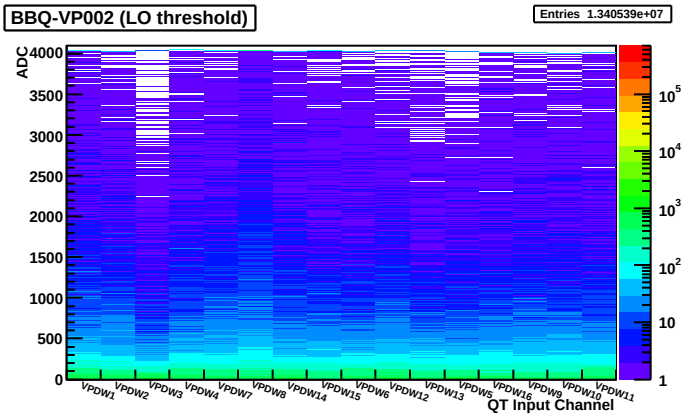

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.340539e+07
TAC
QT Input Channel

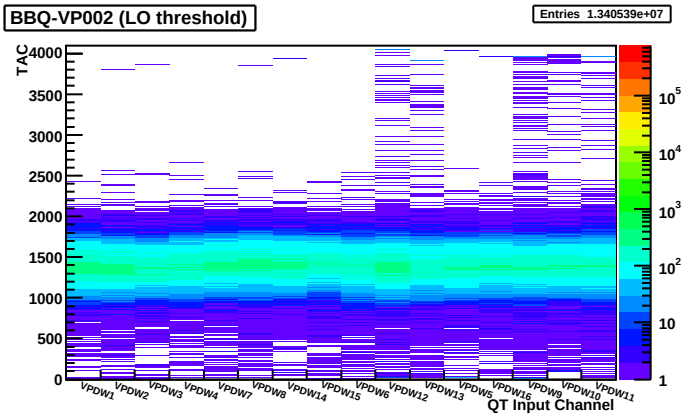

BBQ-ZD001 (ZDC TOWER)
Entries 1.340539e+07
ADC

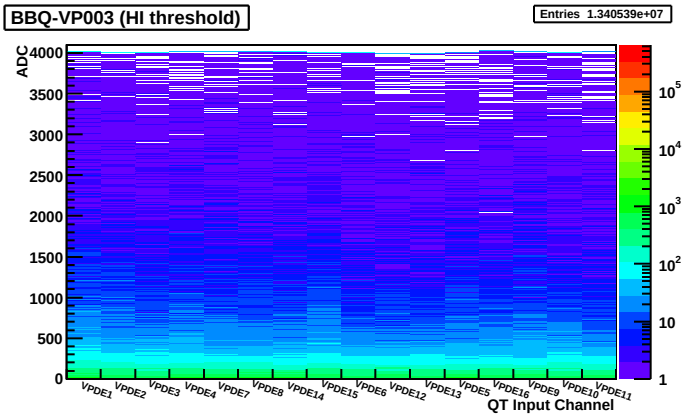

BBQ-ZD001 (ZDC TOWER)
Entries 1.340539e+07
TAC

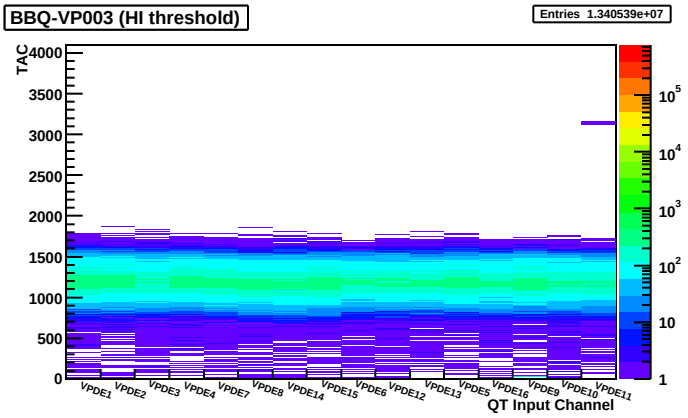

BBQ-VP001 (LO threshold)
Entries 1.340539e+07
ADC
QT Input Channel

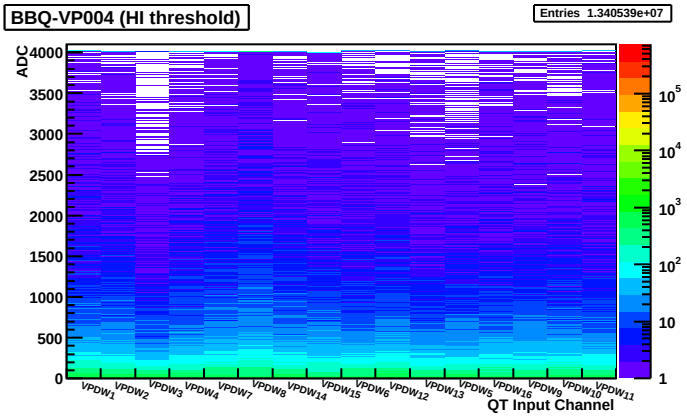

BBQ-VP001 (LO threshold)
Entries 1.340539e+07
TAC
QT Input Channel

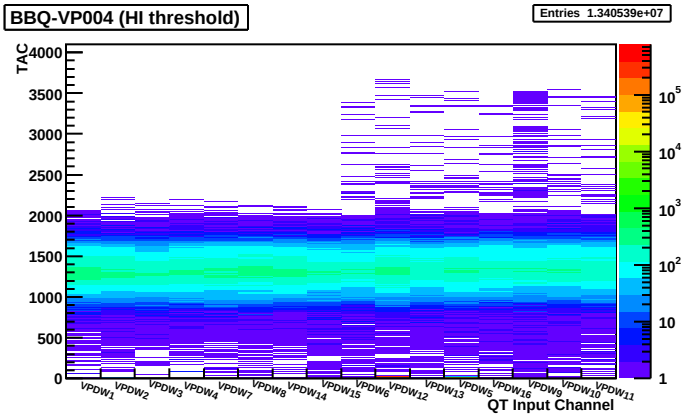

BBQ-VP002 (LO threshold)
Entries 1.340539e+07
ADC
QT Input Channel

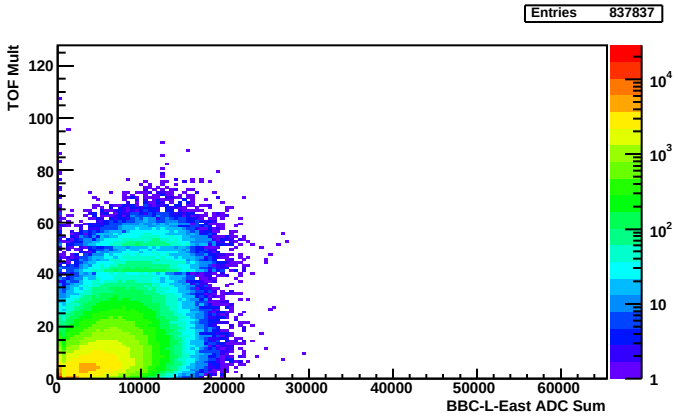

BBQ-VP002 (LO threshold)
Entries 1.340539e+07
TAC
QT Input Channel

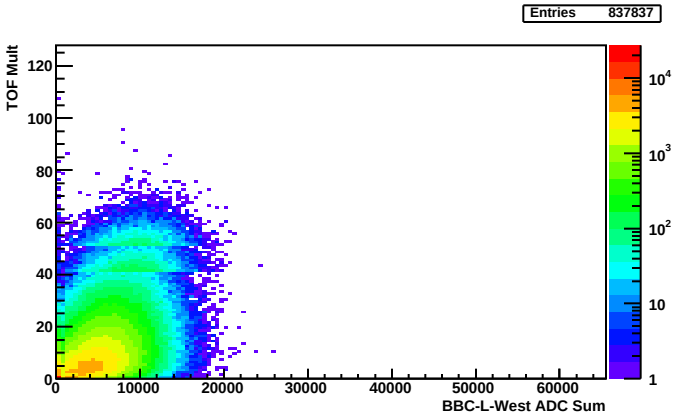

BBQ-VP003 (HI threshold)
Entries 1.340539e+07
ADC
QT Input Channel

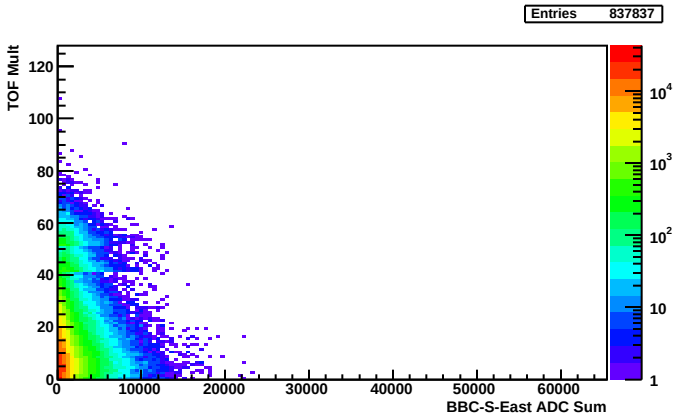

BBQ-VP003 (HI threshold)
Entries 1.340539e+07
TAC
QT Input Channel

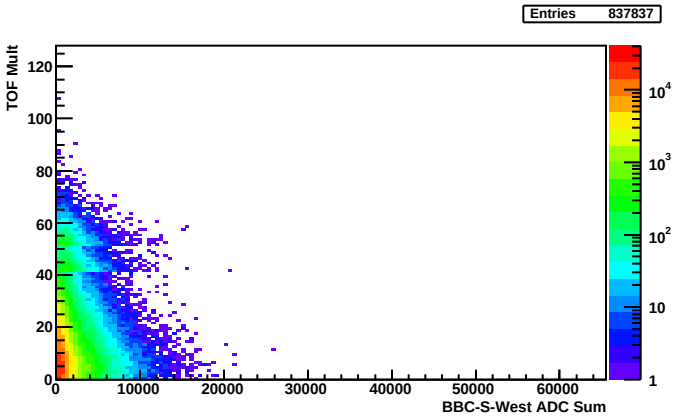

BBQ-VP004 (HI threshold)
Entries 1.340539e+07
ADC
QT Input Channel

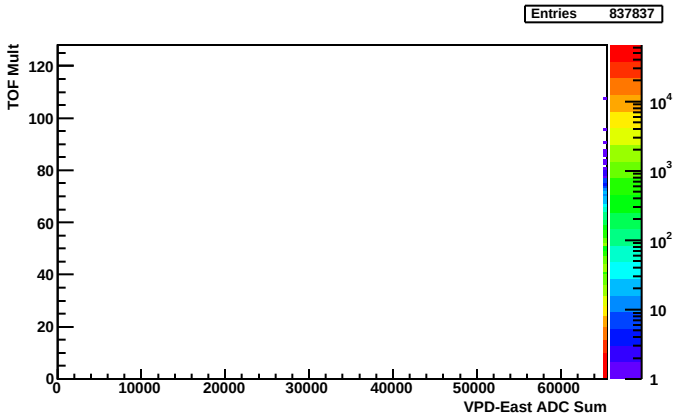

BBQ-VP004 (HI threshold)
Entries 1.340539e+07
TAC
QT Input Channel

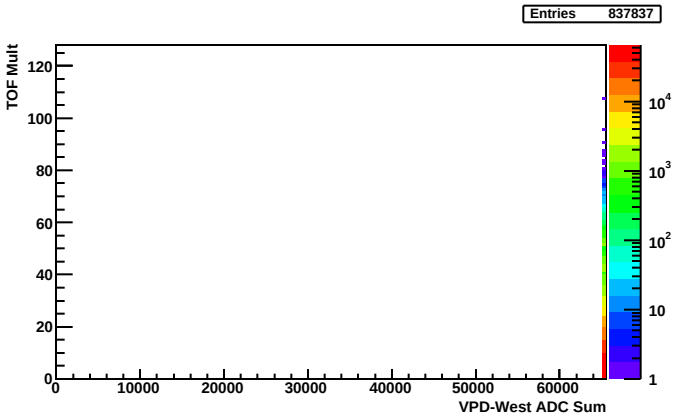

Entries 837837
TOF Mult
BBC-L-East ADC Sum

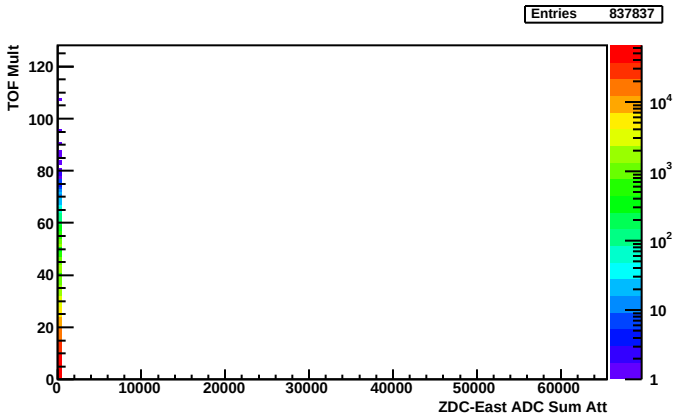

Entries 837837
TOF Mult
BBC-L-West ADC Sum

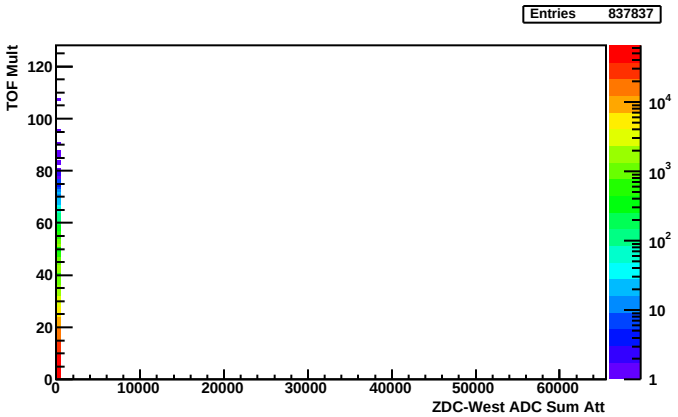

Entries 837837
TOF Mult
BBC-S-East ADC Sum

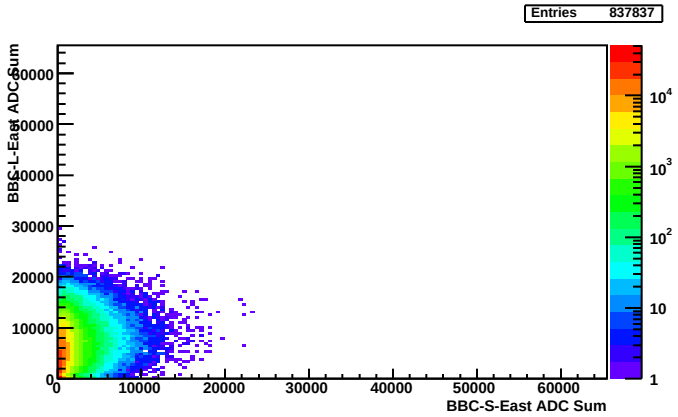

Entries 837837
TOF Mult
BBC-S-West ADC Sum

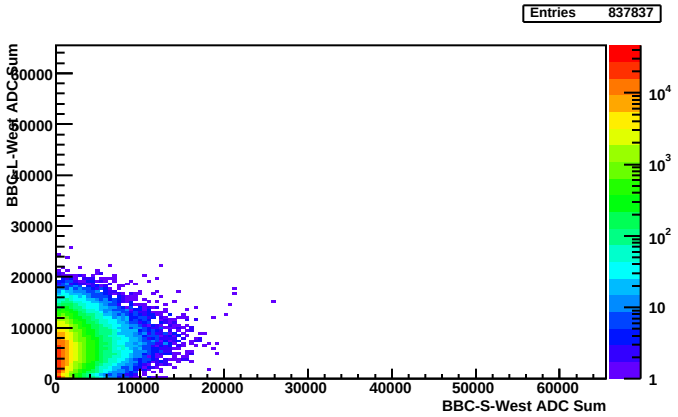

Entries 837837
TOF Mult
VPD-East ADC Sum

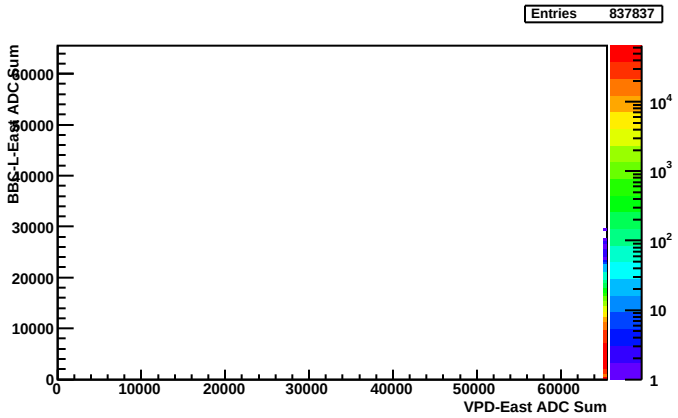

Entries 837837
TOF Mult
VPD-West ADC Sum

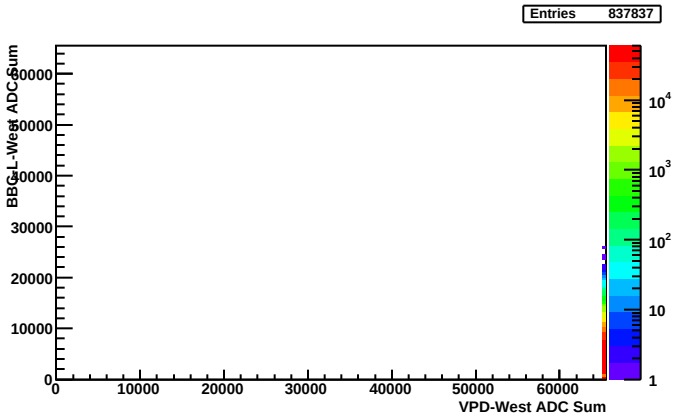

Entries 837837
TOF Mult
ZDC-East ADC Sum Att

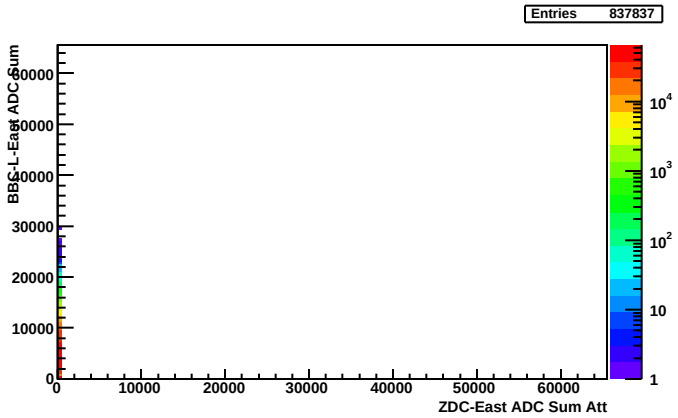

Entries 837837
TOF Mult
ZDC-West ADC Sum Att

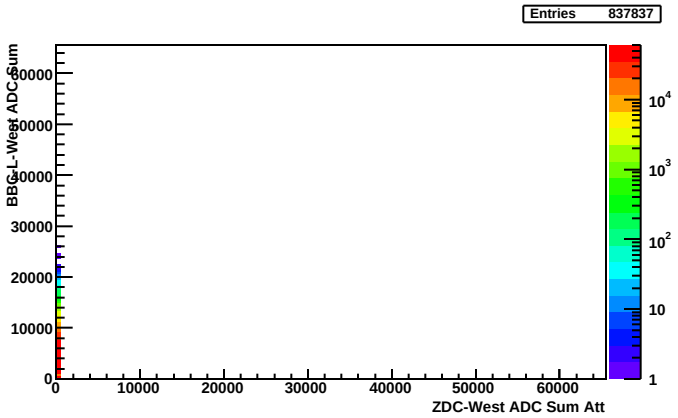

Entries 837837
BBC-L-East ADC Sum
BBC-S-East ADC Sum

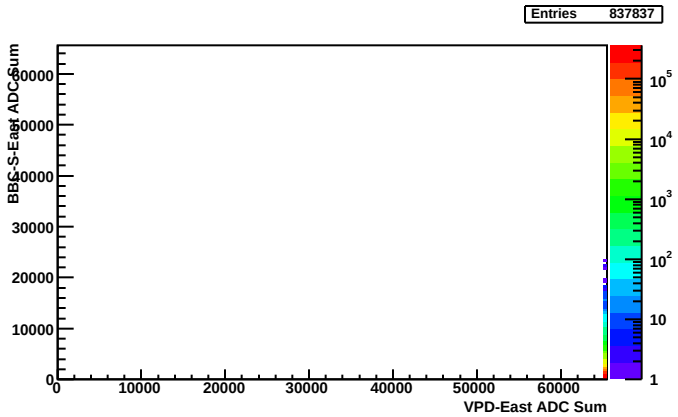

Entries 837837
BBC-L-West ADC Sum
BBC-S-West ADC Sum

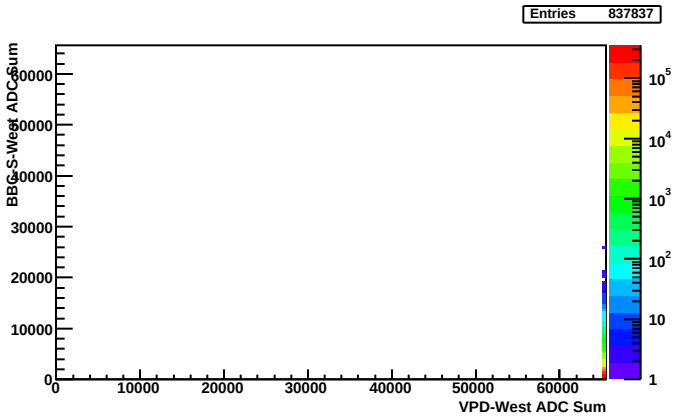

Entries 837837
BBC-L-East ADC Sum
VPD-East ADC Sum

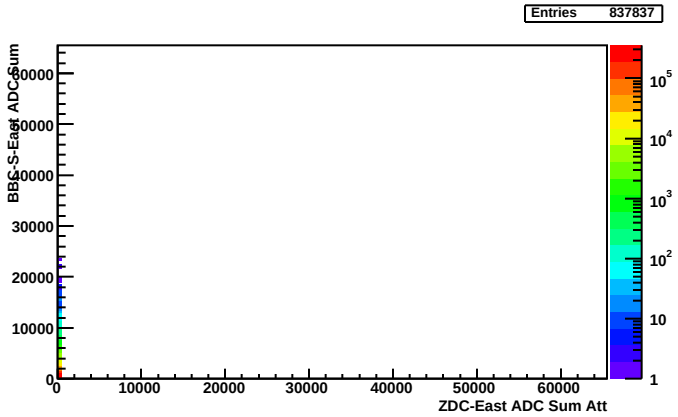

Entries 837837
BBC-L-West ADC Sum
VPD-West ADC Sum

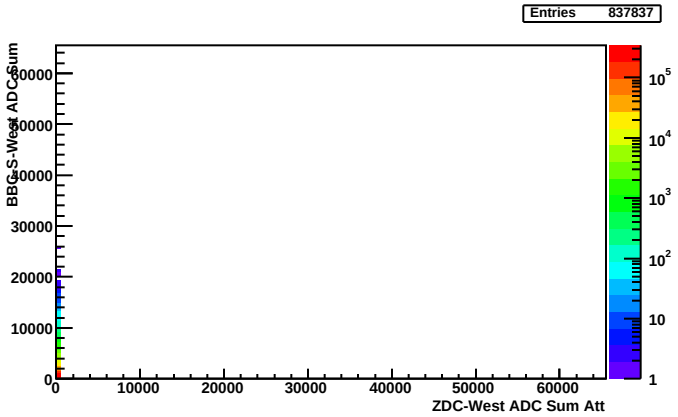

Entries 837837
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

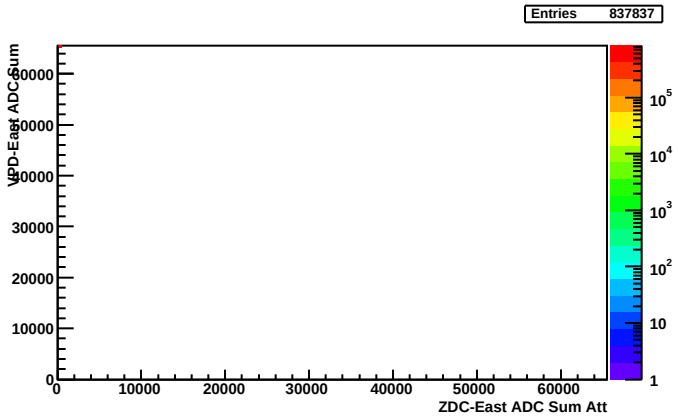

Entries 837837
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

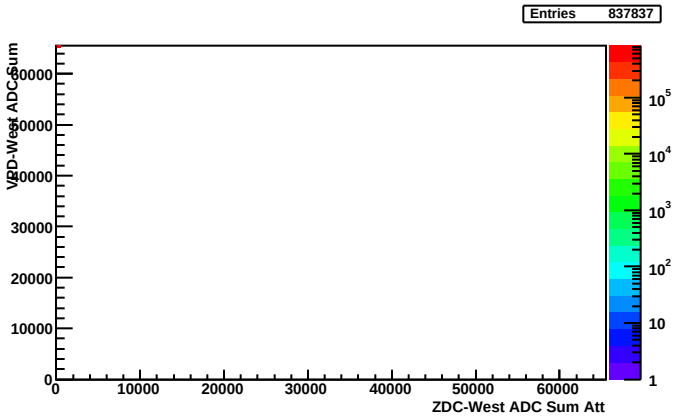

Entries 837837
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 837837
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 837837
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 837837
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 837837
VPD-East ADC Sum
ZDC-East ADC Sum Att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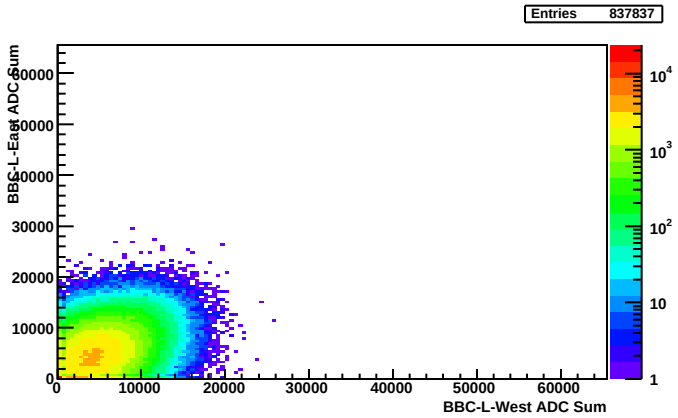
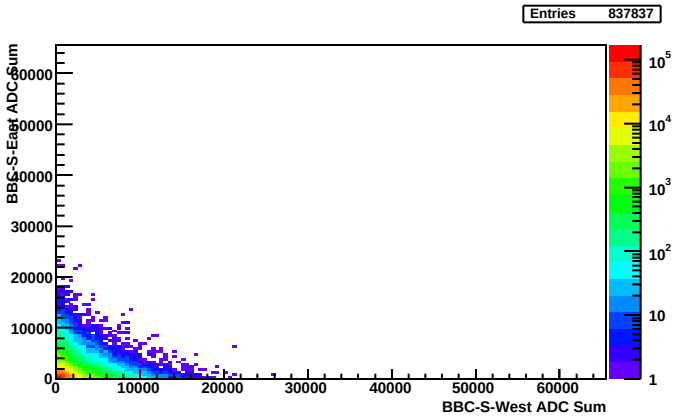
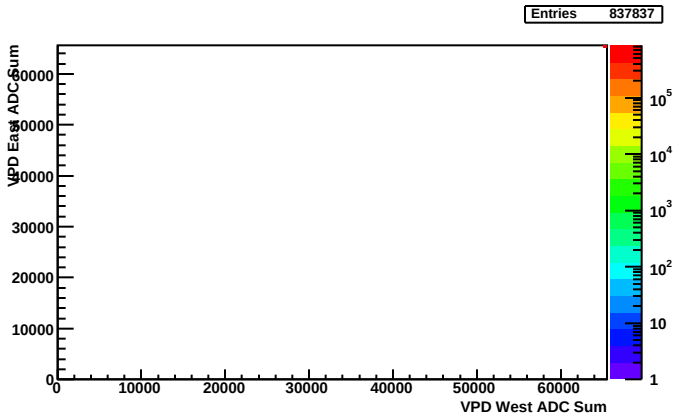
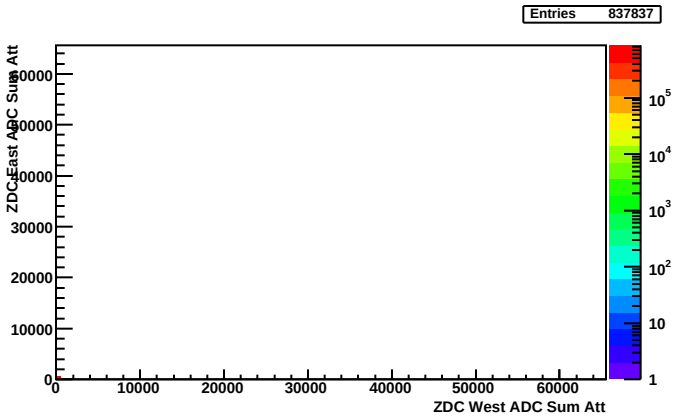
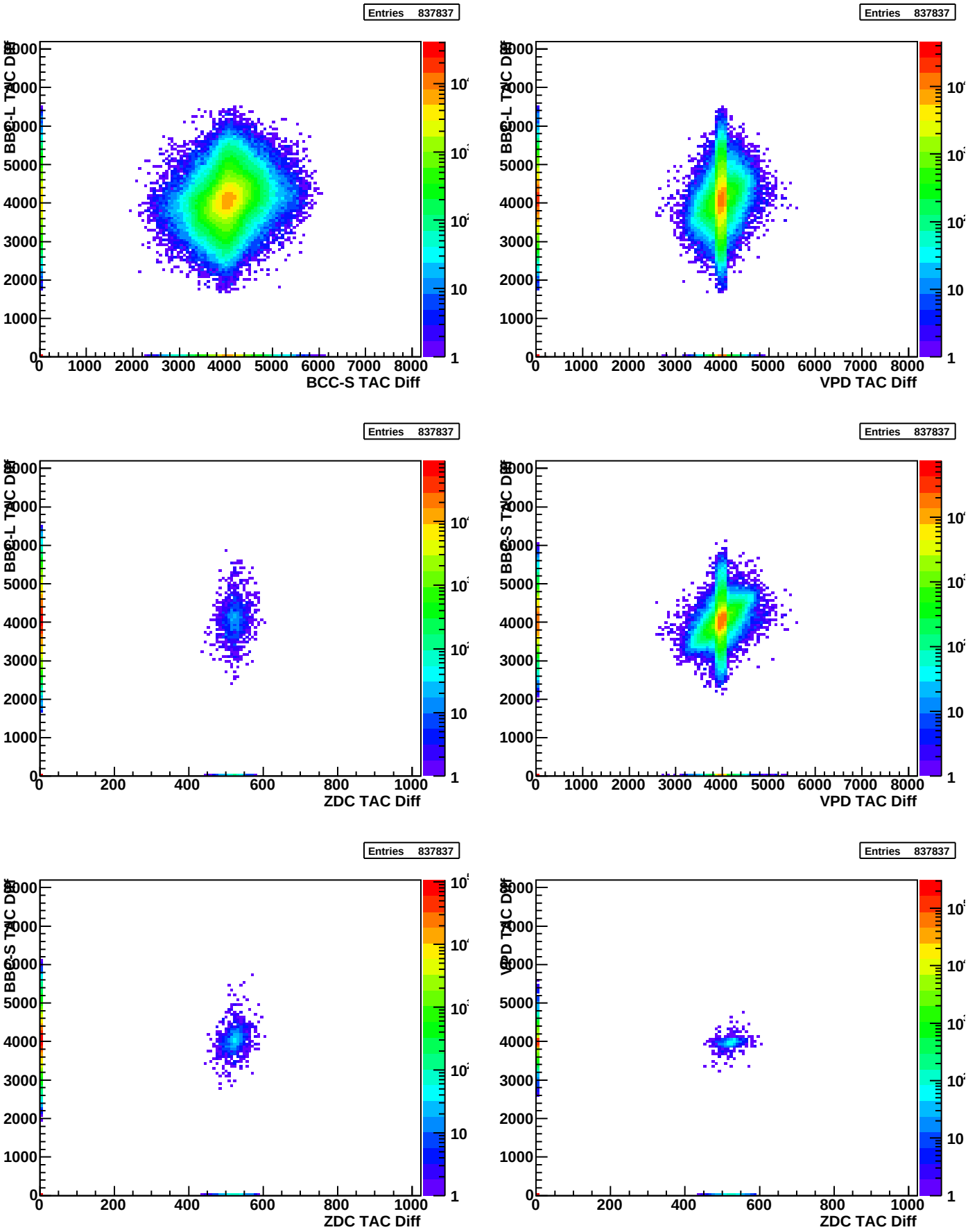
VPD-West ADC Sum
ZDC-West ADC Sum Att

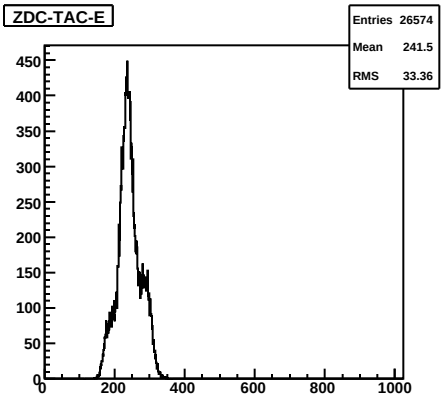

Entries 837837
BBC-L-East ADC Sum
BBC-L-West ADC Sum

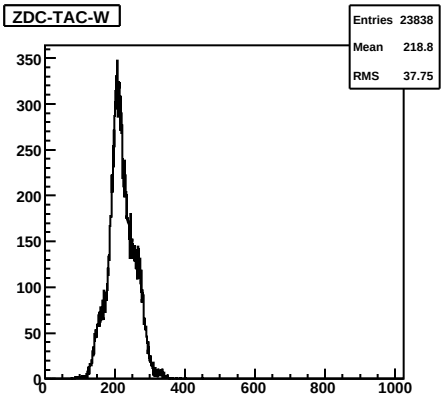

Entries 837837
BBC-S-East ADC Sum
BBC-S-West ADC Sum

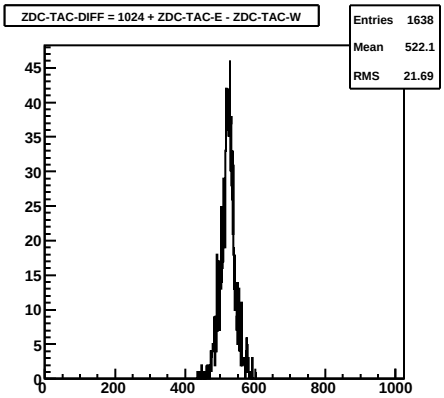

Entries 837837
VPD East ADC Sum
VPD West ADC Sum

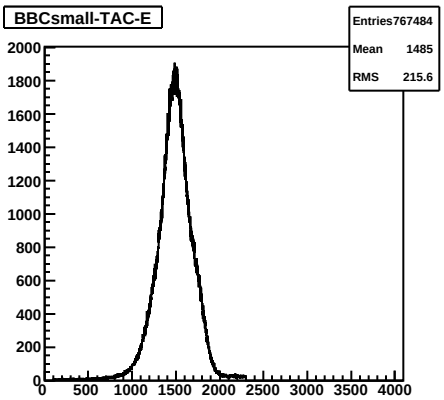

Entries 837837
ZDC East ADC Sum Att
ZDC West ADC Sum Att

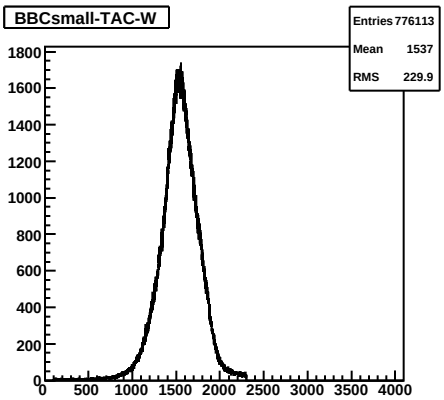

Entries 837837
BBC-L TAC Diff
BCC-S TAC Diff
Entries 837837
BBC-L TAC Diff
VPD TAC Diff
Entries 837837
BBC-L TAC Diff
ZDC TAC Diff
Entries 837837
BBC-S TAC Diff
VPD TAC Diff
Entries 837837
BBC-S TAC Diff
ZDC TAC Diff
Entries 837837
VPD TAC Diff
ZDC TAC Diff

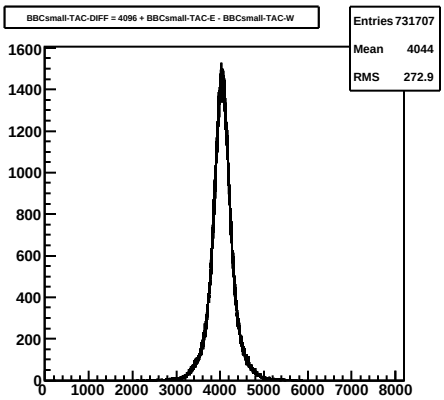

ZDC-TAC-E
Entries 26574
Mean 241.5
RMS 33.36

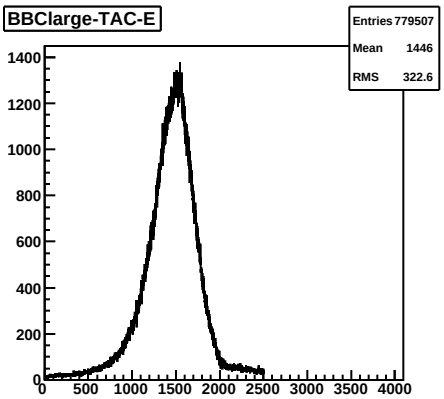

ZDC-TAC-W
Entries 23838
Mean 218.8
RMS 37.75

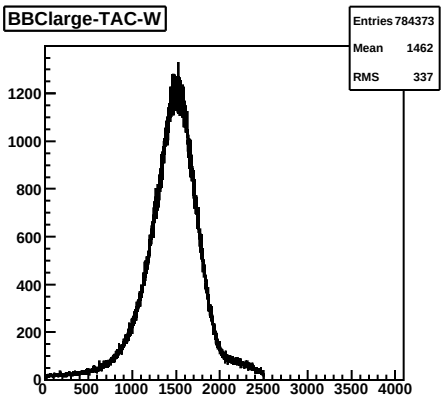

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1638
Mean 522.1
RMS 21.69

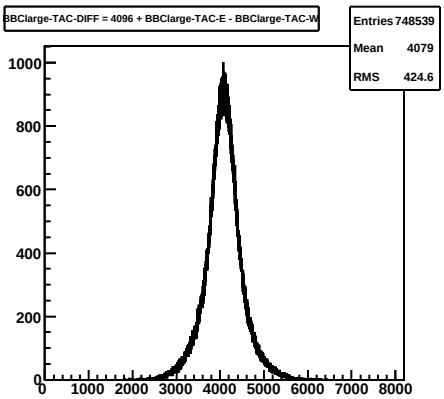

BBCsmall-TAC-E
Entries 767484
Mean 1485
RMS 215.6

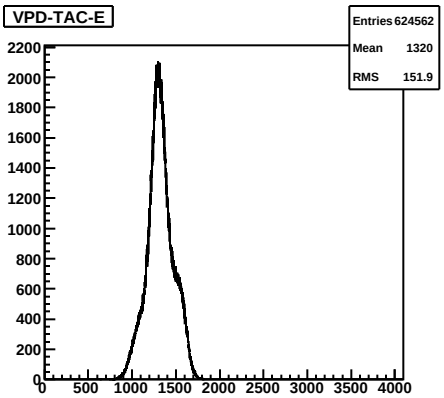

BBCsmall-TAC-W
Entries 776113
Mean 1537
RMS 229.9

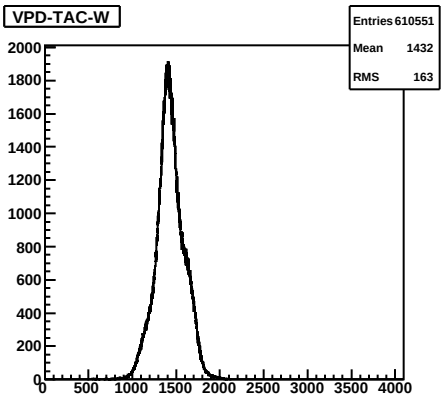

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 731707
Mean 4044
RMS 272.9

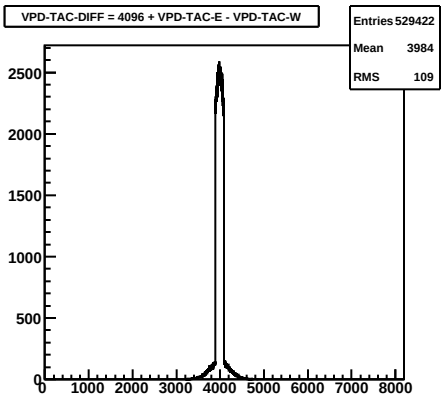

BBClarge-TAC-E
Entries 779507
Mean 1446
RMS 322.6

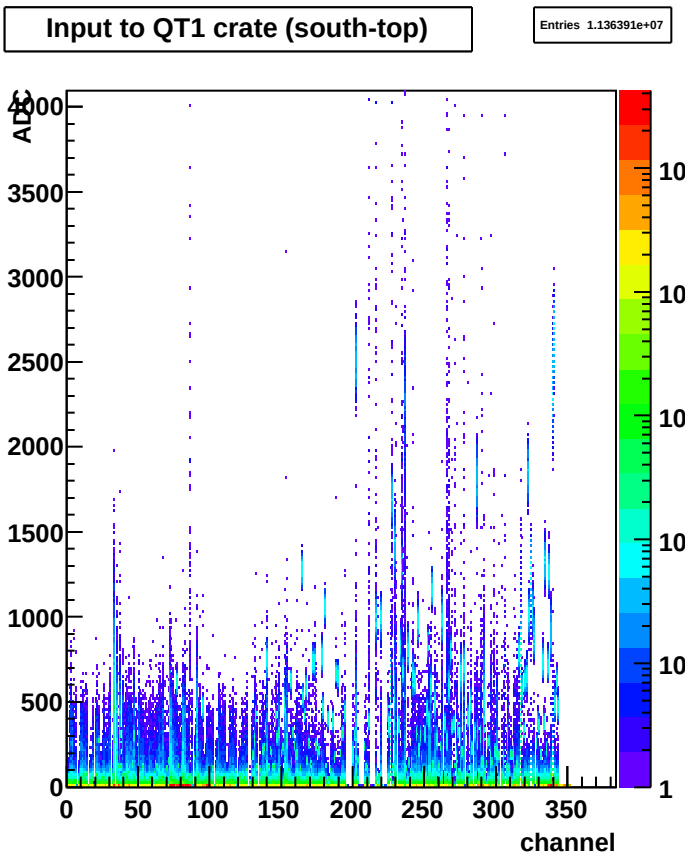

BBClarge-TAC-W
Entries 784373
Mean 1462
RMS 337

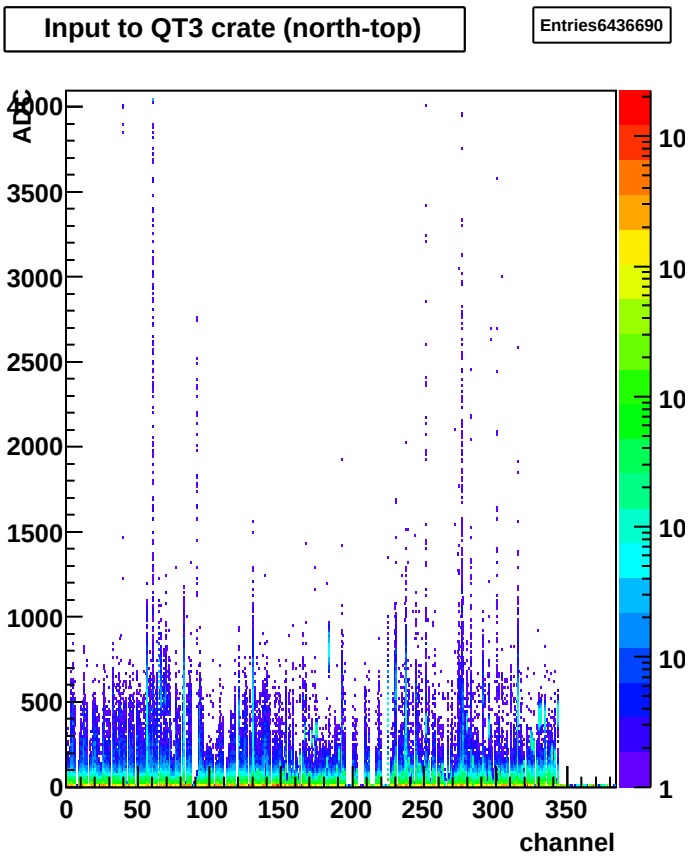

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 748539
Mean 4079
RMS 424.6

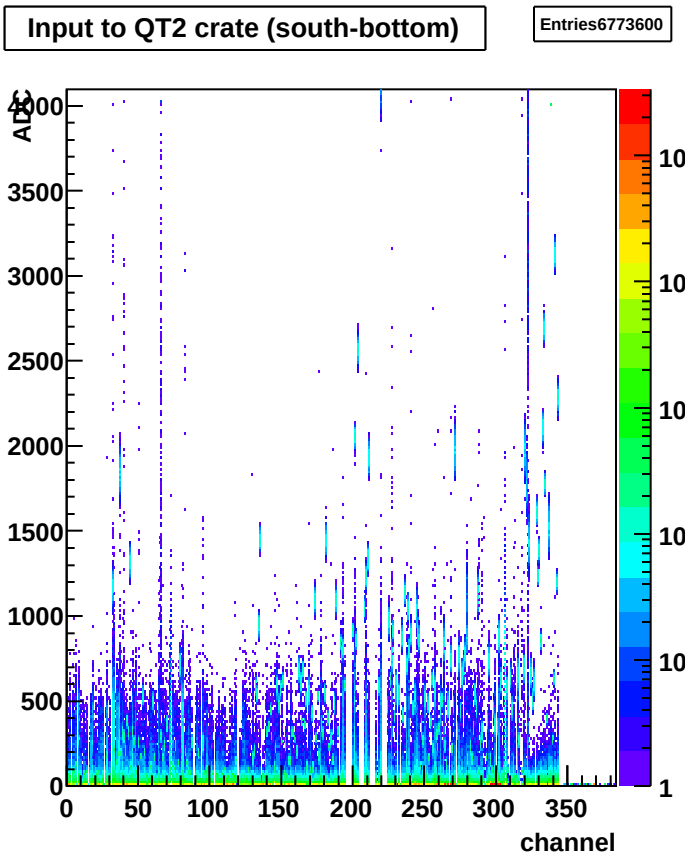

VPD-TAC-E
Entries 624562
Mean 1320
RMS 151.9

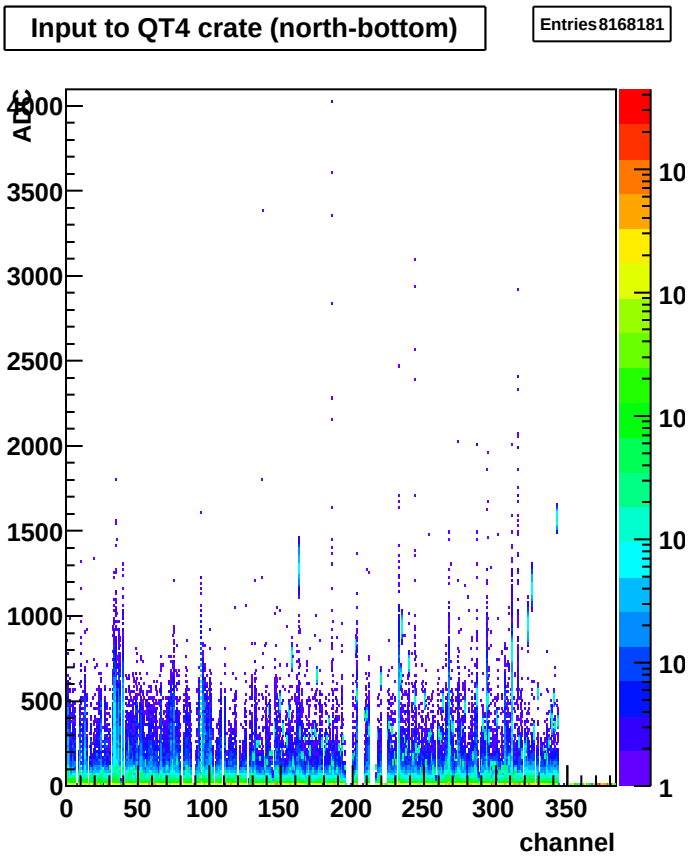

VPD-TAC-W
Entries 610551
Mean 1432
RMS 163

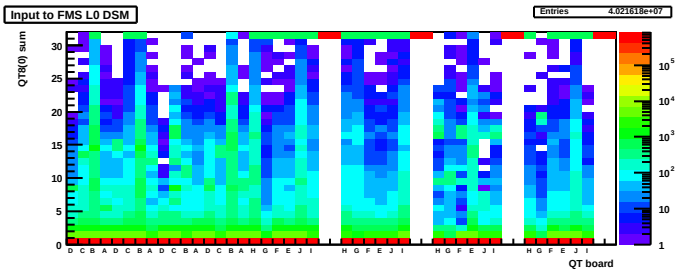

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 529422
Mean 3984
RMS 109

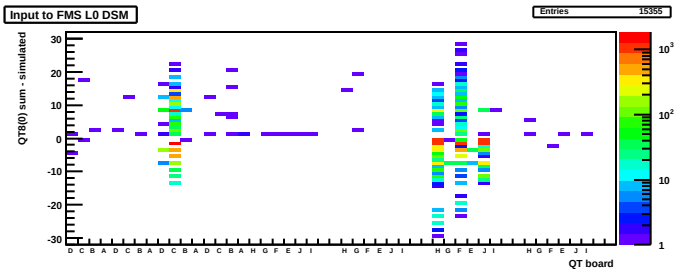

Input to QT1 crate (south-top)
Entries 1.136391e+07
ADC
channel

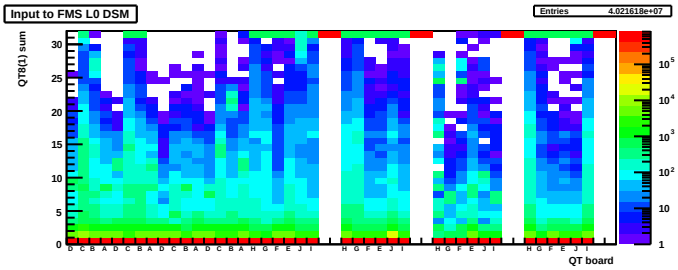

Input to QT3 crate (north-top)
Entries6436690
ADC
channel

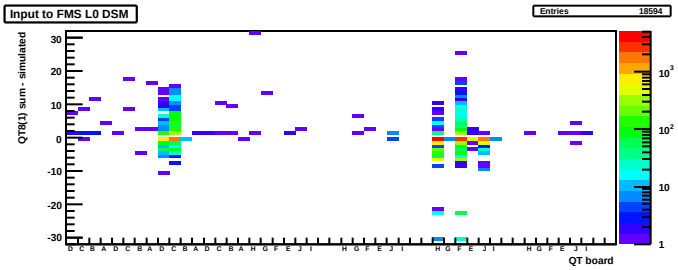

Input to QT2 crate (south-bottom)
Entries6773600
ADC
channel

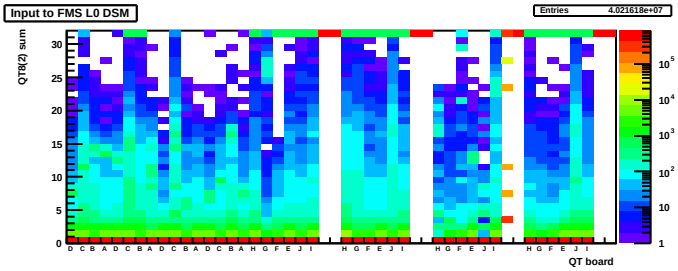

Input to QT4 crate (north-bottom)
Entries8168181
ADC
channel

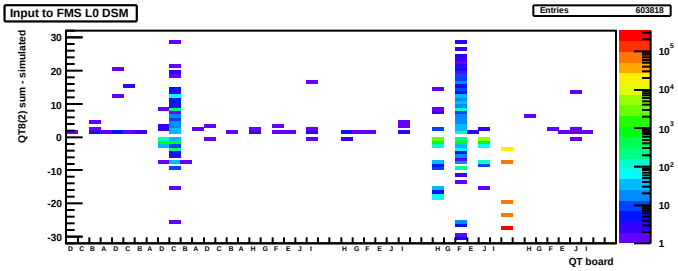

Input to FMS L0 DSM
Entries 4.021618e+07
QT8(0) sum
QT board

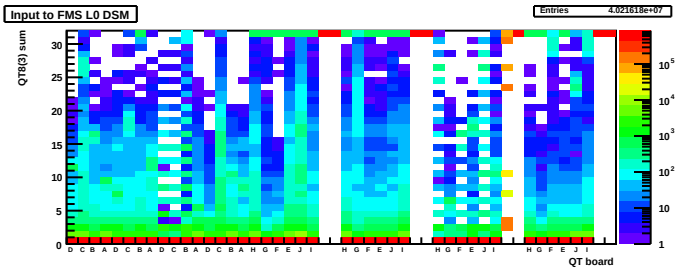

Input to FMS L0 DSM
Entries 15355
QT8(0) sum - simulated
QT board

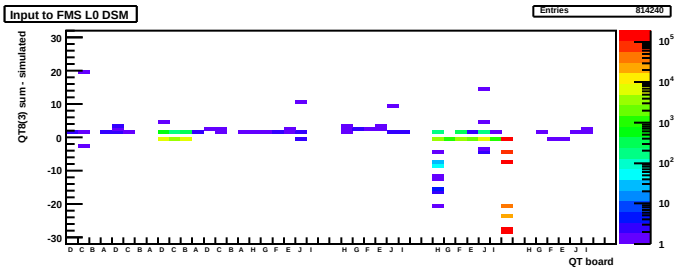

Input to FMS L0 DSM
Entries 4.021618e+07
QT8(1) sum
QT board

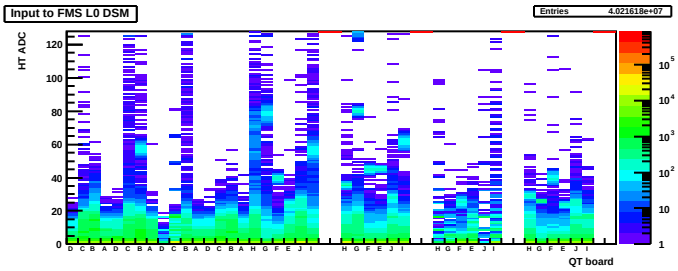

Input to FMS L0 DSM
Entries 18594
QT8(1) sum - simulated
QT board

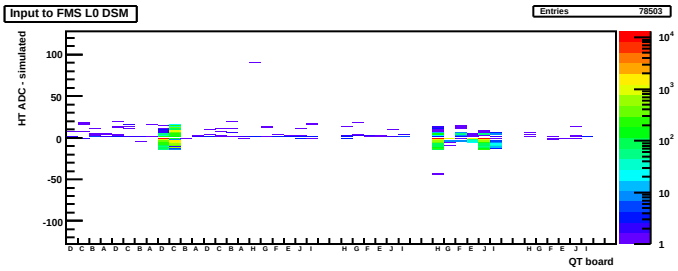

Input to FMS L0 DSM
Entries 4.021618e+07
QT8(2) sum
QT board

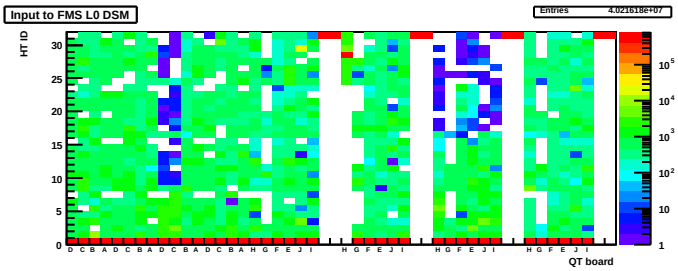

Input to FMS L0 DSM
Entries 603818
QT8(2) sum - simulated
QT board

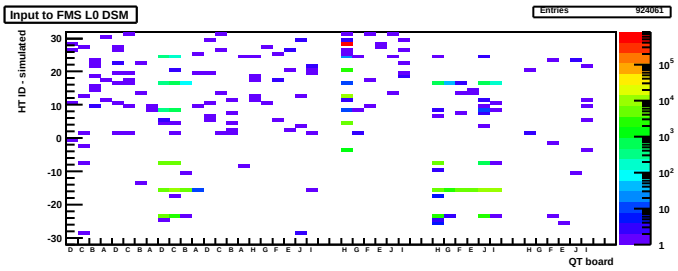

Input to FMS L0 DSM
Entries 4.021618e+07
QT8(3) sum
QT board

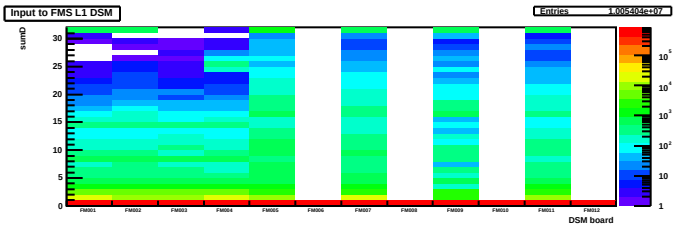

Input to FMS L0 DSM
Entries 814240
QT8(3) sum - simulated
QT board

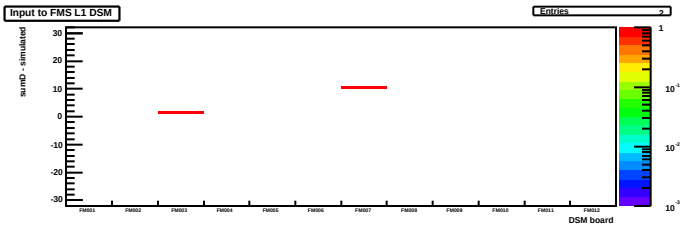

Input to FMS L0 DSM
Entries 4.021618e+07
HT ADC
QT board

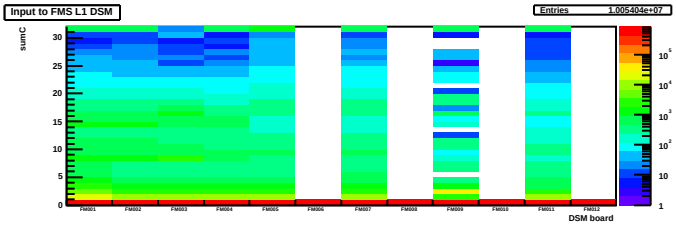

Input to FMS L0 DSM
Entries 78503
HT ADC - simulated
QT board

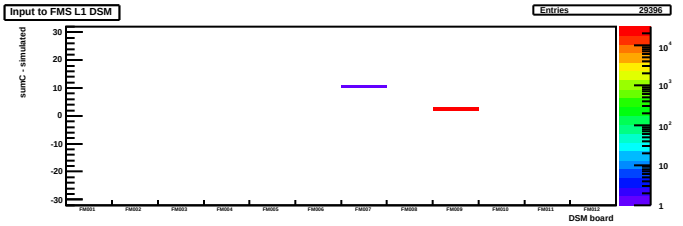

Input to FMS L0 DSM
Entries 4.021618e+07
HT ID
QT board

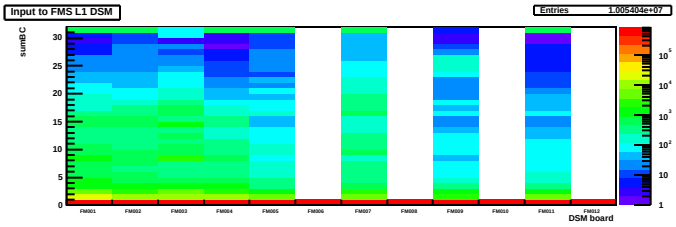

Input to FMS L0 DSM
Entries 924061
HT ID - simulated
QT board

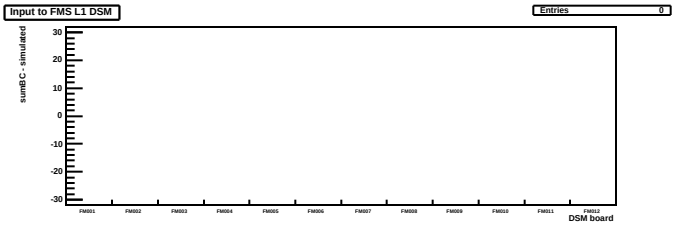

Input to FMS L1 DSM
Entries 1.005404e+07
sumD
DSM board

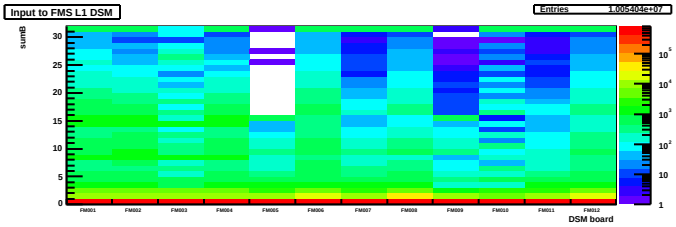

Input to FMS L1 DSM
Entries 2
sumD - simulated
DSM board

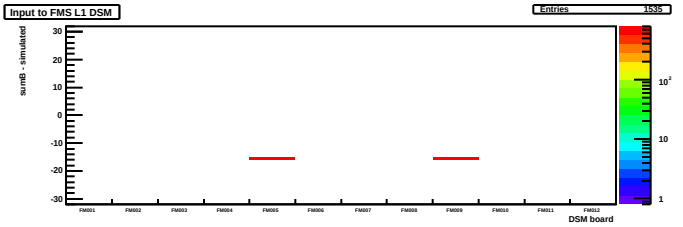

Input to FMS L1 DSM
Entries 1.005404e+07
sumC
DSM board

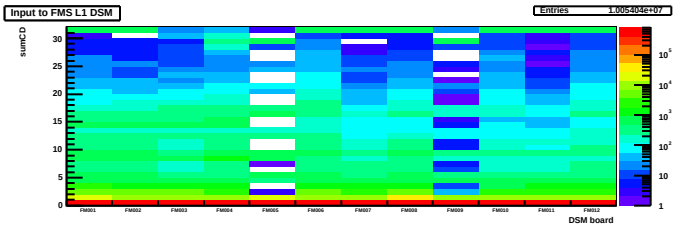

Input to FMS L1 DSM
Entries 29396
sumC - simulated
DSM board

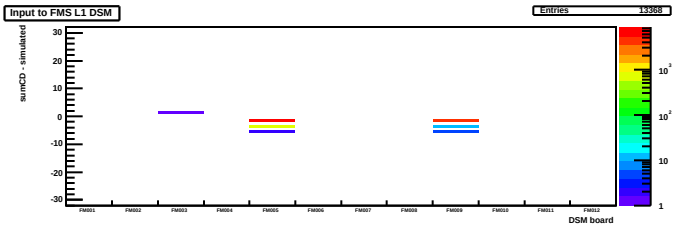

Input to FMS L1 DSM
Entries 1.005404e+07
sumBC
DSM board

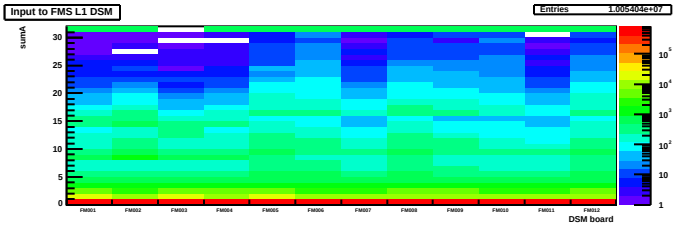

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

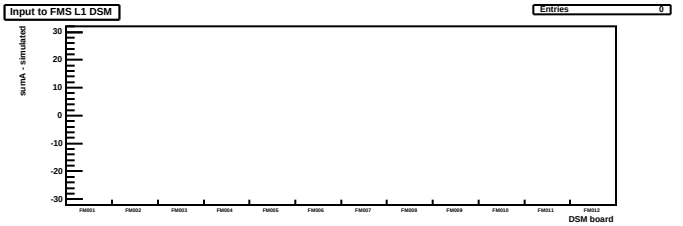

Input to FMS L1 DSM
Entries 1.005404e+07
sumB
DSM board

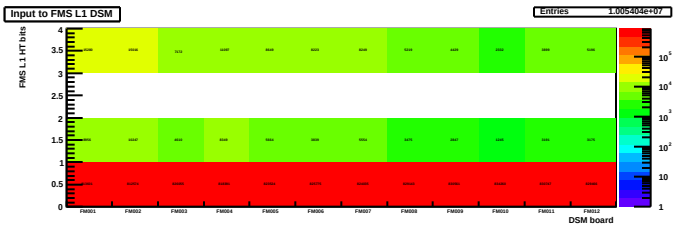

Input to FMS L1 DSM
Entries 1535
sumB - simulated
DSM board

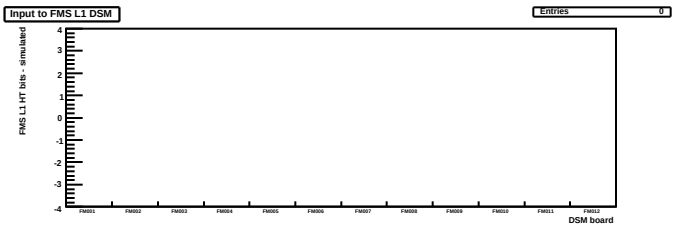

Input to FMS L1 DSM
Entries 1.005404e+07
sumCD
DSM board

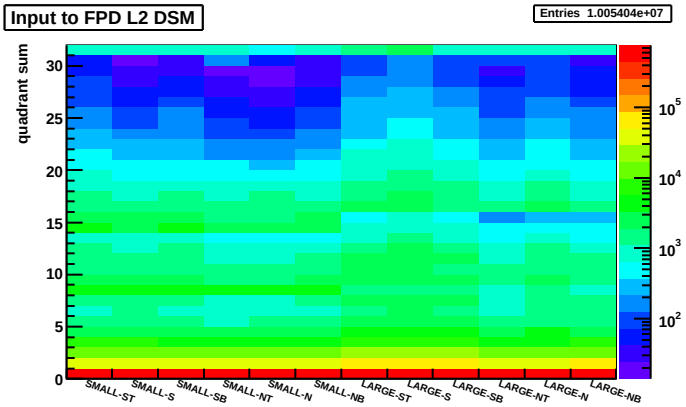

Input to FMS L1 DSM
Entries 13368
sumCD - simulated
DSM board

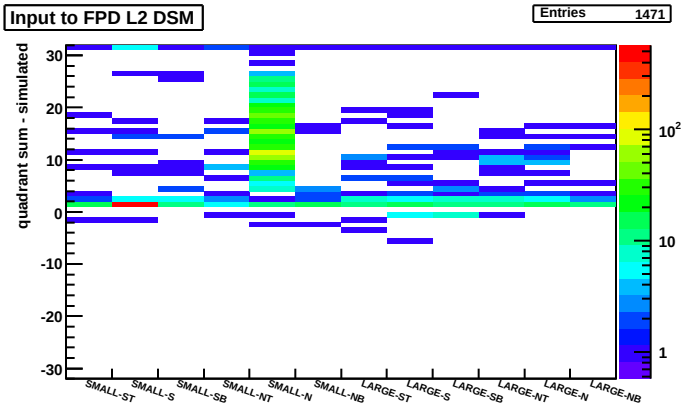

Input to FMS L1 DSM
Entries 1.005404e+07
sumA
DSM board

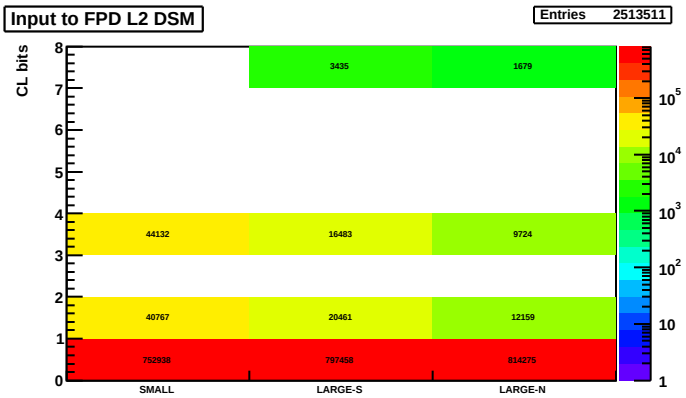

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

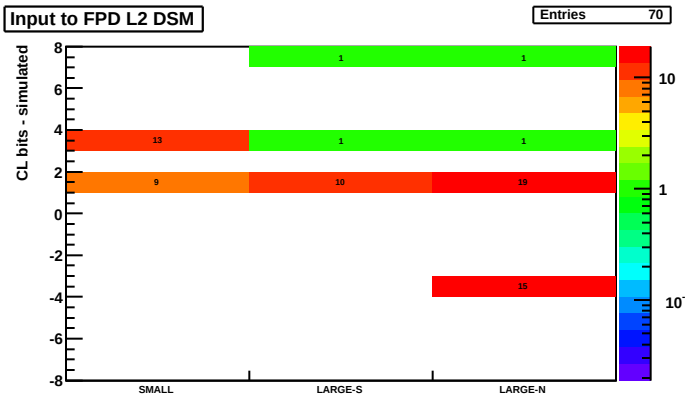

Input to FMS L1 DSM
Entries 1.005404e+07
FMS L1 HT bits
DSM board

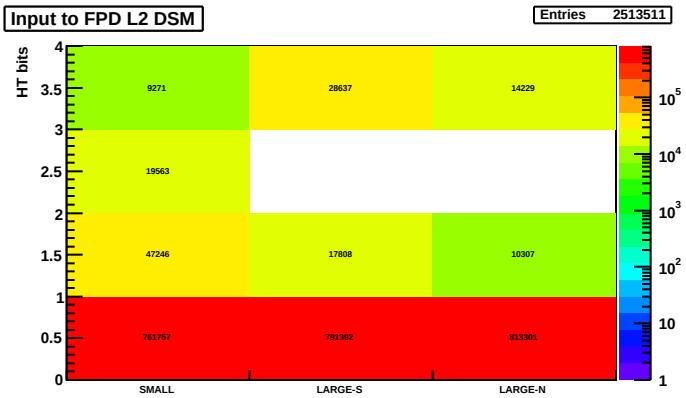

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

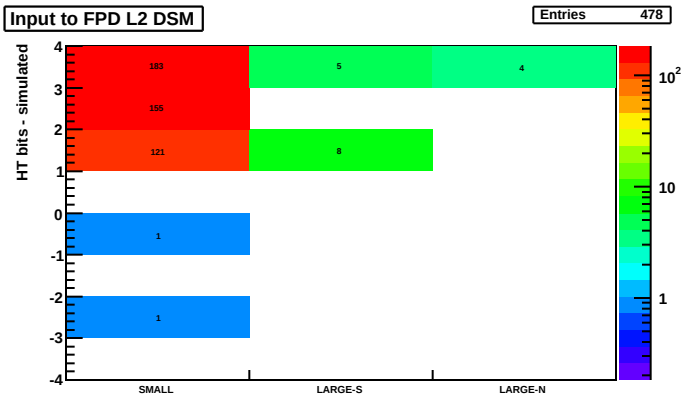

Input to FPD L2 DSM
Entries 1.005404e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

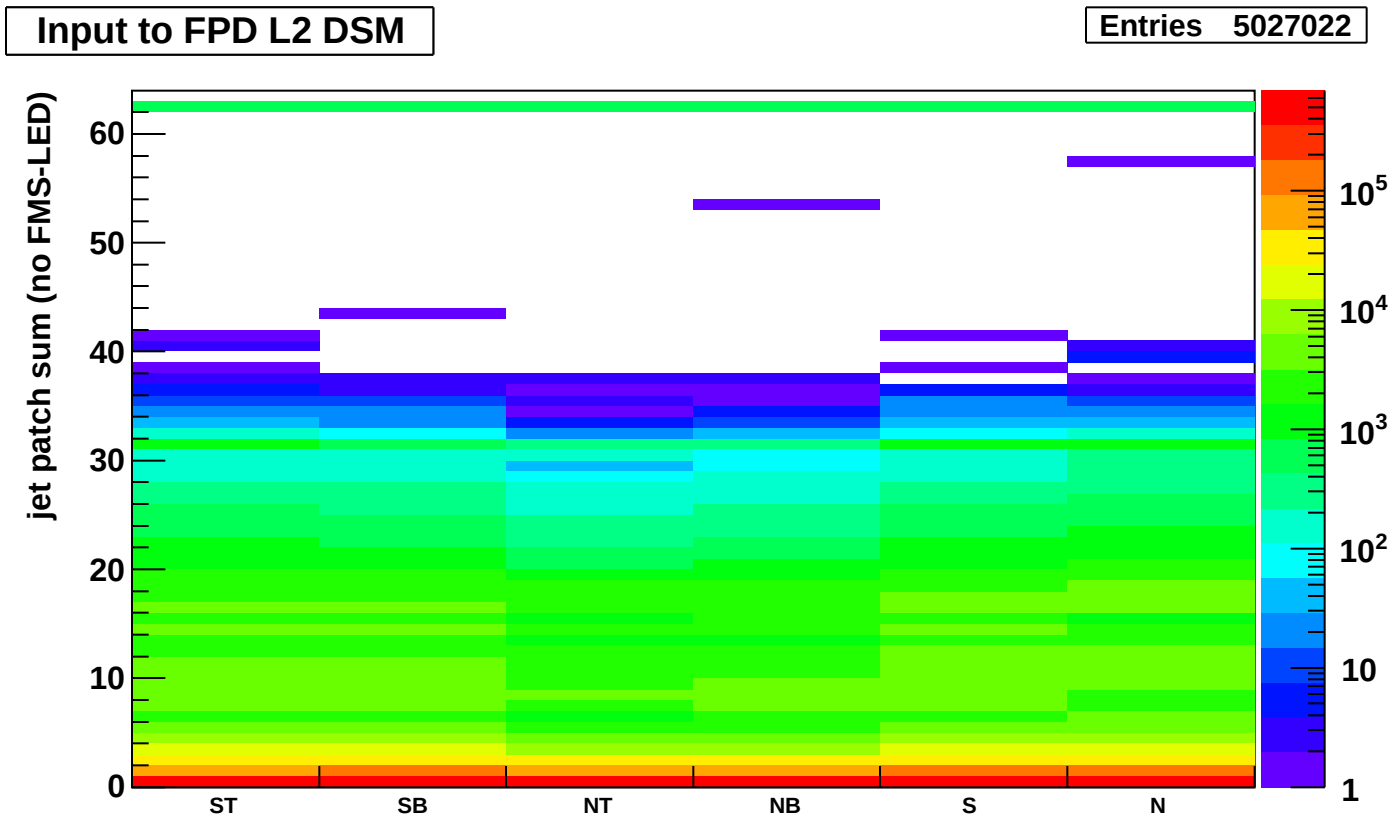

Input to FPD L2 DSM
Entries 1471
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

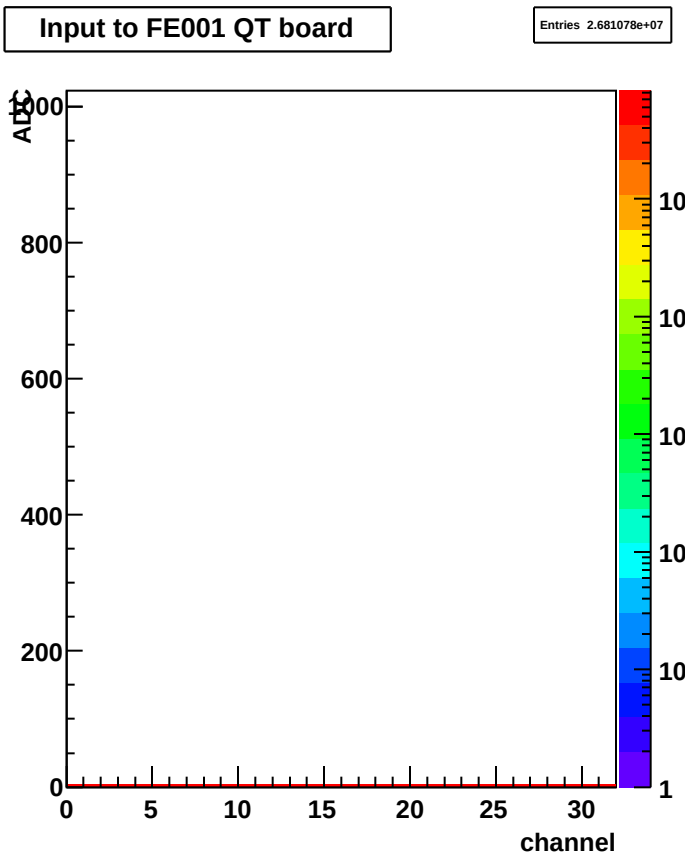

Input to FPD L2 DSM
Entries 2513511
CL bits
3435
1679
44132
16483
9724
40767
20461
12159
752938
797458
814275
SMALL
LARGE-S
LARGE-N

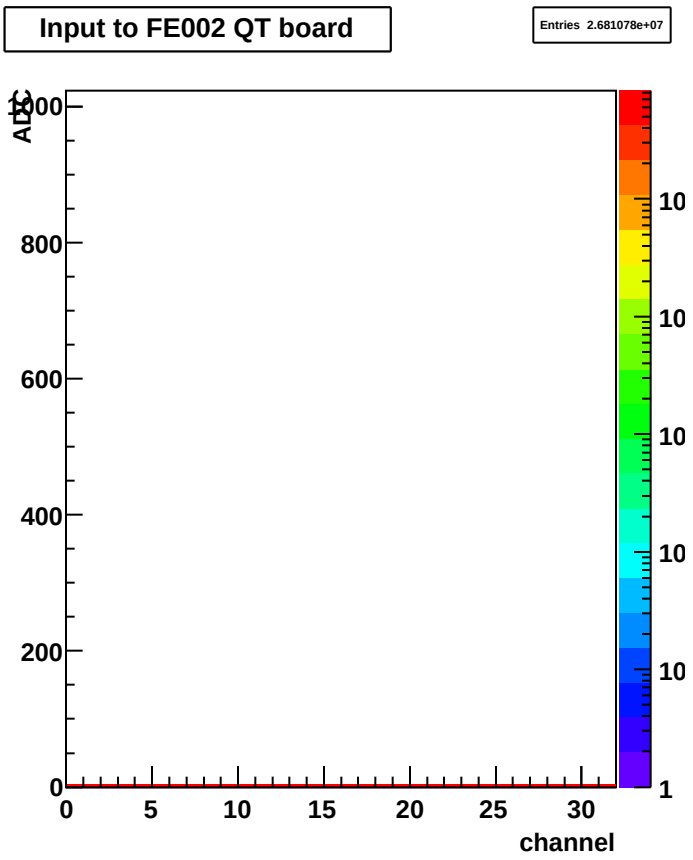

Input to FPD L2 DSM
Entries 70
CL bits - simulated
1
1
13
1
1
9
10
19
15
SMALL
LARGE-S
LARGE-N

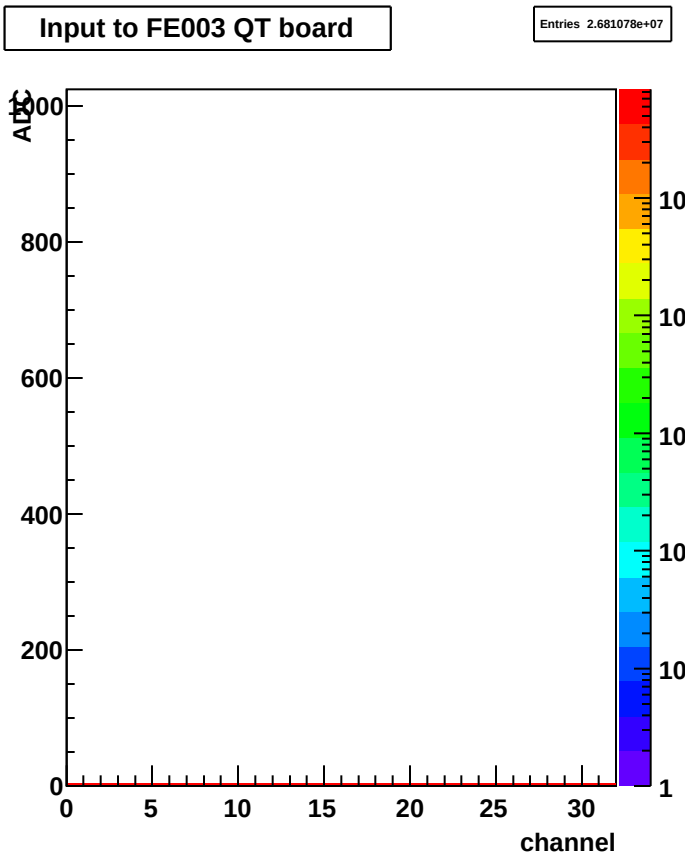

Input to FPD L2 DSM
Entries 2513511
HT bits
9271
28637
14229
19563
47246
17808
10307
761757
791392
813301
SMALL
LARGE-S
LARGE-N

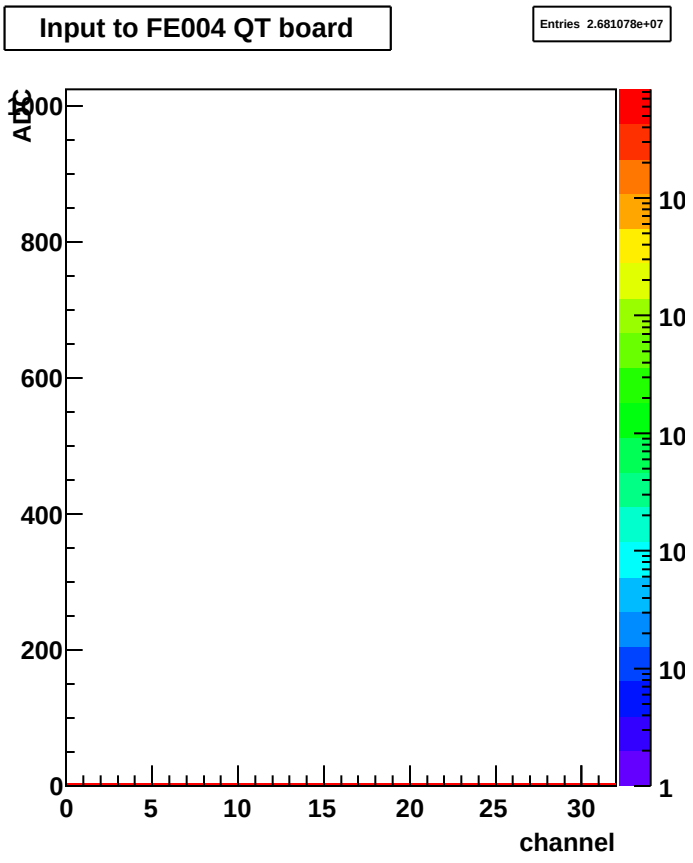

Input to FPD L2 DSM
Entries 478
HT bits - simulated
183
5
4
155
121
8
1
1
SMALL
LARGE-S
LARGE-N

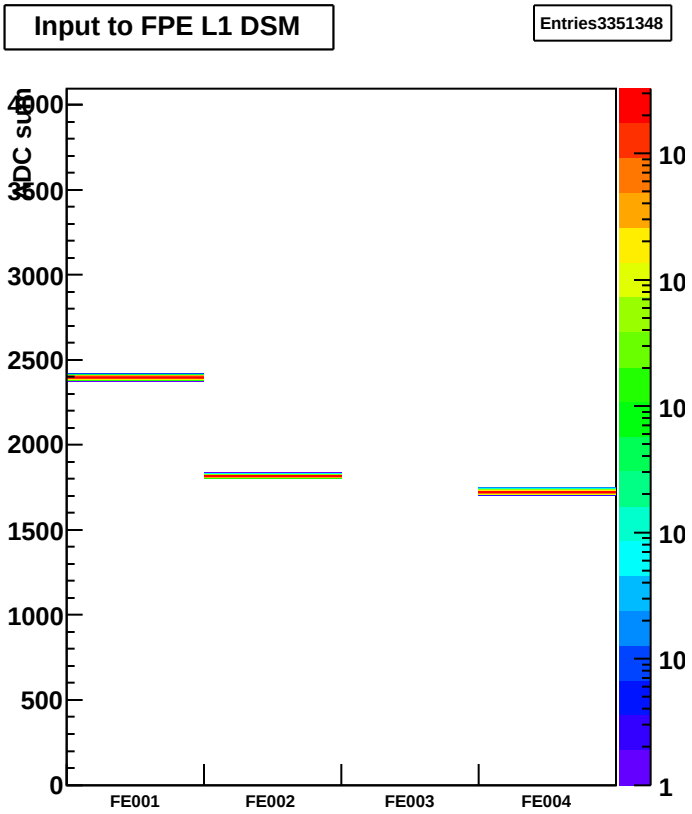

Input to FPD L2 DSM
Entries 5027022
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 2.681078e+07
ADC
channel

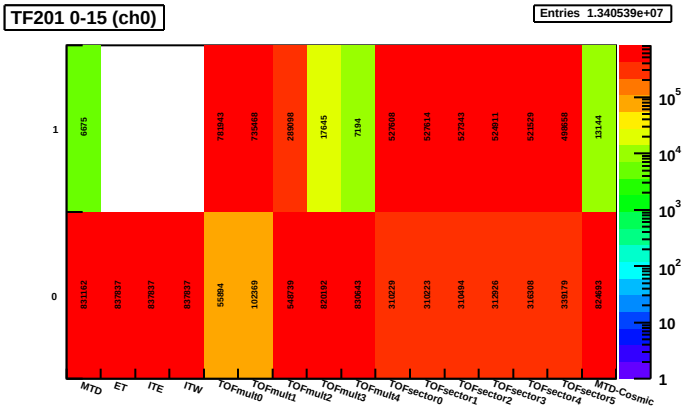

Input to FE002 QT board
Entries 2.681078e+07
ADC
channel


Input to FE003 QT board
Entries 2.681078e+07
ADC
channel

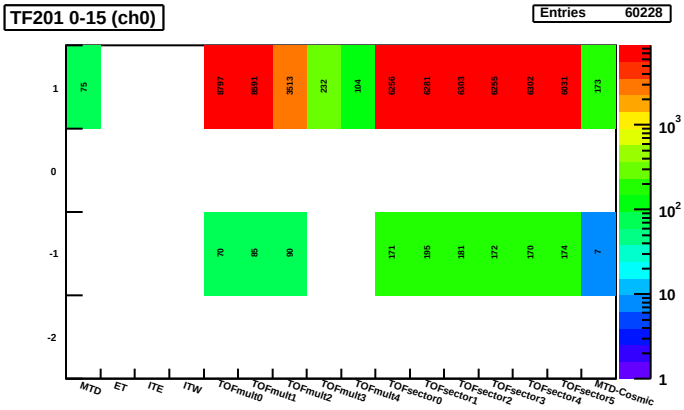

Input to FE004 QT board
Entries 2.681078e+07
ADC
channel


Input to FPE L1 DSM
Entries3351348
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

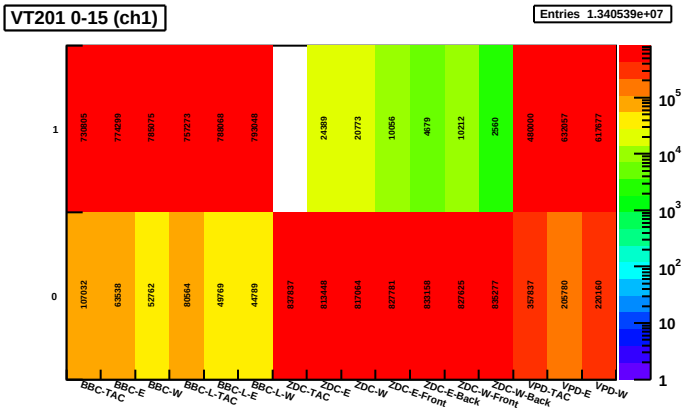
TF201 0-15 (ch0)



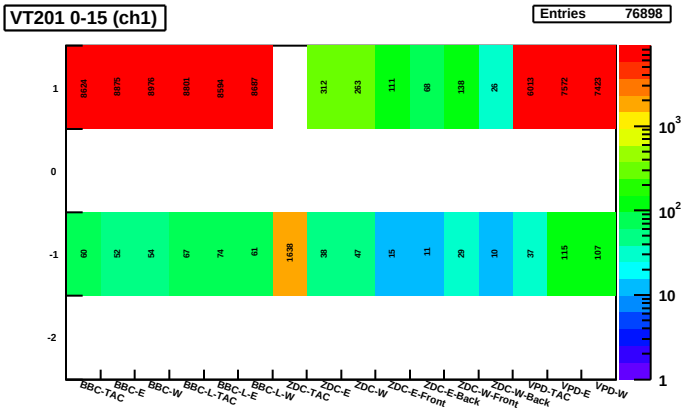
TF201 0-15 (ch0)



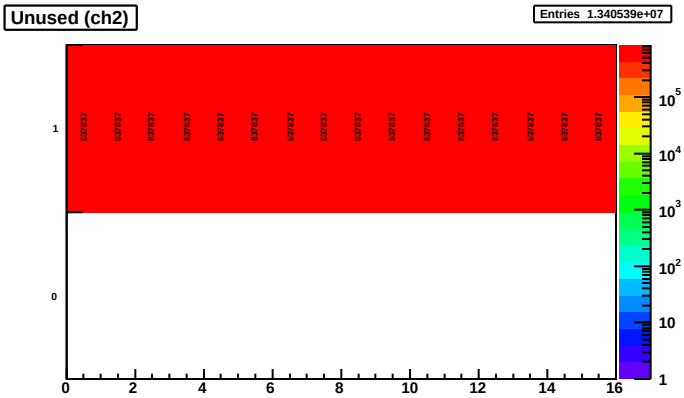
VT201 0-15 (ch1)

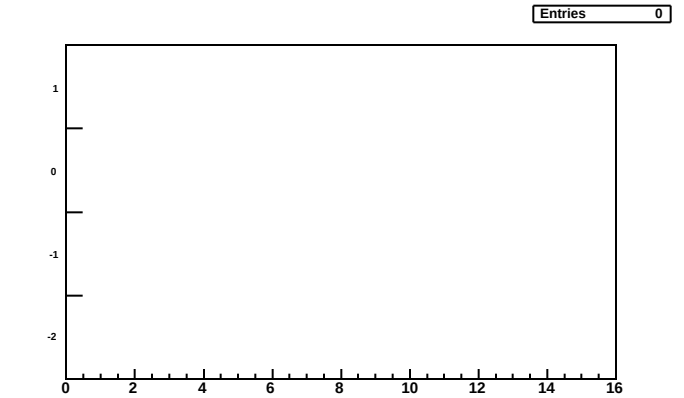


VT201 0-15 (ch1)

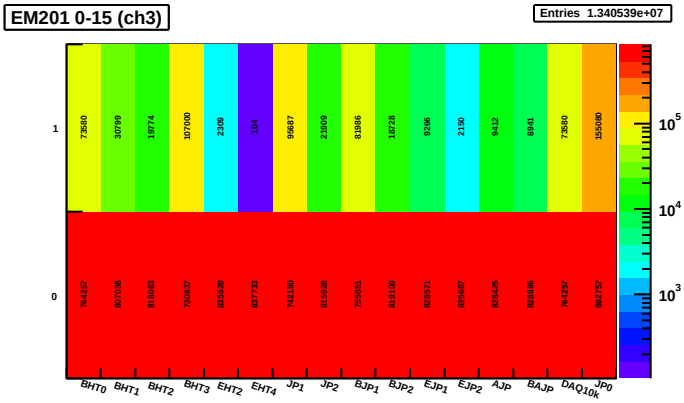


Unused (ch2)





EM201 0-15 (ch3)



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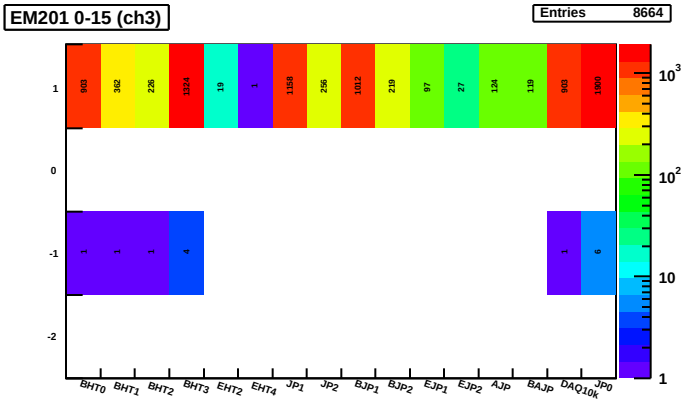
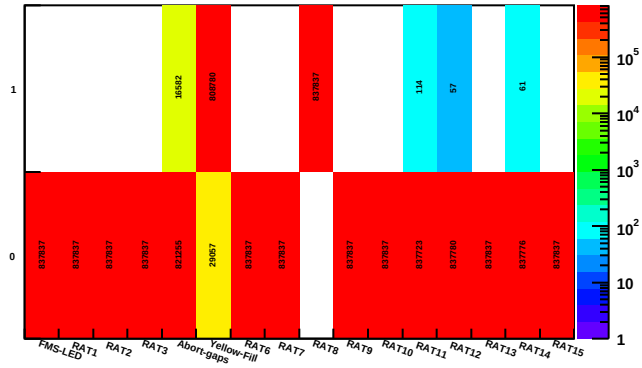
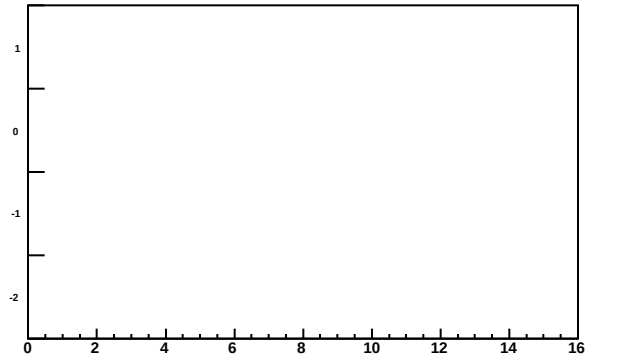
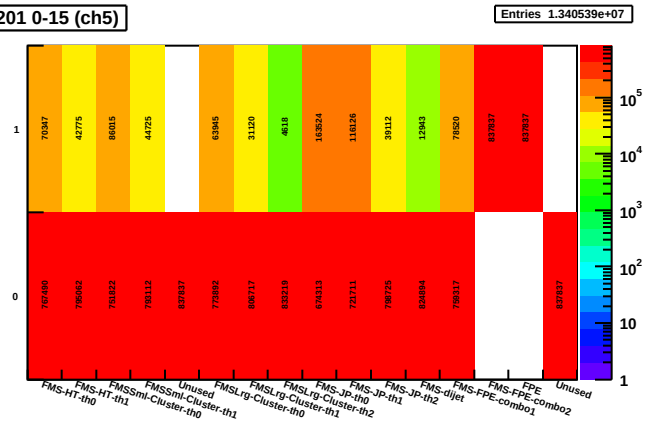


RAT board (ch4)





FP201 0-15 (ch5)



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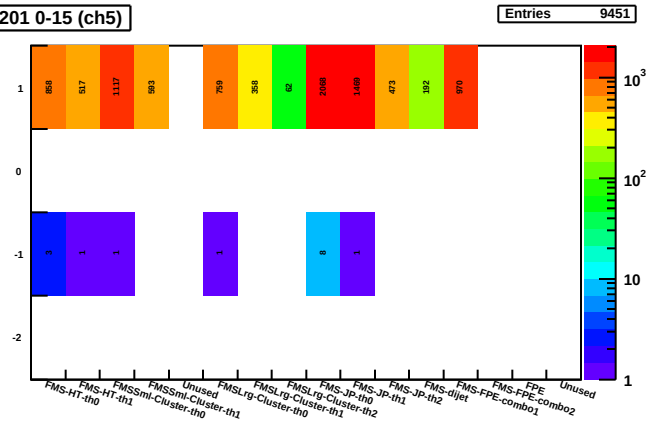

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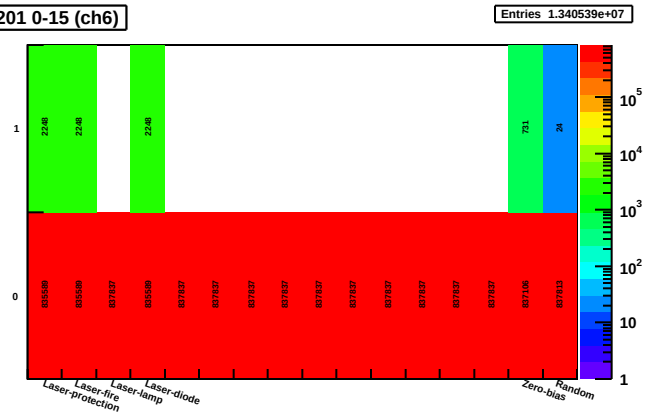

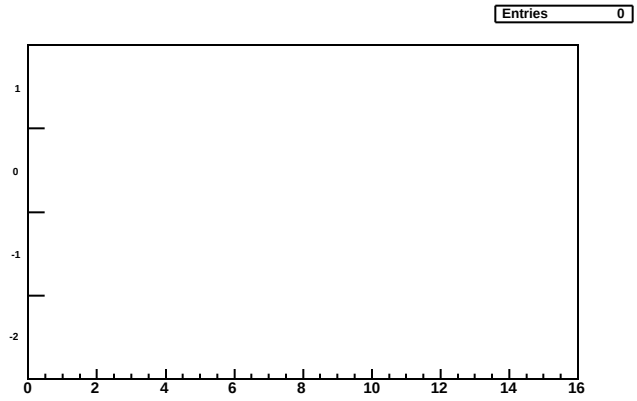
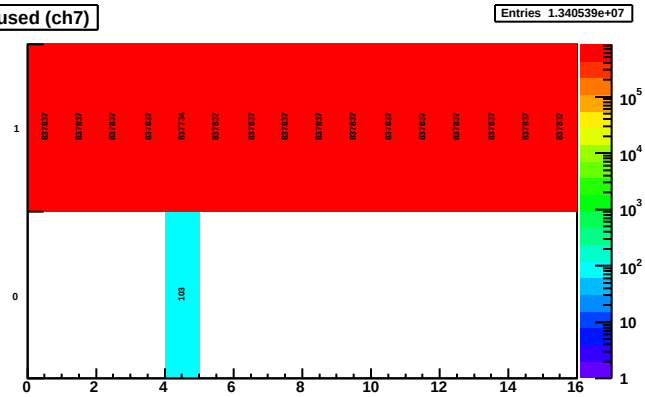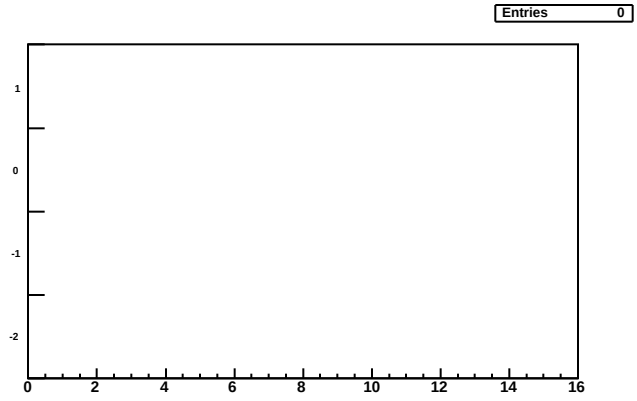
Unused (ch7)