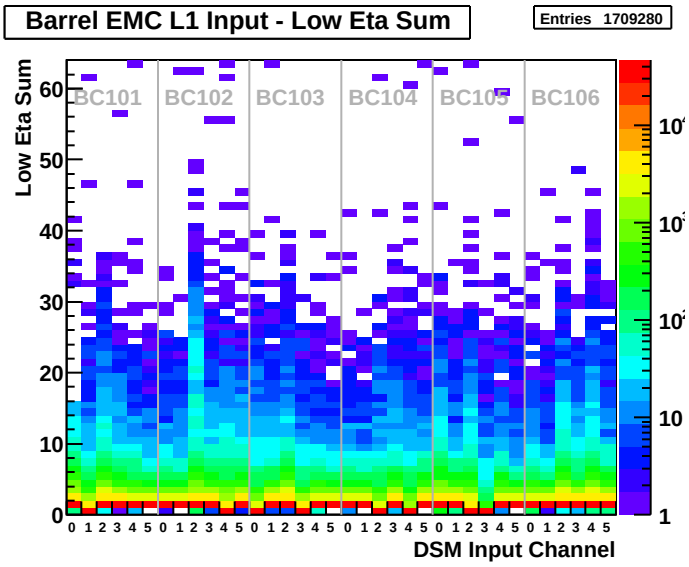
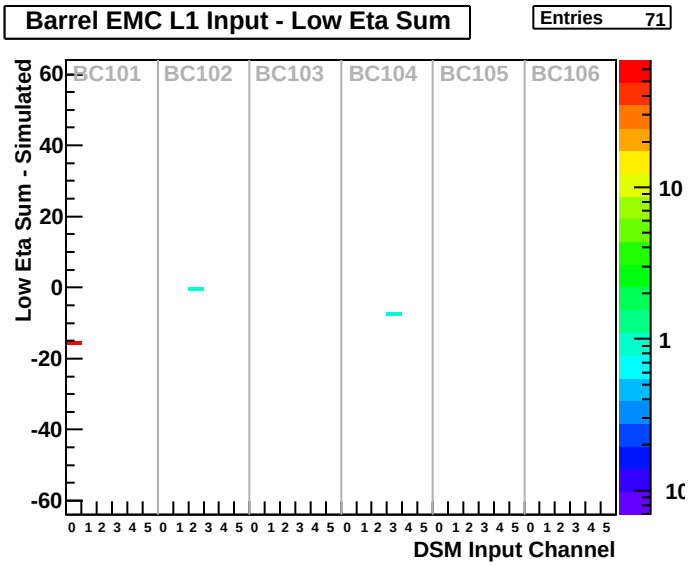
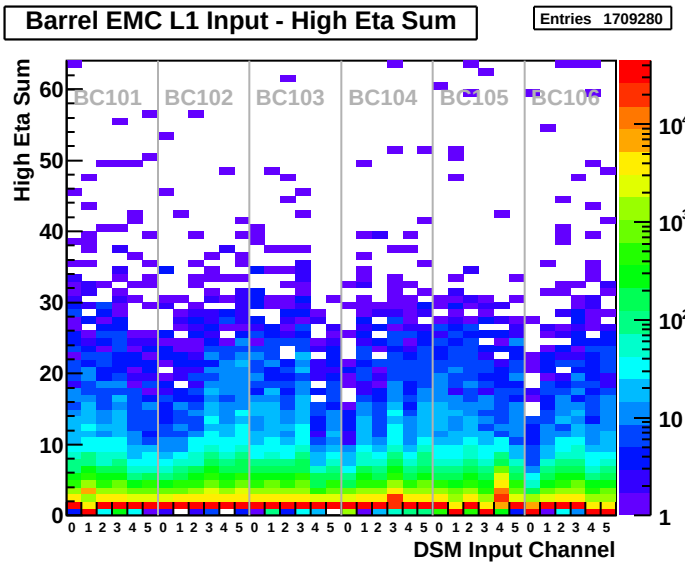
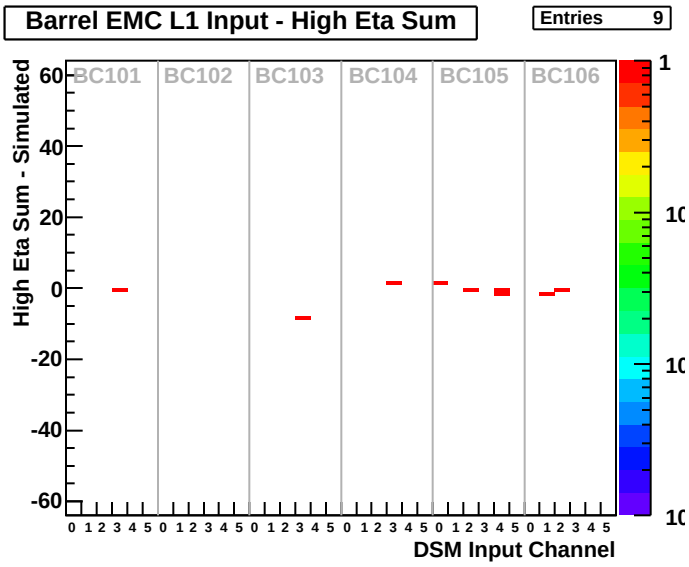
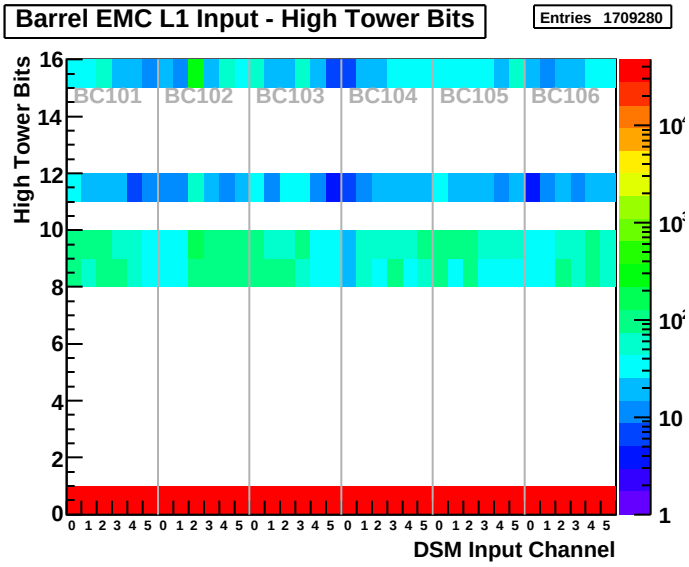
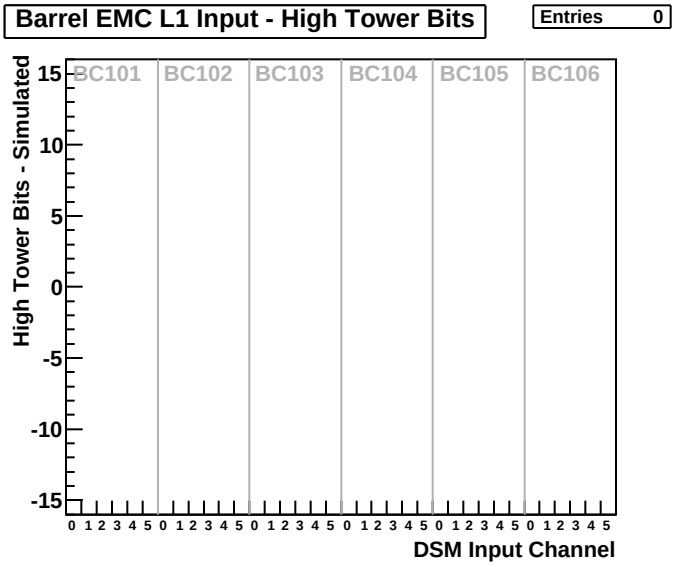

Barrel EMC L1 Input - Low Eta Sum
Entries 1709280
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

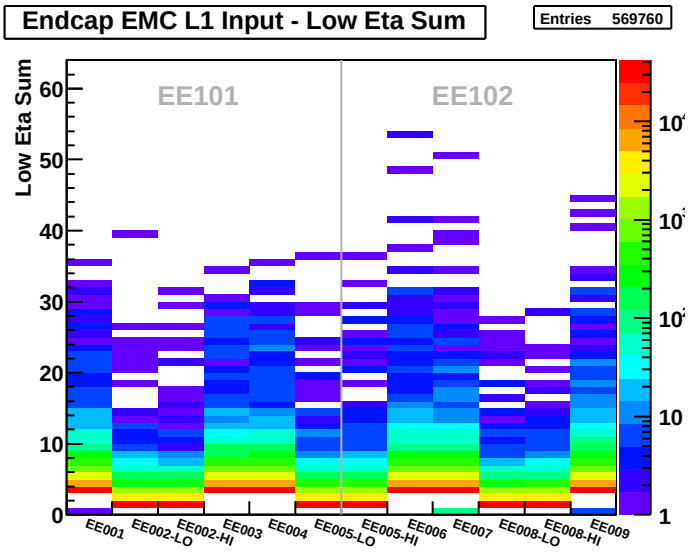

Barrel EMC L1 Input - Low Eta Sum
Entries 71
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

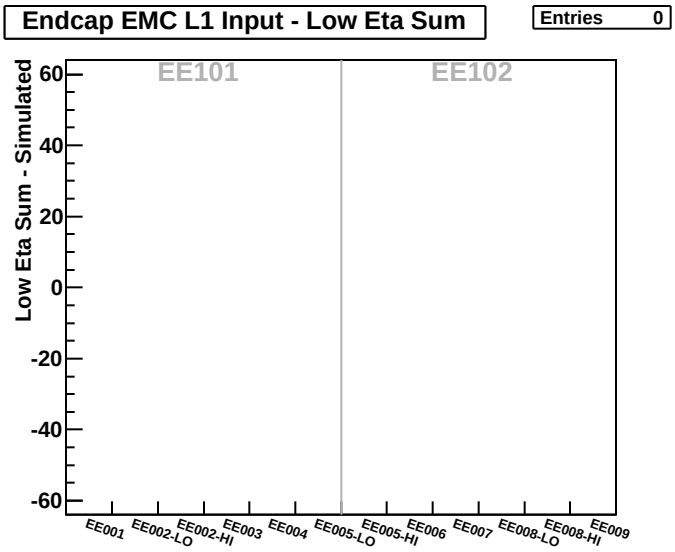

Barrel EMC L1 Input - High Eta Sum
Entries 1709280
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

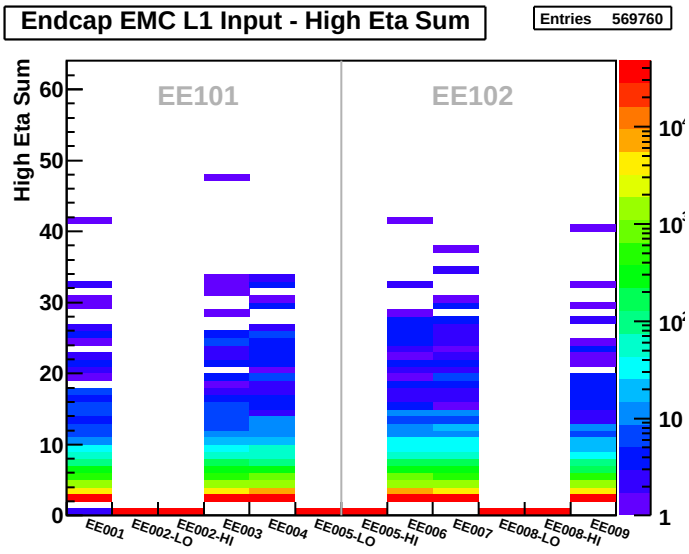

Barrel EMC L1 Input - High Eta Sum
Entries 9
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

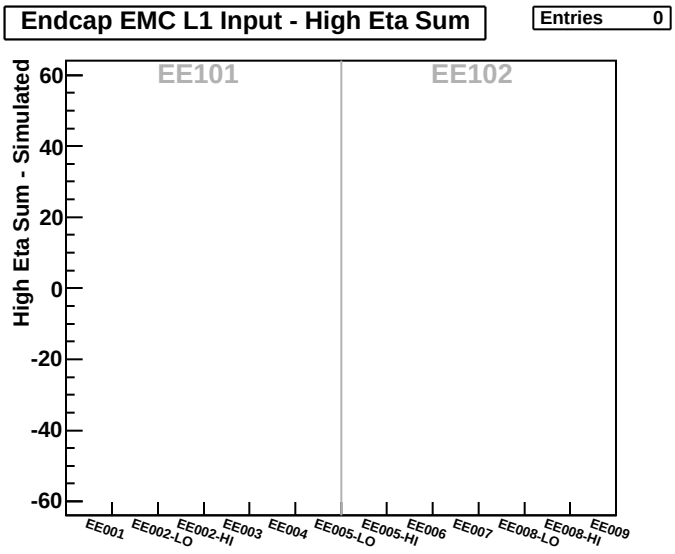

Barrel EMC L1 Input - High Tower Bits
Entries 1709280
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

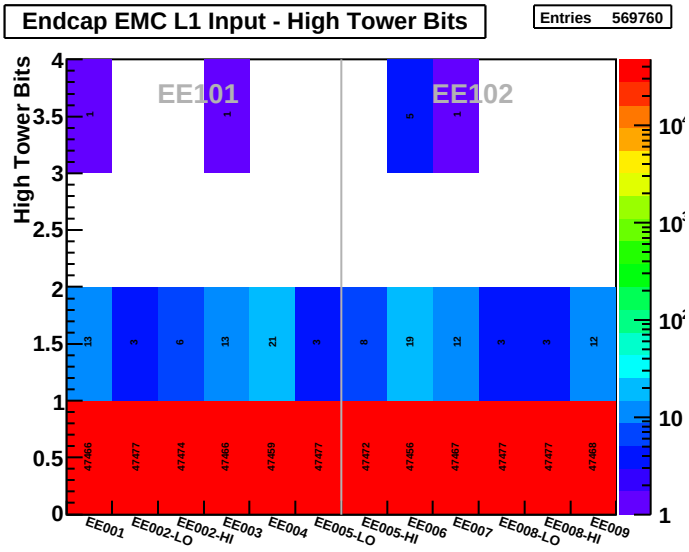

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

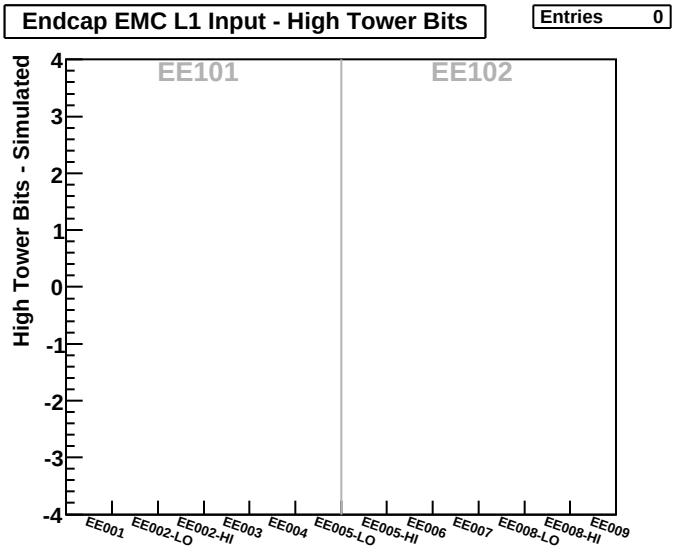

Endcap EMC L1 Input - Low Eta Sum
Entries 569760
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

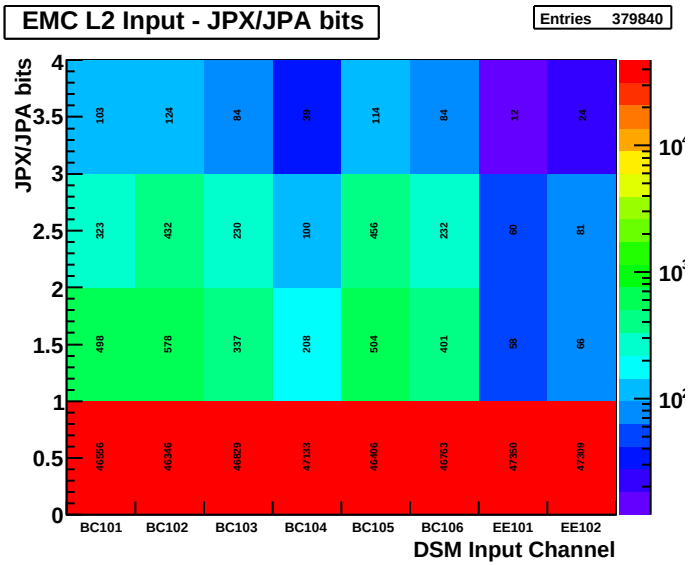

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

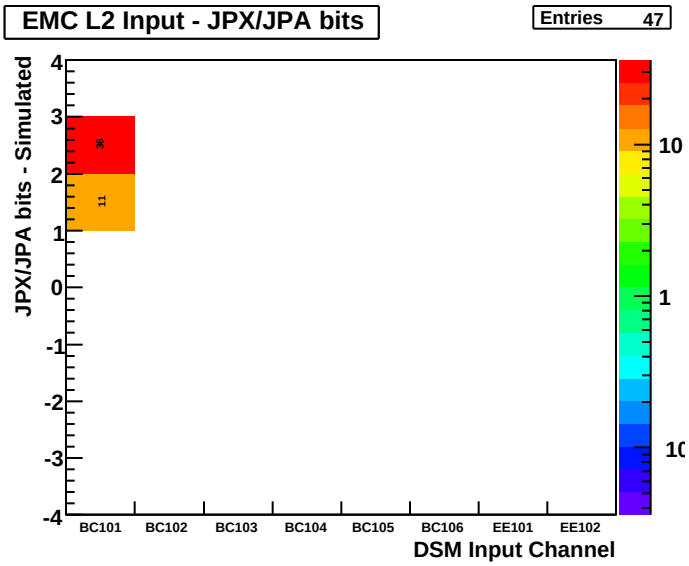

Endcap EMC L1 Input - High Eta Sum
Entries 569760
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

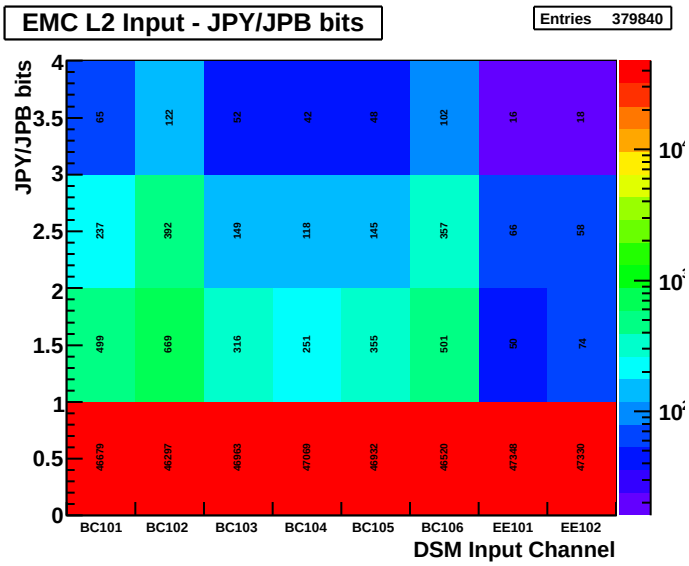

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

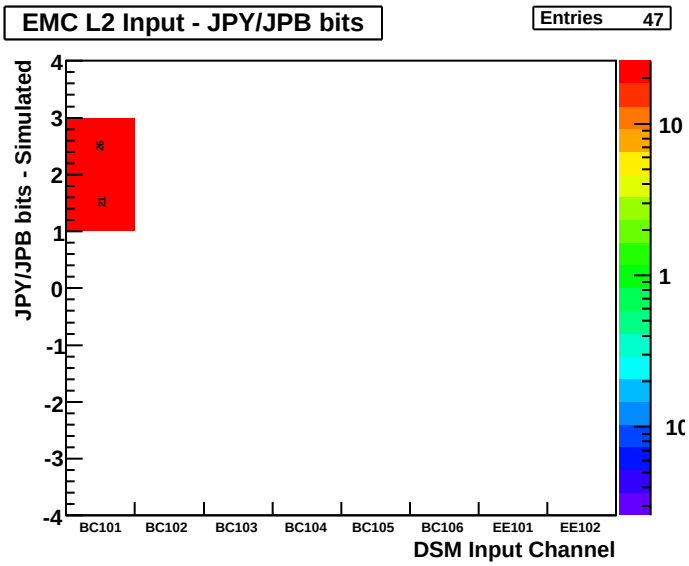

Endcap EMC L1 Input - High Tower Bits
Entries 569760
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

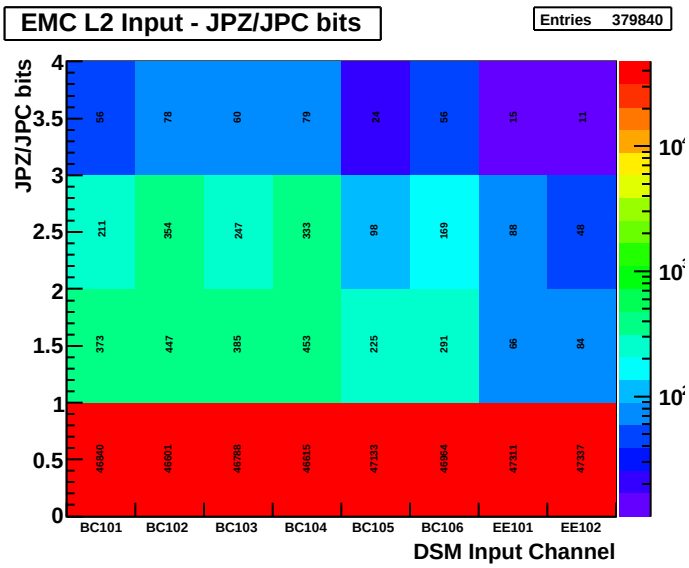

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

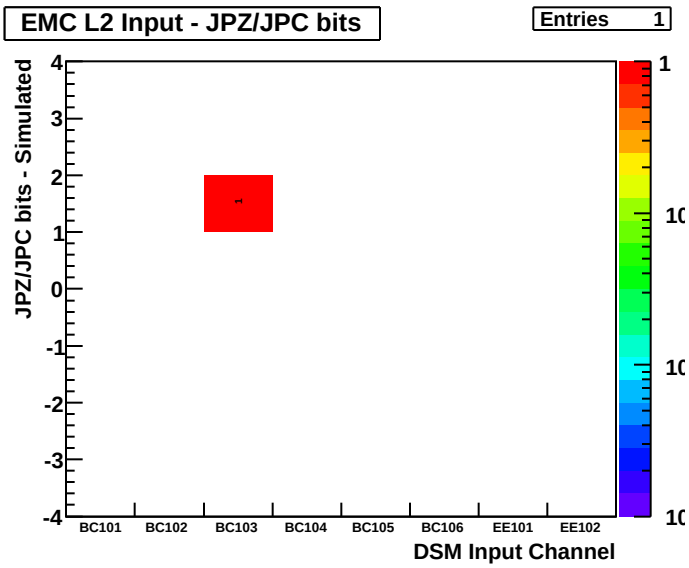

EMC L2 Input - JPX/JPA bits
Entries 379840
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
103 124 84 39 114 84 12 24
323 432 220 100 456 232 60 81
498 578 337 208 504 401 58 66
46556 46346 46629 47133 46406 46763 47350 47309

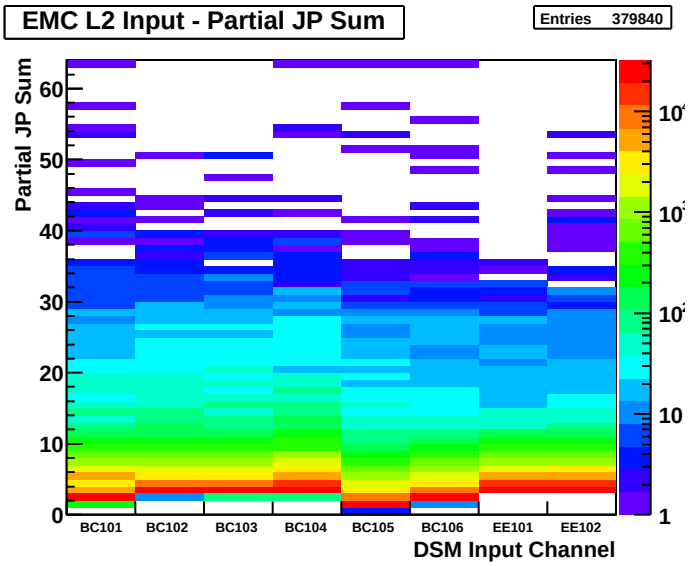

EMC L2 Input - JPX/JPA bits
Entries 47
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36
11

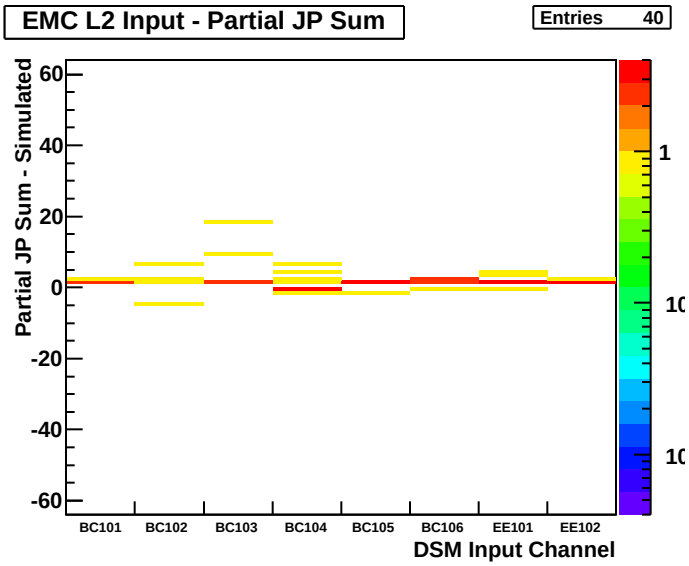

EMC L2 Input - JPY/JPB bits
Entries 379840
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
65 122 52 42 48 102 16 18
237 392 149 118 145 357 66 58
499 669 316 251 355 501 50 74
46679 46297 46863 47069 46932 46520 47348 47330

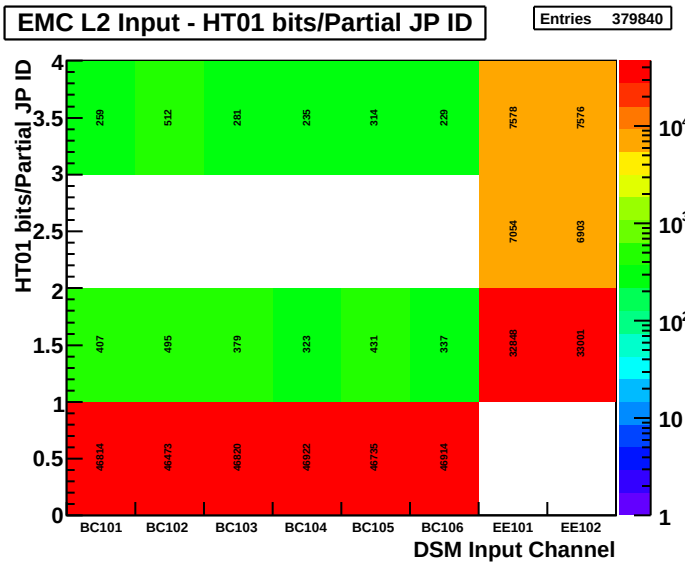

EMC L2 Input - JPY/JPB bits
Entries 47
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
26
21

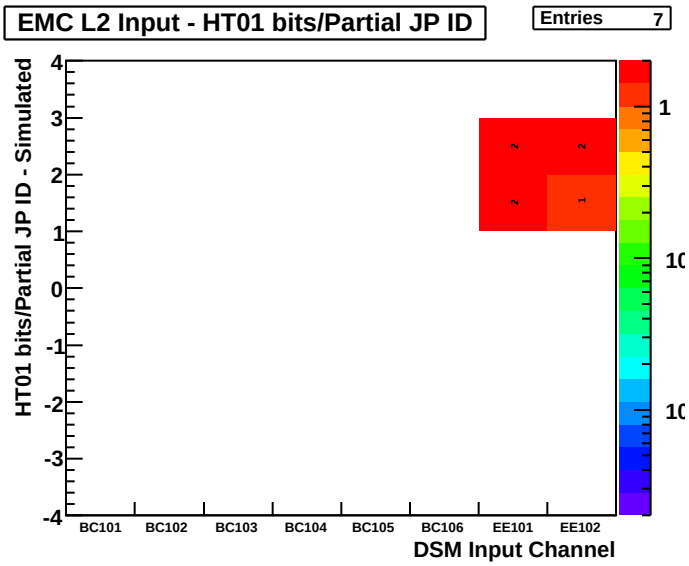

EMC L2 Input - JPZ/JPC bits
Entries 379840
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
56 78 60 79 24 56 15 11
211 354 247 333 98 169 88 48
373 447 385 453 225 291 66 84
46940 46601 46788 46615 47133 46964 47311 47337

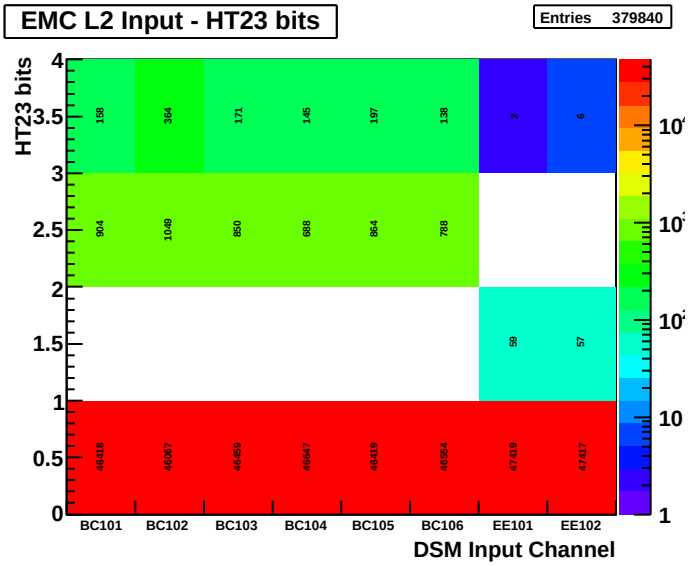

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1

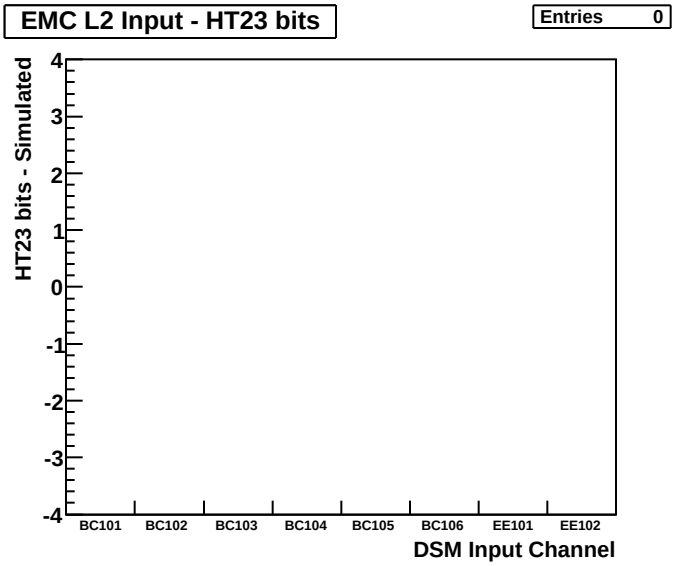

EMC L2 Input - Partial JP Sum
Entries 379840
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

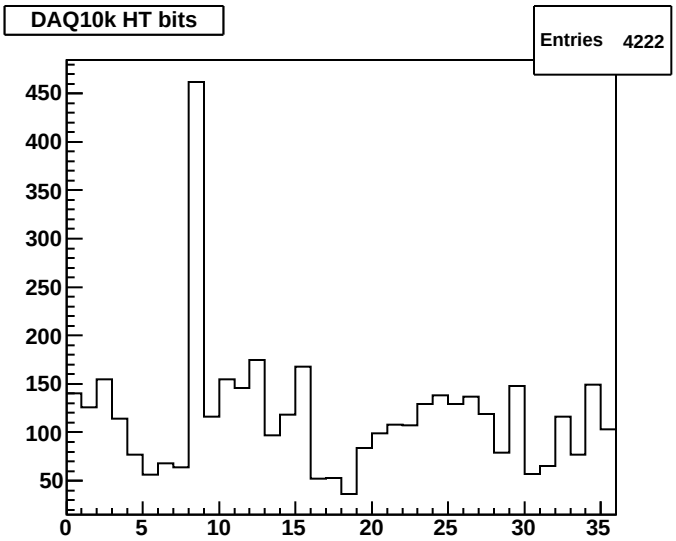

EMC L2 Input - Partial JP Sum
Entries 40
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

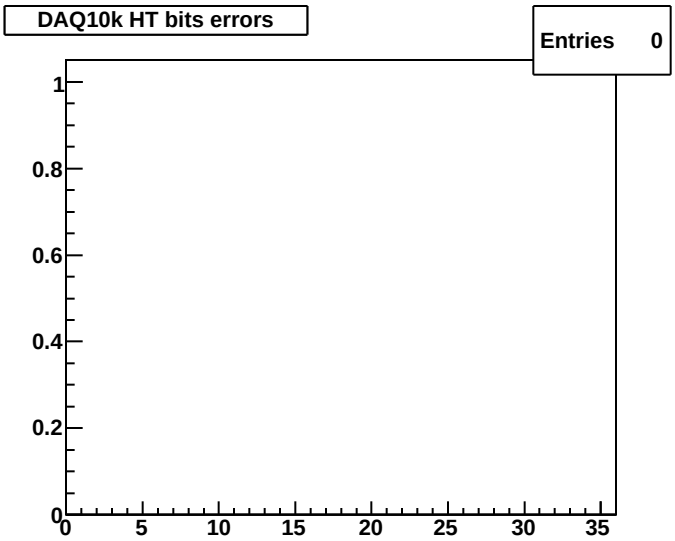

EMC L2 Input - HT01 bits/Partial JP ID
Entries 379840
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

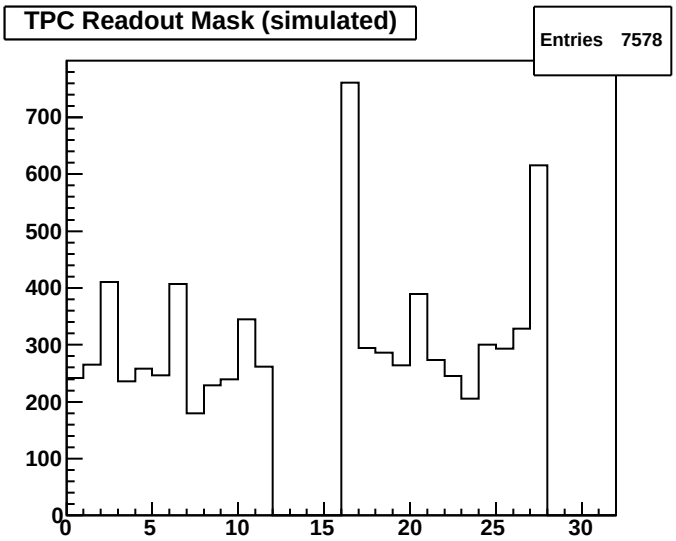

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

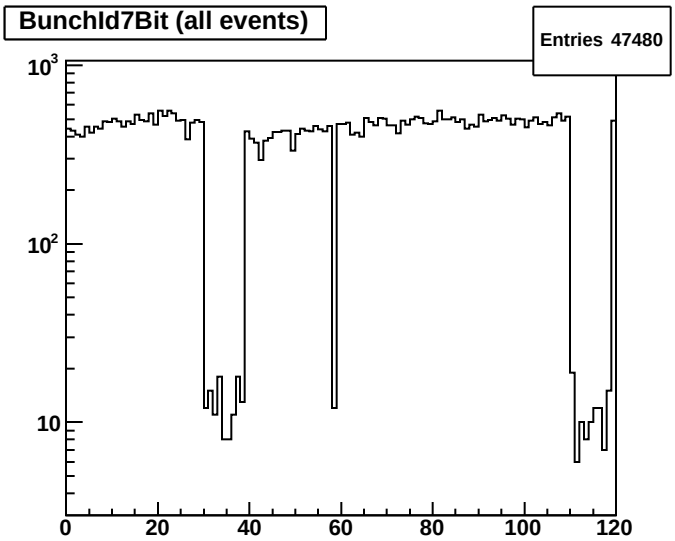

EMC L2 Input - HT23 bits
Entries 379840
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

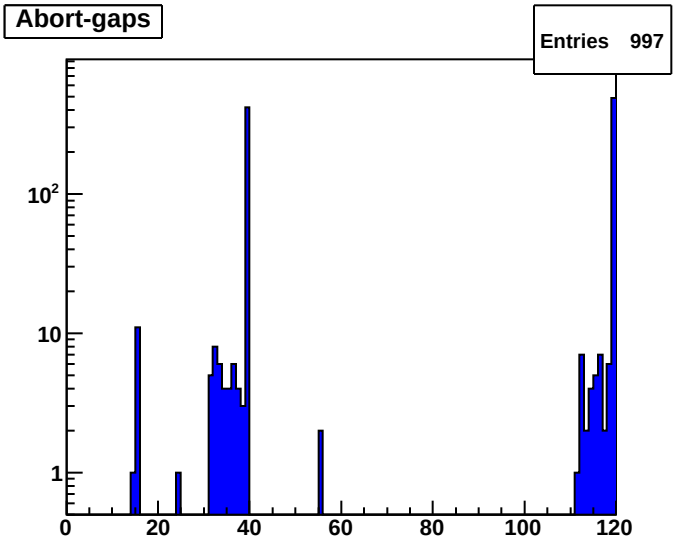

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

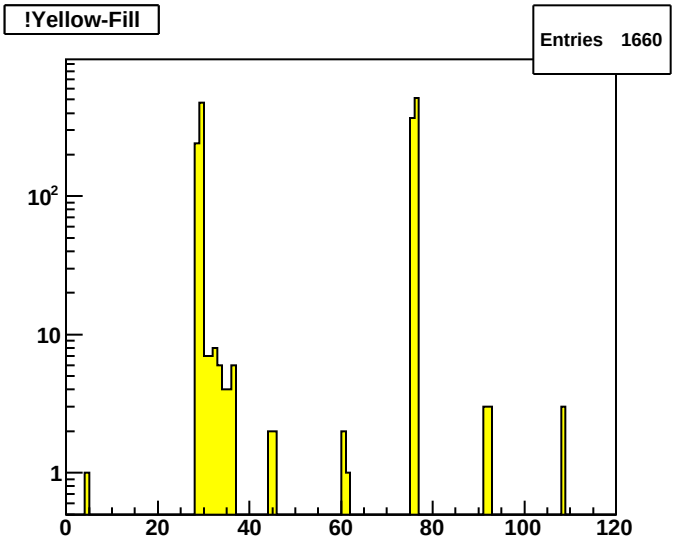

DAQ10k HT bits
Entries 4222

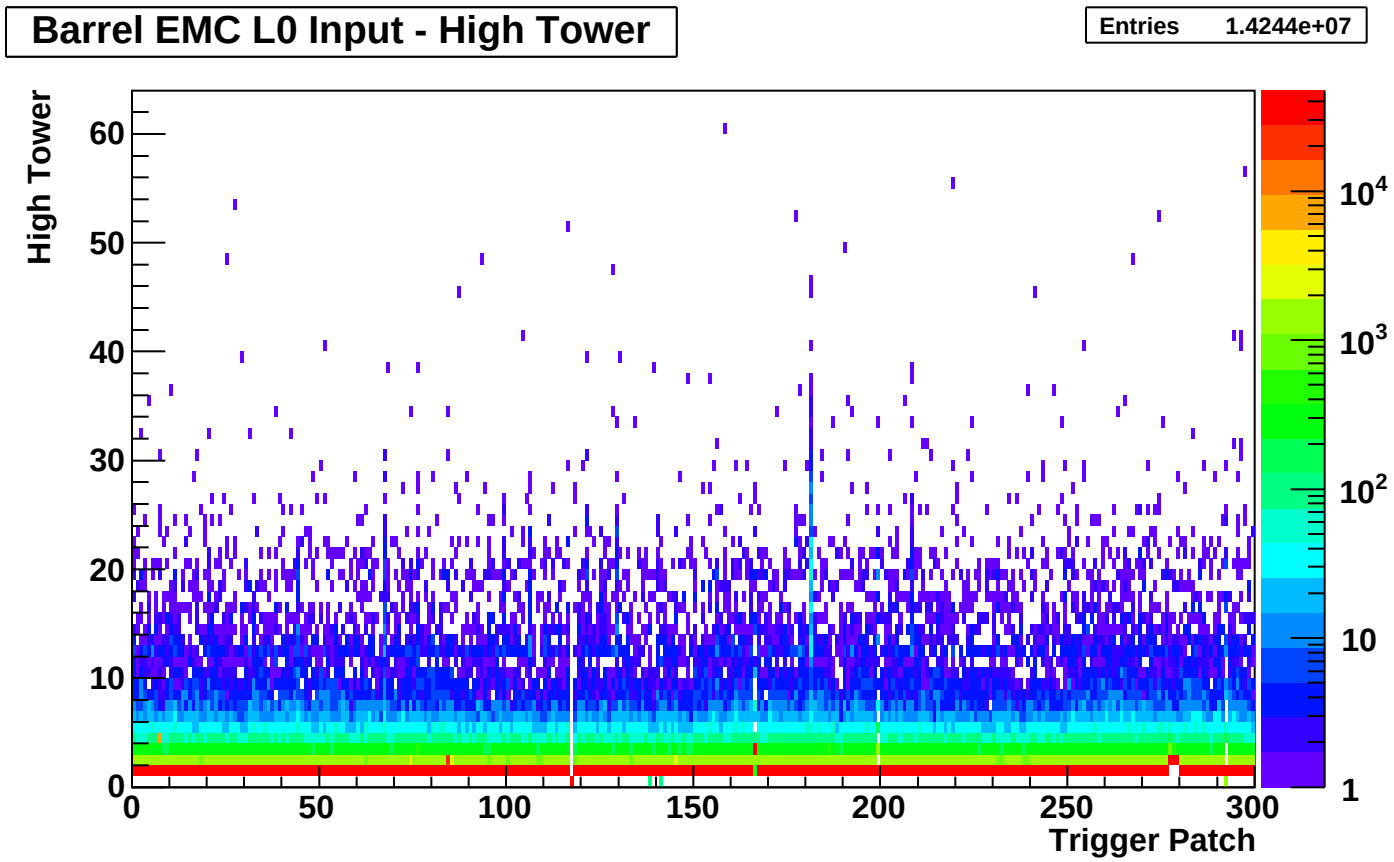

DAQ10k HT bits errors
Entries 0

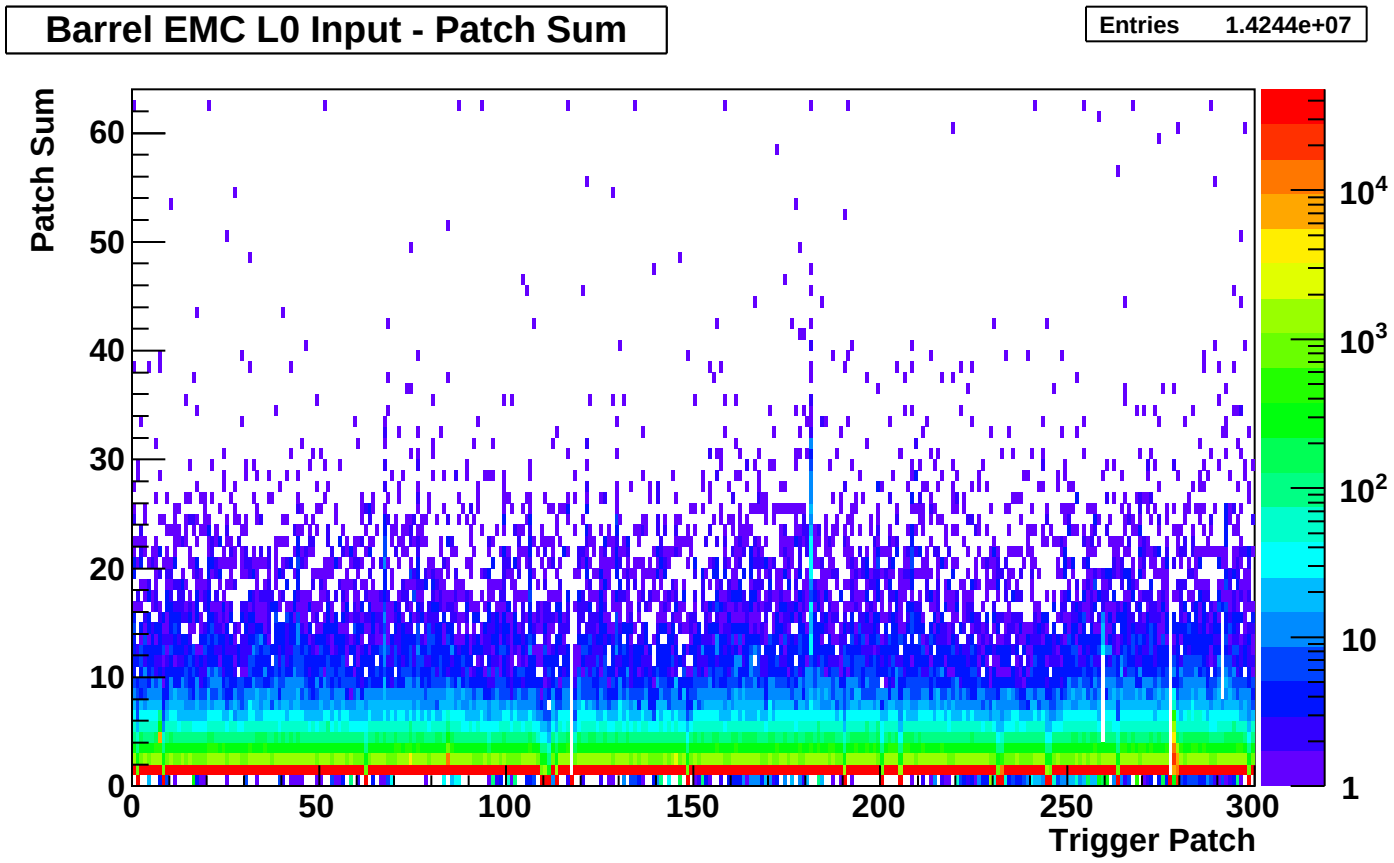

TPC Readout Mask (simulated)
Entries 7578

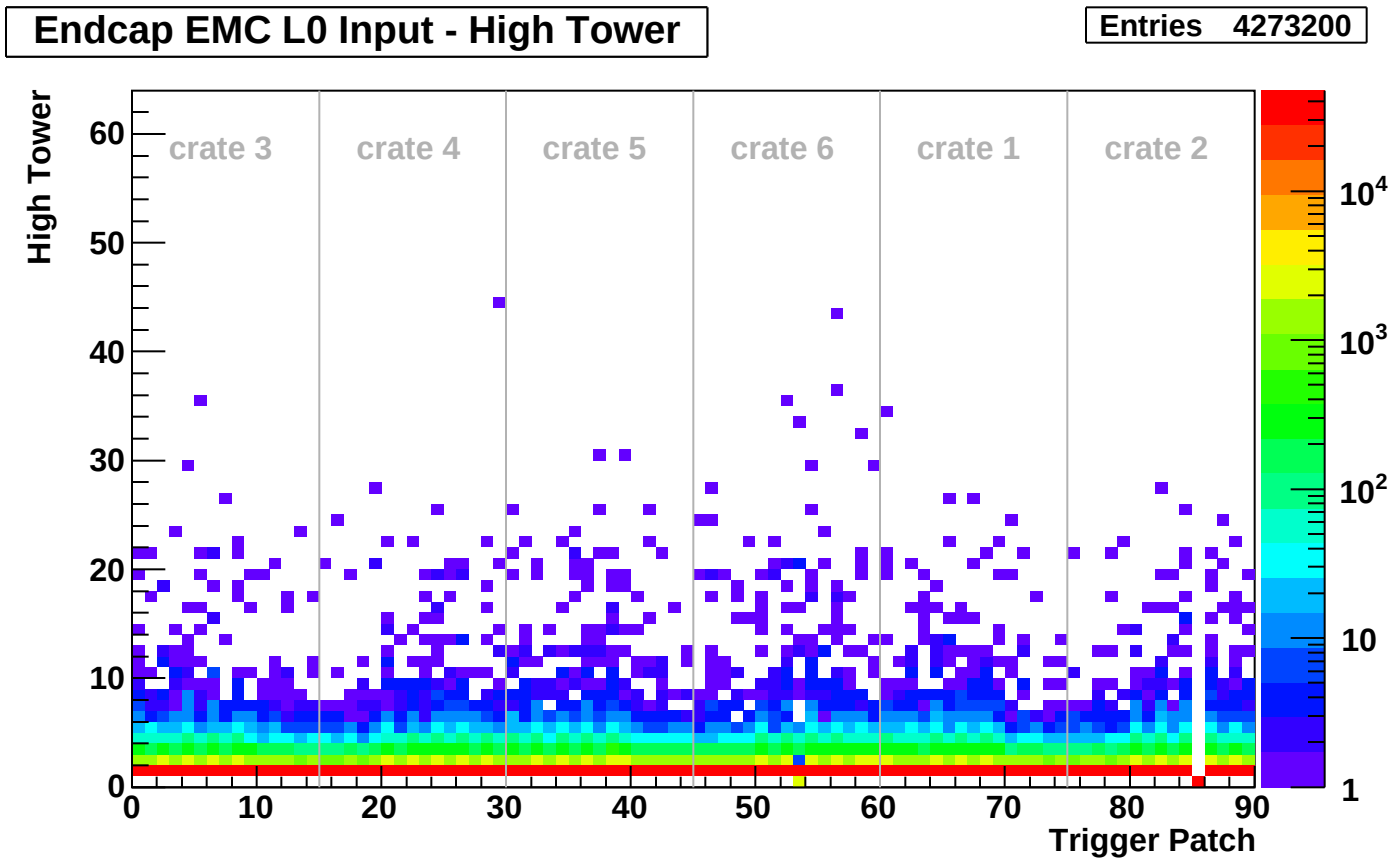

BunchId7Bit (all events)
Entries 47480

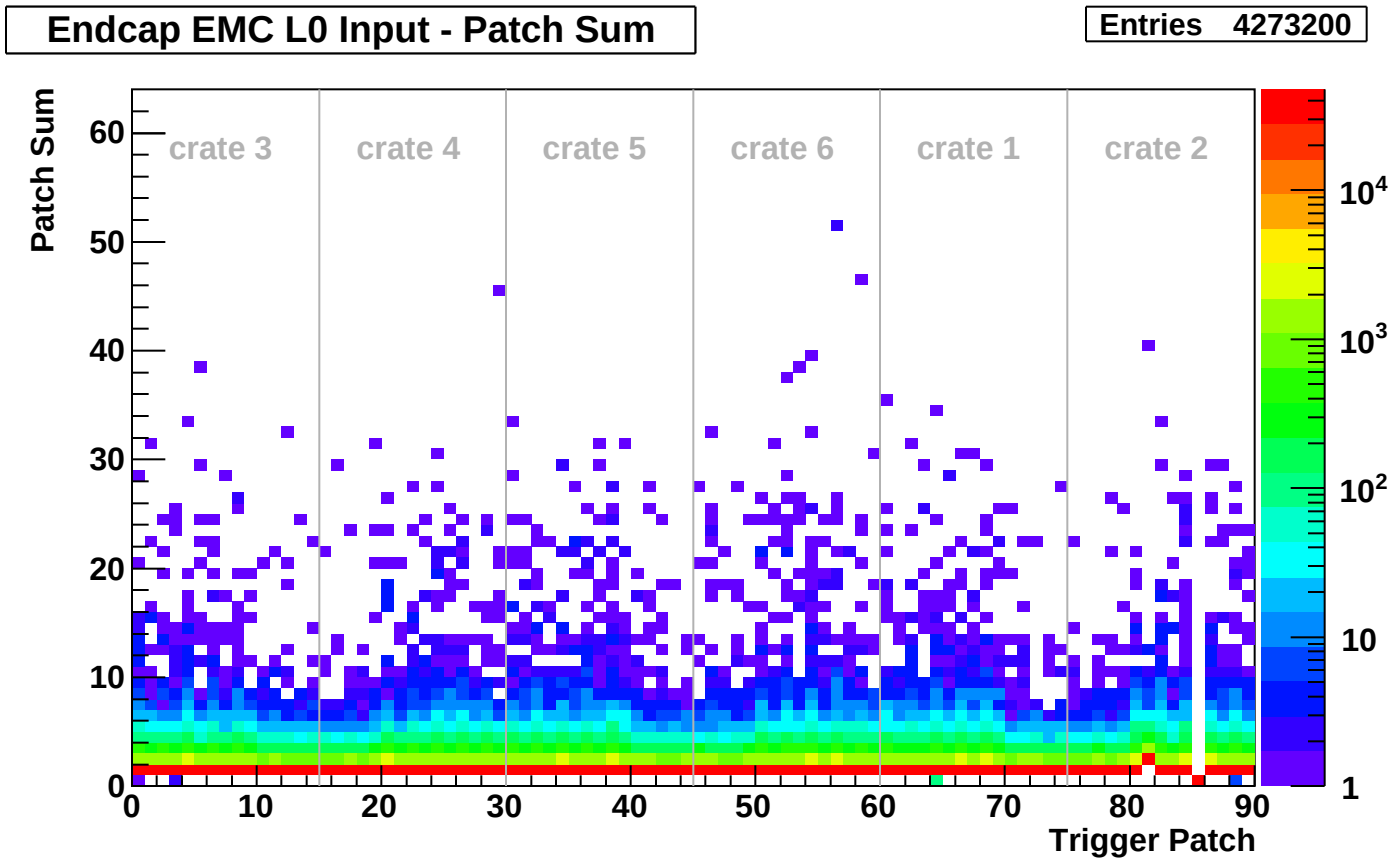

Abort-gaps
Entries 997

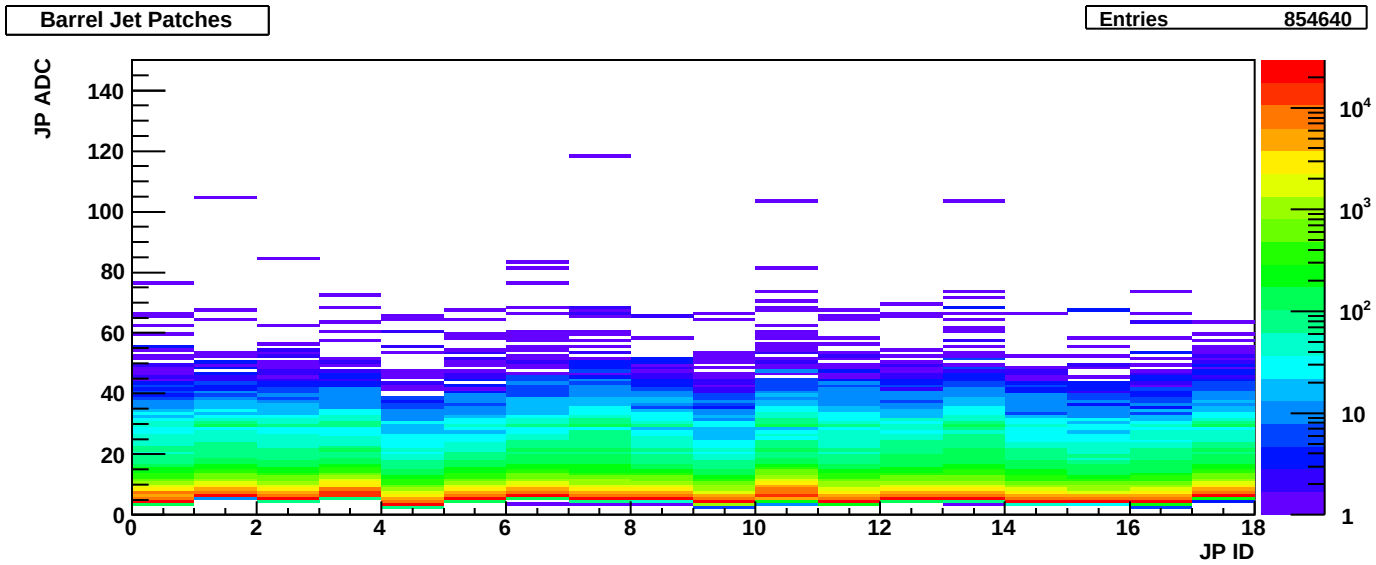

!Yellow-Fill
Entries 1660

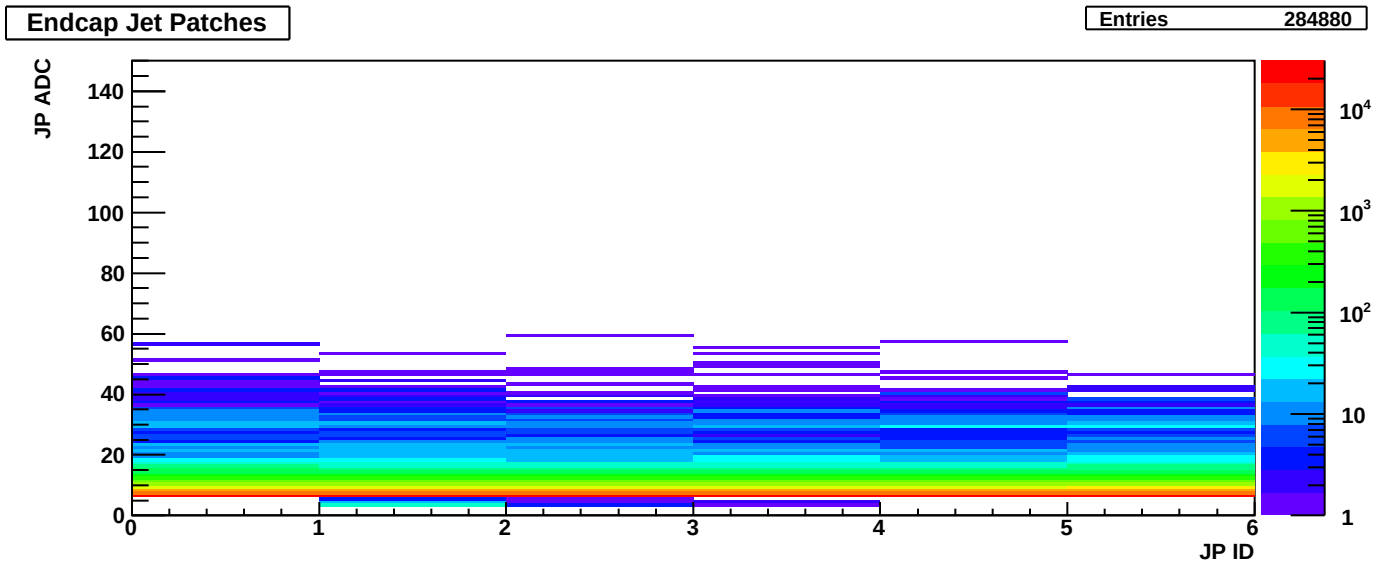

Barrel EMC L0 Input - High Tower
Entries 1.4244e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

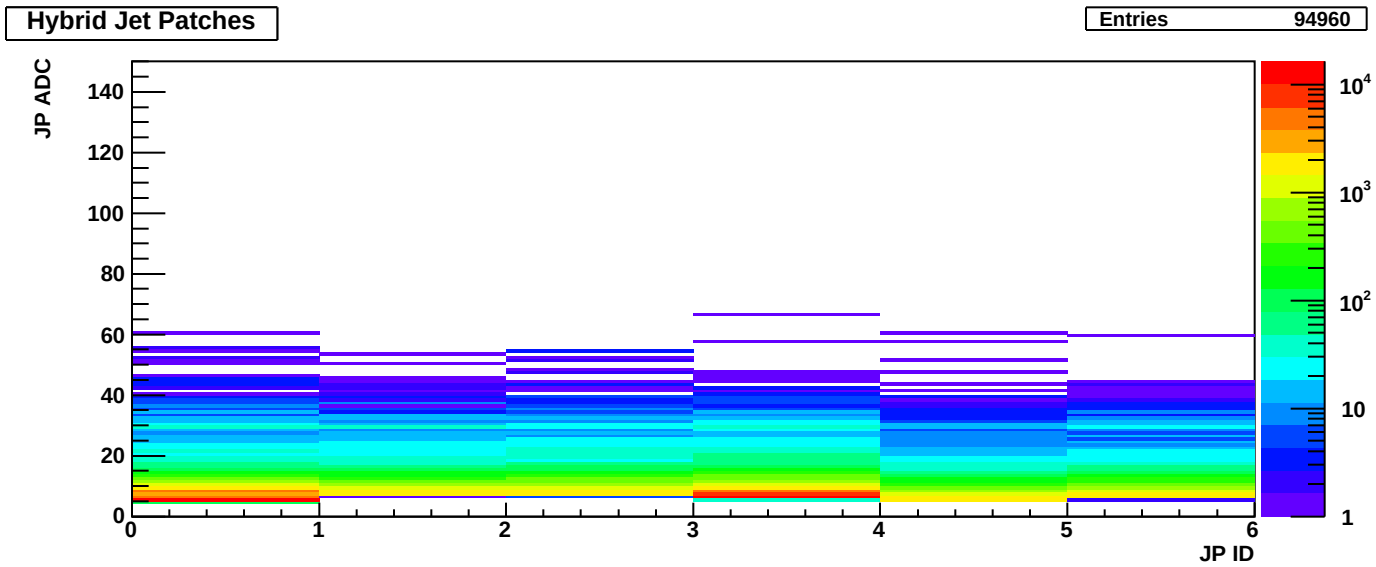

Barrel EMC L0 Input - Patch Sum
Entries 1.4244e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

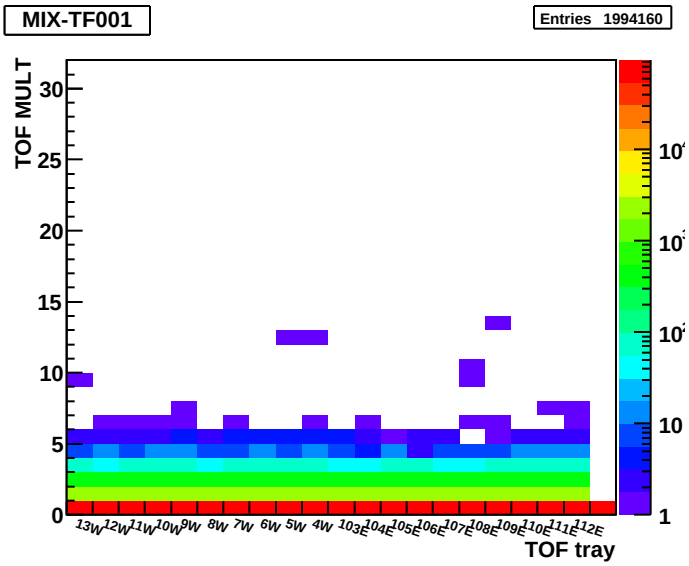

Endcap EMC L0 Input - High Tower
Entries 4273200
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

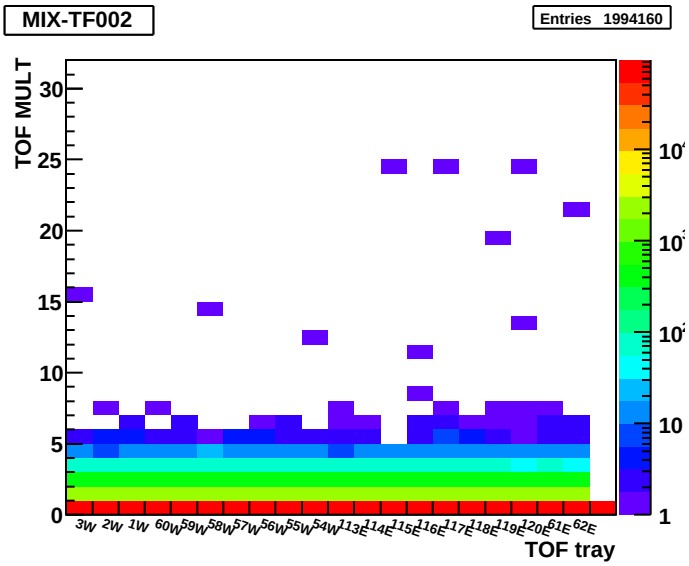

Endcap EMC L0 Input - Patch Sum
Entries 4273200
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

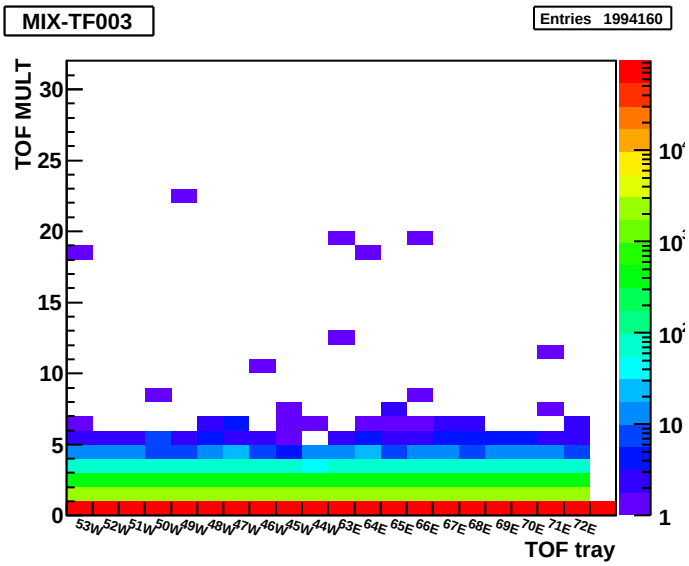

Barrel Jet Patches
Entries 854640
JP ADC
JP ID

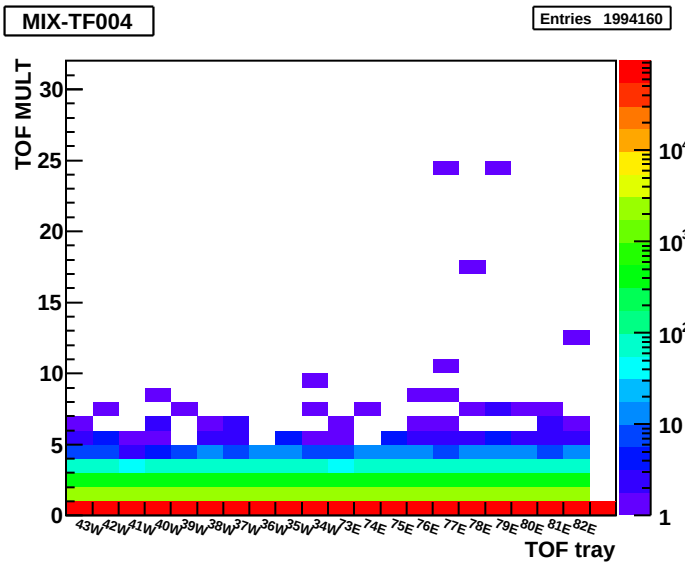

Endcap Jet Patches
Entries 284880
JP ADC
JP ID

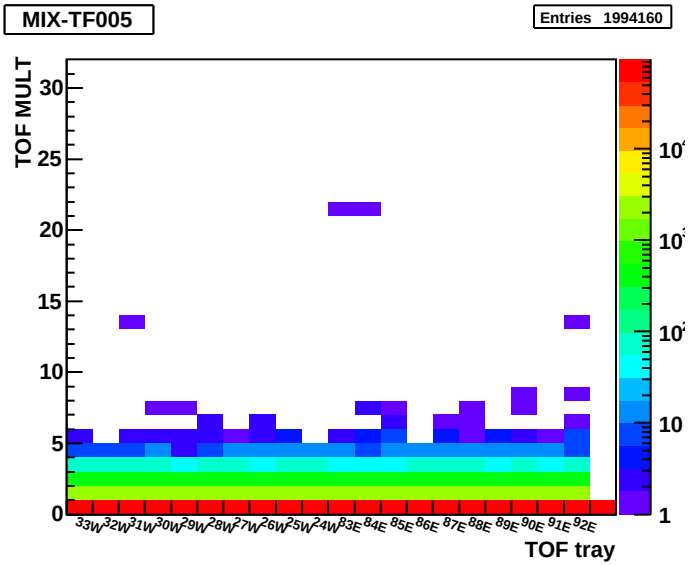

Hybrid Jet Patches
Entries 94960
JP ADC
JP ID

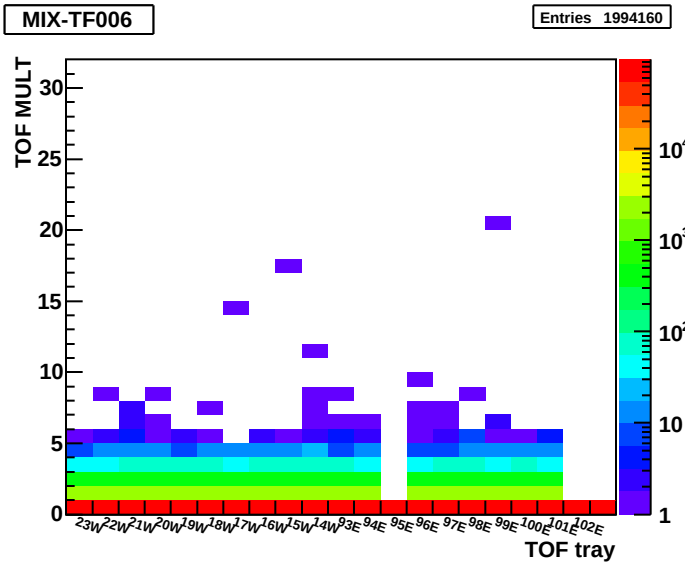

MIX-TF001
Entries 1994160
TOF MULT
TOF tray

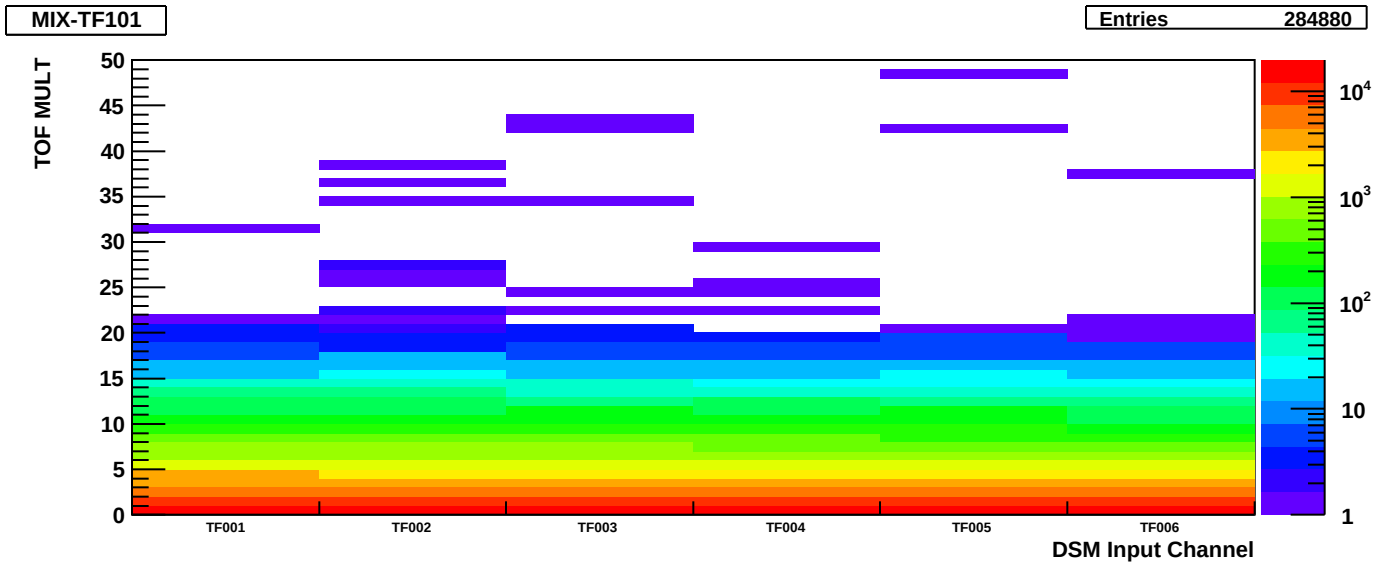

MIX-TF002
Entries 1994160
TOF MULT
TOF tray

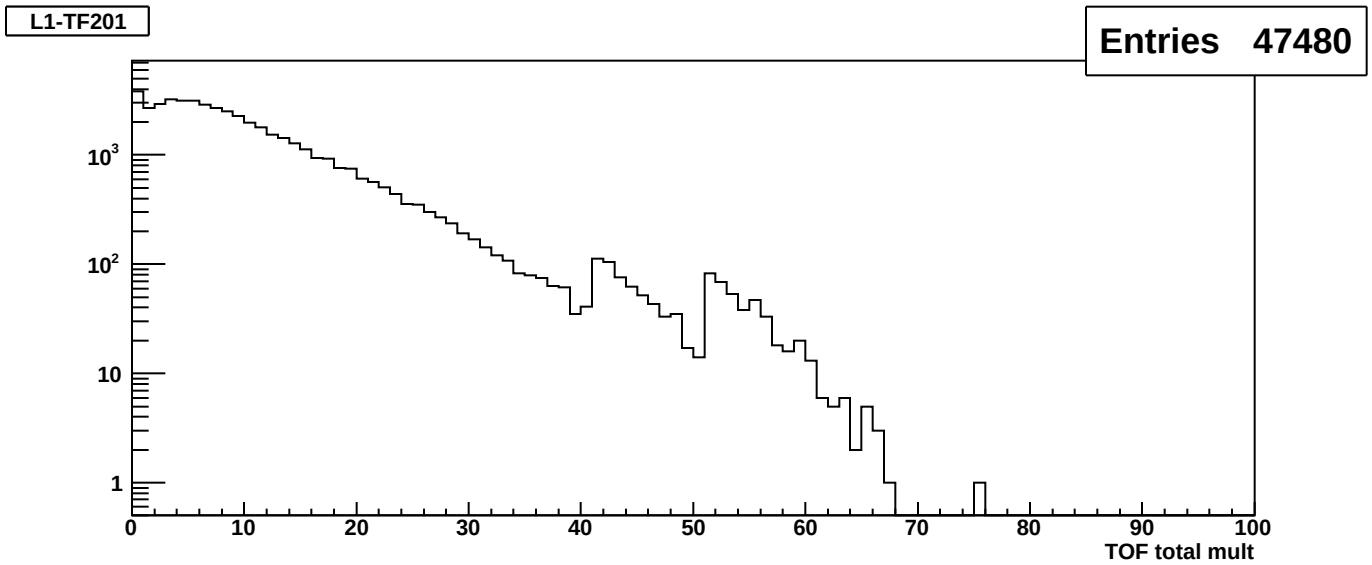

MIX-TF003
Entries 1994160
TOF MULT
TOF tray

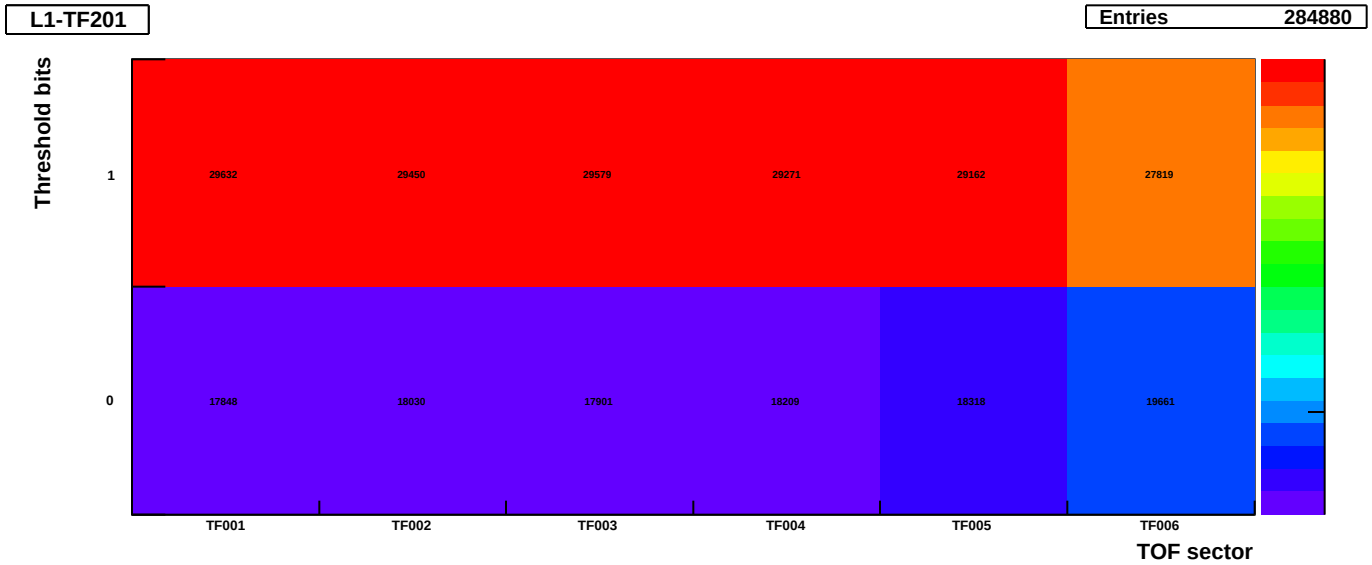

MIX-TF004
Entries 1994160
TOF MULT
TOF tray

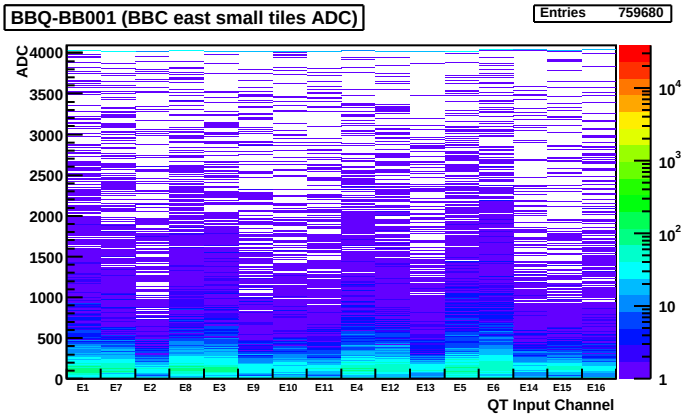

MIX-TF005
Entries 1994160
TOF MULT
TOF tray

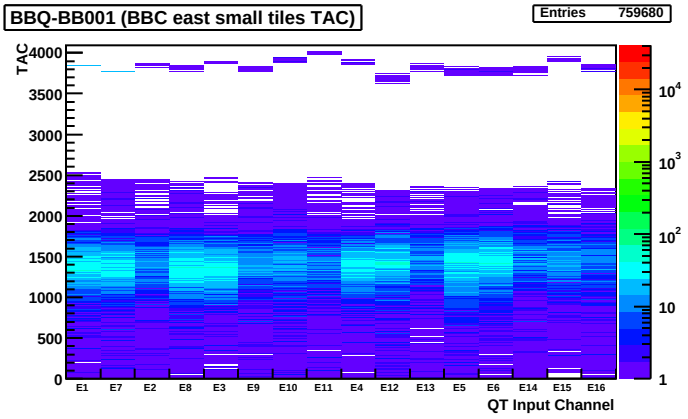

MIX-TF006
Entries 1994160
TOF MULT
TOF tray

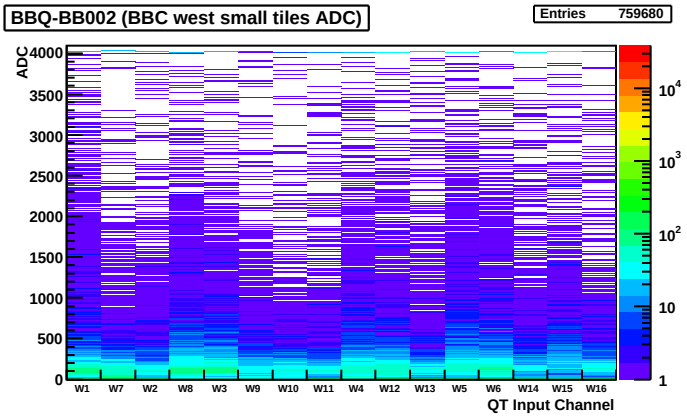

MIX-TF101
Entries 284880
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

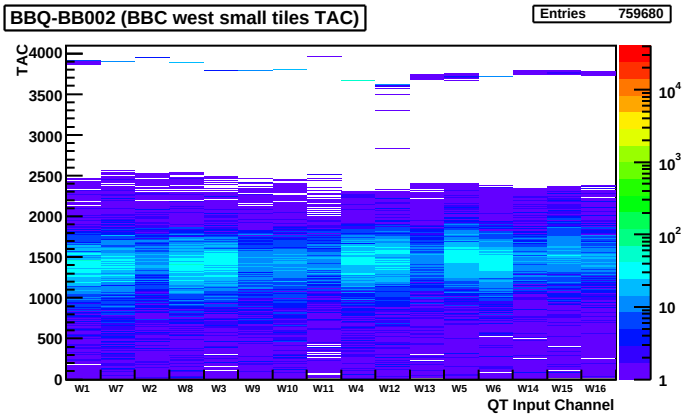

L1-TF201
Entries 47480
TOF total mult

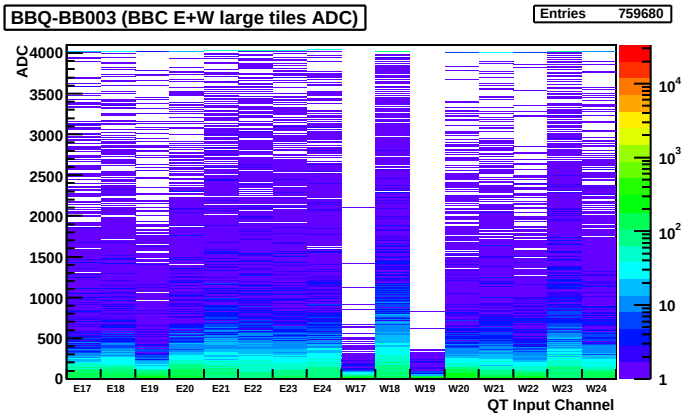

L1-TF201
Entries 284880
Threshold bits
29632
29450
29579
29271
29162
27819
17848
18030
17901
18209
18318
19661
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

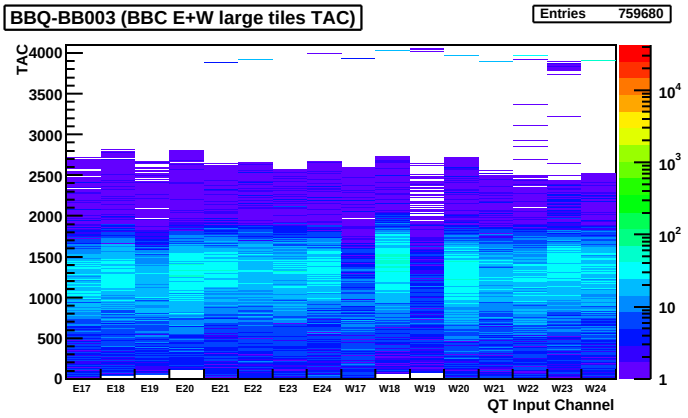

BBQ-BB001 (BBC east small tiles ADC)
Entries 759680
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

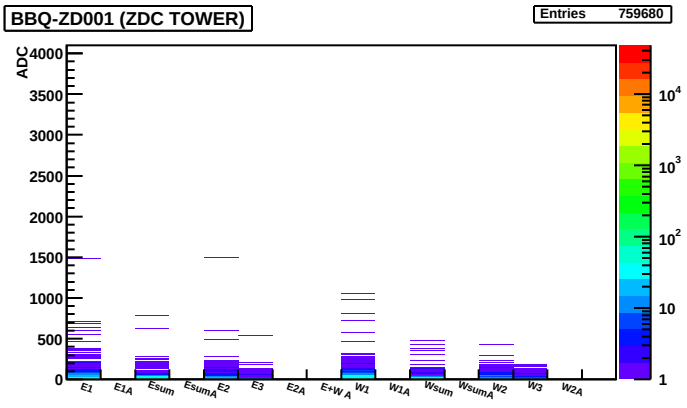

BBQ-BB001 (BBC east small tiles TAC)
Entries 759680
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

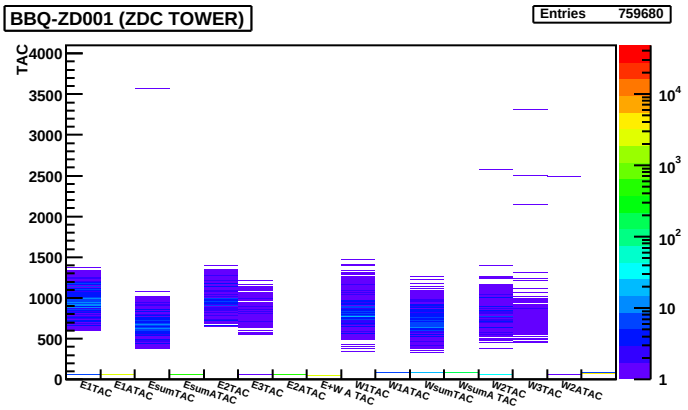

BBQ-BB002 (BBC west small tiles ADC)
Entries 759680
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

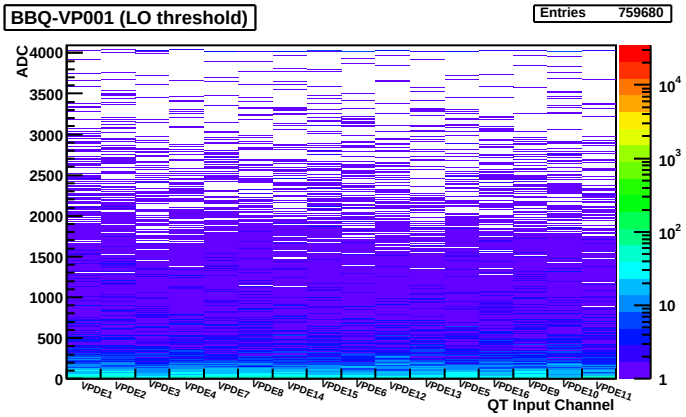

BBQ-BB002 (BBC west small tiles TAC)
Entries 759680
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

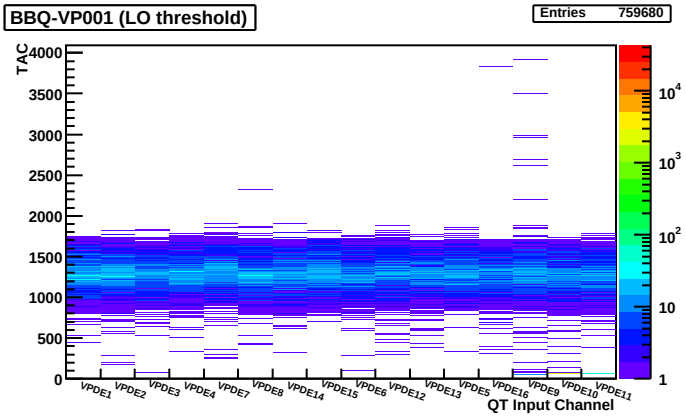

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 759680
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

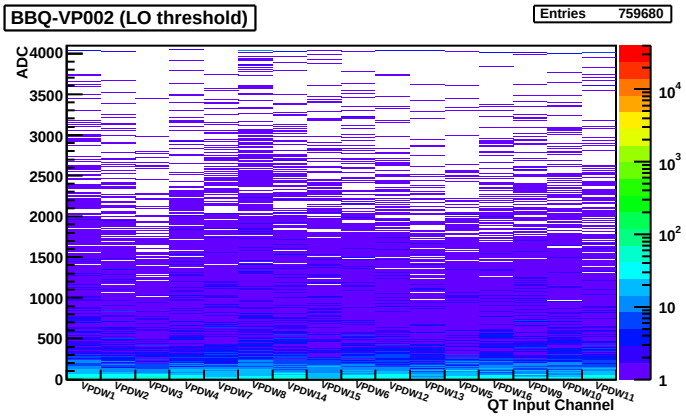

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 759680
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

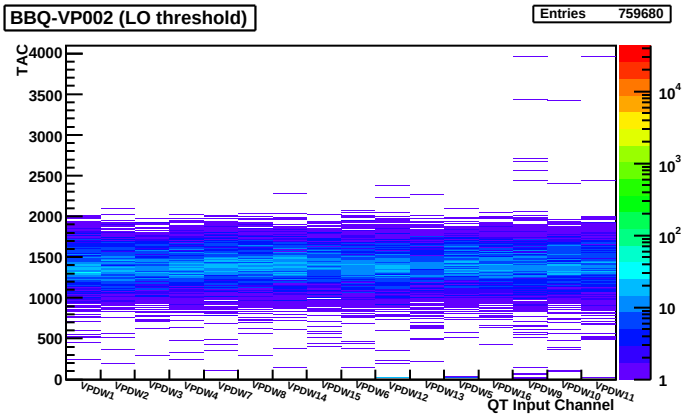

BBQ-ZD001 (ZDC TOWER)
Entries 759680
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

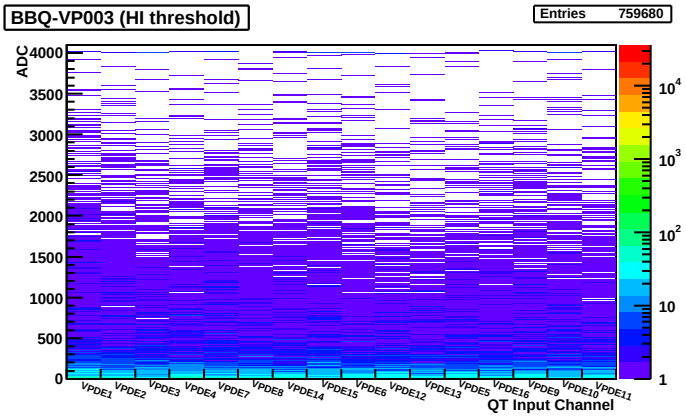

BBQ-ZD001 (ZDC TOWER)
Entries 759680
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

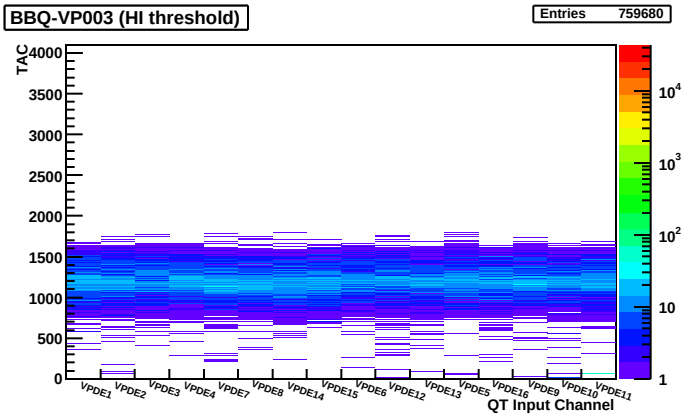

BBQ-VP001 (LO threshold)
Entries 759680
ADC
QT Input Channel

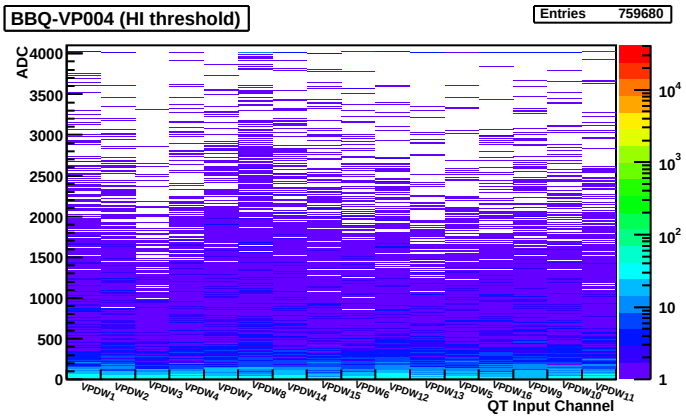

BBQ-VP001 (LO threshold)
Entries 759680
TAC
QT Input Channel

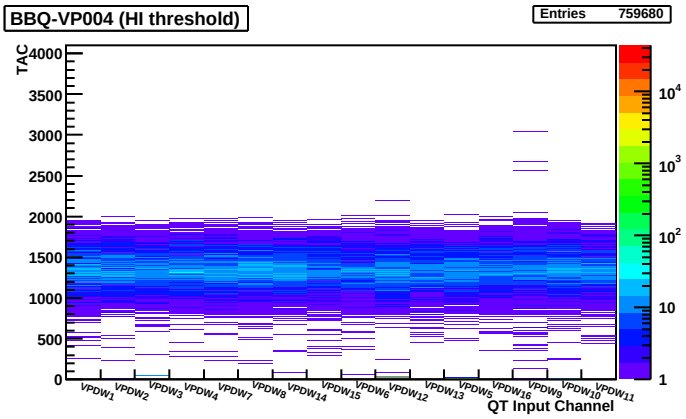

BBQ-VP002 (LO threshold)
Entries 759680
ADC
QT Input Channel

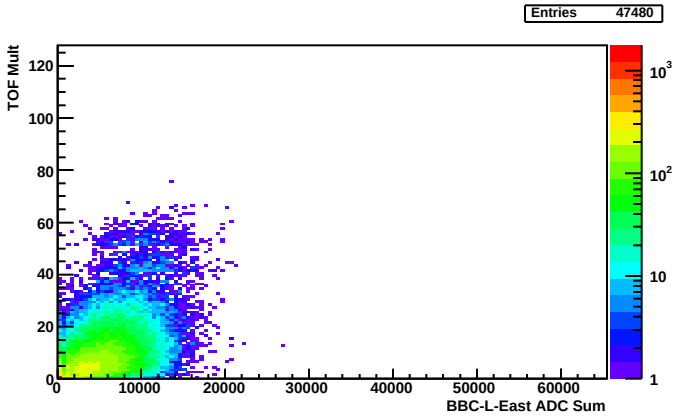

BBQ-VP002 (LO threshold)
Entries 759680
TAC
QT Input Channel

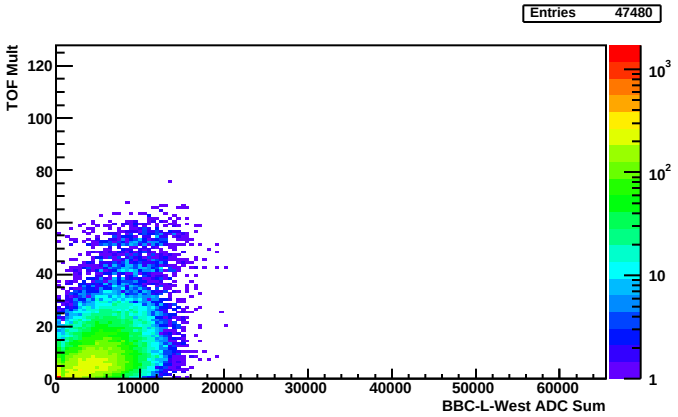

BBQ-VP003 (HI threshold)
Entries 759680
ADC
QT Input Channel

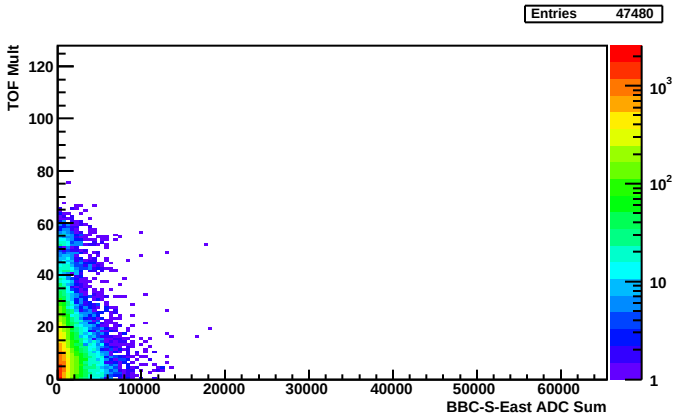

BBQ-VP003 (HI threshold)
Entries 759680
TAC
QT Input Channel

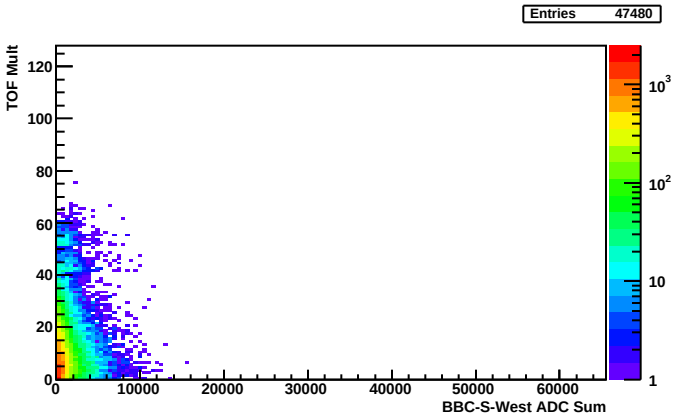

BBQ-VP004 (HI threshold)
Entries 759680
ADC
QT Input Channel

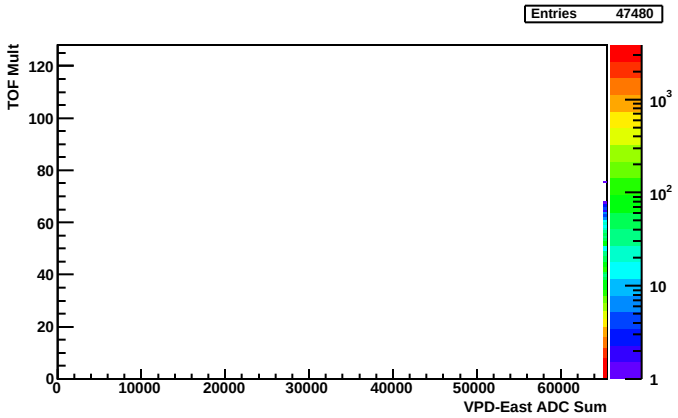

BBQ-VP004 (HI threshold)
Entries 759680
TAC
QT Input Channel

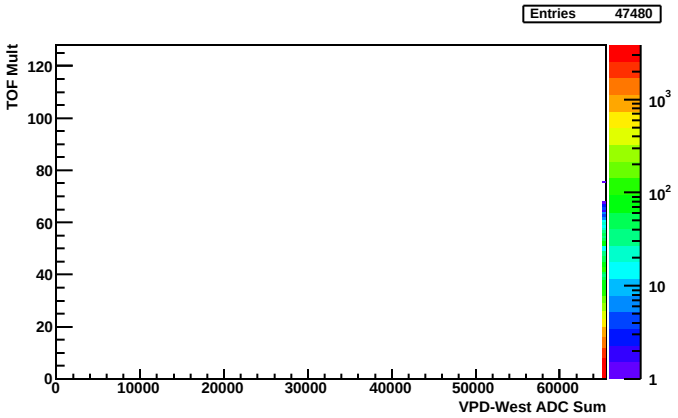

Entries 47480
TOF Mult
BBC-L-East ADC Sum

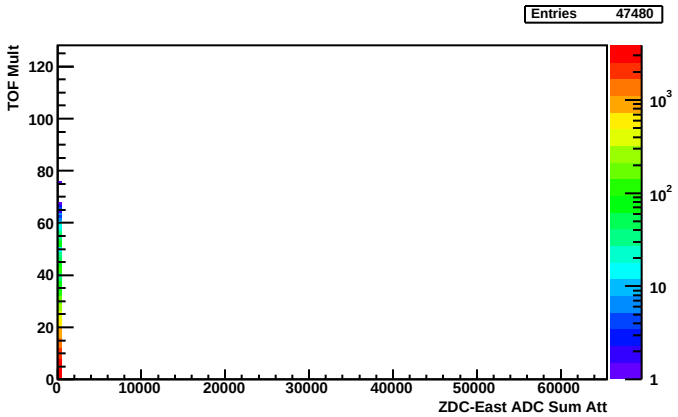

Entries 47480
TOF Mult
BBC-L-West ADC Sum

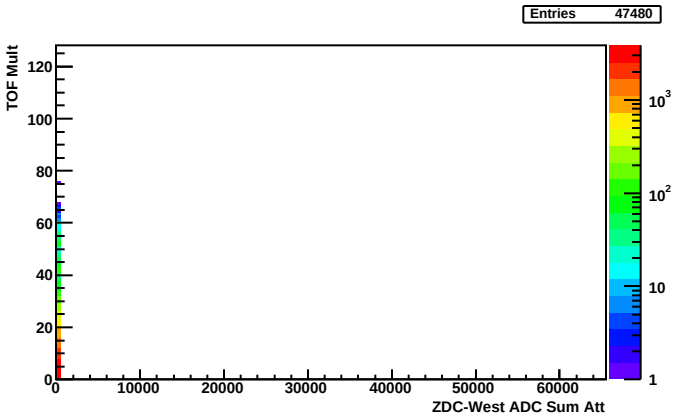

Entries 47480
TOF Mult
BBC-S-East ADC Sum

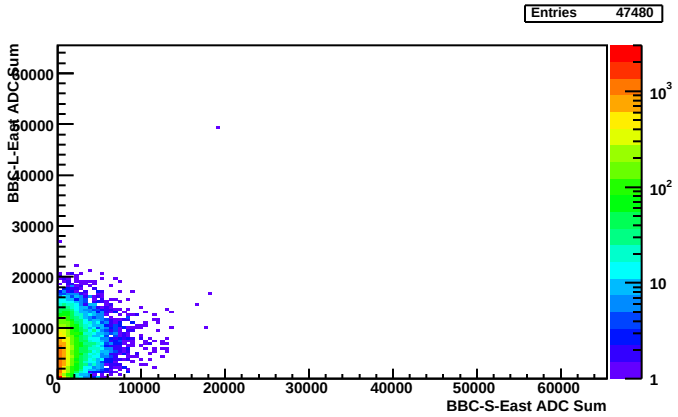

Entries 47480
TOF Mult
BBC-S-West ADC Sum

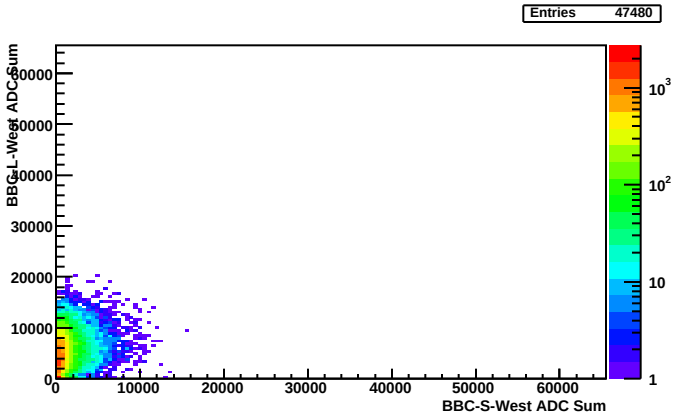

Entries 47480
TOF Mult
VPD-East ADC Sum

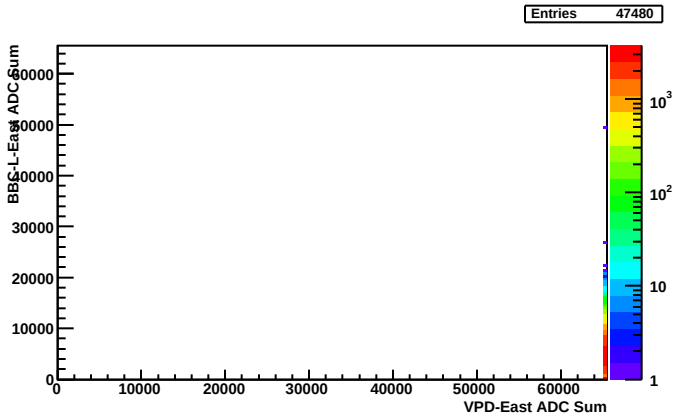

Entries 47480
TOF Mult
VPD-West ADC Sum

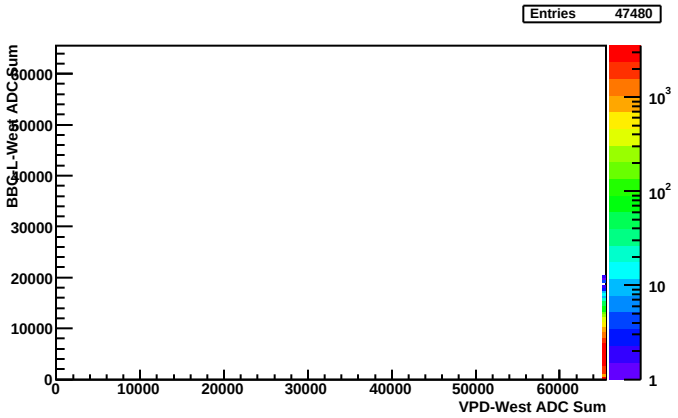

Entries 47480
TOF Mult
ZDC-East ADC Sum Att

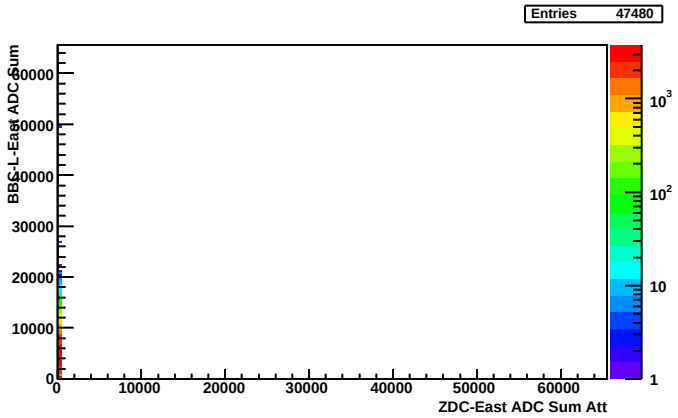

Entries 47480
TOF Mult
ZDC-West ADC Sum Att

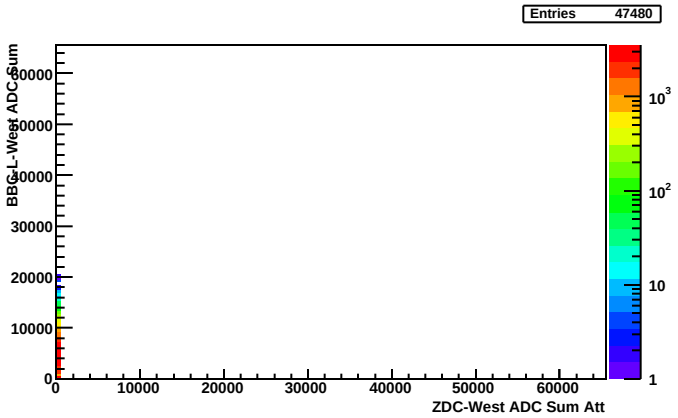

Entries 47480
BBC-L-East ADC Sum
BBC-S-East ADC Sum

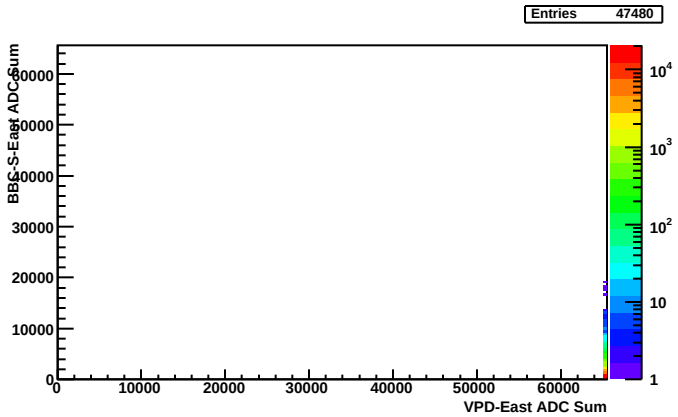

Entries 47480
BBC-L-West ADC Sum
BBC-S-West ADC Sum

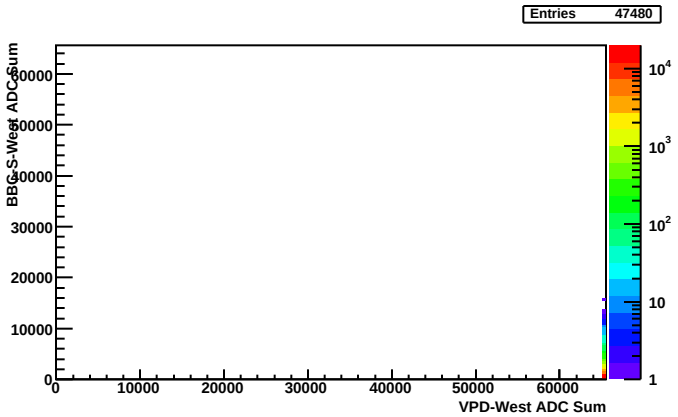

Entries 47480
BBC-L-East ADC Sum
VPD-East ADC Sum

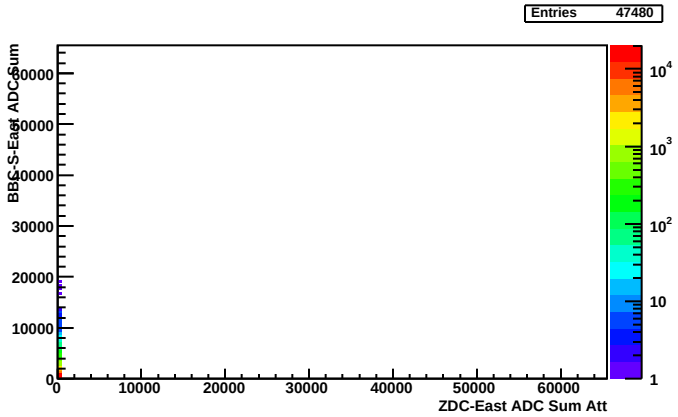

Entries 47480
BBC-L-West ADC Sum
VPD-West ADC Sum

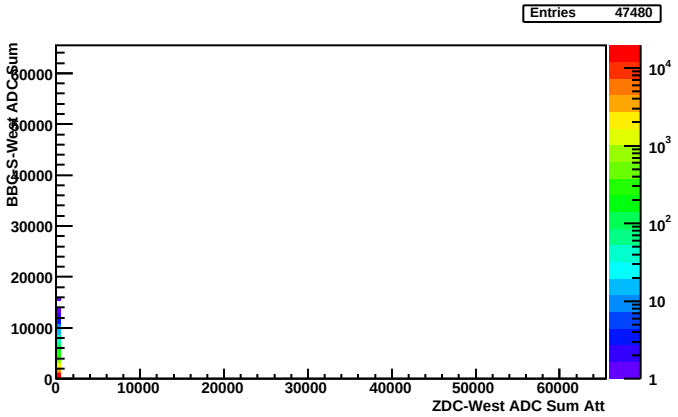

Entries 47480
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

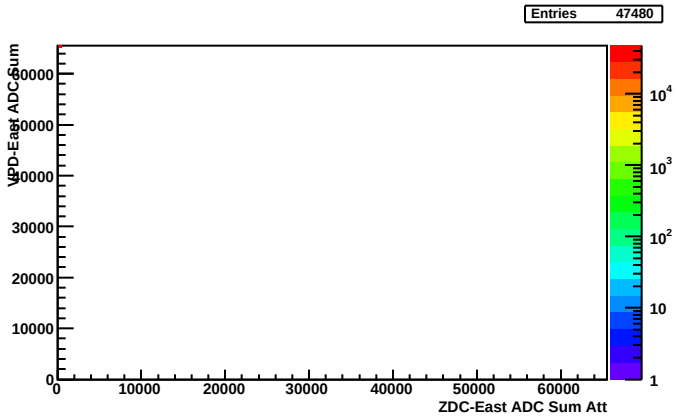

Entries 47480
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

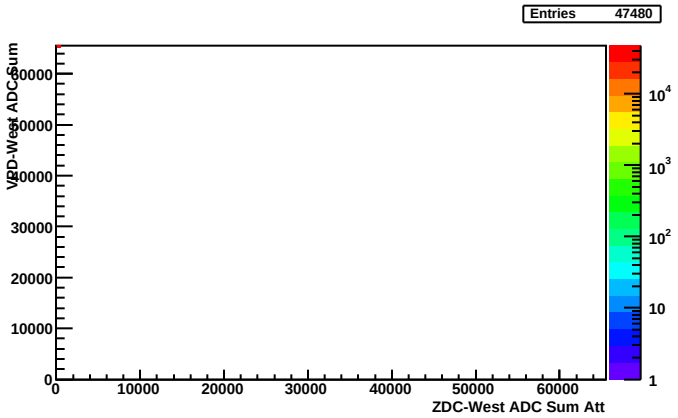

Entries 47480
BBC-S-East ADC Sum
VPD-East ADC Sum

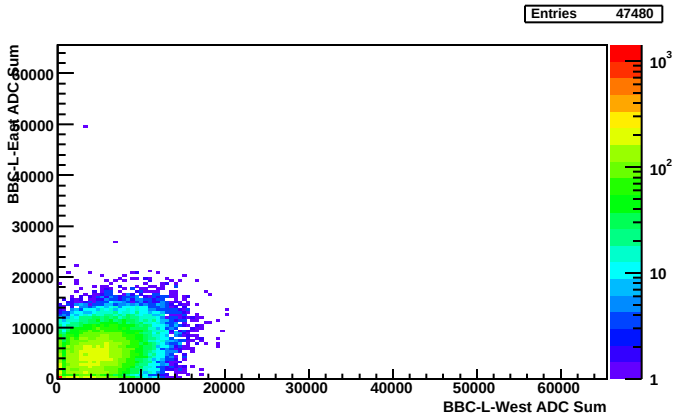

Entries 47480
BBC-S-West ADC Sum
VPD-West ADC Sum

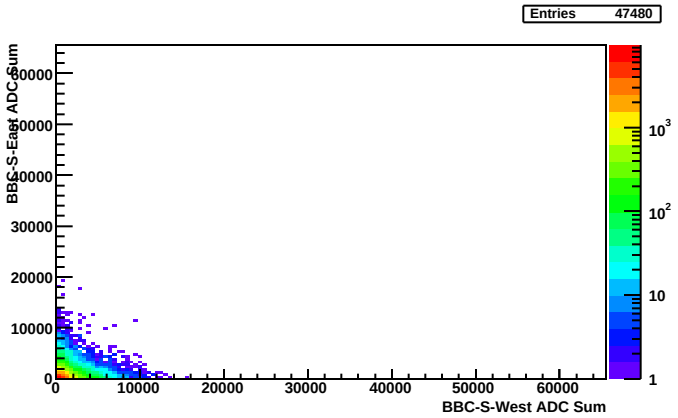

Entries 47480
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

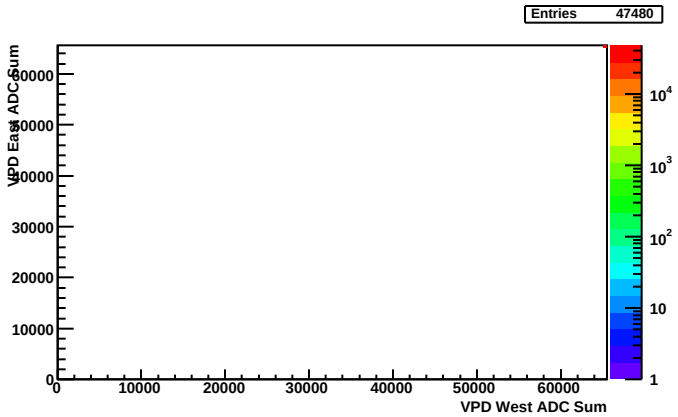

Entries 47480
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

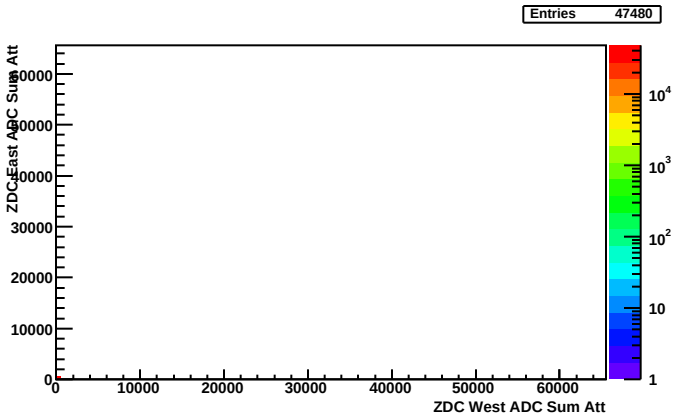

Entries 47480
VPD-East ADC Sum
ZDC-East ADC Sum Att

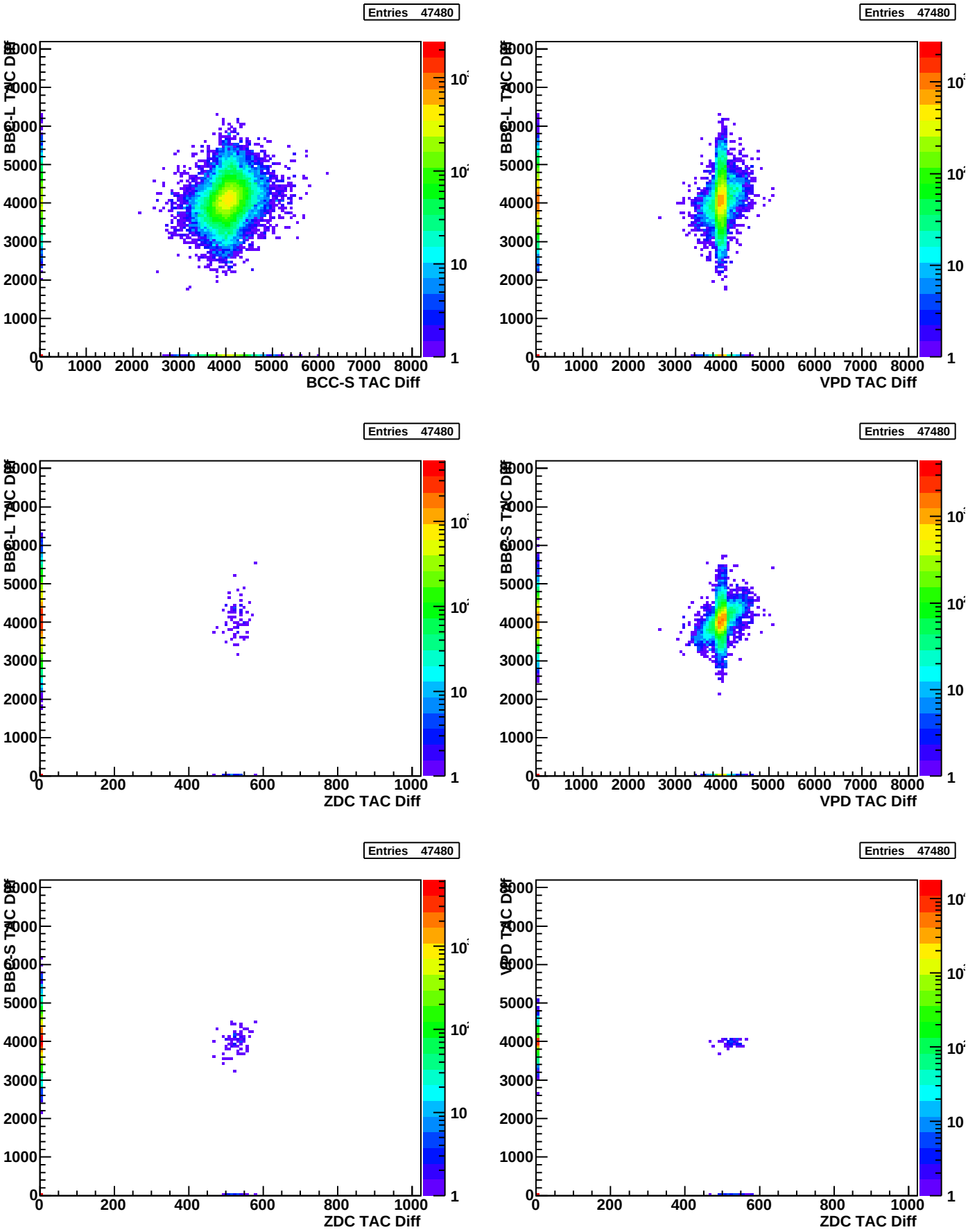

Entries 47480
VPD-West ADC Sum
ZDC-West ADC Sum Att

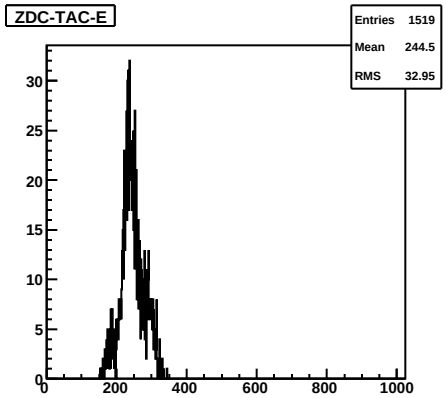

Entries 47480
BBC-L-East ADC Sum
BBC-L-West ADC Sum

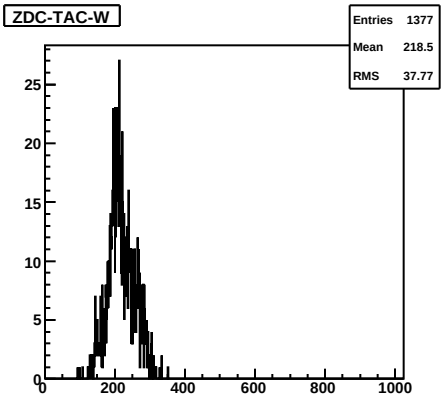

Entries 47480
BBC-S-East ADC Sum
BBC-S-West ADC Sum

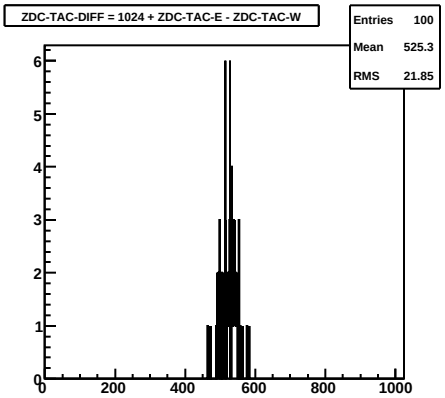

Entries 47480
VPD East ADC Sum
VPD West ADC Sum

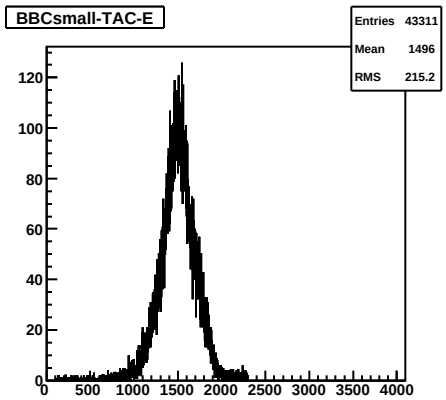

Entries 47480
ZDC East ADC Sum Att
ZDC West ADC Sum Att

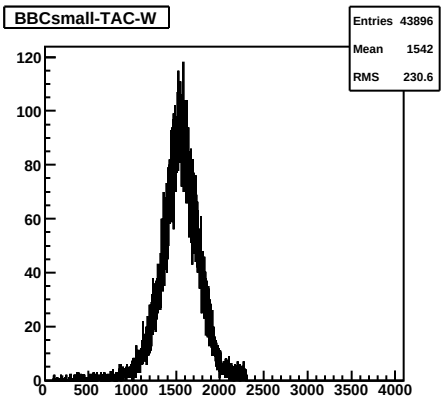

Entries 47480
BBC-L TAC Diff
BCC-S TAC Diff
Entries 47480
BBC-L TAC Diff
VPD TAC Diff
Entries 47480
BBC-L TAC Diff
ZDC TAC Diff
Entries 47480
BBC-S TAC Diff
VPD TAC Diff
Entries 47480
BBC-S TAC Diff
ZDC TAC Diff
Entries 47480
VPD TAC Diff
ZDC TAC Diff

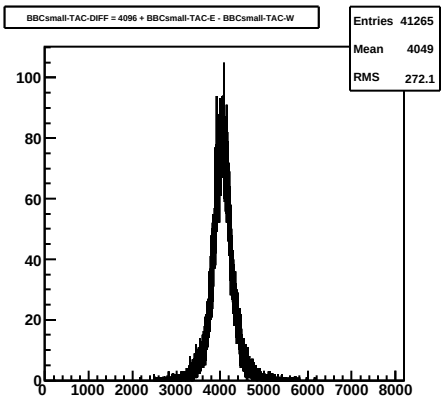

ZDC-TAC-E
Entries 1519
Mean 244.5
RMS 32.95

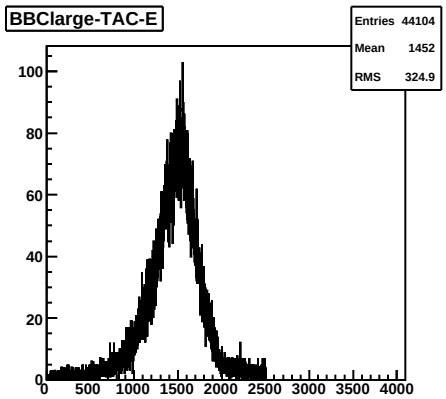

ZDC-TAC-W
Entries 1377
Mean 218.5
RMS 37.77

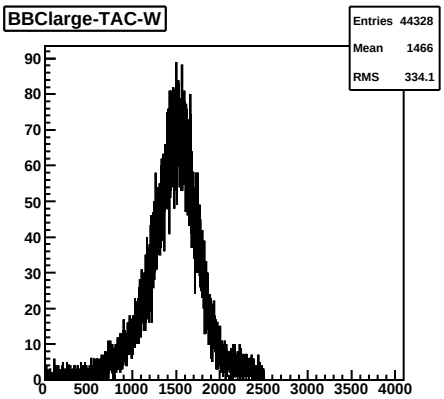

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 100
Mean 525.3
RMS 21.85

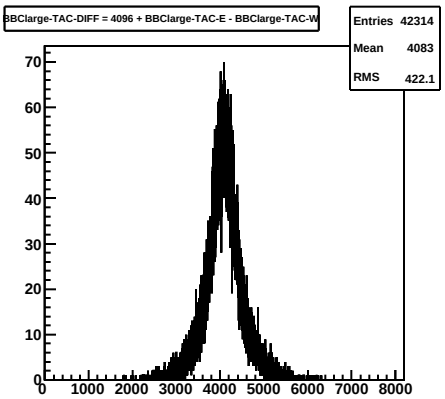

BBCsmall-TAC-E
Entries 43311
Mean 1496
RMS 215.2

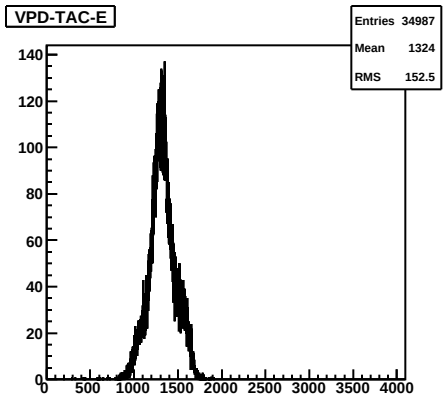

BBCsmall-TAC-W
Entries 43896
Mean 1542
RMS 230.6

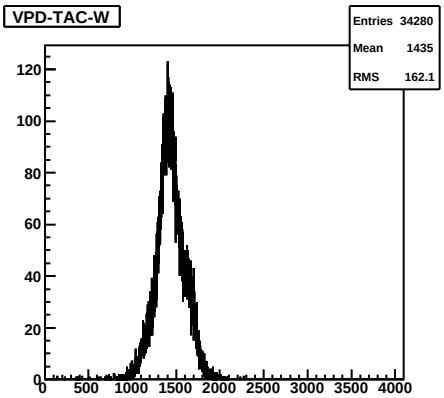

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 41265
Mean 4049
RMS 272.1

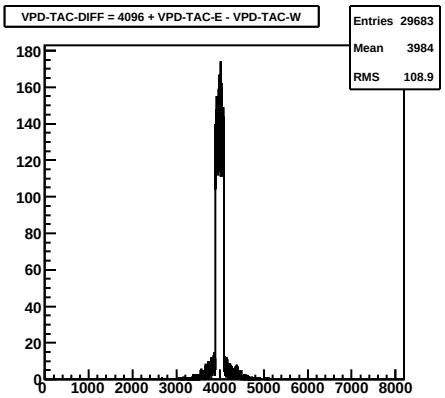

BBClarge-TAC-E
Entries 44104
Mean 1452
RMS 324.9

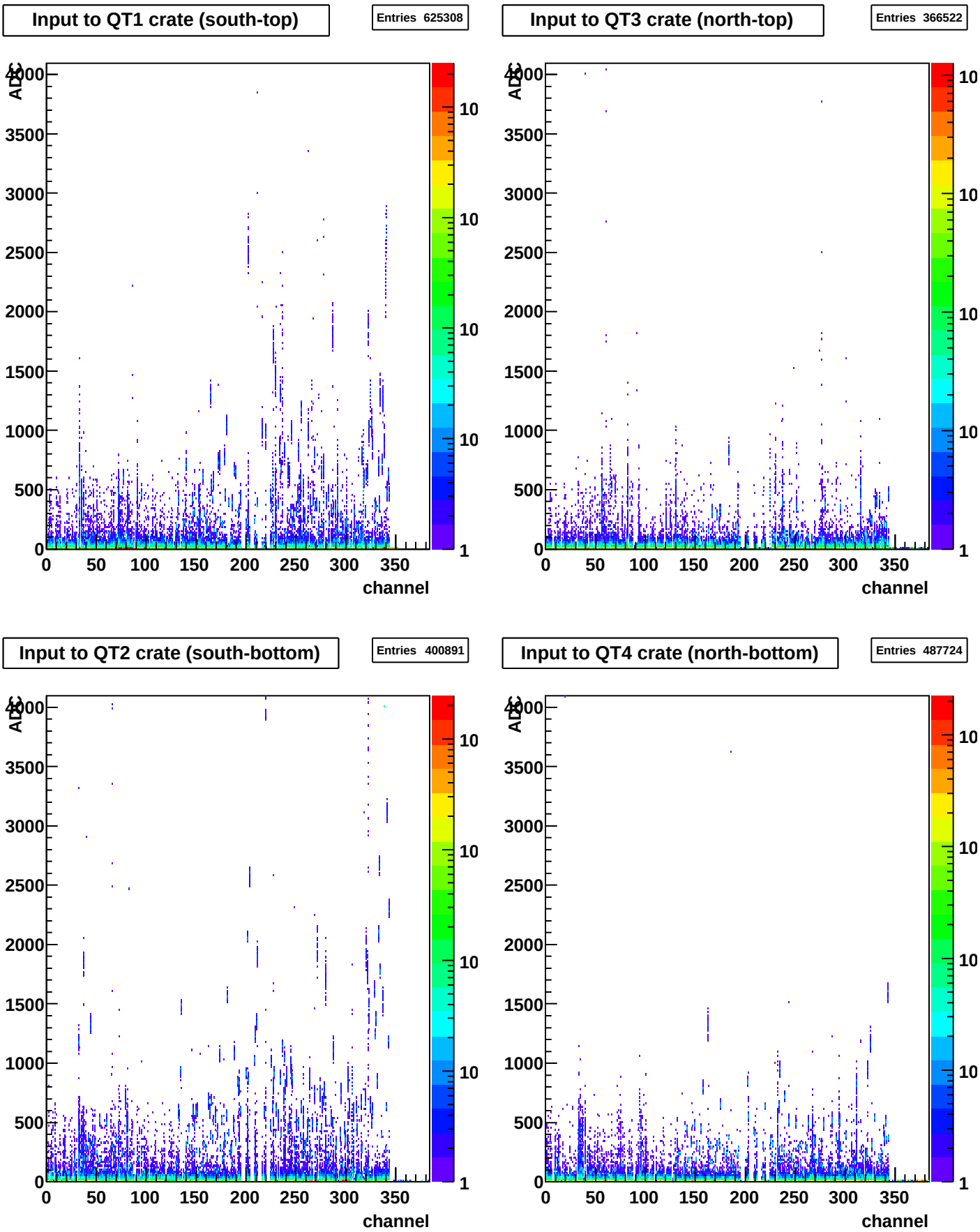

BBClarge-TAC-W
Entries 44328
Mean 1466
RMS 334.1

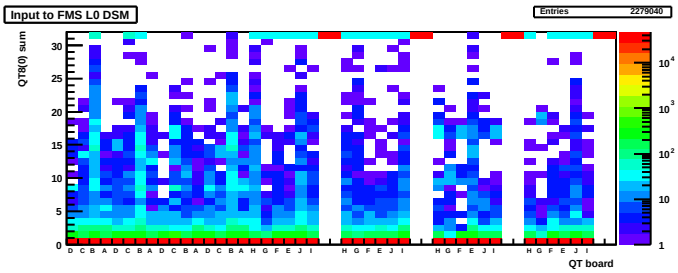

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 42314
Mean 4083
RMS 422.1

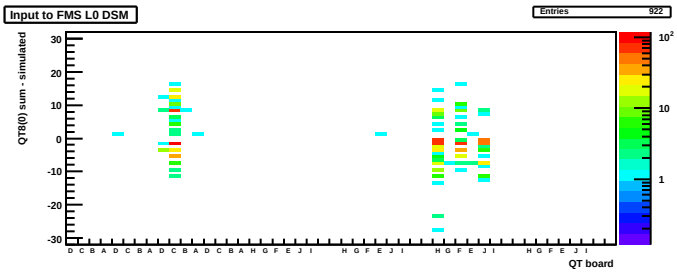

VPD-TAC-E
Entries 34987
Mean 1324
RMS 152.5

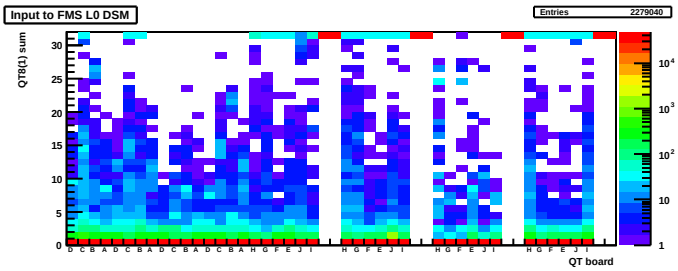

VPD-TAC-W
Entries 34280
Mean 1435
RMS 162.1

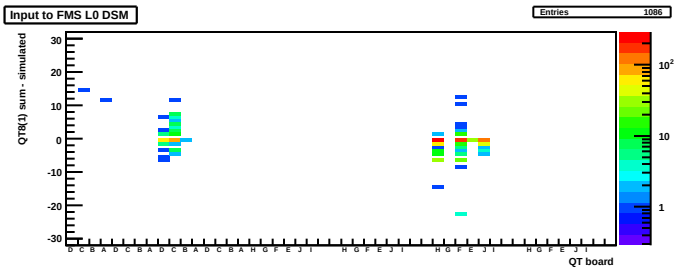

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 29683
Mean 3984
RMS 108.9

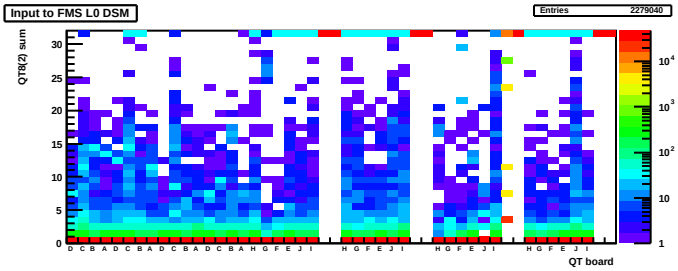

Input to QT1 crate (south-top)
Entries 625308
Input to QT3 crate (north-top)
Entries 366522
Input to QT2 crate (south-bottom)
Entries 400891
Input to QT4 crate (north-bottom)
Entries 487724
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

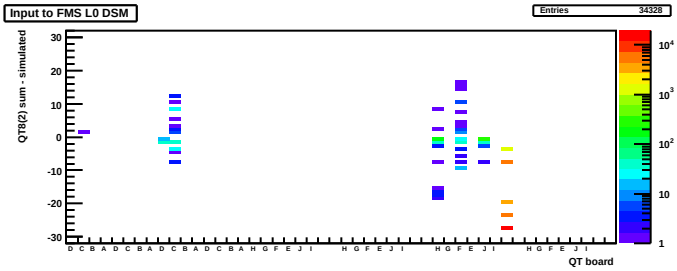

Input to FMS L0 DSM
Entries 2279040
QT8(0) sum
QT board

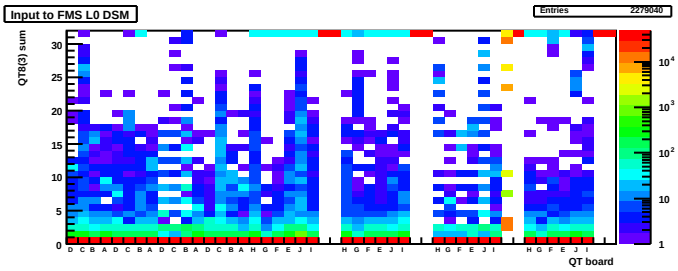

Input to FMS L0 DSM
Entries 922
QT8(0) sum - simulated
QT board

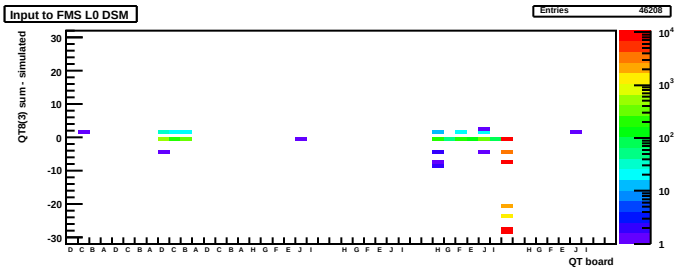

Input to FMS L0 DSM
Entries 2279040
QT8(1) sum
QT board

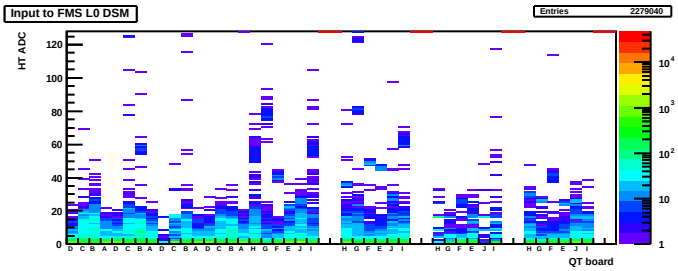

Input to FMS L0 DSM
Entries 1086
QT8(1) sum - simulated
QT board

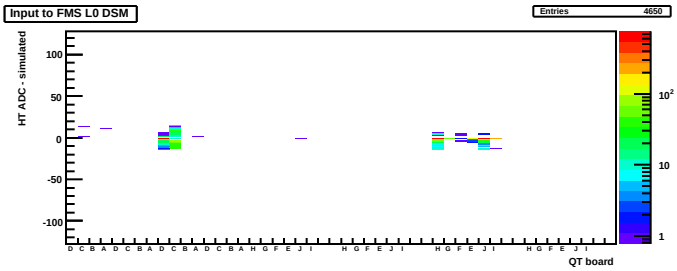

Input to FMS L0 DSM
Entries 2279040
QT8(2) sum
QT board

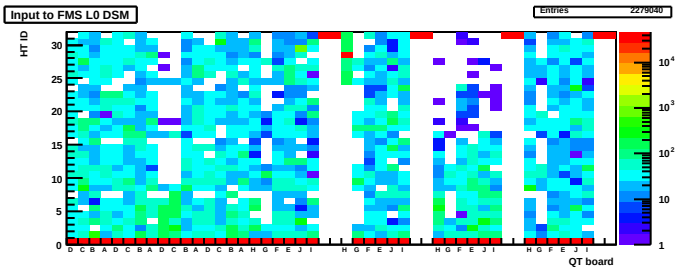

Input to FMS L0 DSM
Entries 34328
QT8(2) sum - simulated
QT board

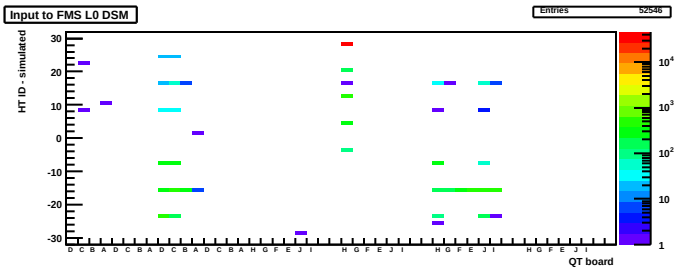

Input to FMS L0 DSM
Entries 2279040
QT8(3) sum
QT board

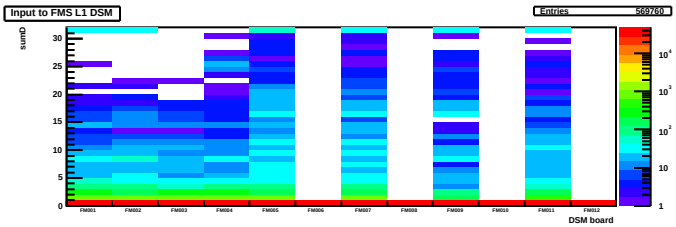

Input to FMS L0 DSM
Entries 46208
QT8(3) sum - simulated
QT board

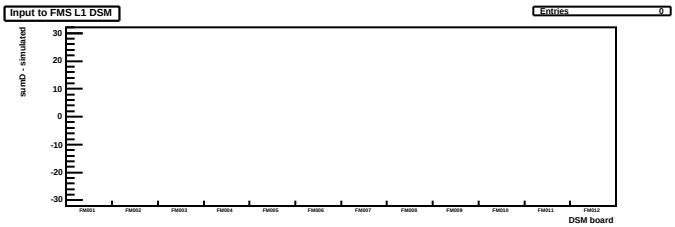

Input to FMS L0 DSM
Entries 2279040
HT ADC
QT board

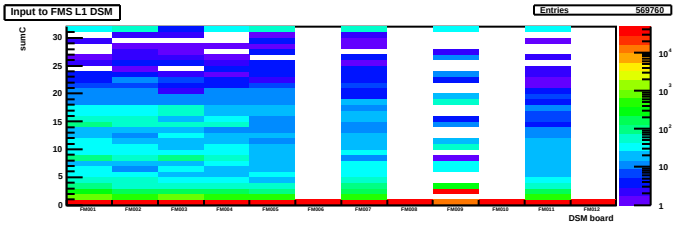

Input to FMS L0 DSM
Entries 4650
HT ADC - simulated
QT board

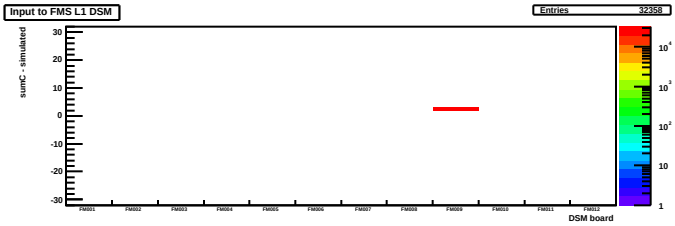

Input to FMS L0 DSM
Entries 2279040
HT ID
QT board

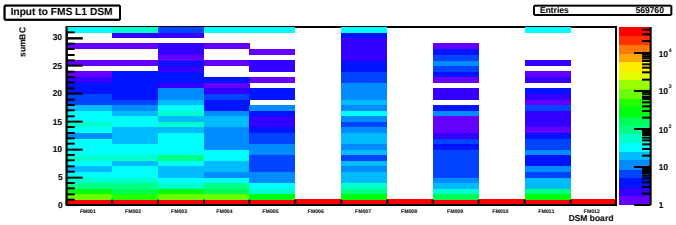

Input to FMS L0 DSM
Entries 52546
HT ID - simulated
QT board

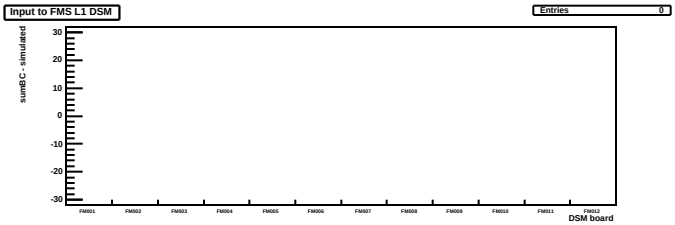

Input to FMS L1 DSM
Entries 569760
sumD
DSM board

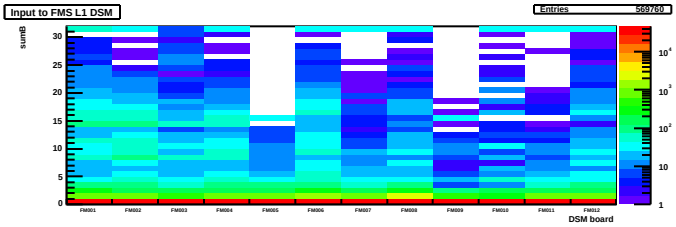

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

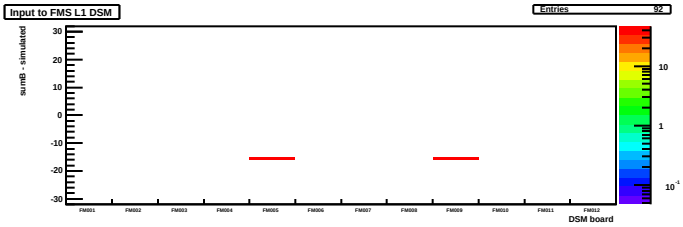

Input to FMS L1 DSM
Entries 569760
sumC
DSM board

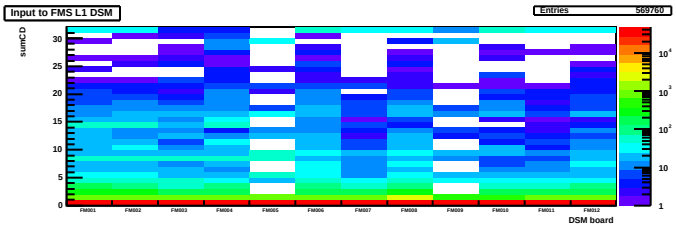

Input to FMS L1 DSM
Entries 32358
sumC - simulated
DSM board

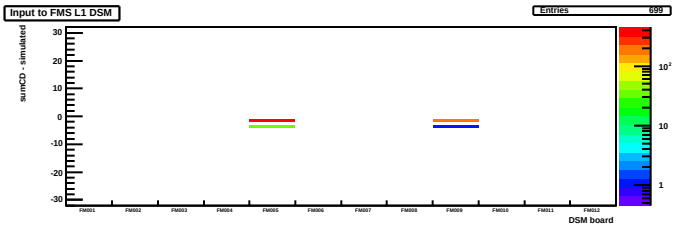

Input to FMS L1 DSM
Entries 569760
sumBC
DSM board

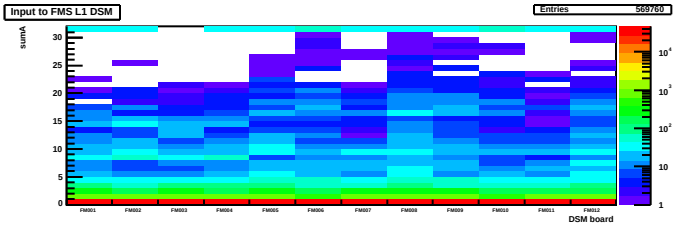

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

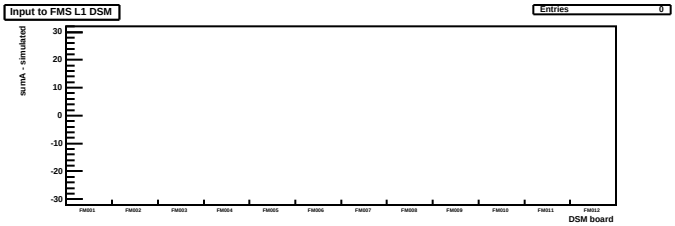

Input to FMS L1 DSM
Entries 569760
sumB
DSM board

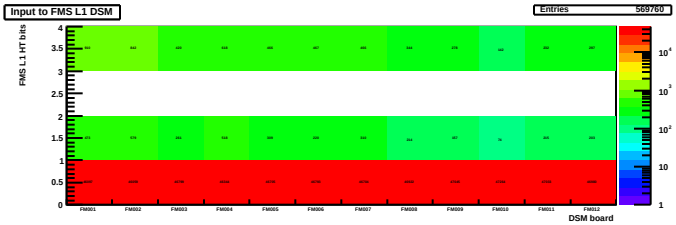

Input to FMS L1 DSM
Entries 92
sumB - simulated
DSM board

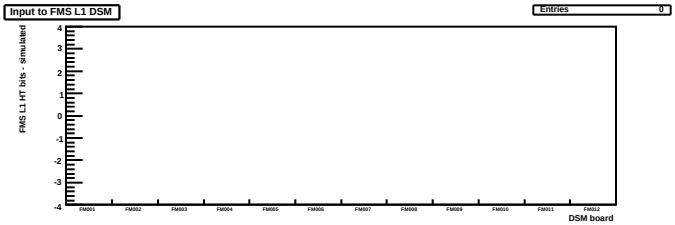

Input to FMS L1 DSM
Entries 569760
sumCD
DSM board

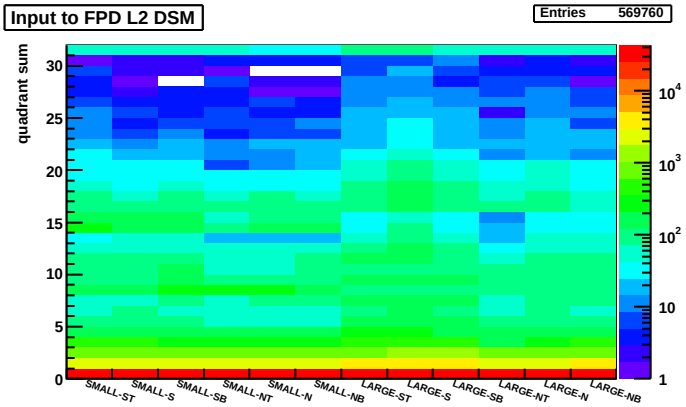

Input to FMS L1 DSM
Entries 699
sumCD - simulated
DSM board

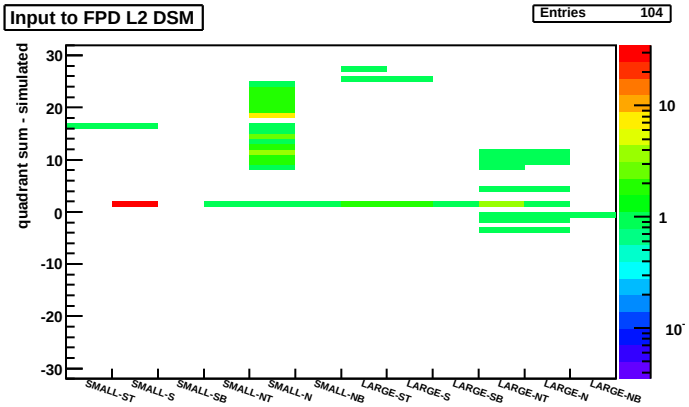

Input to FMS L1 DSM
Entries 569760
sumA
DSM board

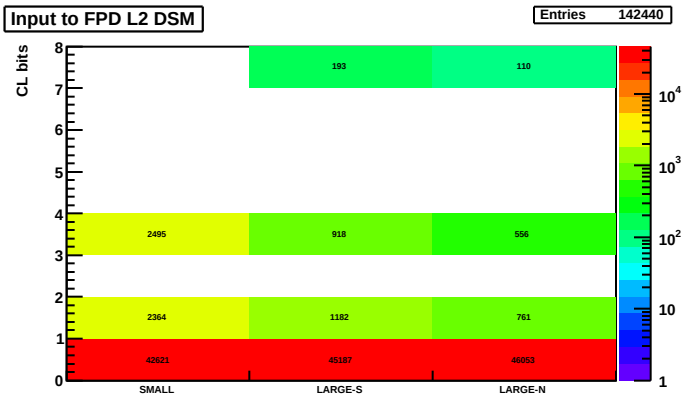

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

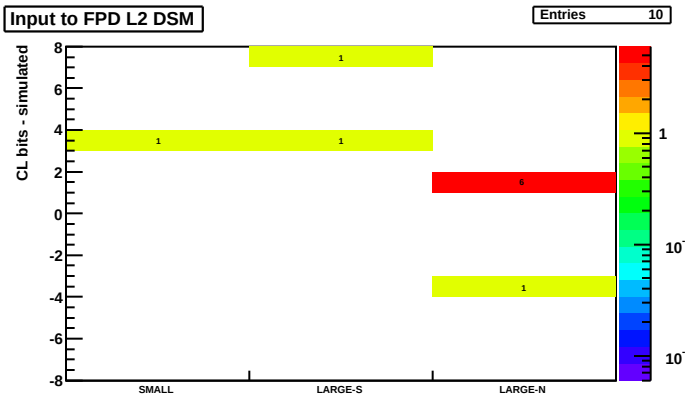

Input to FMS L1 DSM
Entries 569760
FMS L1 HT bits
DSM board

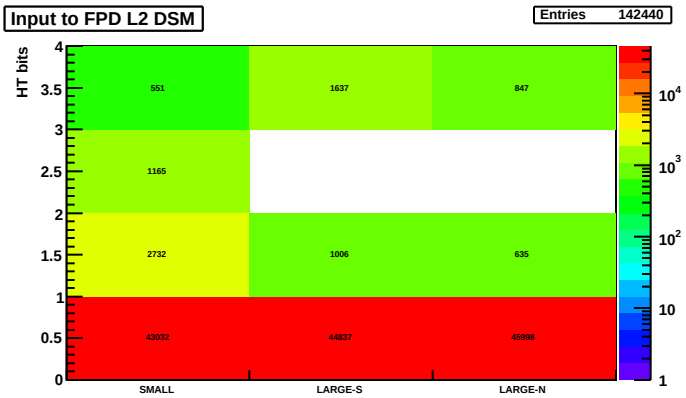

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

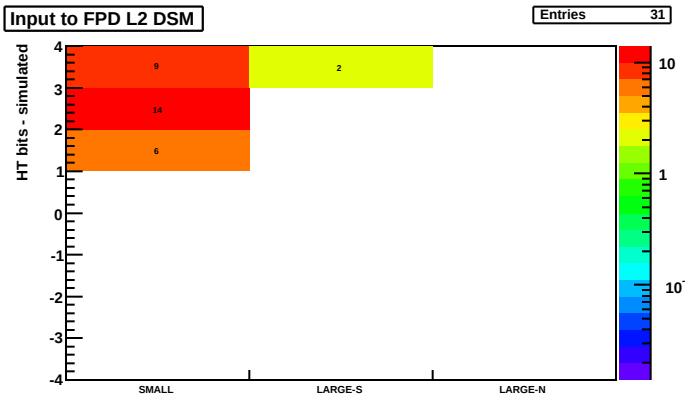

Input to FPD L2 DSM
Entries 569760
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

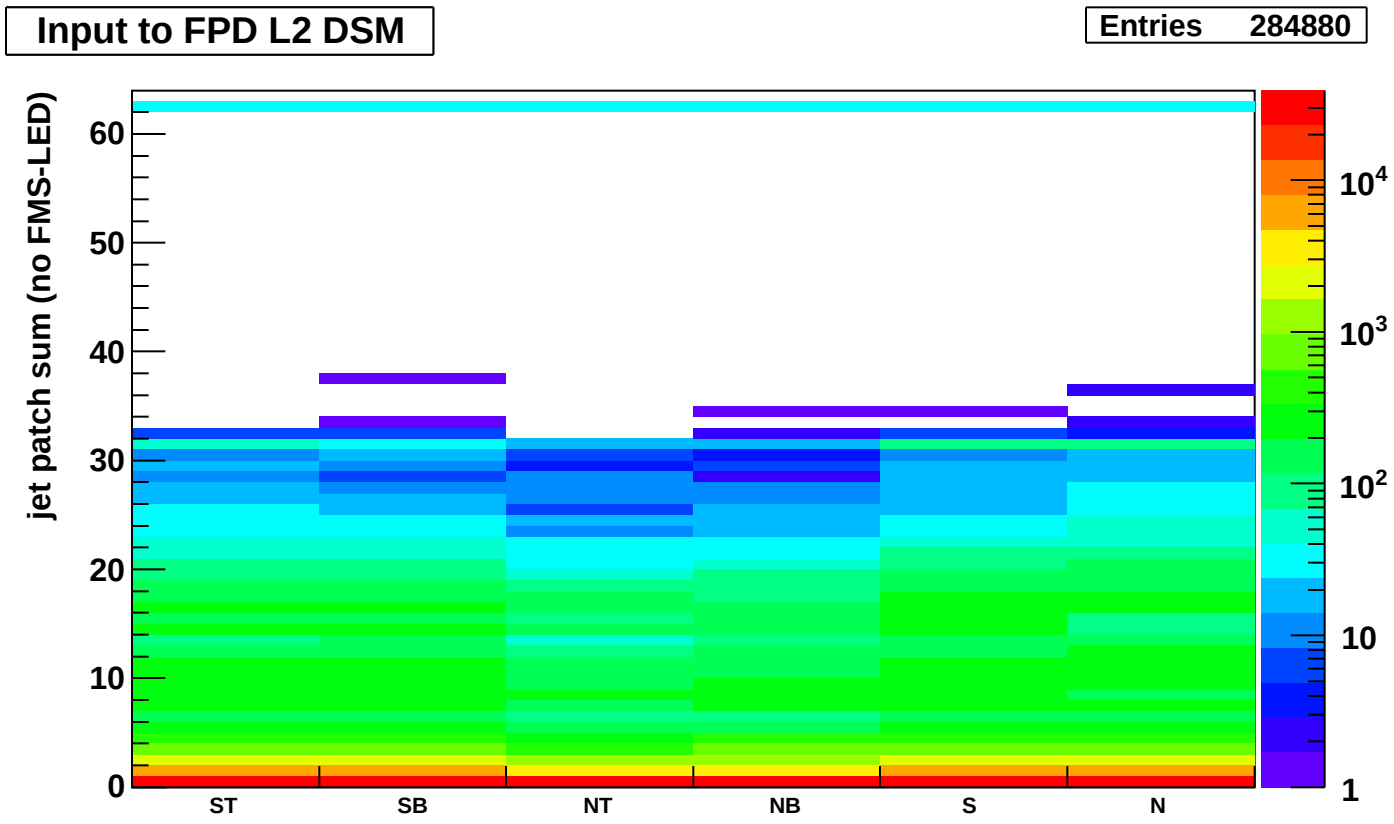

Input to FPD L2 DSM
Entries 104
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

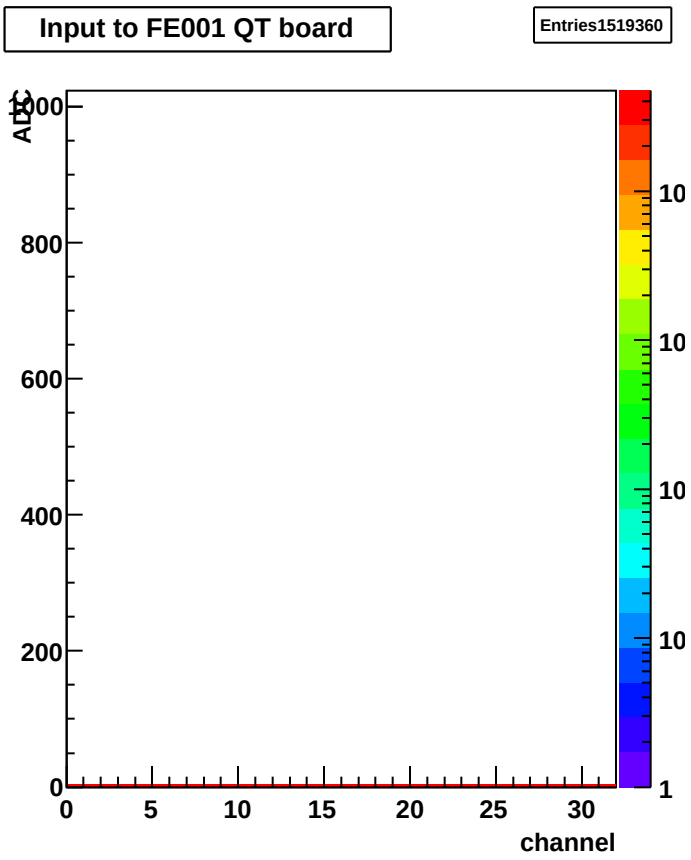

Input to FPD L2 DSM
Entries 142440
CL bits
193
110
2495
918
556
2364
1182
761
42621
45187
46053
SMALL
LARGE-S
LARGE-N

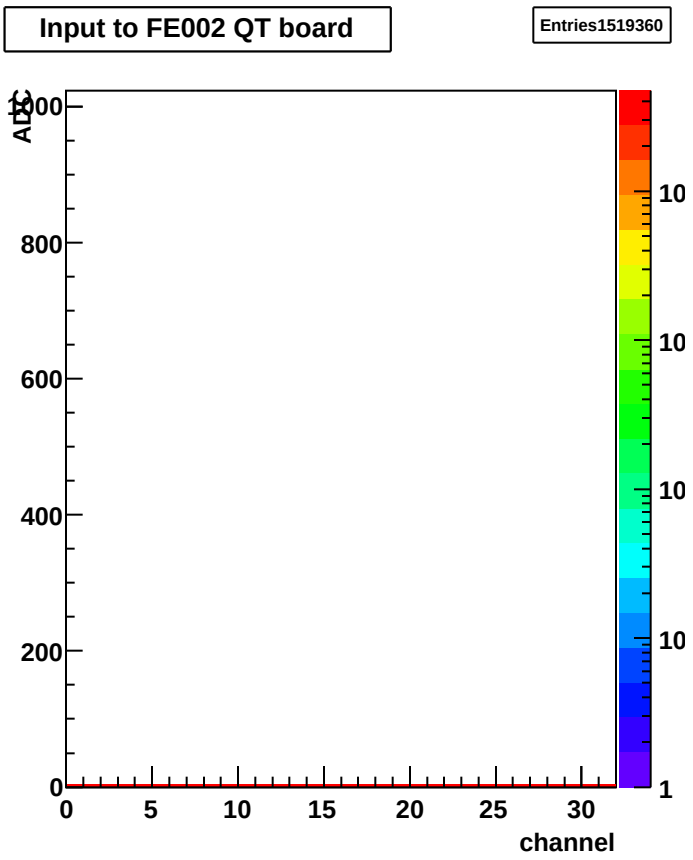

Input to FPD L2 DSM
Entries 10
CL bits - simulated
1
1
1
6
1
SMALL
LARGE-S
LARGE-N

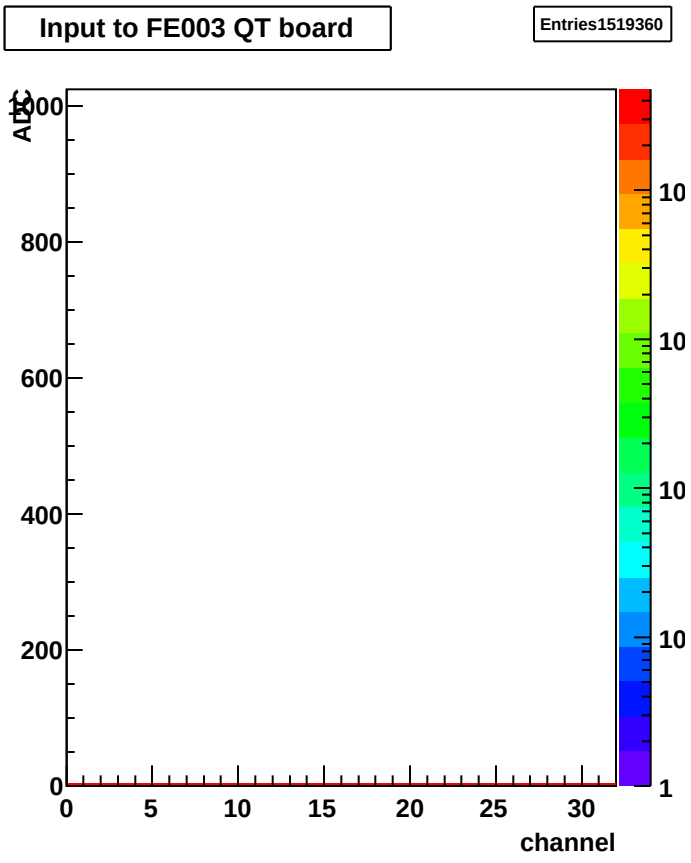

Input to FPD L2 DSM
Entries 142440
HT bits
551
1637
847
1165
2732
1006
635
43032
44837
45998
SMALL
LARGE-S
LARGE-N

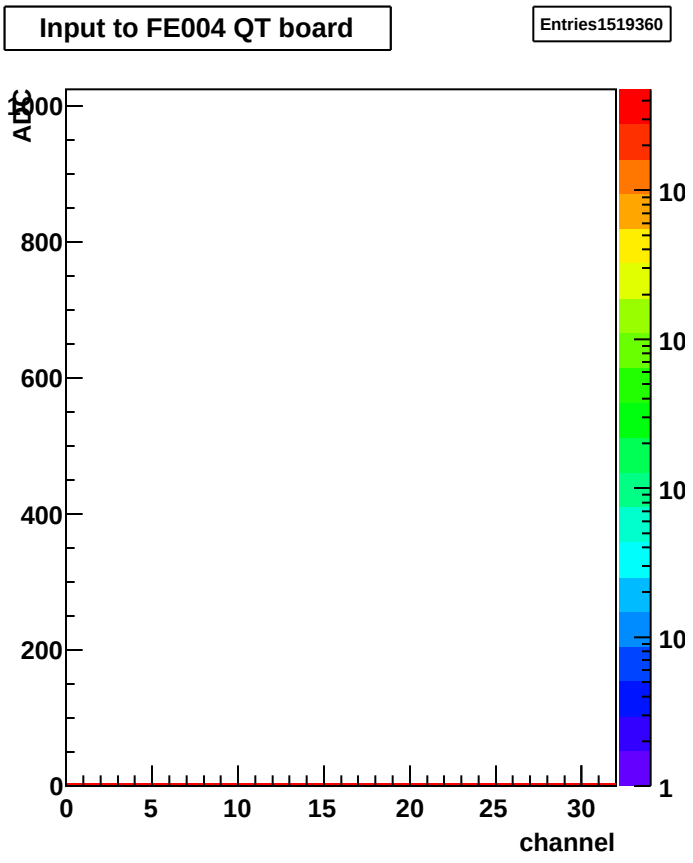

Input to FPD L2 DSM
Entries 31
HT bits - simulated
9
2
14
6
SMALL
LARGE-S
LARGE-N

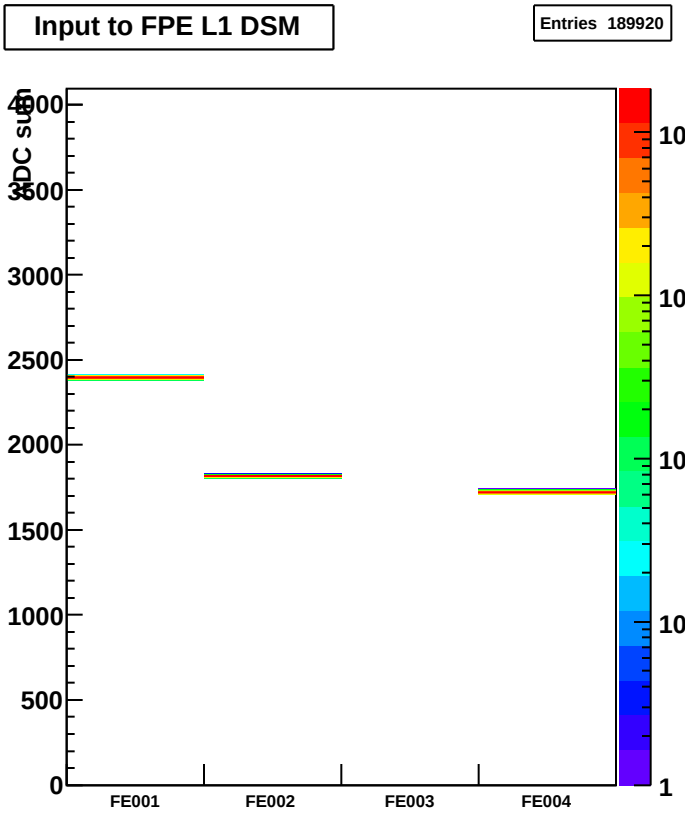

Input to FPD L2 DSM
Entries 284880
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

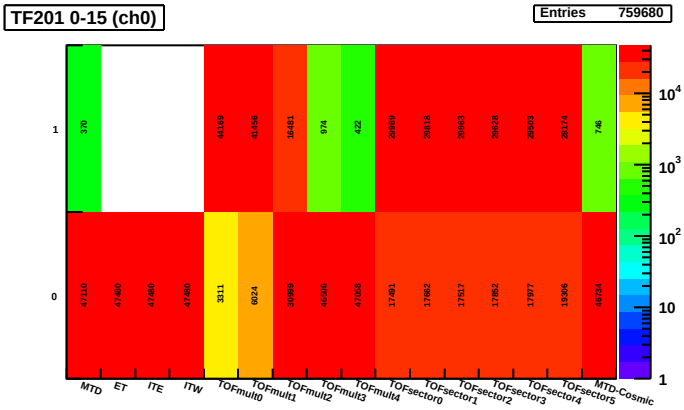

Input to FE001 QT board
Entries1519360
ADC
channel

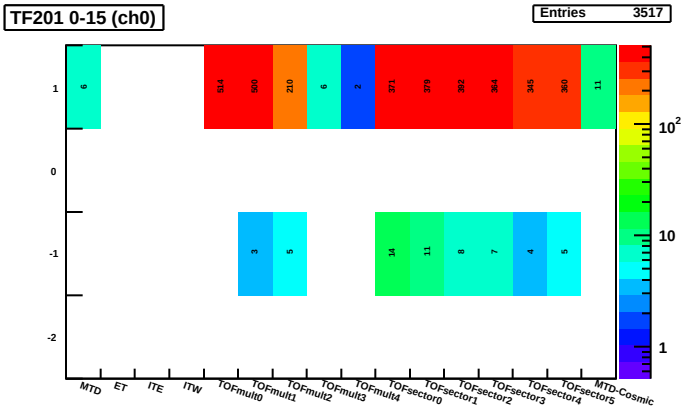

Input to FE002 QT board
Entries1519360
ADC
channel

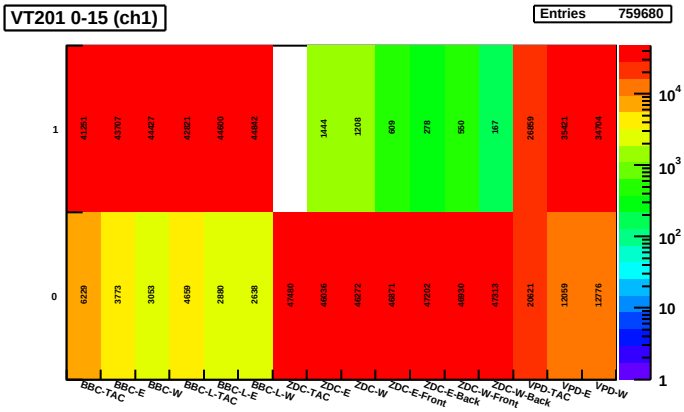

Input to FE003 QT board
Entries1519360
ADC
channel

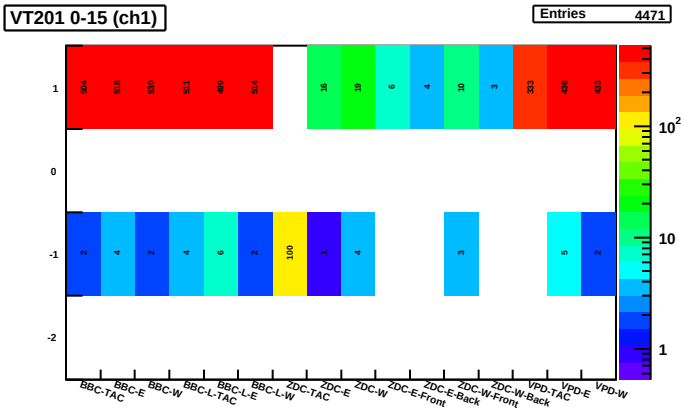

Input to FE004 QT board
Entries1519360
ADC
channel

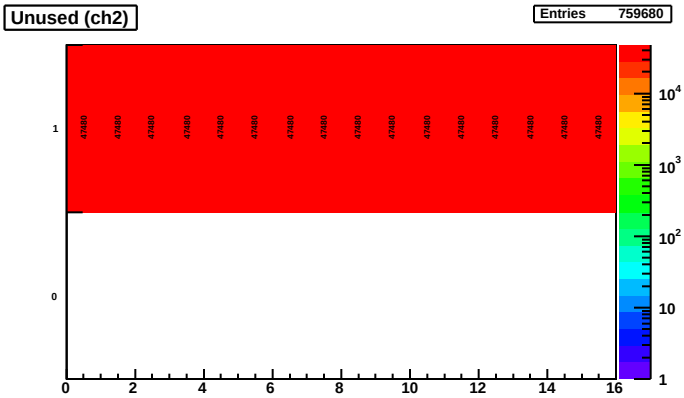

Input to FPE L1 DSM
Entries 189920
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

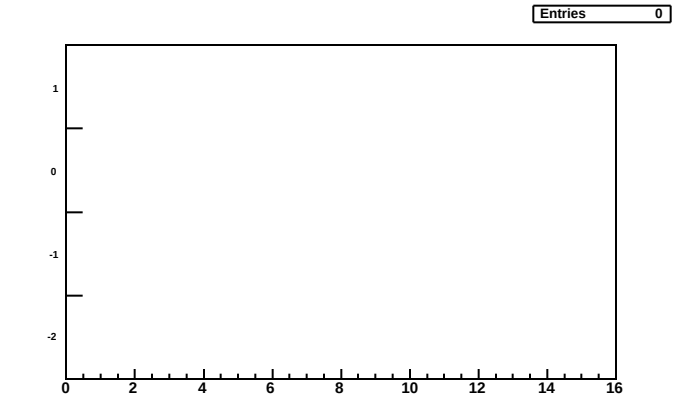

TF201 0-15 (ch0)
Entries 759680
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 3517
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 759680
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 4471
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 759680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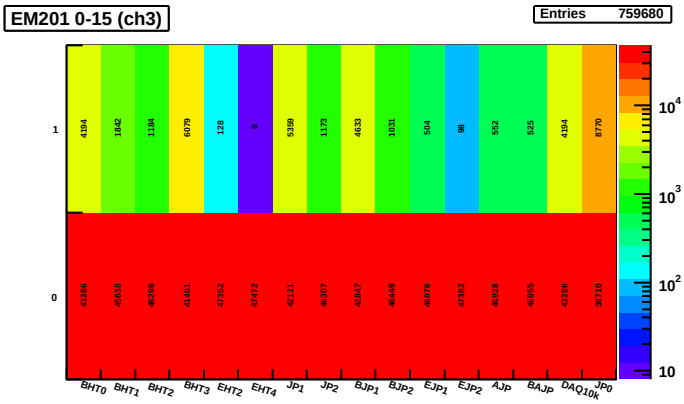
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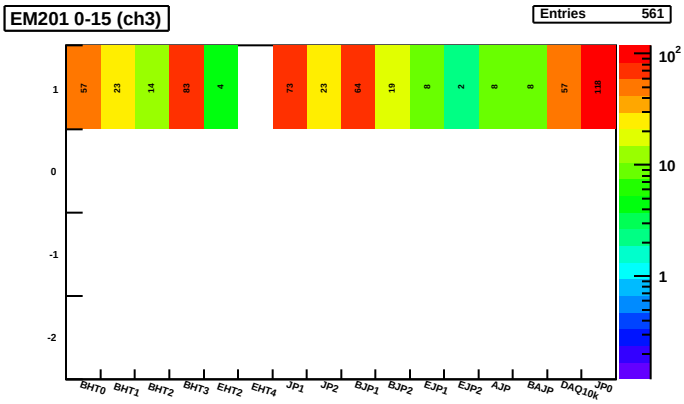
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Entries 561
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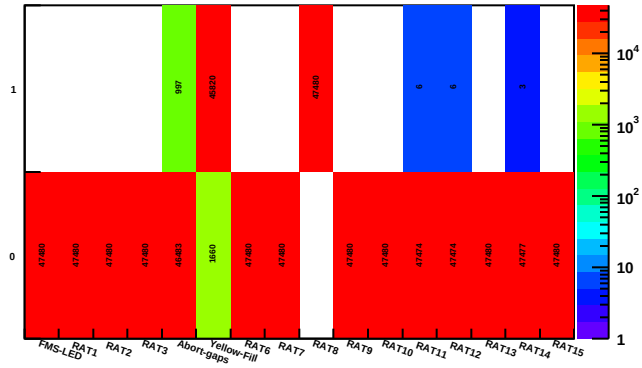
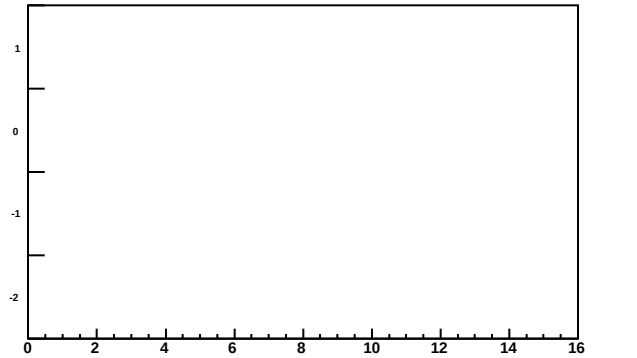
RAT board (ch4)





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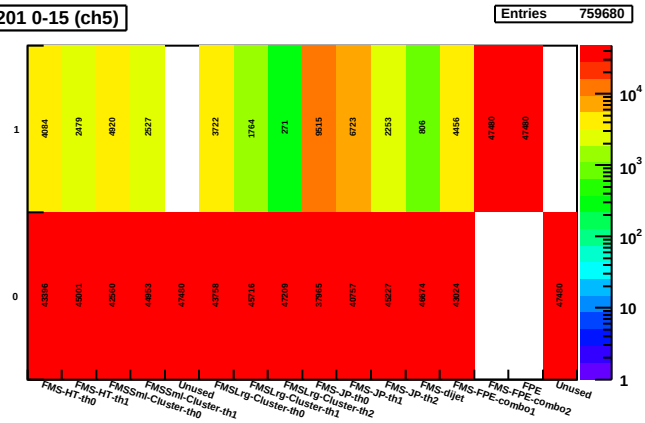

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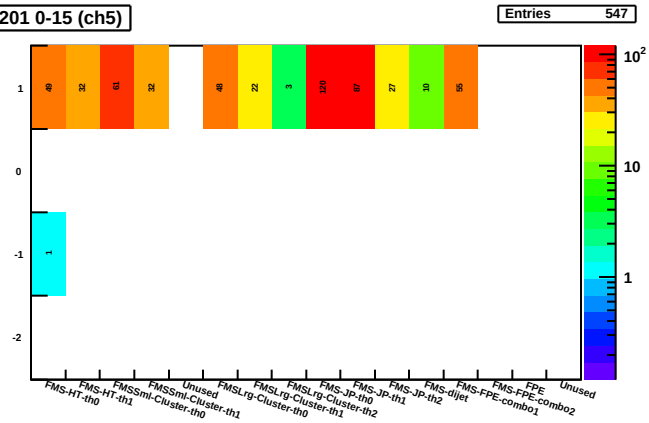

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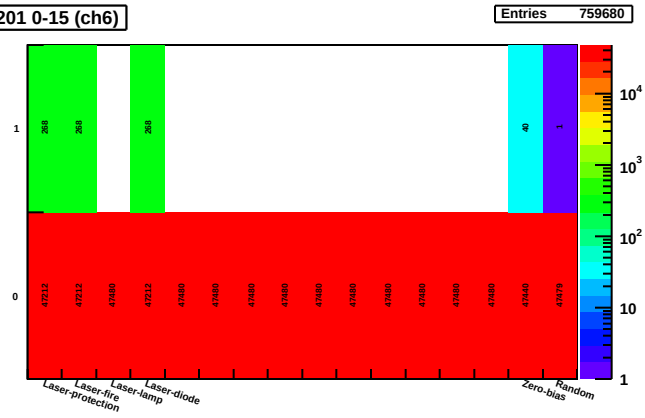

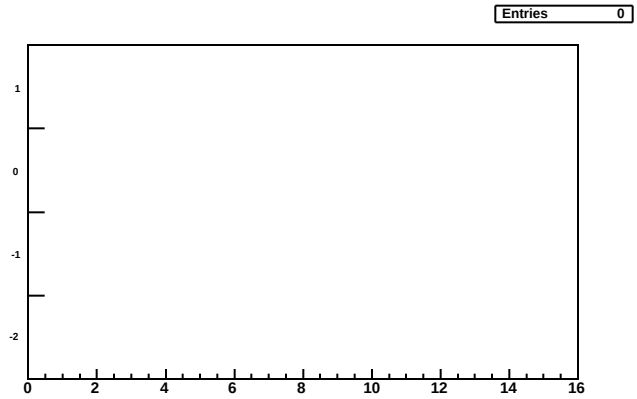
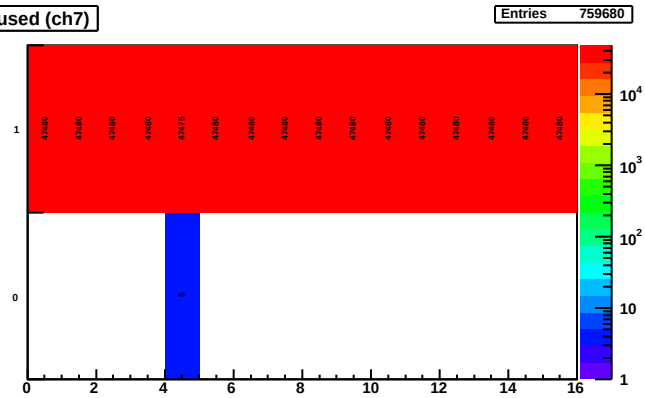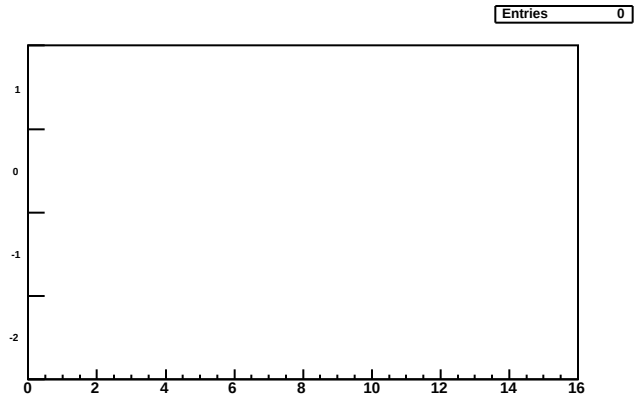
Unused (ch7)