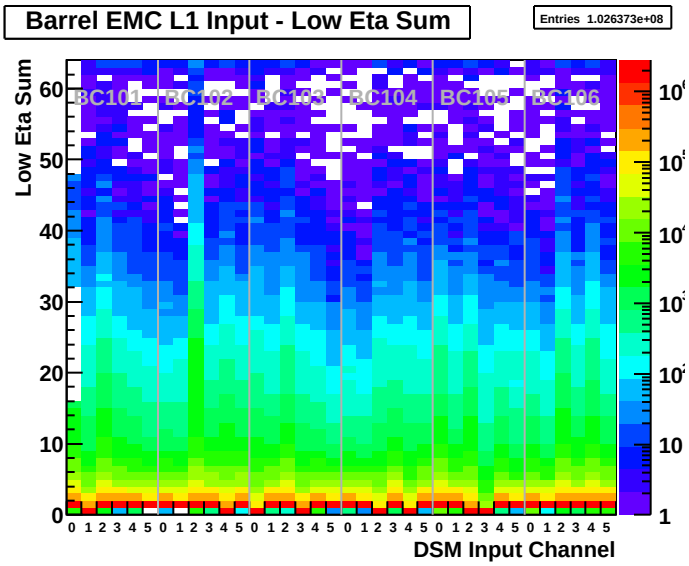
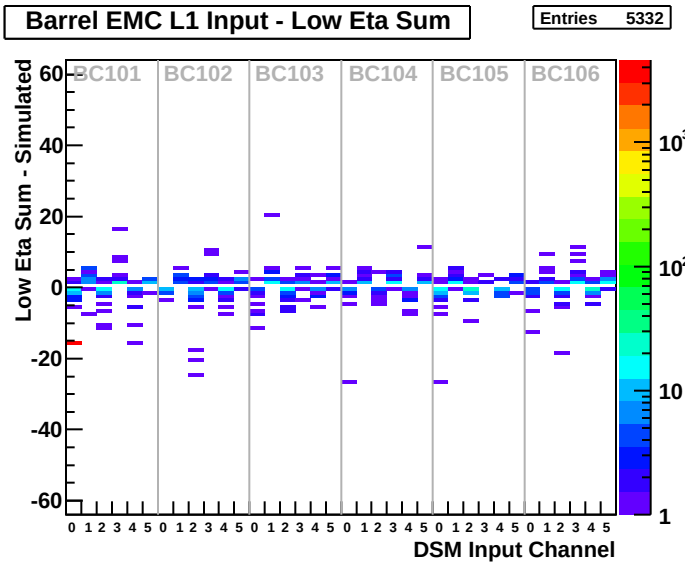
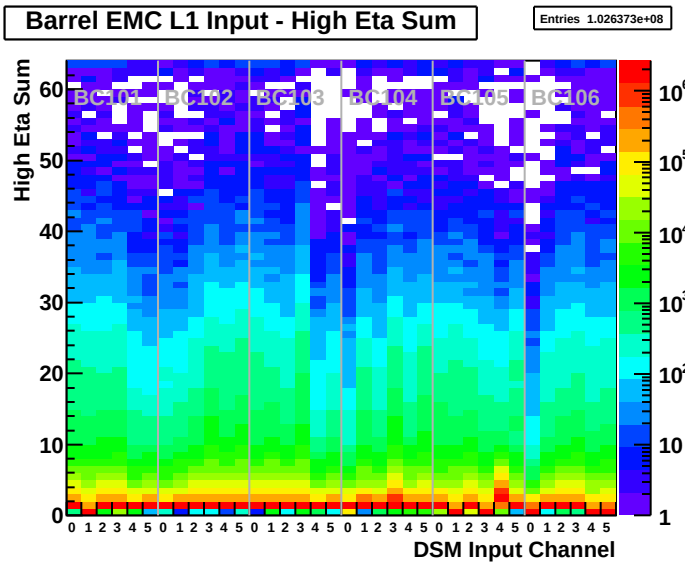
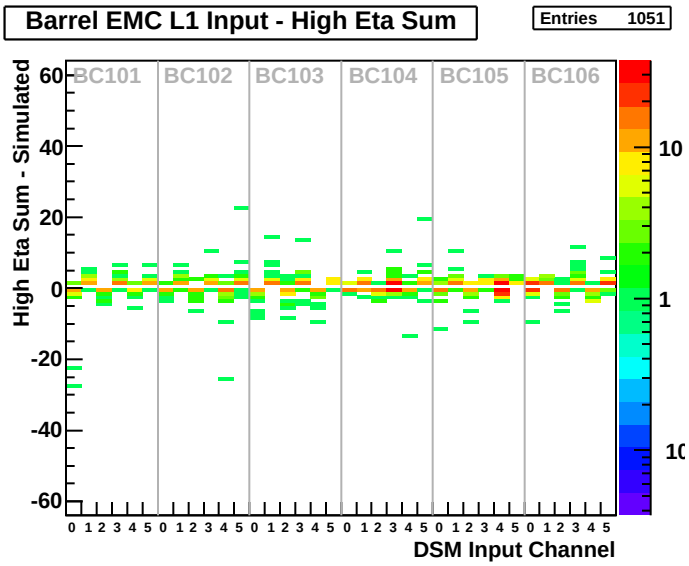
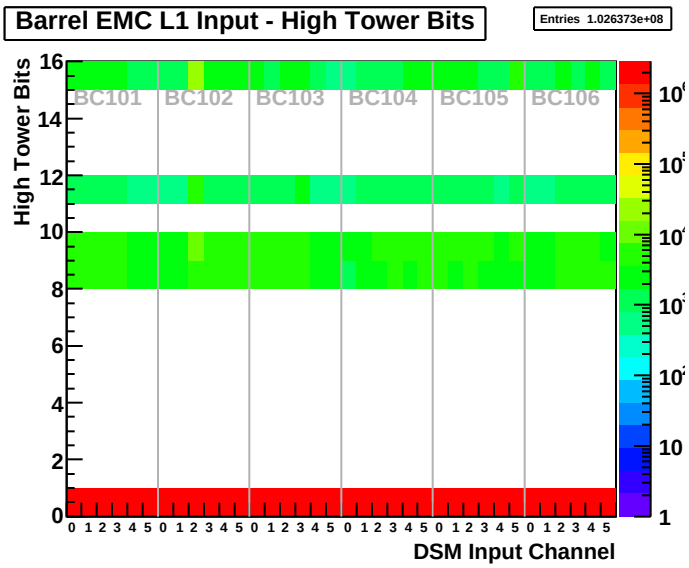
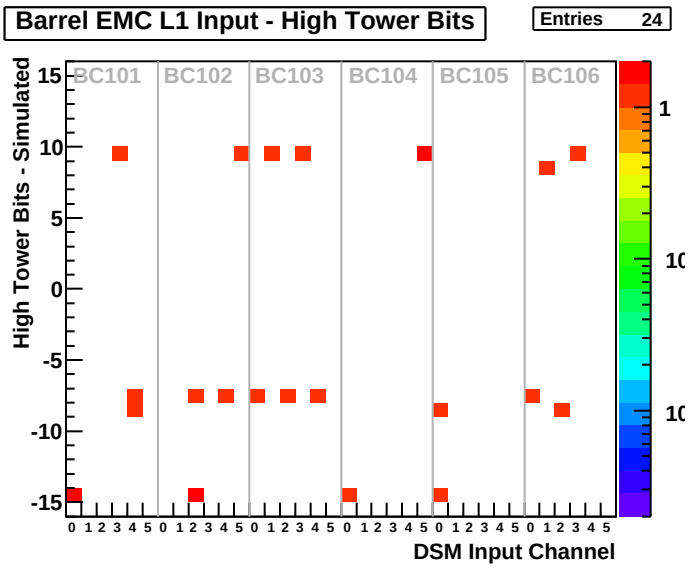

Barrel EMC L1 Input - Low Eta Sum
Entries 1.026373e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

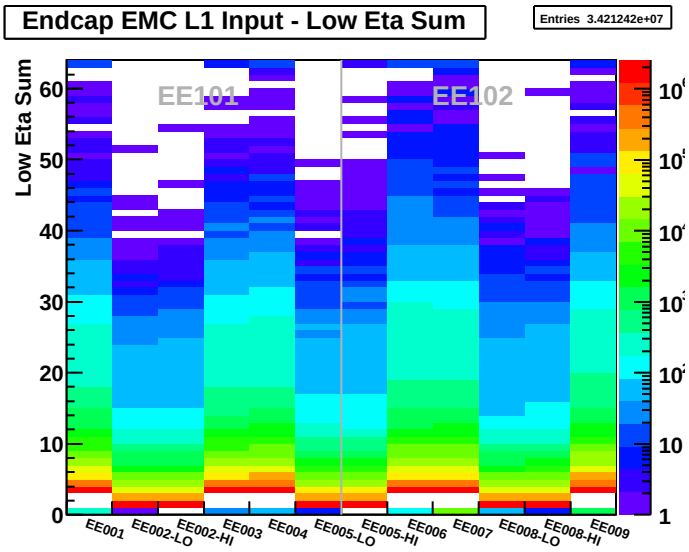

Barrel EMC L1 Input - Low Eta Sum
Entries 5332
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

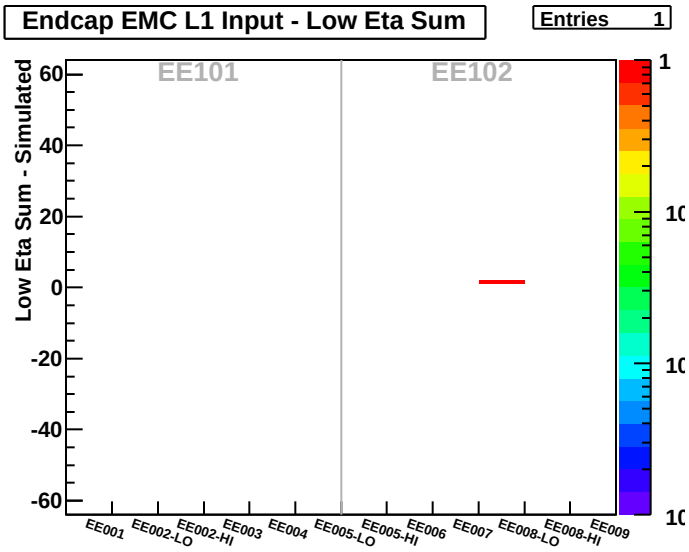

Barrel EMC L1 Input - High Eta Sum
Entries 1.026373e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

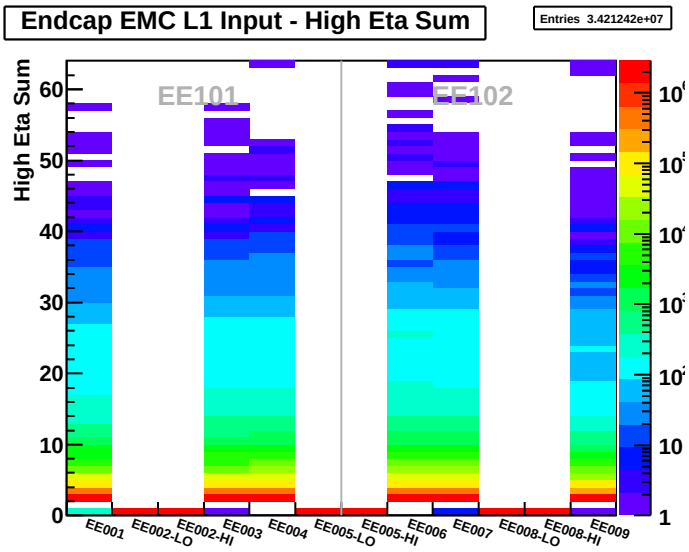

Barrel EMC L1 Input - High Eta Sum
Entries 1051
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

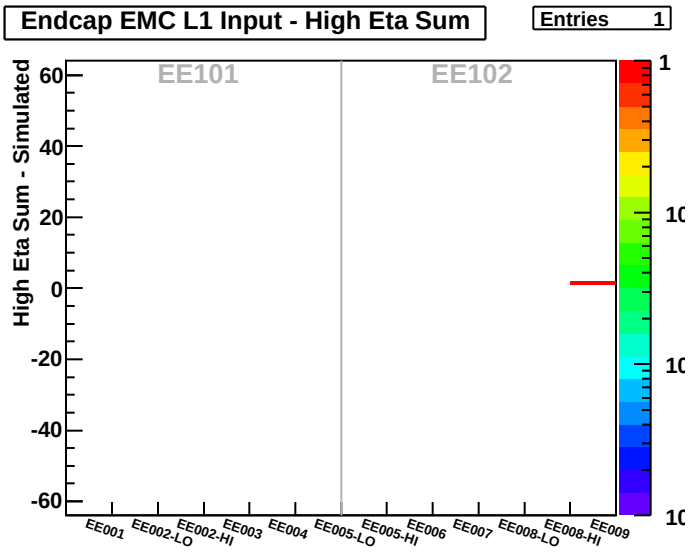

Barrel EMC L1 Input - High Tower Bits
Entries 1.026373e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

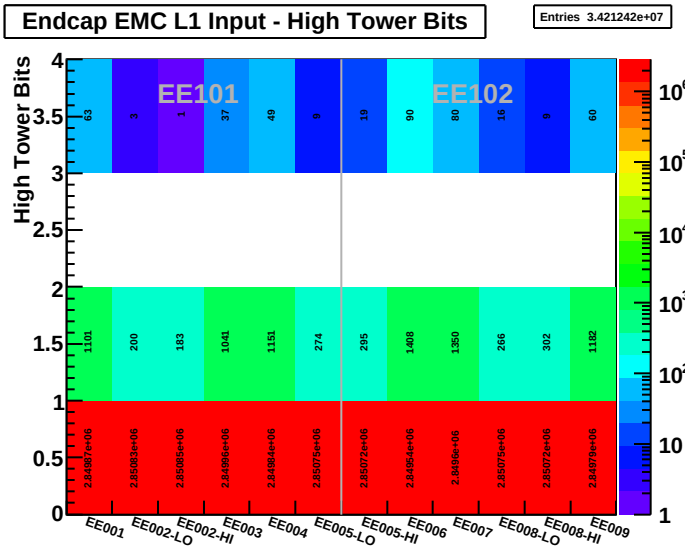

Barrel EMC L1 Input - High Tower Bits
Entries 24
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

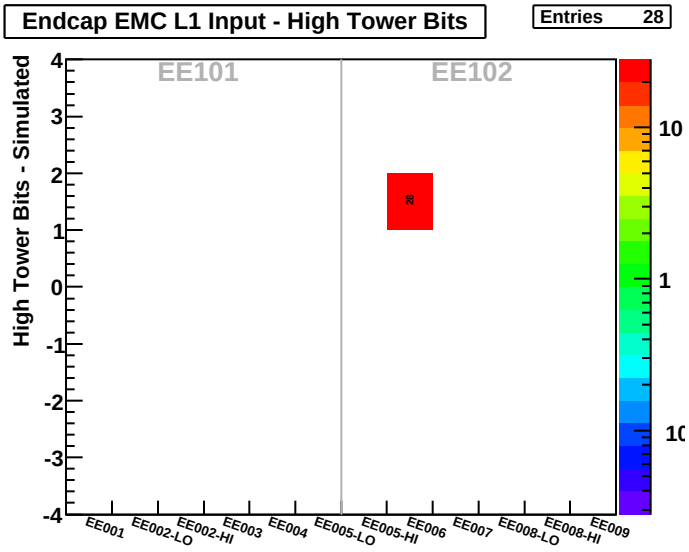

Endcap EMC L1 Input - Low Eta Sum
Entries 3.421242e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

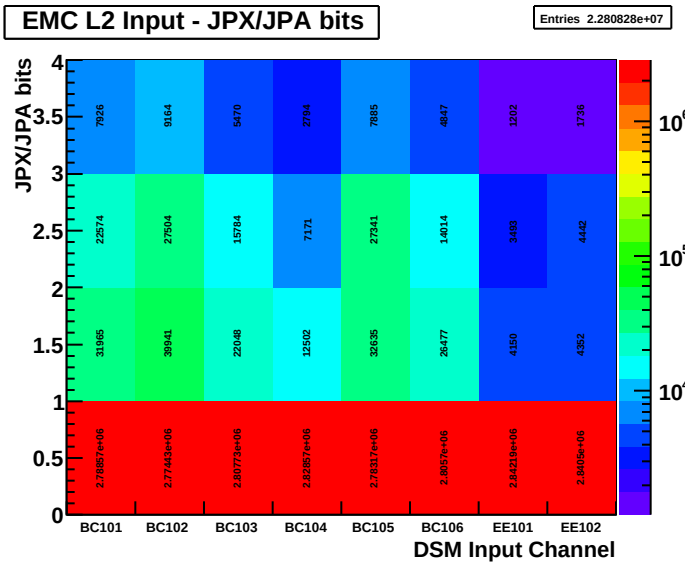

Endcap EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

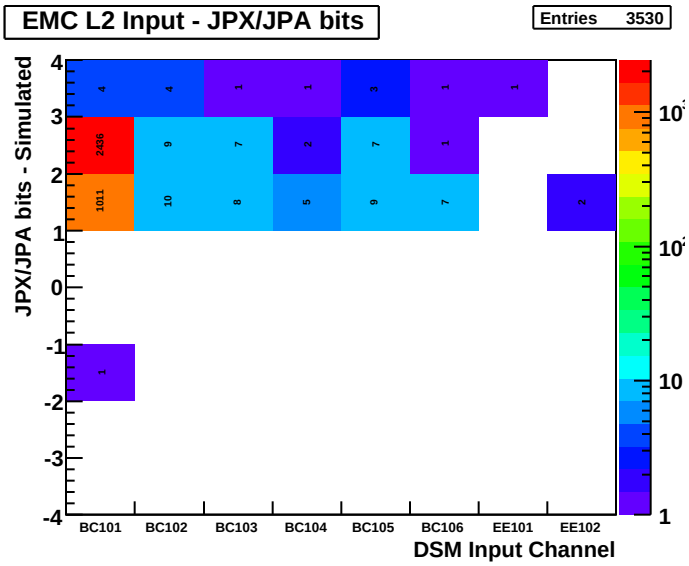

Endcap EMC L1 Input - High Eta Sum
Entries 3.421242e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

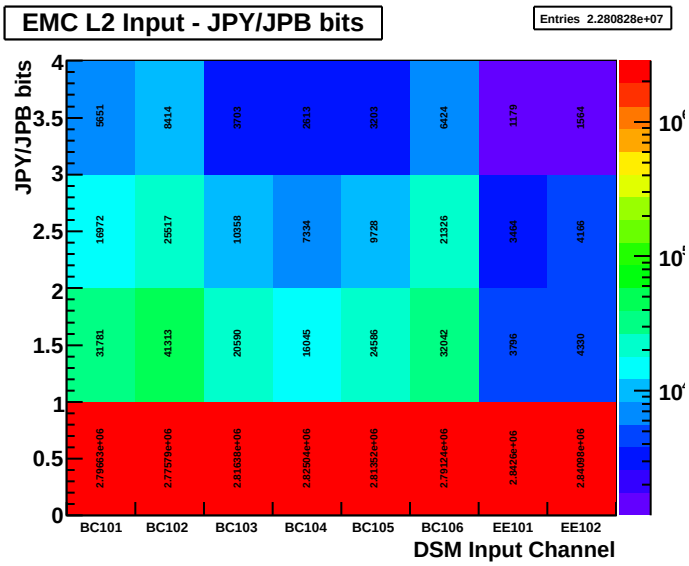

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

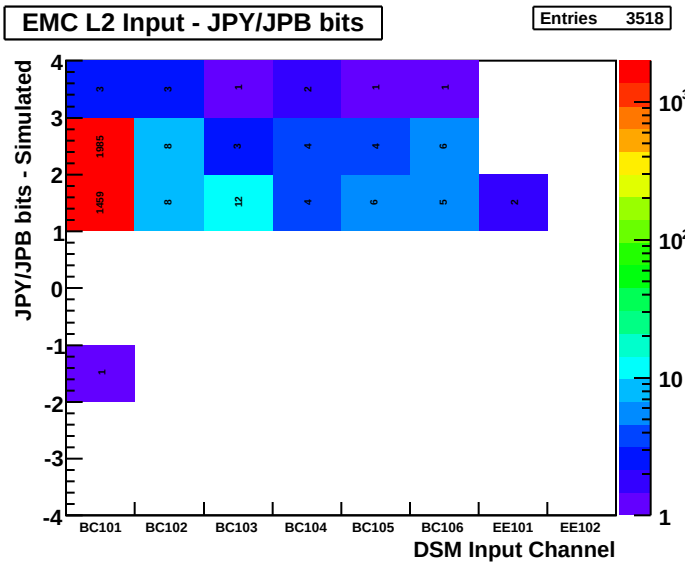

Endcap EMC L1 Input - High Tower Bits
Entries 3.421242e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

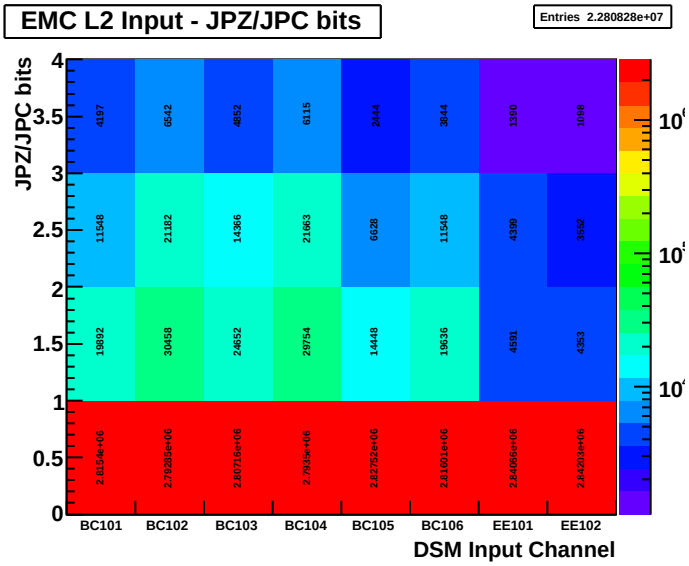

Endcap EMC L1 Input - High Tower Bits
Entries 28
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

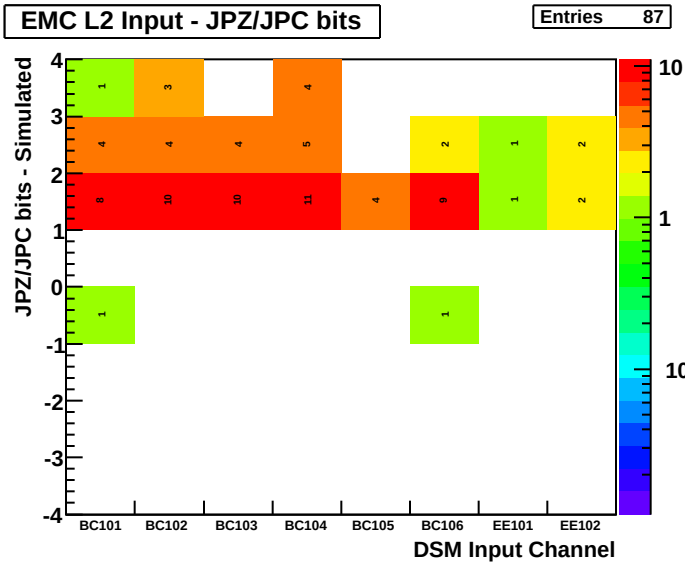

EMC L2 Input - JPX/JPA bits
Entries 2.280828e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

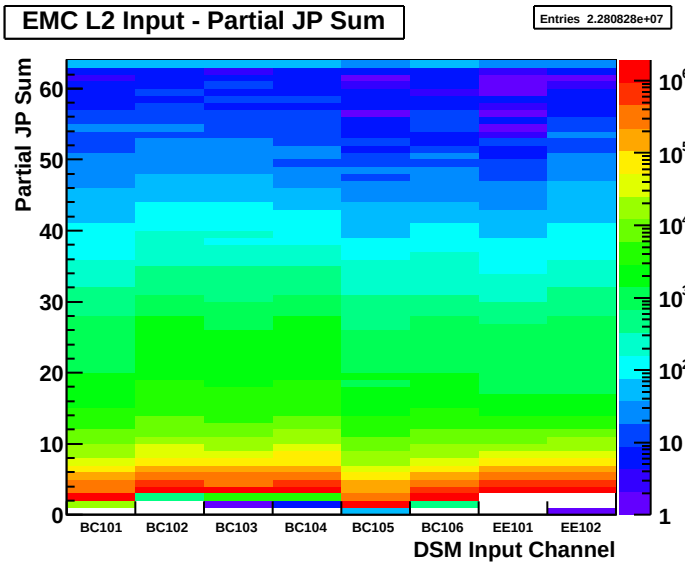

EMC L2 Input - JPX/JPA bits
Entries 3530
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

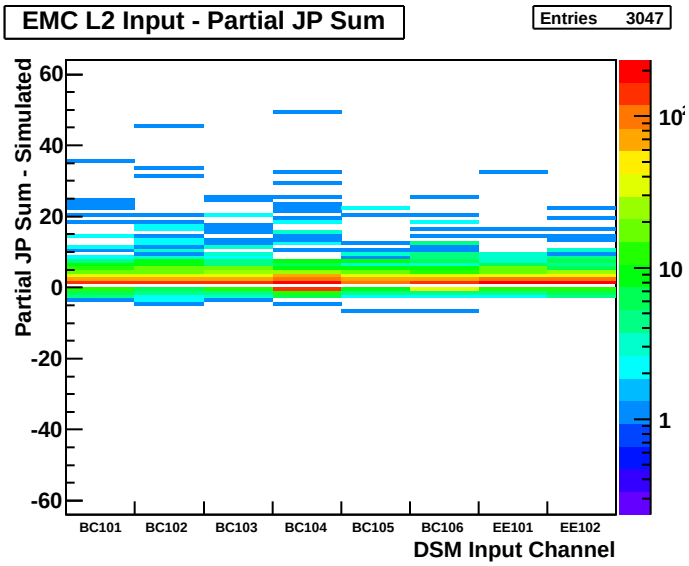

EMC L2 Input - JPY/JPB bits
Entries 2.280828e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

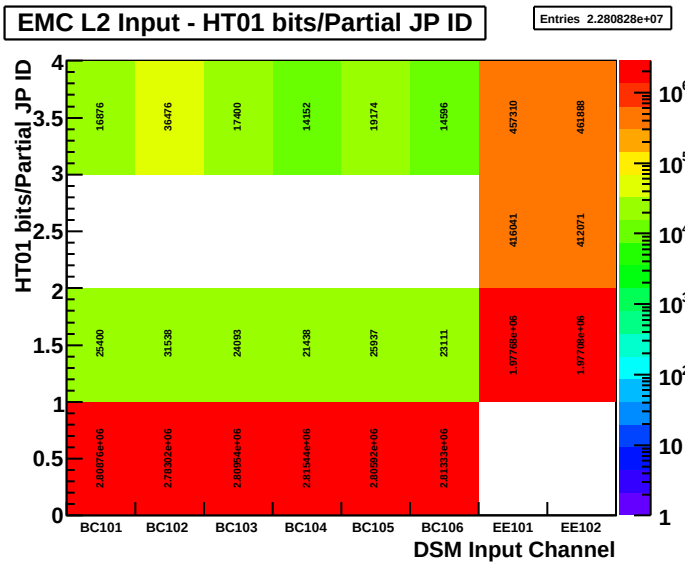

EMC L2 Input - JPY/JPB bits
Entries 3518
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

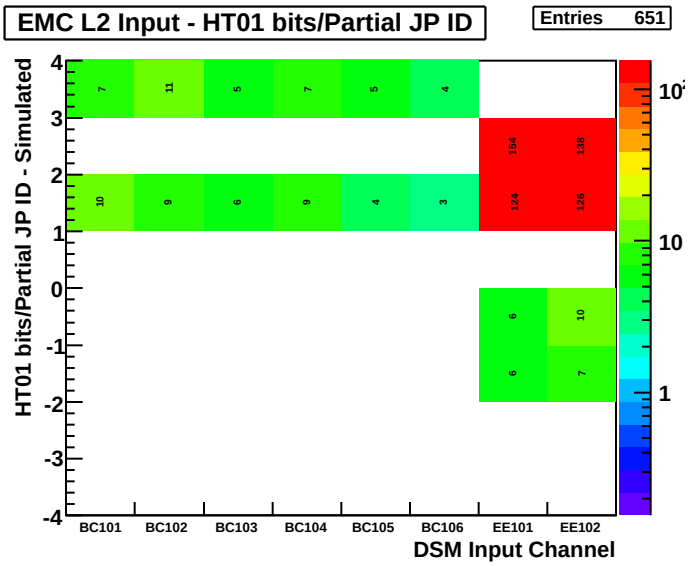

EMC L2 Input - JPZ/JPC bits
Entries 2.280828e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

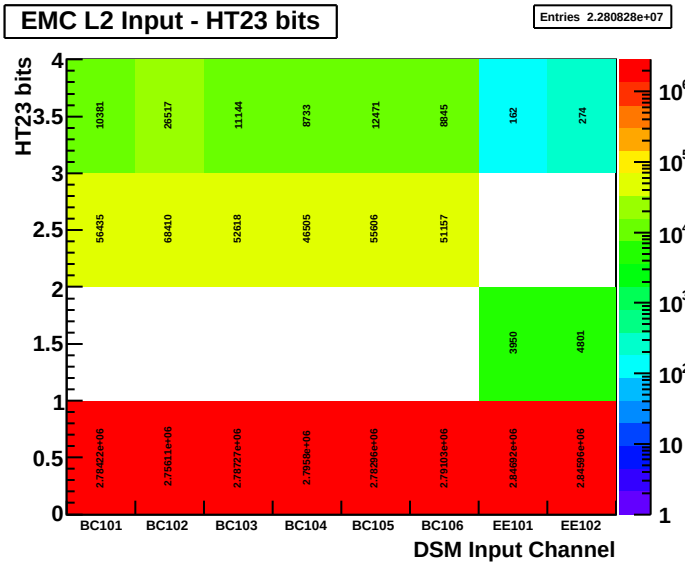

EMC L2 Input - JPZ/JPC bits
Entries 87
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

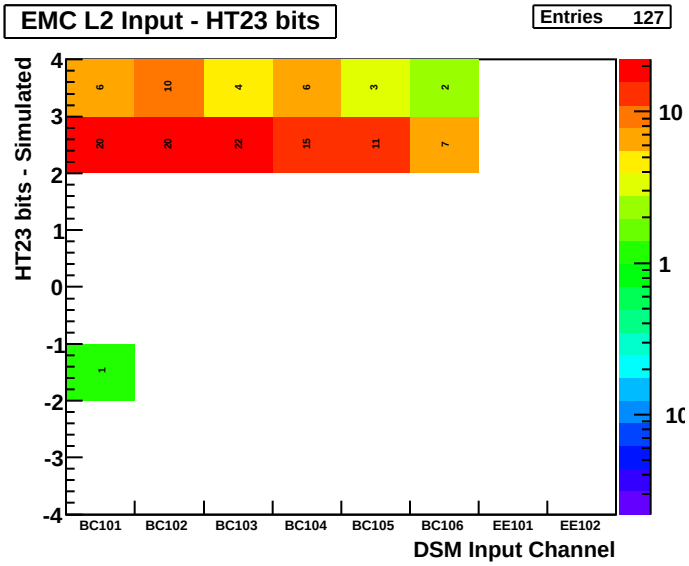

EMC L2 Input - Partial JP Sum
Entries 2.280828e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

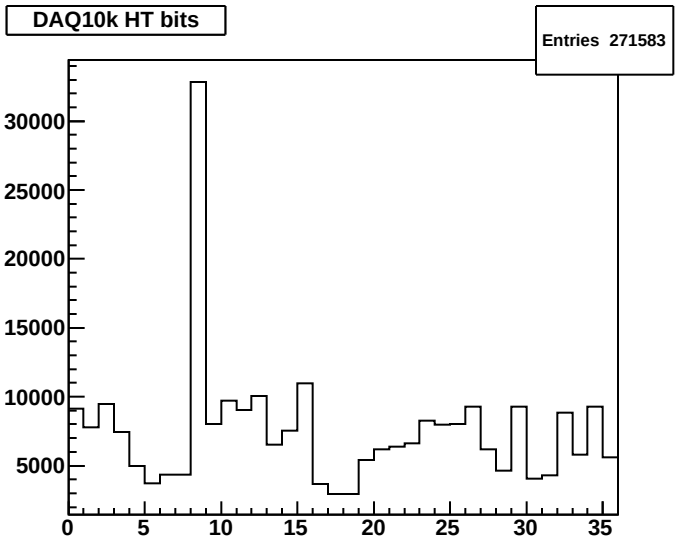

EMC L2 Input - Partial JP Sum
Entries 3047
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

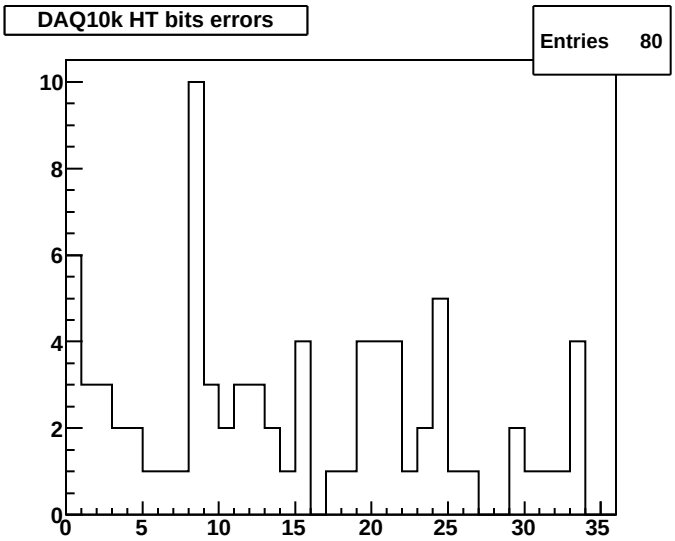

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.280828e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

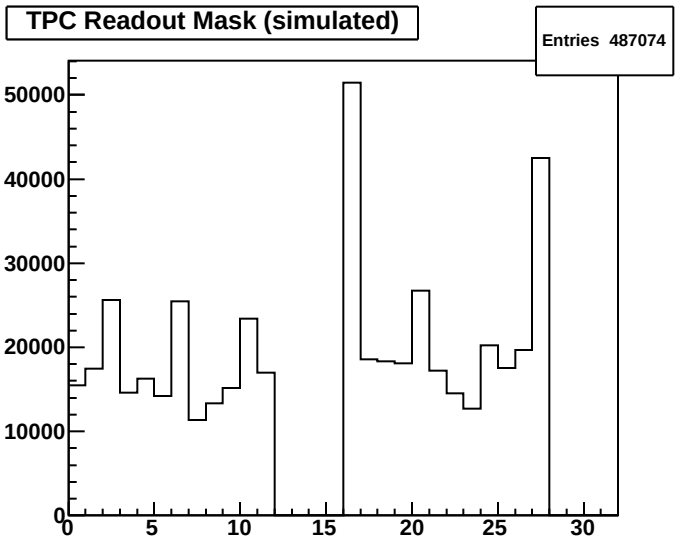

EMC L2 Input - HT01 bits/Partial JP ID
Entries 651
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

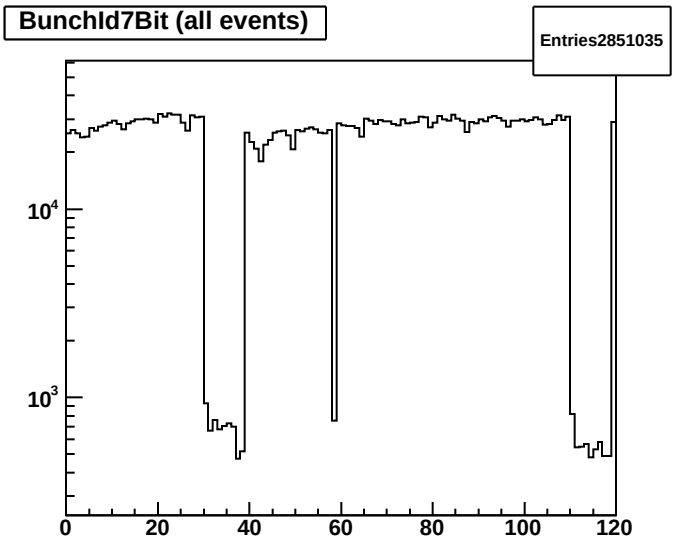

EMC L2 Input - HT23 bits
Entries 2.280828e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

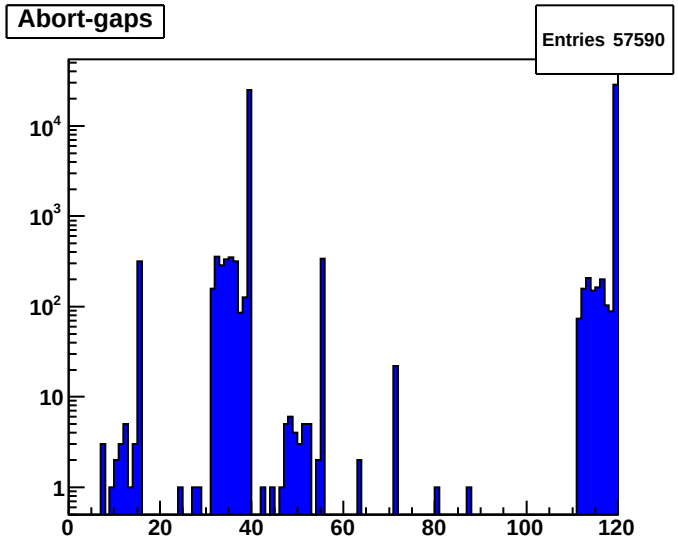

EMC L2 Input - HT23 bits
Entries 127
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

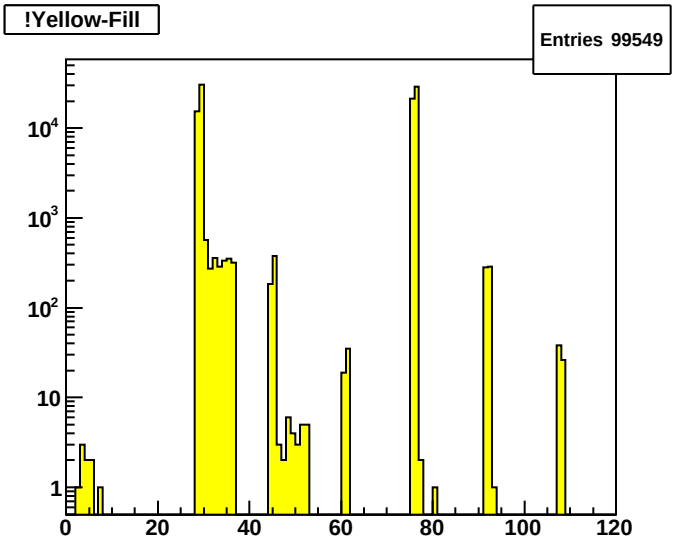

DAQ10k HT bits
Entries 271583
30000
25000
20000
15000
10000
5000
0
5
10
15
20
25
30
35

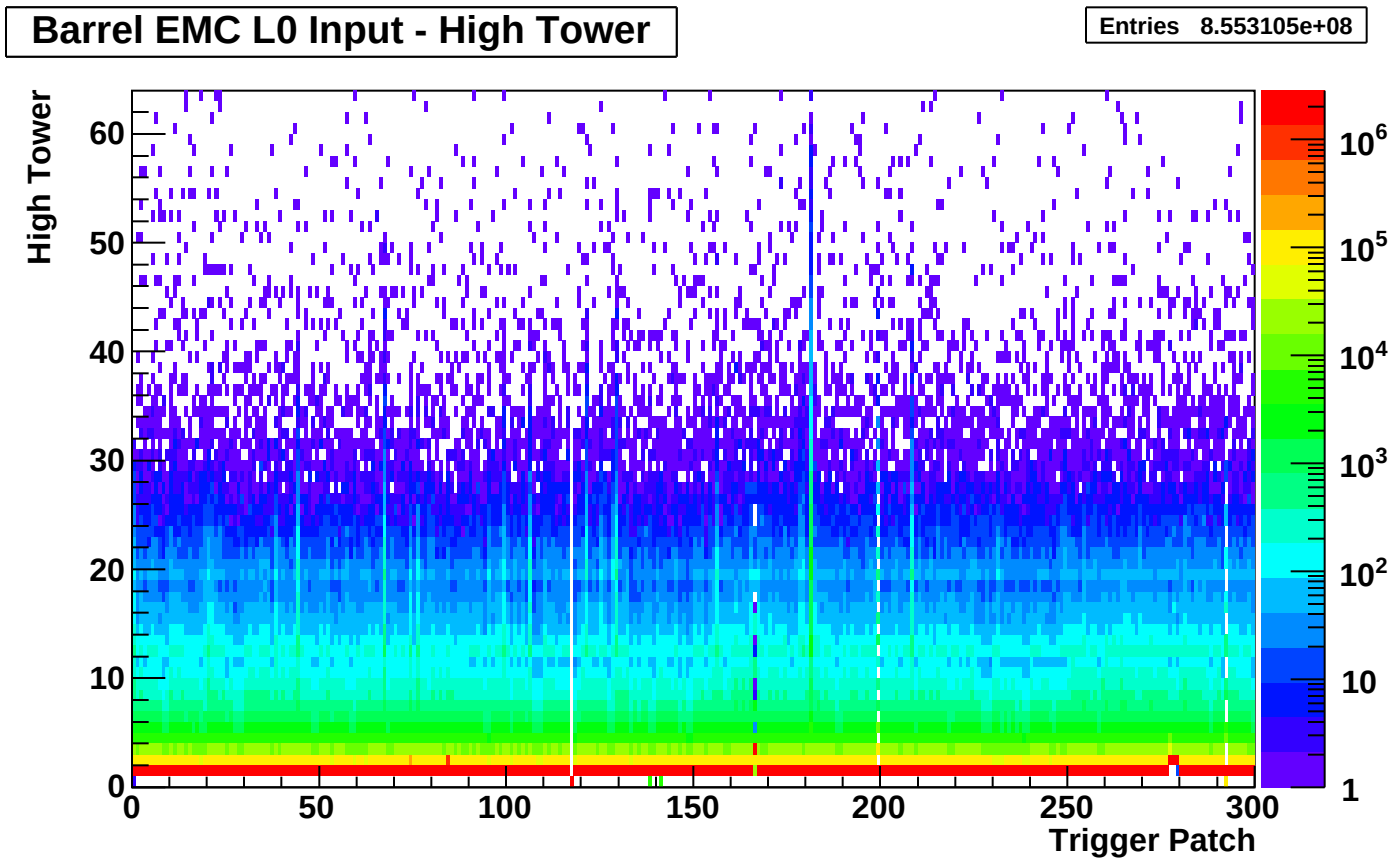

DAQ10k HT bits errors
Entries 80
10
8
6
4
2
0
5
10
15
20
25
30
35

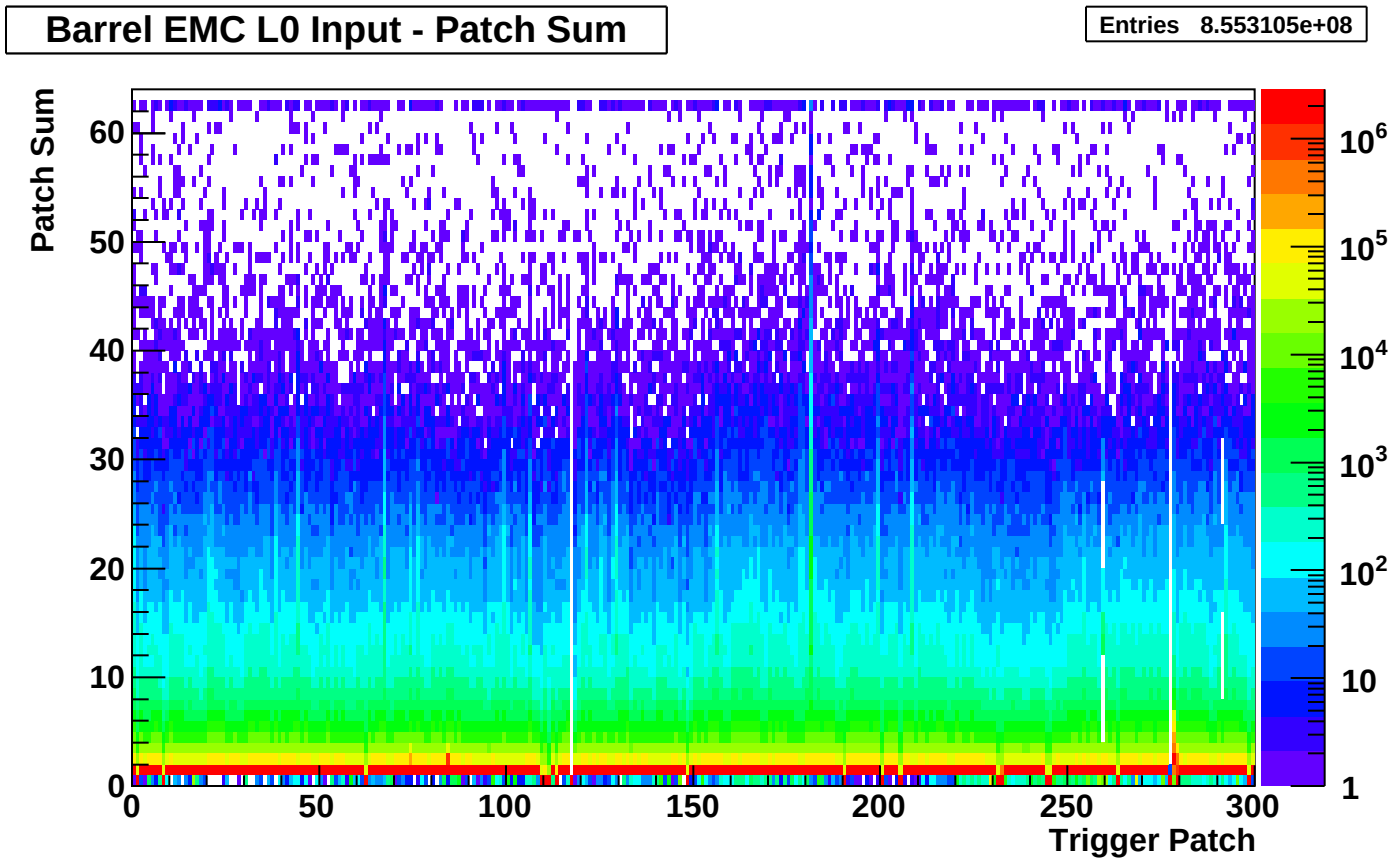

TPC Readout Mask (simulated)
Entries 487074
50000
40000
30000
20000
10000
0
5
10
15
20
25
30

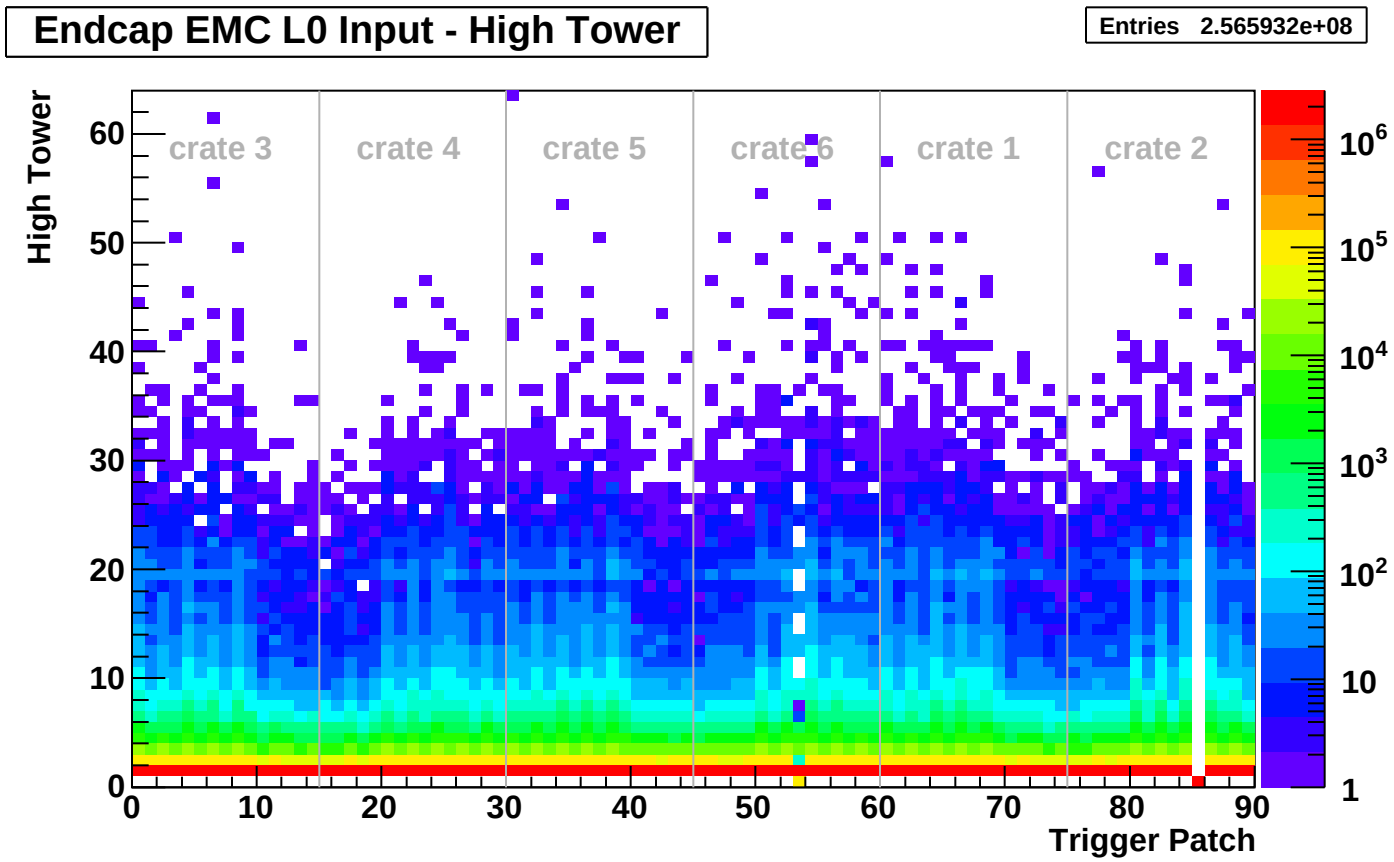

BunchId7Bit (all events)
Entries2851035
10^4
10^3
0
20
40
60
80
100
120

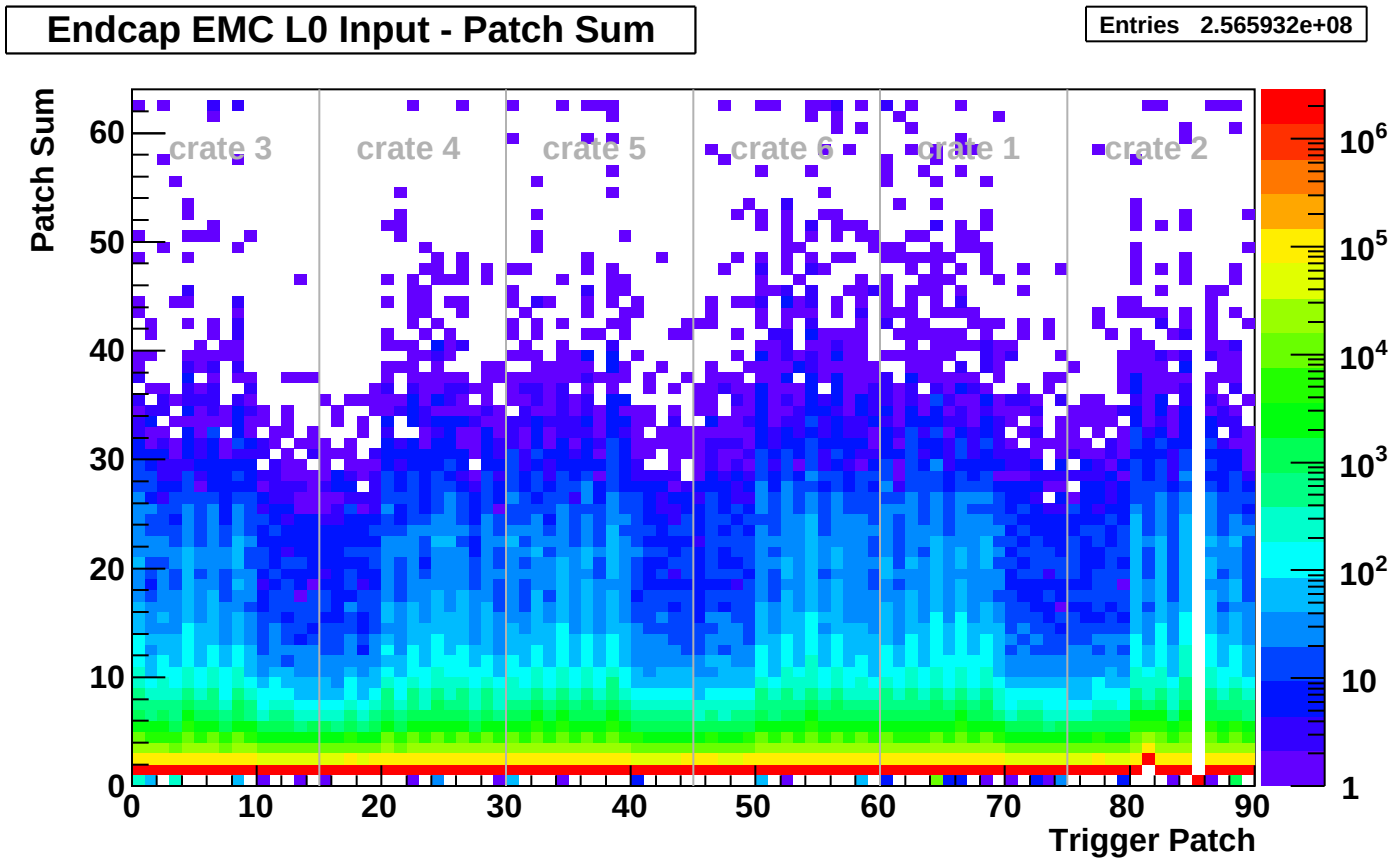

Abort-gaps
Entries 57590
10^4
10^3
10^2
10
1
0
20
40
60
80
100
120

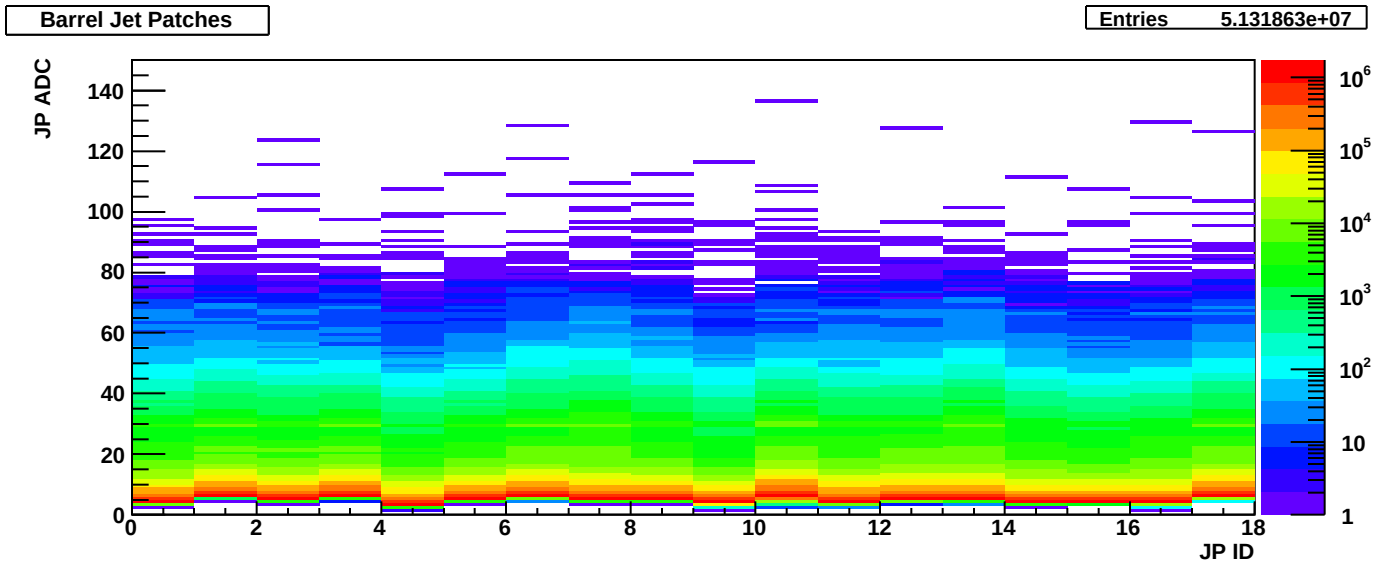

!Yellow-Fill
Entries 99549
10^4
10^3
10^2
10
1
0
20
40
60
80
100
120

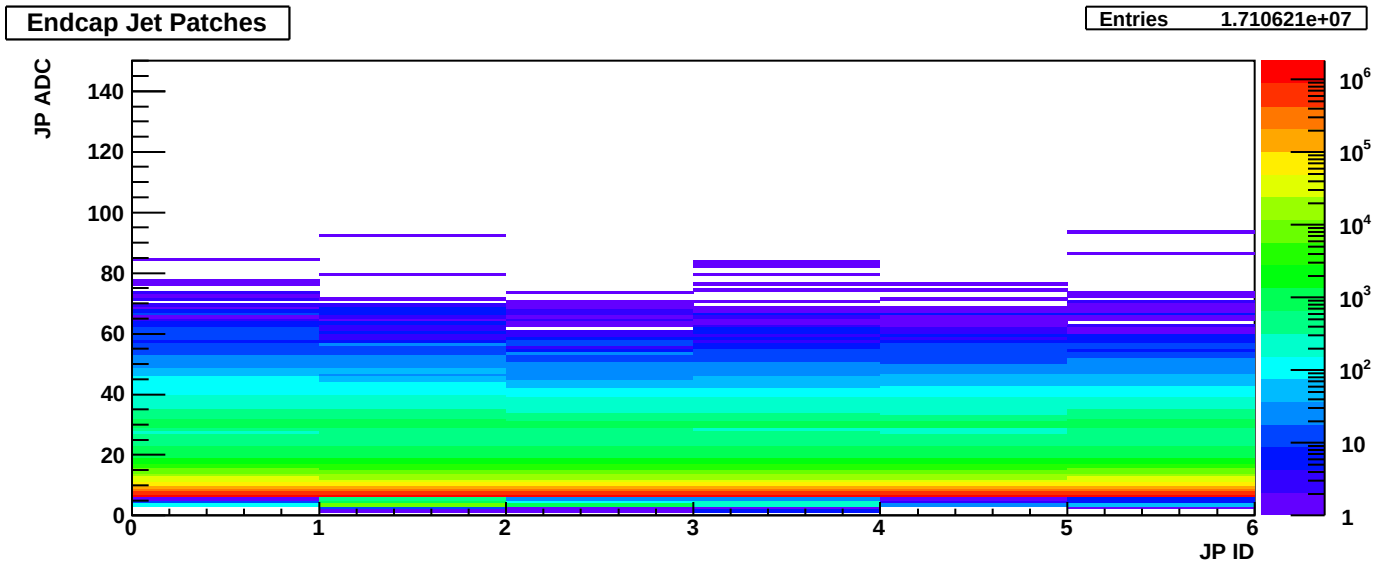

Barrel EMC L0 Input - High Tower
Entries 8.553105e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

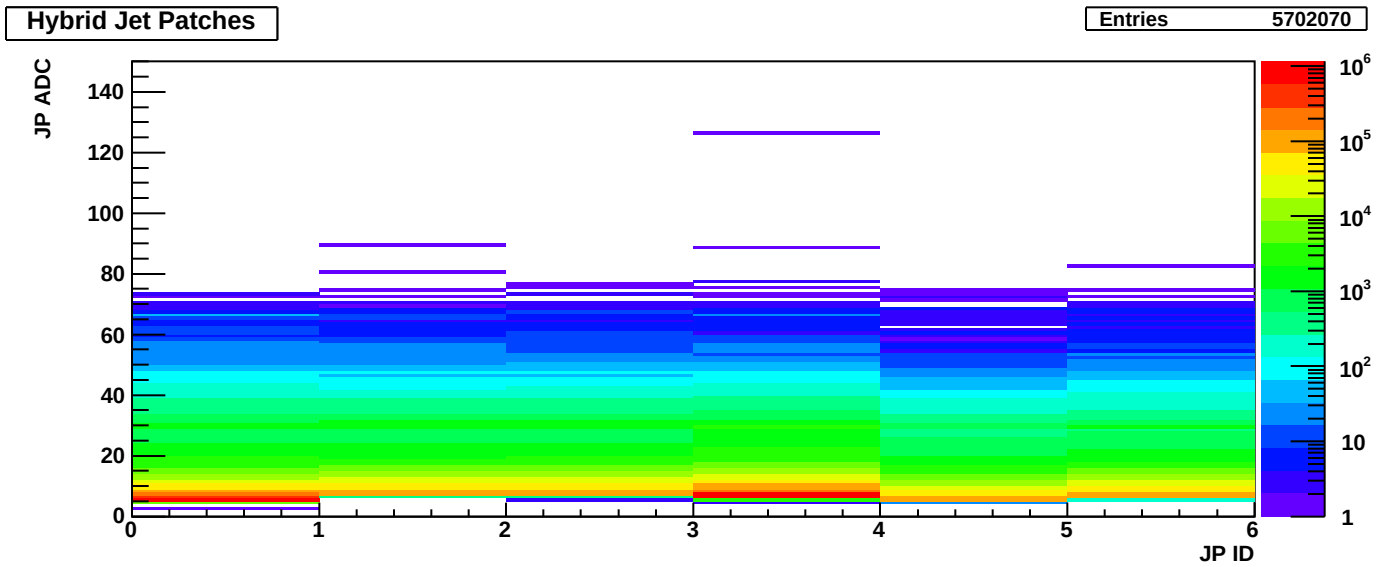

Barrel EMC L0 Input - Patch Sum
Entries 8.553105e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

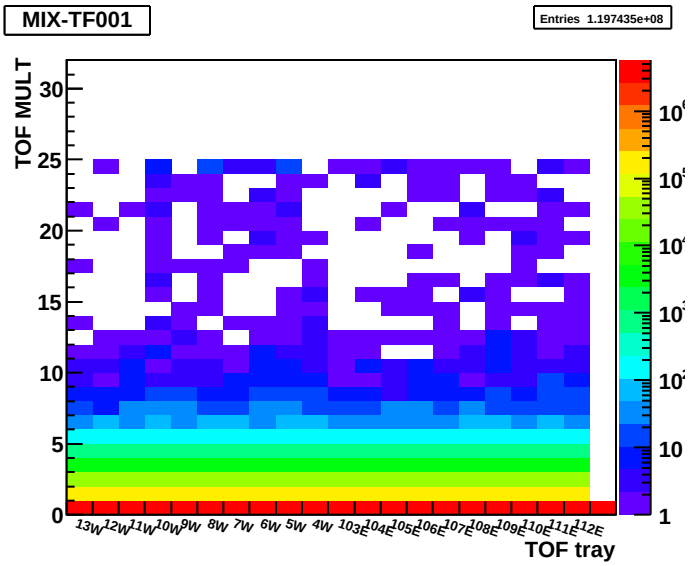

Endcap EMC L0 Input - High Tower
Entries 2.565932e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

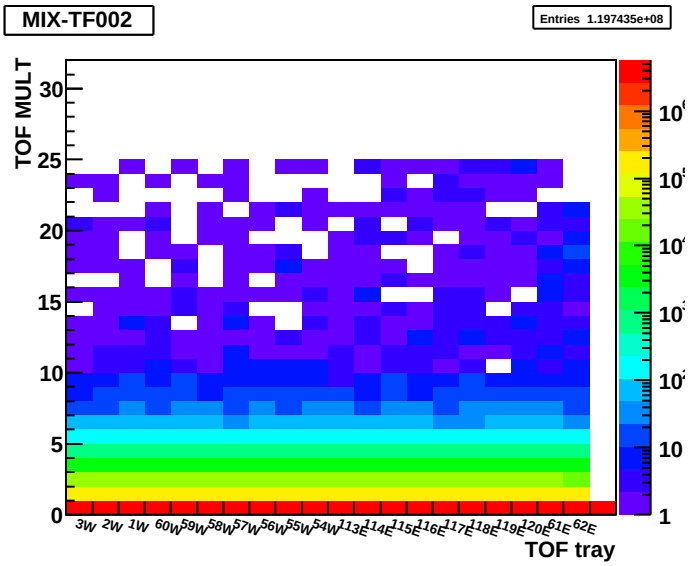

Endcap EMC L0 Input - Patch Sum
Entries 2.565932e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

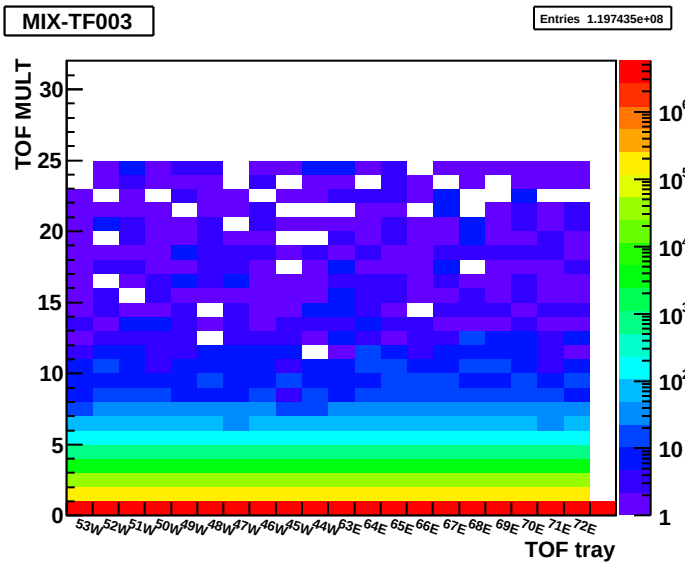

Barrel Jet Patches
Entries 5.131863e+07
JP ADC
JP ID

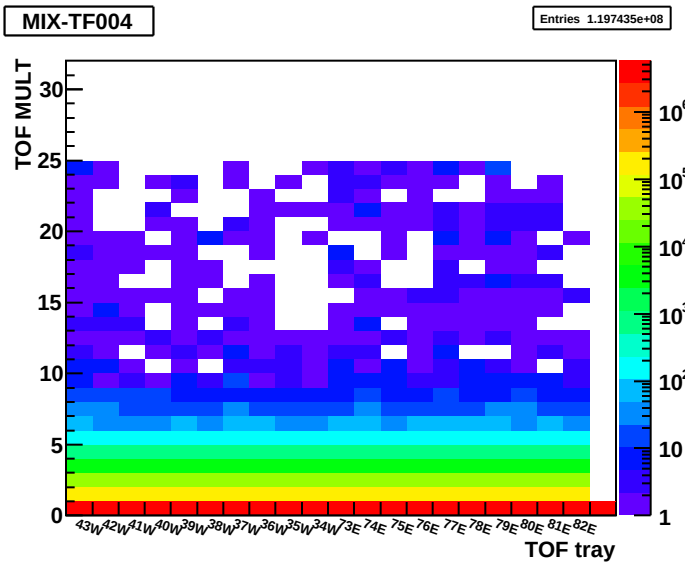

Endcap Jet Patches
Entries 1.710621e+07
JP ADC
JP ID

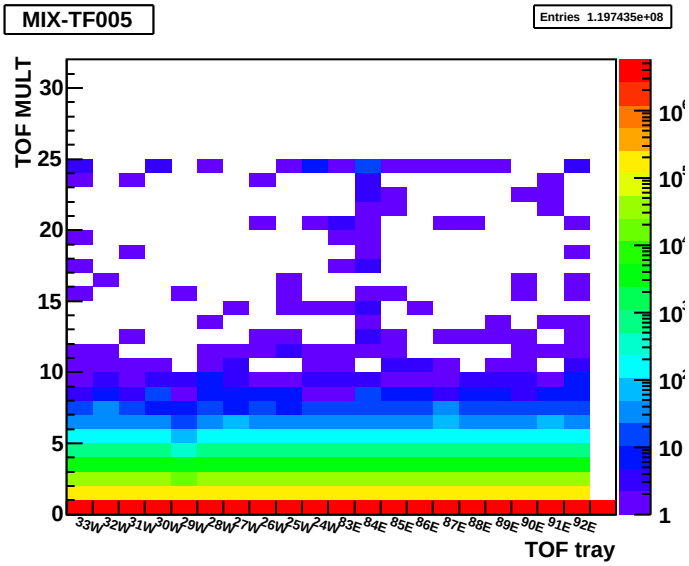

Hybrid Jet Patches
Entries 5702070
JP ADC
JP ID

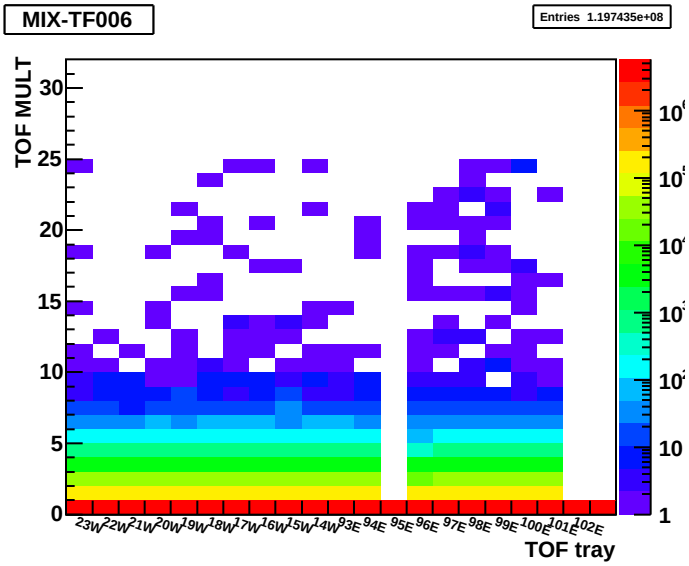

MIX-TF001
Entries 1.197435e+08
TOF MULT
TOF tray

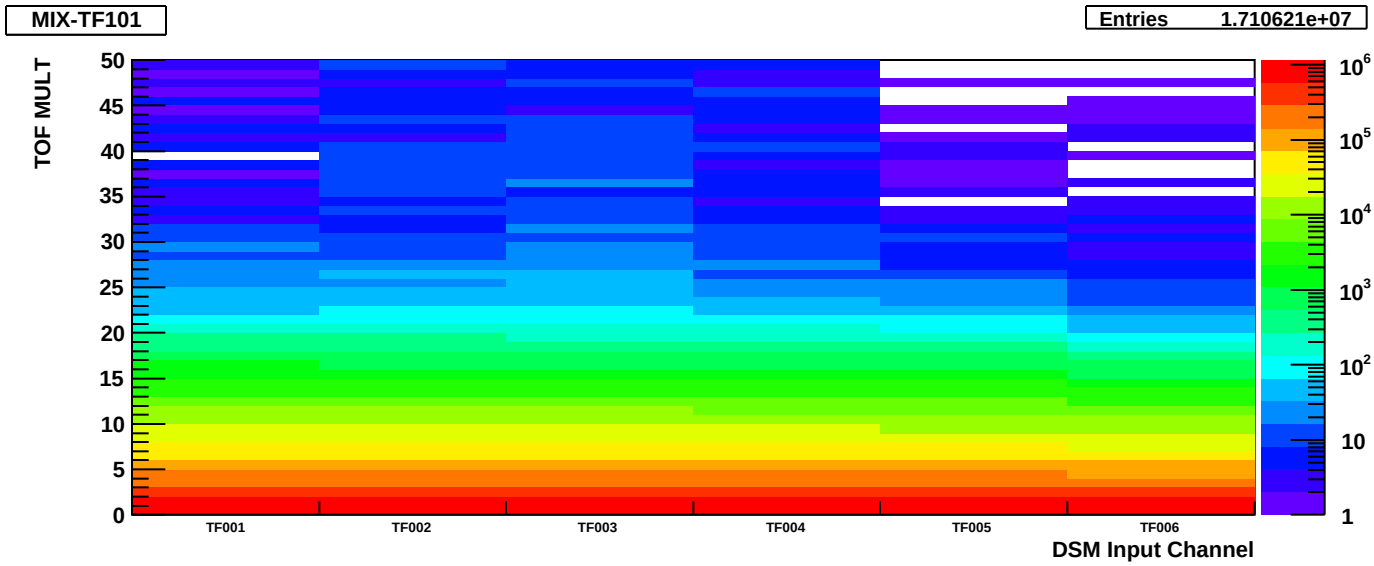

MIX-TF002
Entries 1.197435e+08
TOF MULT
TOF tray

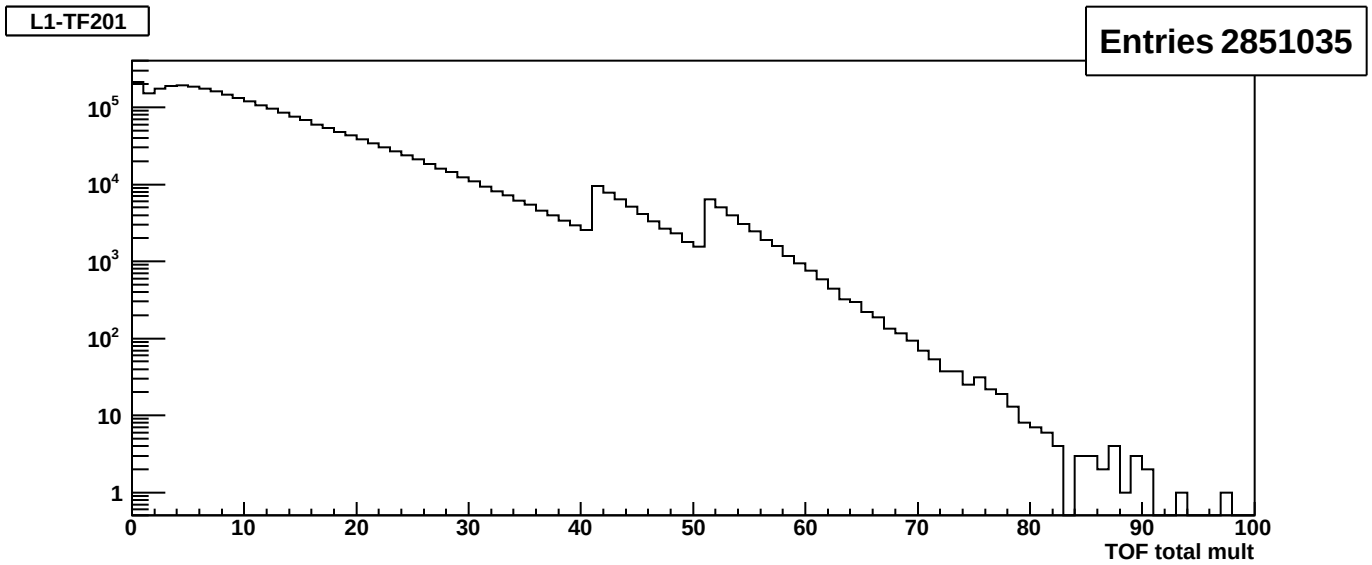

MIX-TF003
Entries 1.197435e+08
TOF MULT
TOF tray

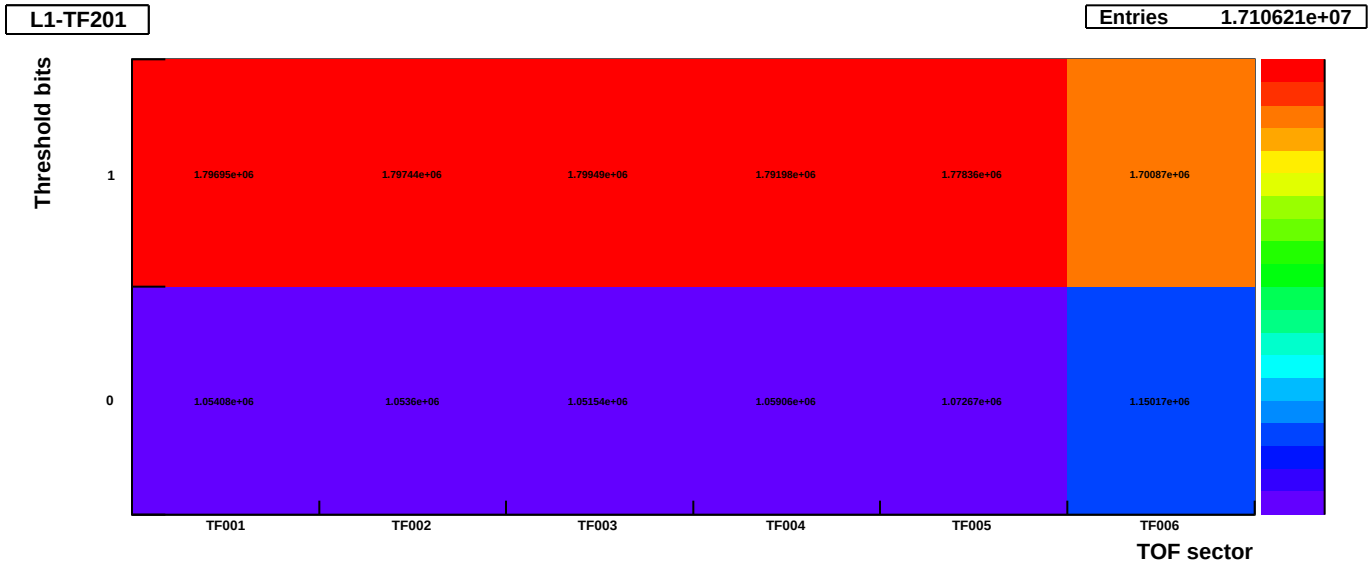

MIX-TF004
Entries 1.197435e+08
TOF MULT
TOF tray

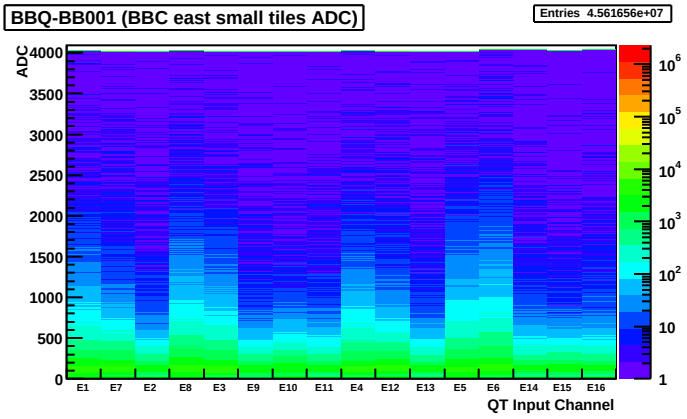

MIX-TF005
Entries 1.197435e+08
TOF MULT
TOF tray

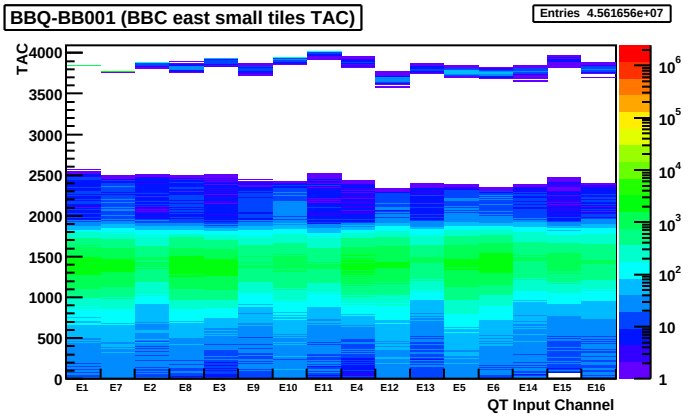

MIX-TF006
Entries 1.197435e+08
TOF MULT
TOF tray

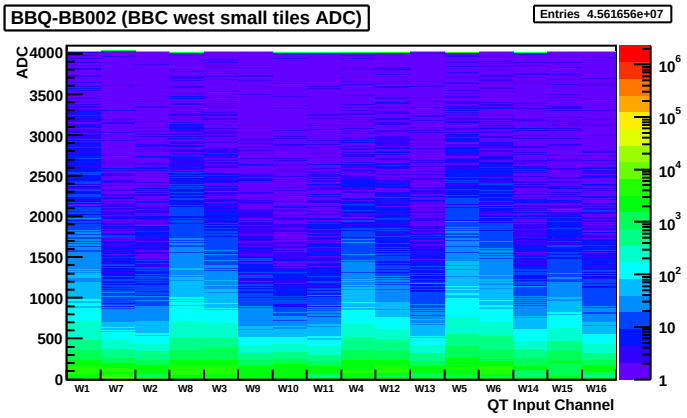

MIX-TF101
Entries 1.710621e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

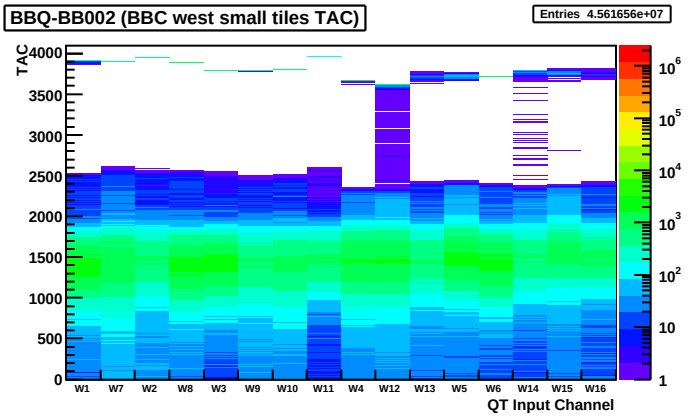

L1-TF201
Entries 2851035
TOF total mult

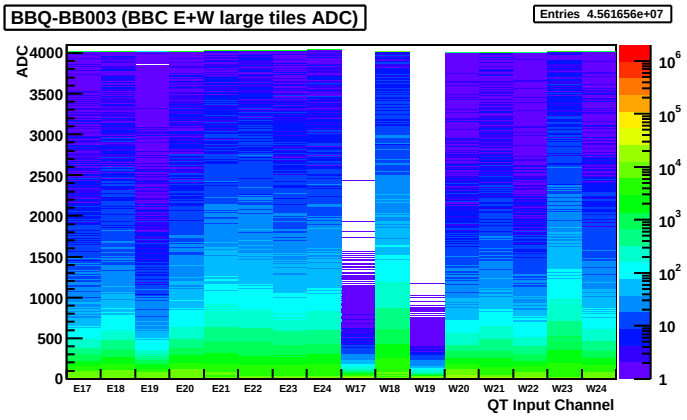

L1-TF201
Entries 1.710621e+07
Threshold bits
1.79695e+06
1.79744e+06
1.79949e+06
1.79198e+06
1.77836e+06
1.70087e+06
1.05408e+06
1.0536e+06
1.05154e+06
1.05906e+06
1.07267e+06
1.15017e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

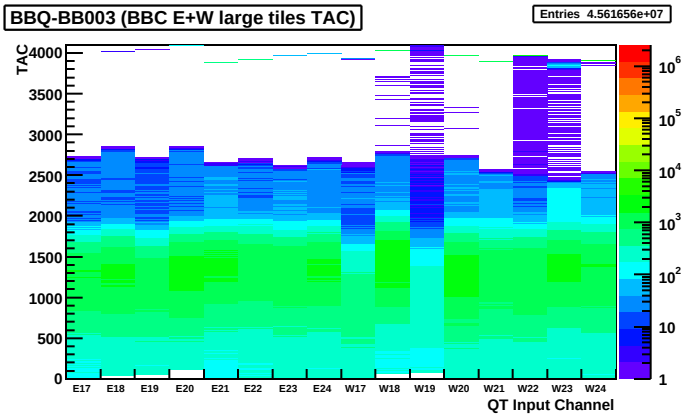

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.561656e+07
ADC
QT Input Channel

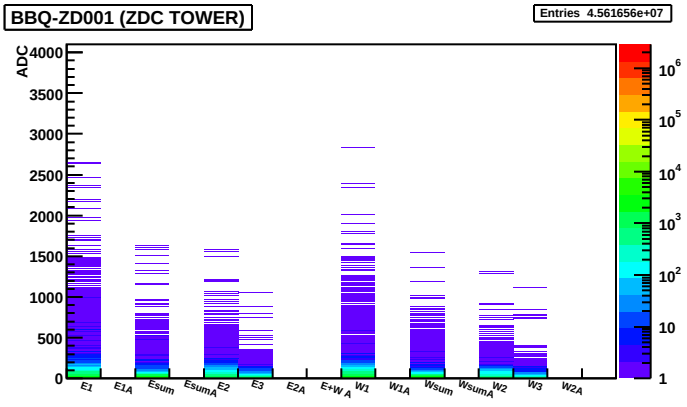

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.561656e+07
TAC
QT Input Channel

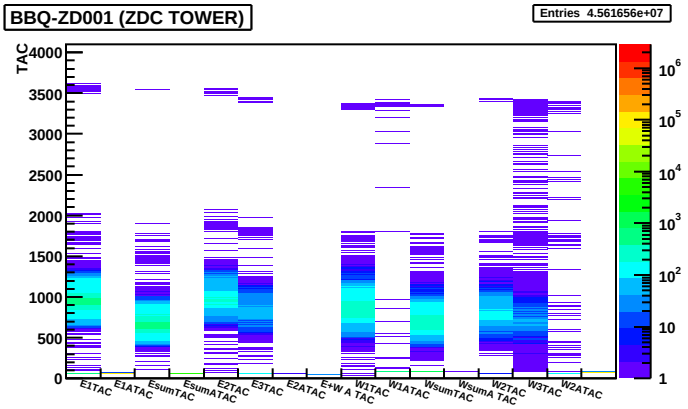

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.561656e+07
ADC
QT Input Channel

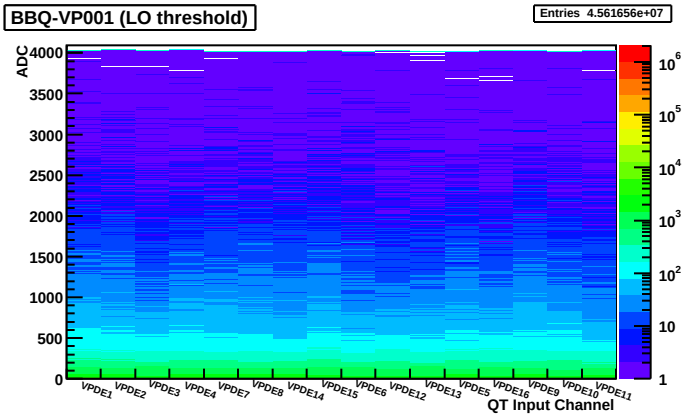

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.561656e+07
TAC
QT Input Channel

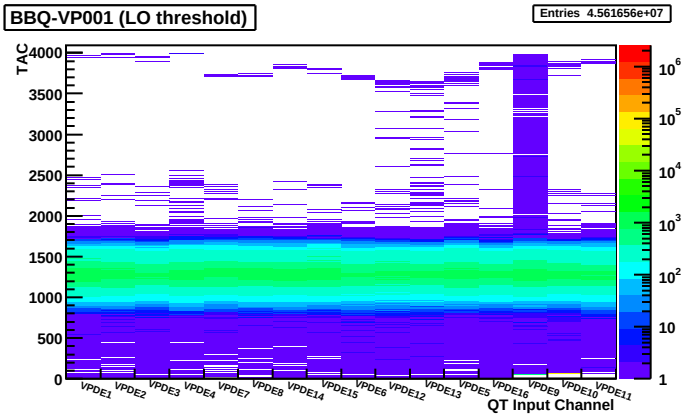

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.561656e+07
ADC
QT Input Channel

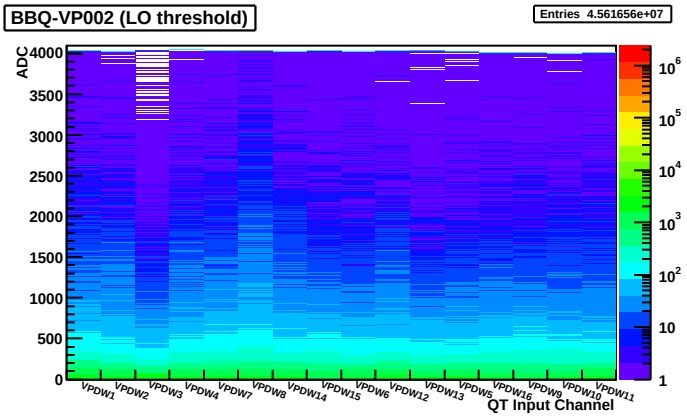

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.561656e+07
TAC
QT Input Channel

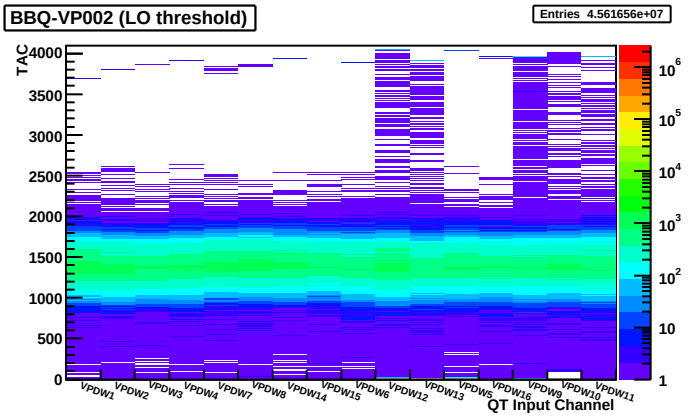

BBQ-ZD001 (ZDC TOWER)
Entries 4.561656e+07
ADC

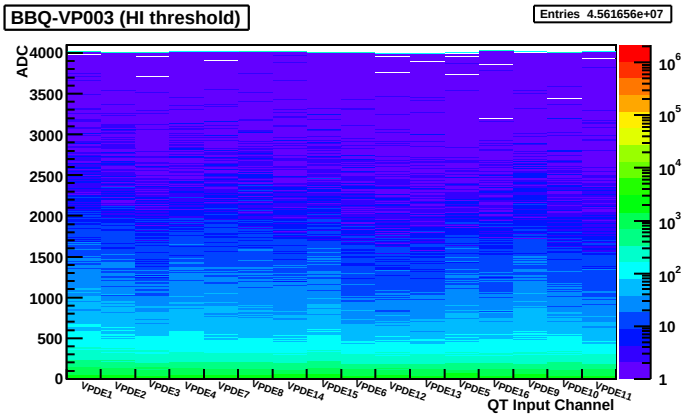

BBQ-ZD001 (ZDC TOWER)
Entries 4.561656e+07
TAC

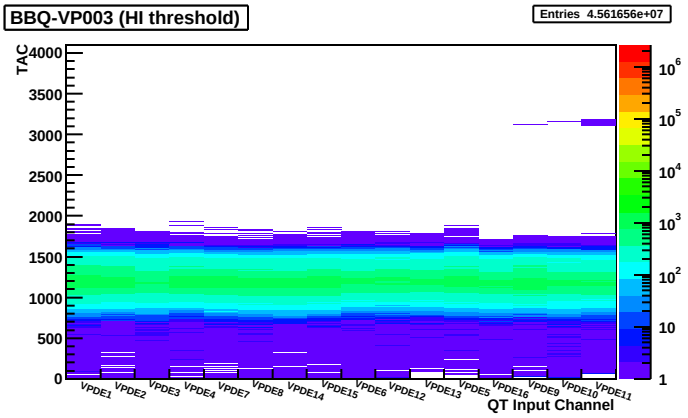

BBQ-VP001 (LO threshold)
Entries 4.561656e+07
ADC
QT Input Channel

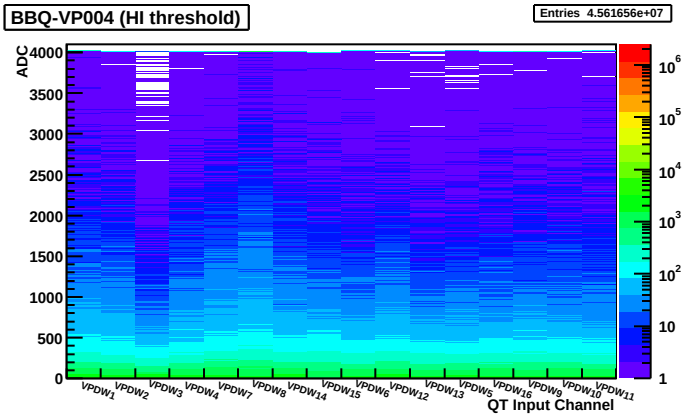

BBQ-VP001 (LO threshold)
Entries 4.561656e+07
TAC
QT Input Channel

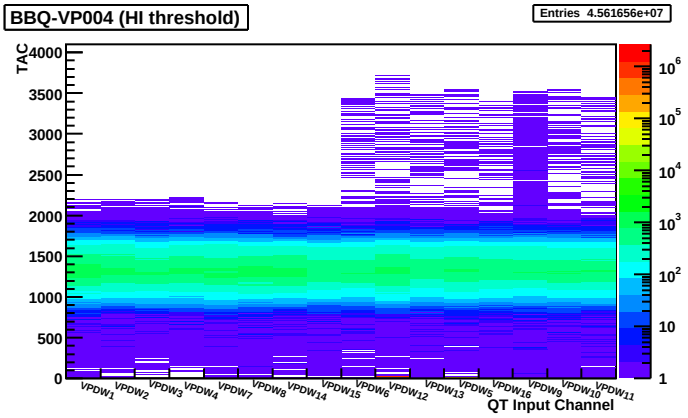

BBQ-VP002 (LO threshold)
Entries 4.561656e+07
ADC
QT Input Channel

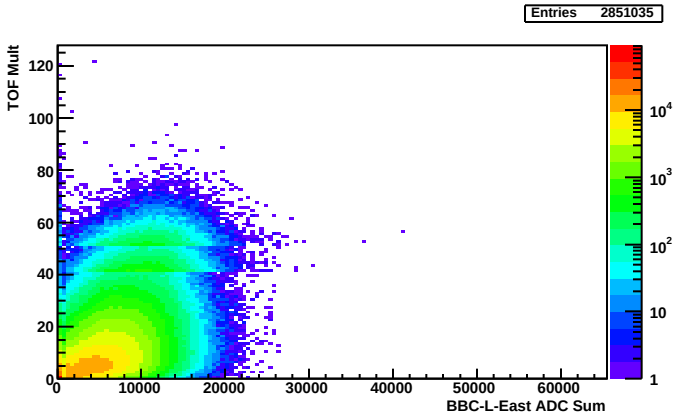

BBQ-VP002 (LO threshold)
Entries 4.561656e+07
TAC
QT Input Channel

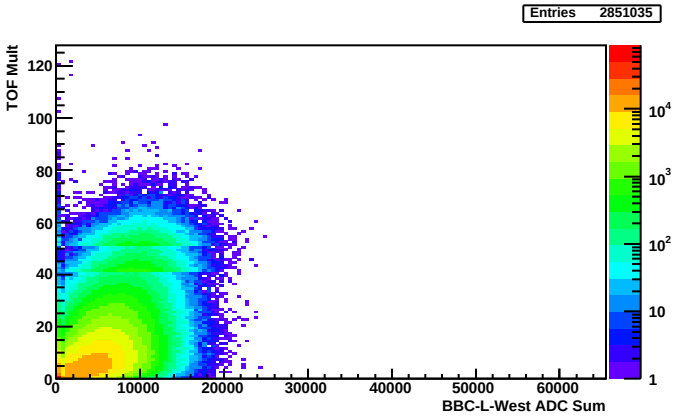

BBQ-VP003 (HI threshold)
Entries 4.561656e+07
ADC
QT Input Channel

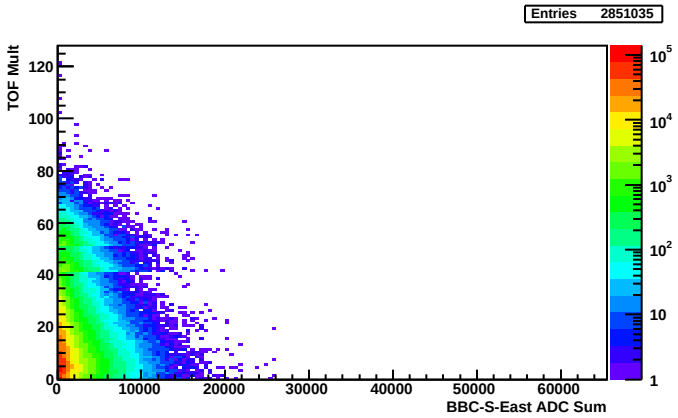

BBQ-VP003 (HI threshold)
Entries 4.561656e+07
TAC
QT Input Channel

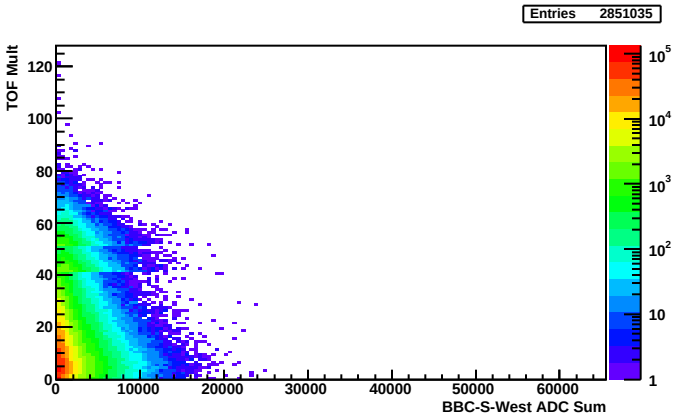

BBQ-VP004 (HI threshold)
Entries 4.561656e+07
ADC
QT Input Channel

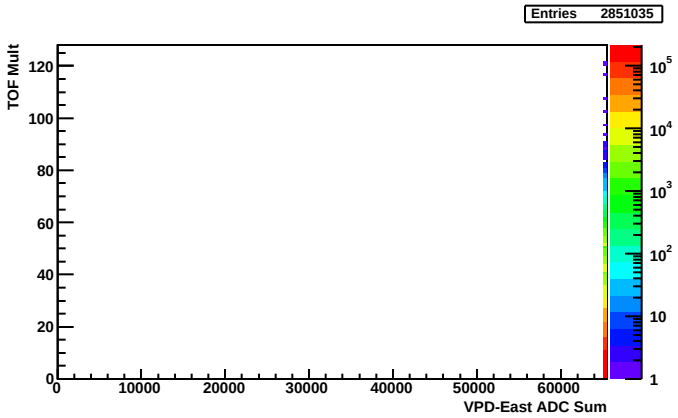

BBQ-VP004 (HI threshold)
Entries 4.561656e+07
TAC
QT Input Channel

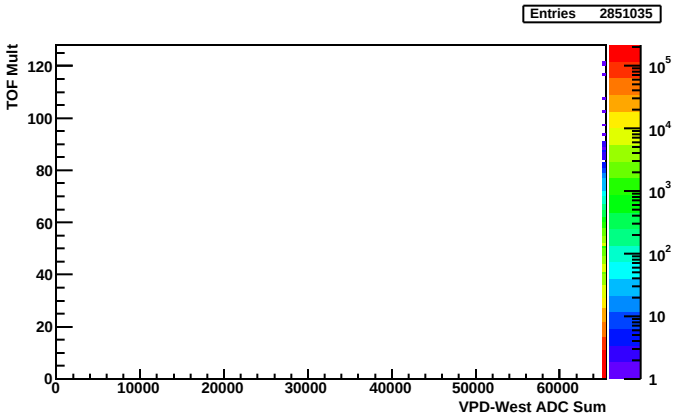

Entries 2851035
TOF Mult
BBC-L-East ADC Sum

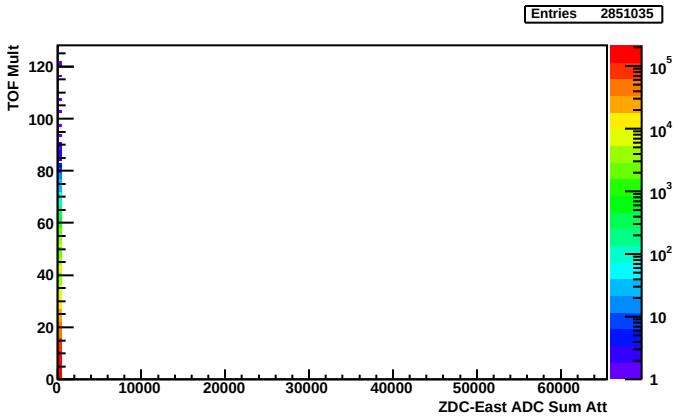

Entries 2851035
TOF Mult
BBC-L-West ADC Sum

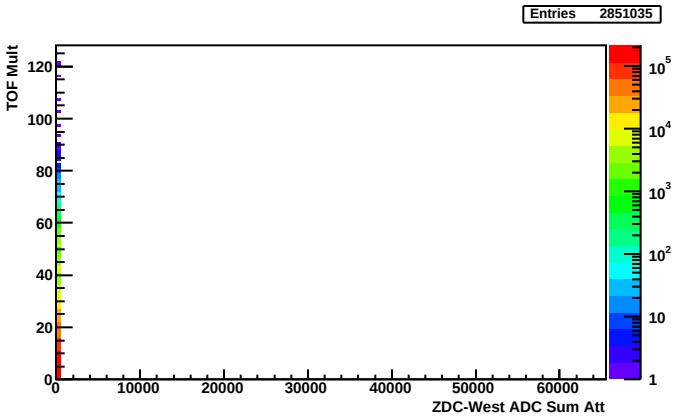

Entries 2851035
TOF Mult
BBC-S-East ADC Sum

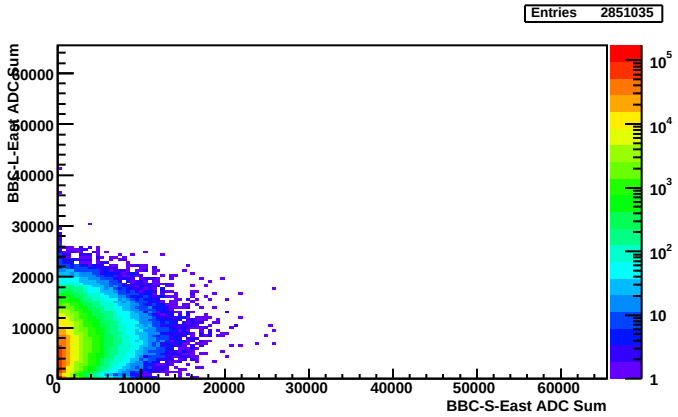

Entries 2851035
TOF Mult
BBC-S-West ADC Sum

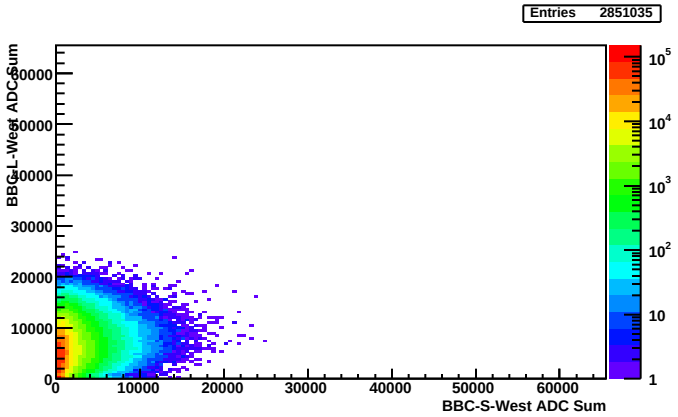

Entries 2851035
TOF Mult
VPD-East ADC Sum

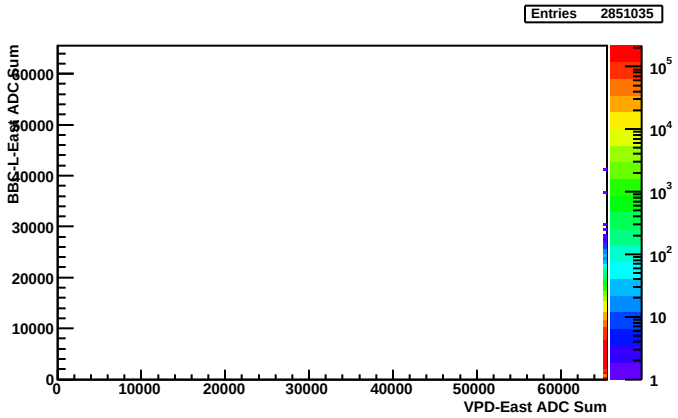

Entries 2851035
TOF Mult
VPD-West ADC Sum

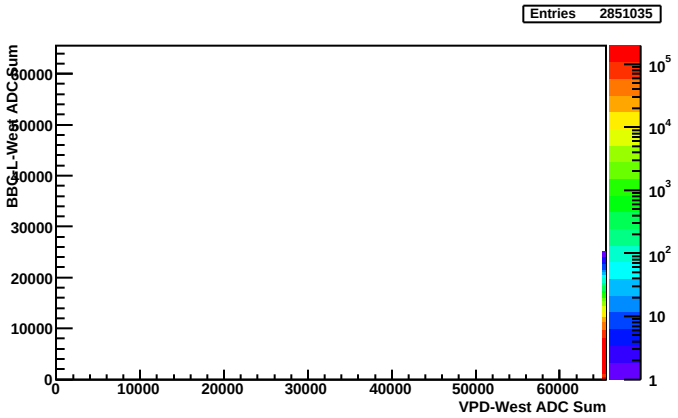

Entries 2851035
TOF Mult
ZDC-East ADC Sum Att

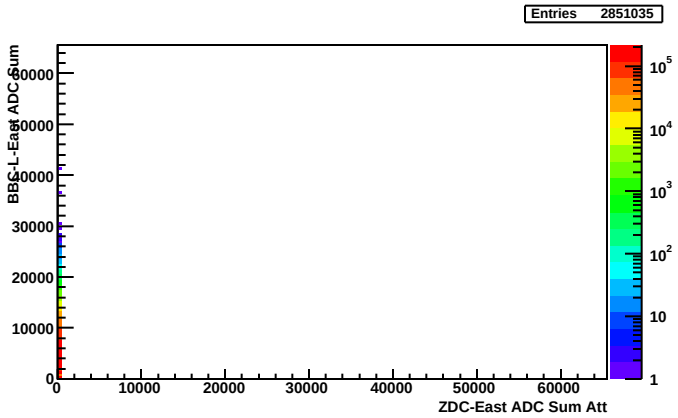

Entries 2851035
TOF Mult
ZDC-West ADC Sum Att

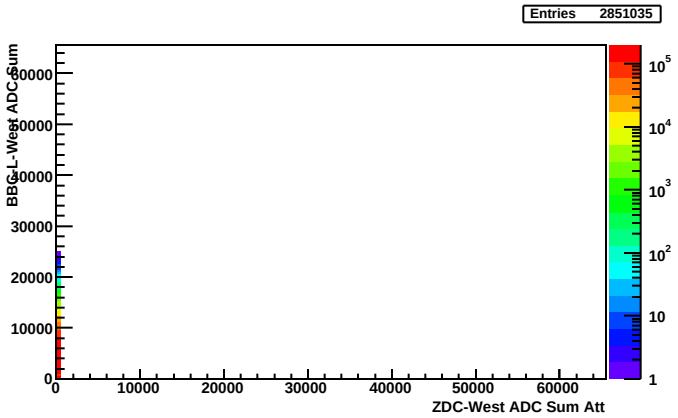

Entries 2851035
BBC-L-East ADC Sum
BBC-S-East ADC Sum

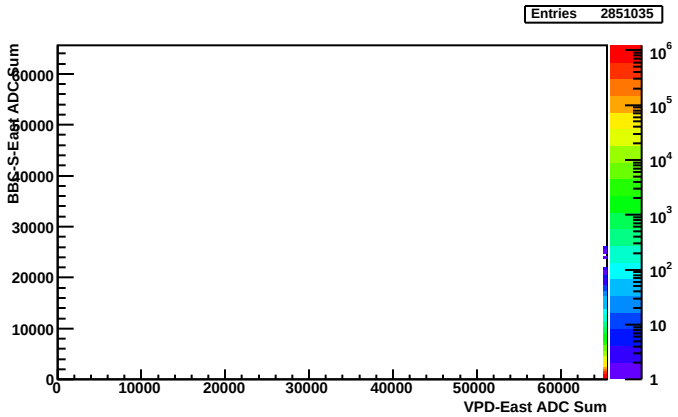

Entries 2851035
BBC-L-West ADC Sum
BBC-S-West ADC Sum

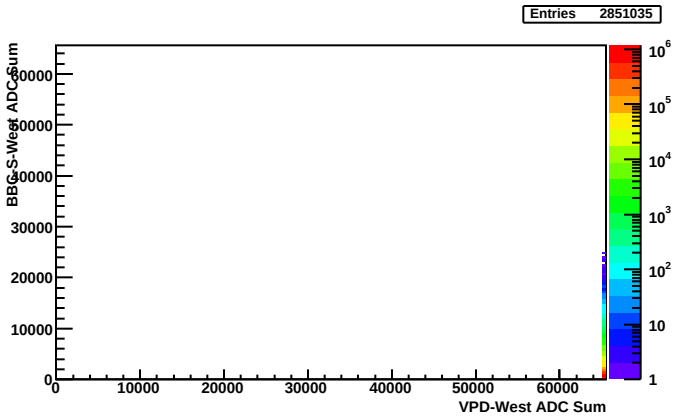

Entries 2851035
BBC-L-East ADC Sum
VPD-East ADC Sum

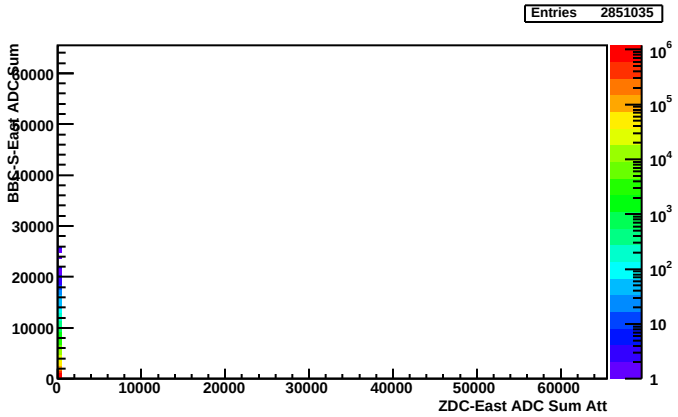

Entries 2851035
BBC-L-West ADC Sum
VPD-West ADC Sum

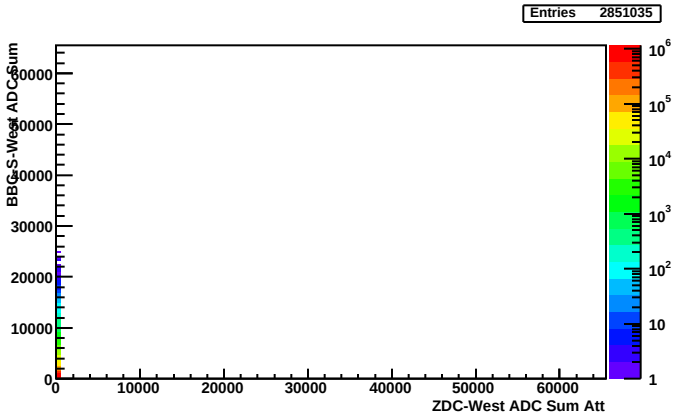

Entries 2851035
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

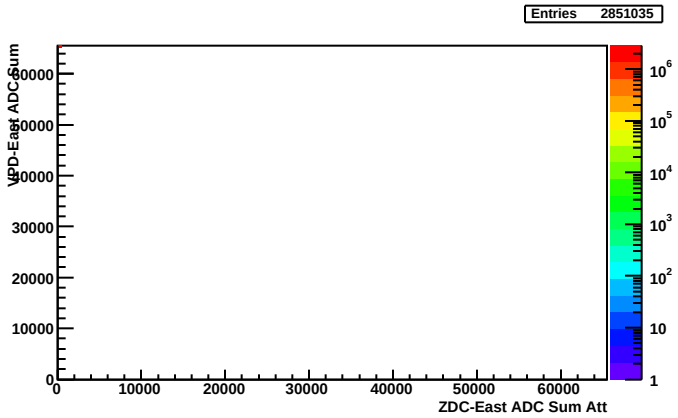

Entries 2851035
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

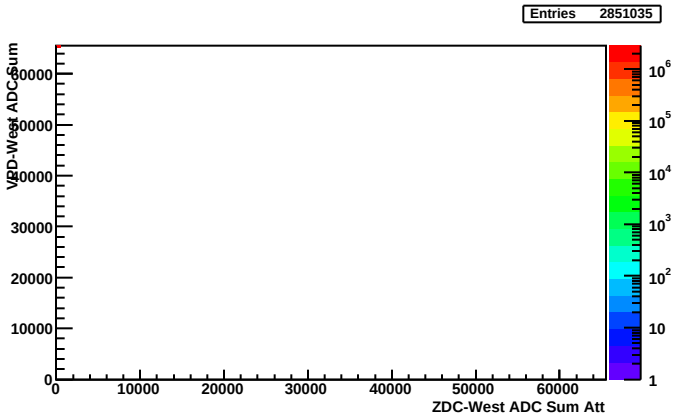

Entries 2851035
BBC-S-East ADC Sum
VPD-East ADC Sum

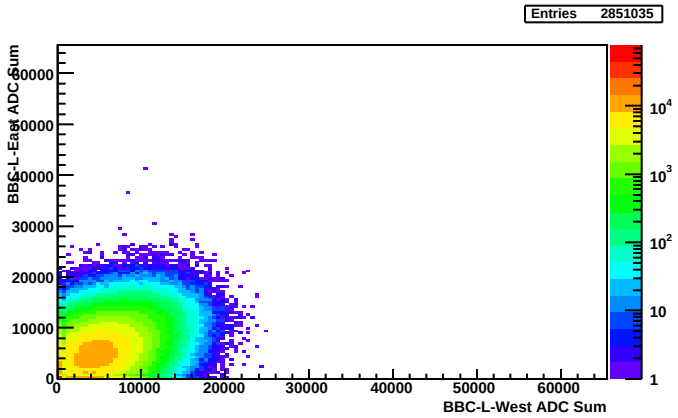

Entries 2851035
BBC-S-West ADC Sum
VPD-West ADC Sum

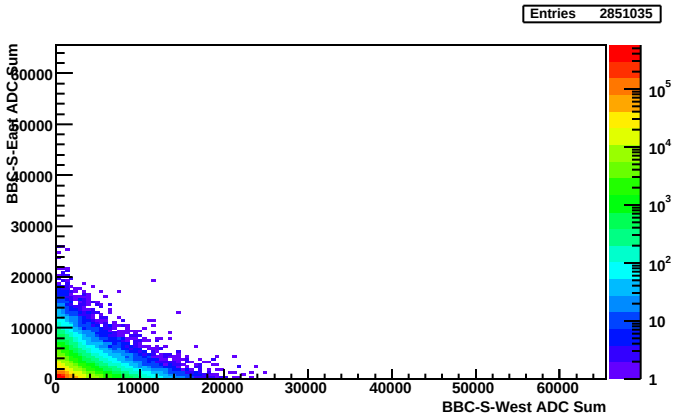

Entries 2851035
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

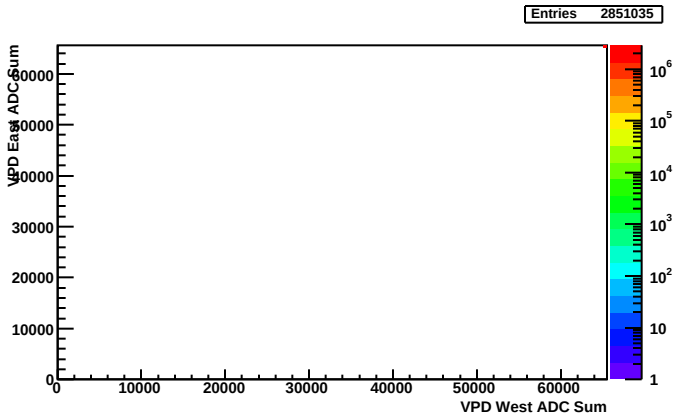

Entries 2851035
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

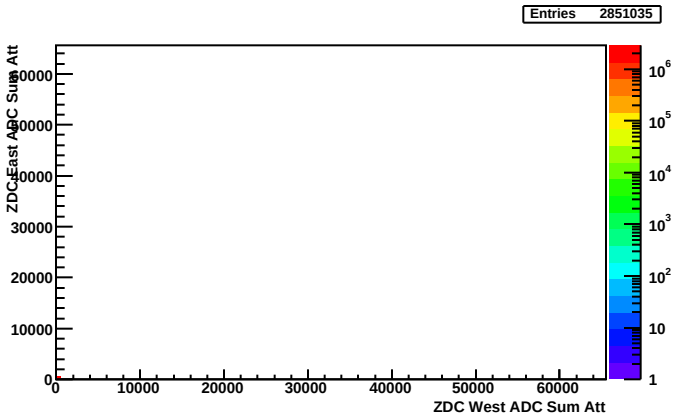

Entries 2851035
VPD-East ADC Sum
ZDC-East ADC Sum Att

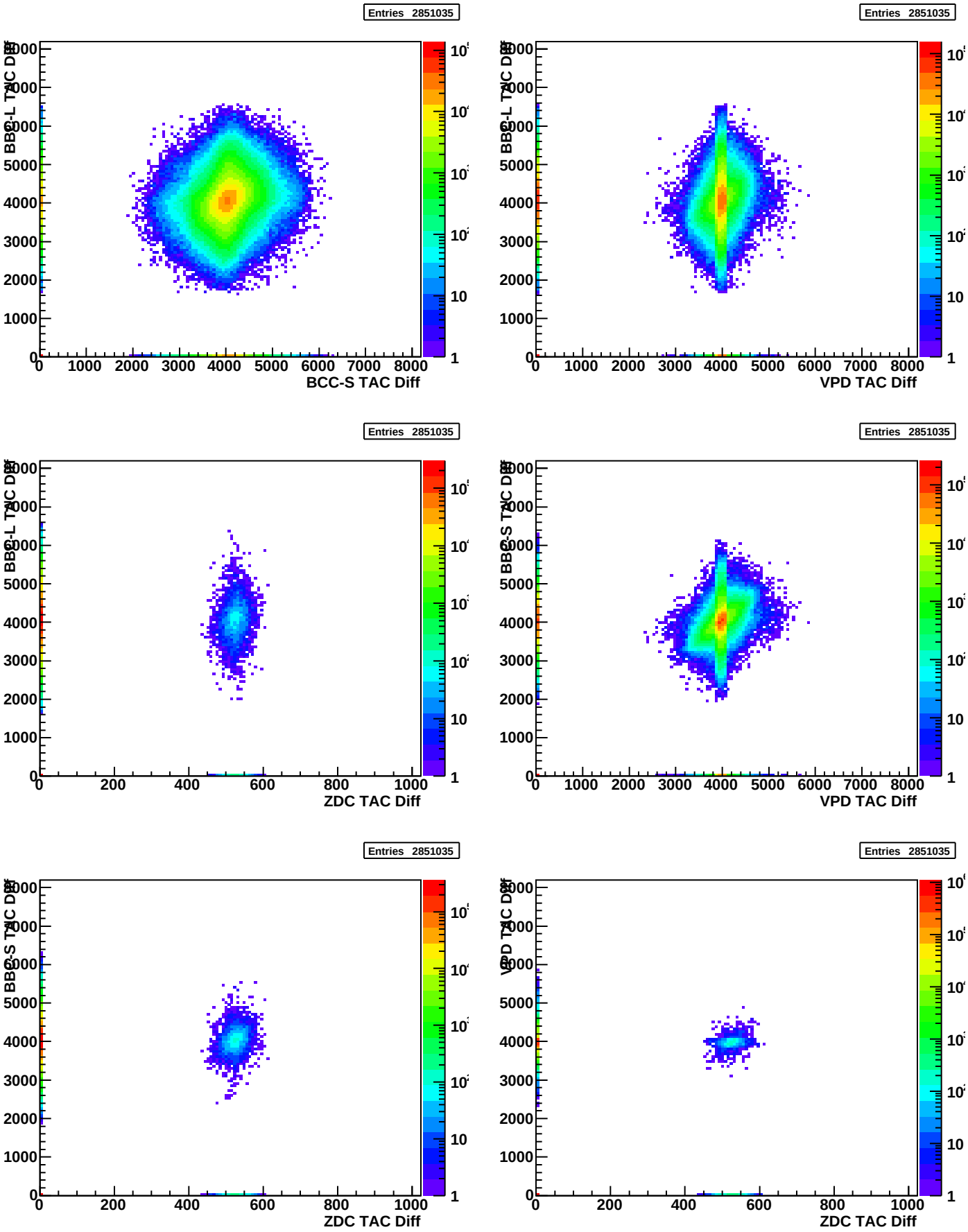

Entries 2851035
VPD-West ADC Sum
ZDC-West ADC Sum Att

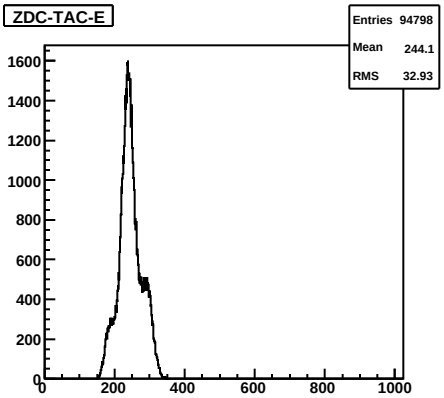

Entries 2851035
BBC-L-East ADC Sum
BBC-L-West ADC Sum

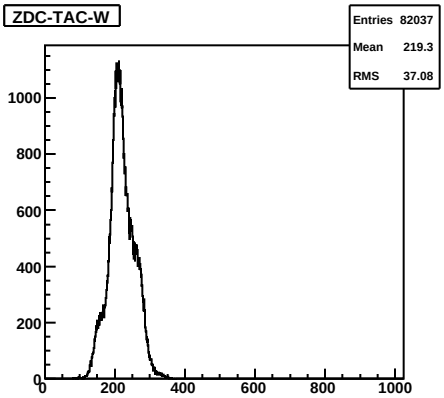

Entries 2851035
BBC-S-East ADC Sum
BBC-S-West ADC Sum

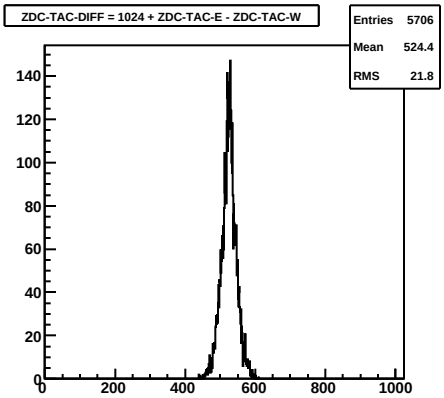

Entries 2851035
VPD East ADC Sum
VPD West ADC Sum

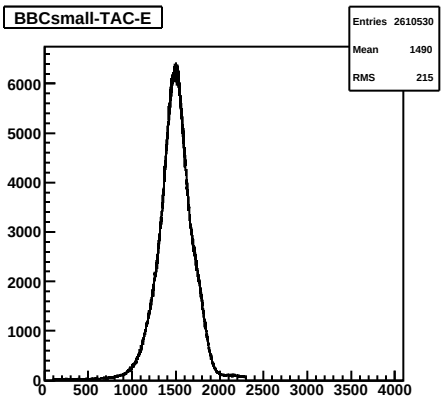

Entries 2851035
ZDC East ADC Sum Att
ZDC West ADC Sum Att

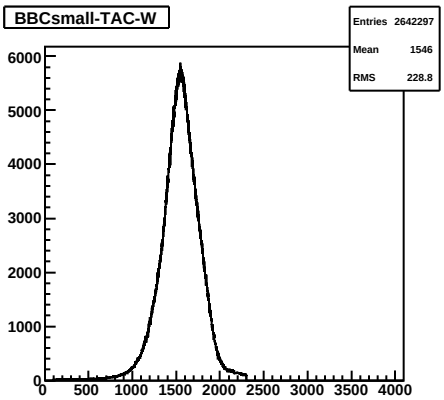

Entries 2851035
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2851035
BBC-L TAC Diff
VPD TAC Diff
Entries 2851035
BBC-L TAC Diff
ZDC TAC Diff
Entries 2851035
BBC-S TAC Diff
VPD TAC Diff
Entries 2851035
BBC-S TAC Diff
ZDC TAC Diff
Entries 2851035
VPD TAC Diff
ZDC TAC Diff

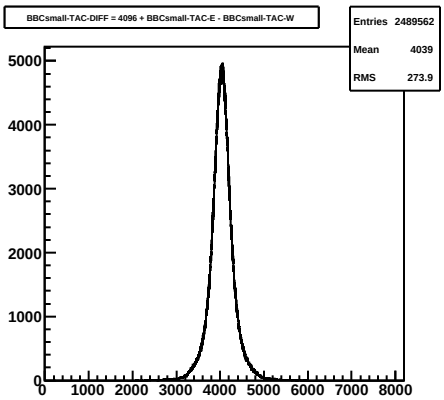

ZDC-TAC-E
Entries 94798
Mean 244.1
RMS 32.93

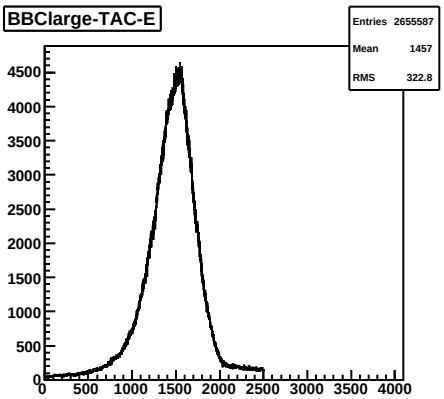

ZDC-TAC-W
Entries 82037
Mean 219.3
RMS 37.08

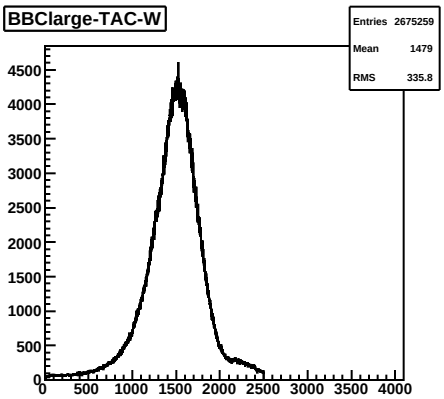

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5706
Mean 524.4
RMS 21.8

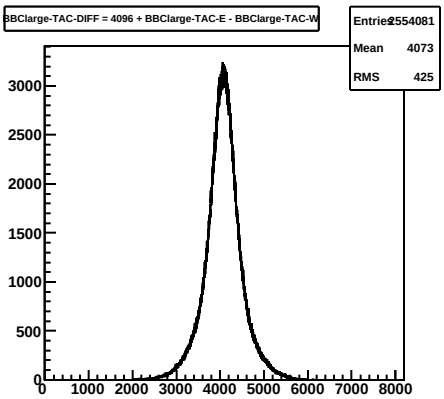

BBCsmall-TAC-E
Entries 2610530
Mean 1490
RMS 215

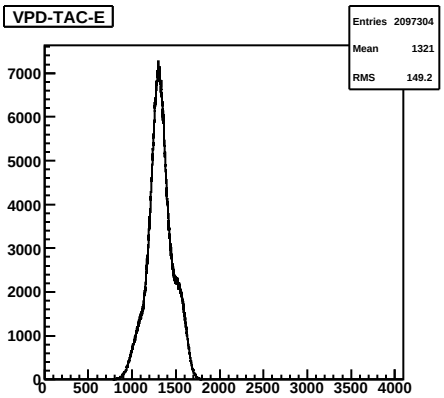

BBCsmall-TAC-W
Entries 2642297
Mean 1546
RMS 228.8

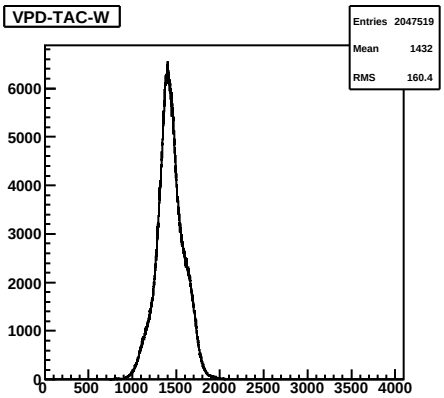

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2489562
Mean 4039
RMS 273.9

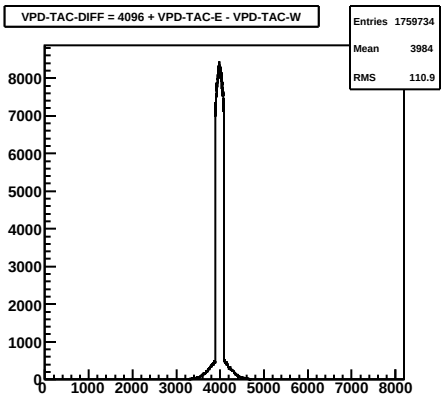

BBClarge-TAC-E
Entries 2655587
Mean 1457
RMS 322.8

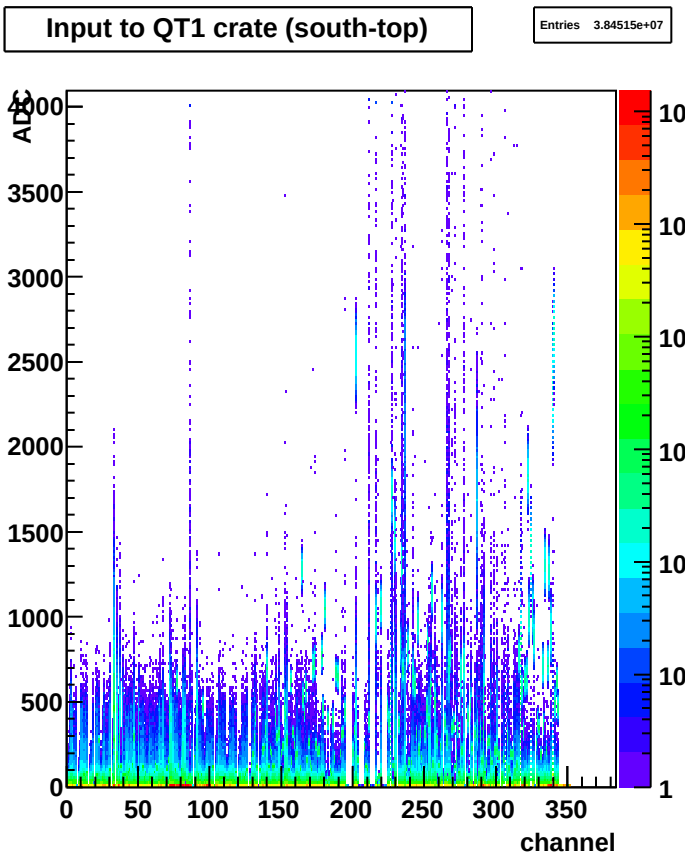

BBClarge-TAC-W
Entries 2675259
Mean 1479
RMS 335.8

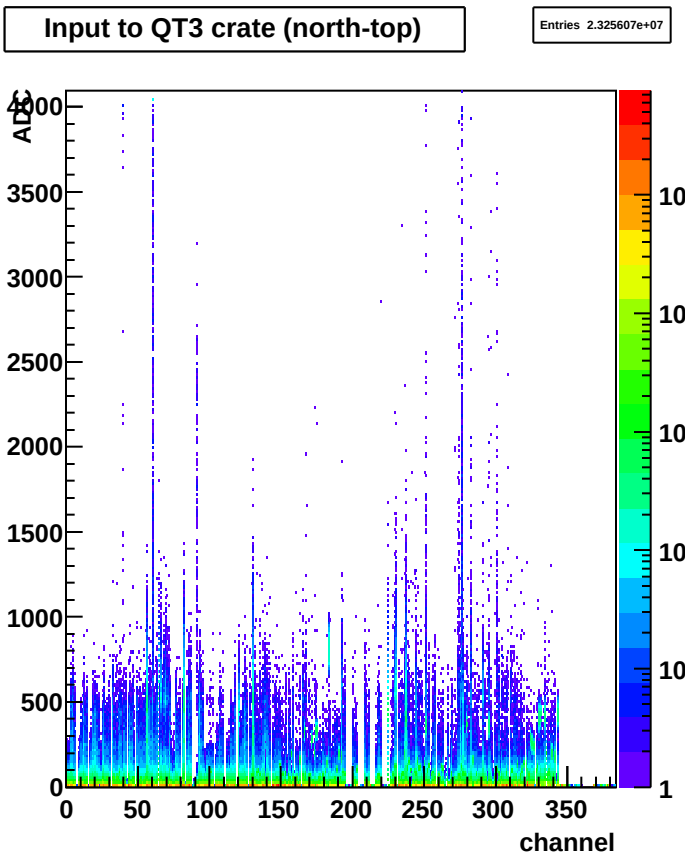

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2554081
Mean 4073
RMS 425

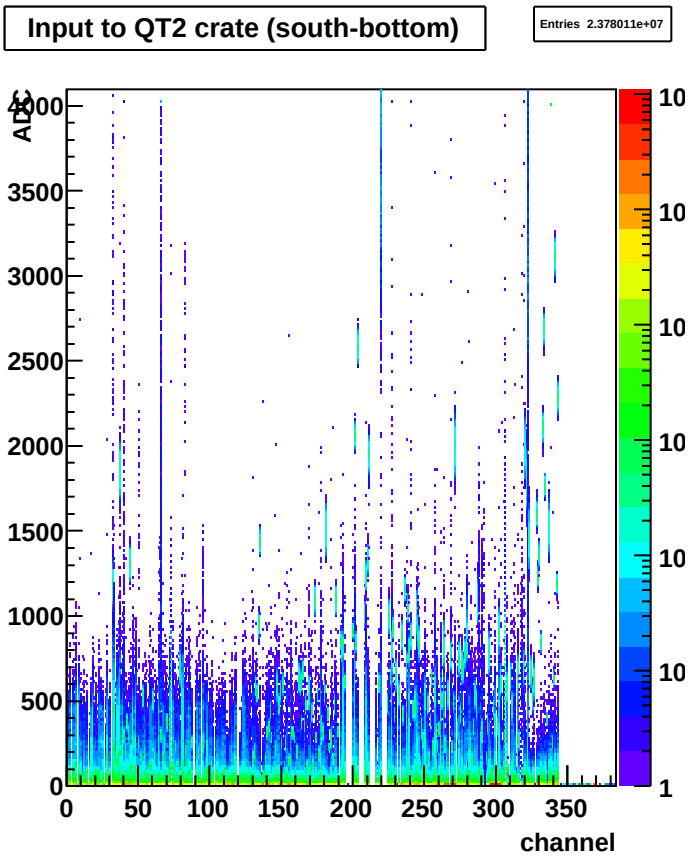

VPD-TAC-E
Entries 2097304
Mean 1321
RMS 149.2

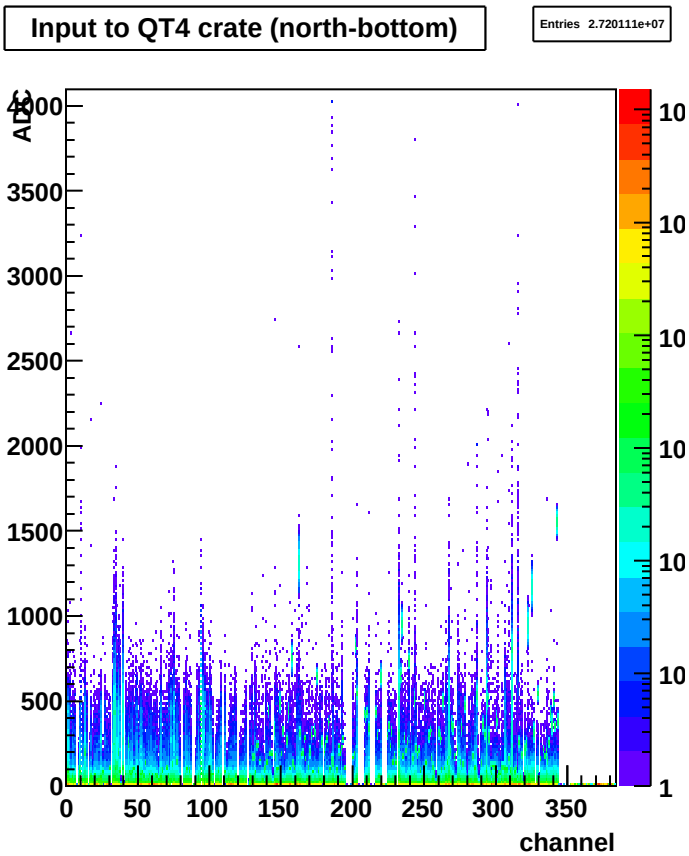

VPD-TAC-W
Entries 2047519
Mean 1432
RMS 160.4

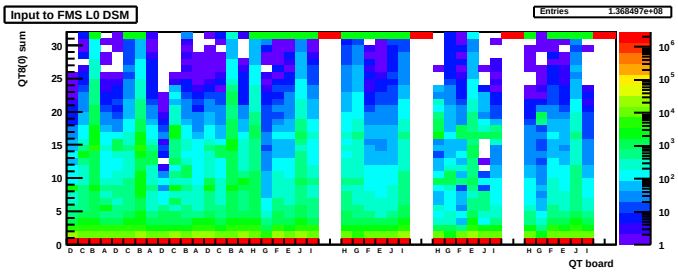

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1759734
Mean 3984
RMS 110.9

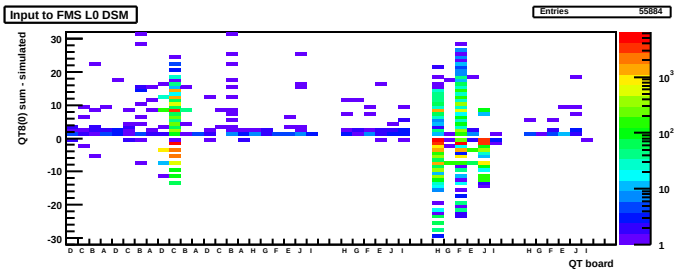

Input to QT1 crate (south-top)
Entries 3.84515e+07
ADC
channel

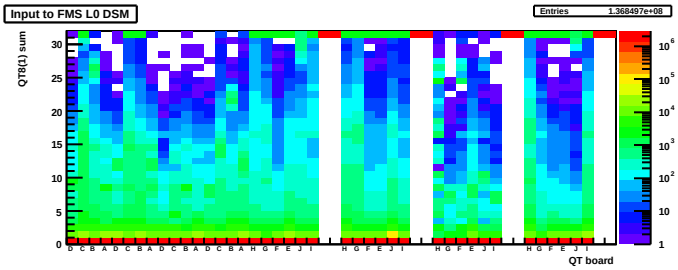

Input to QT3 crate (north-top)
Entries 2.325607e+07
ADC
channel

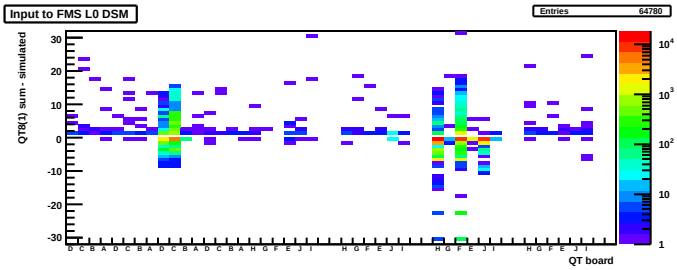

Input to QT2 crate (south-bottom)
Entries 2.378011e+07
ADC
channel

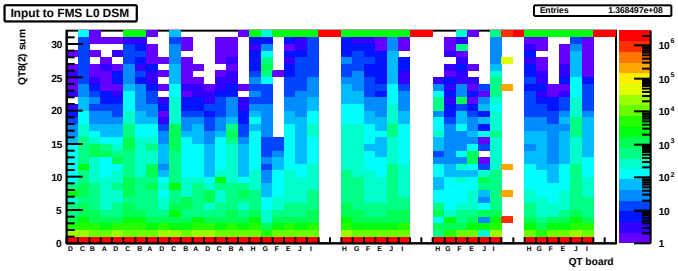

Input to QT4 crate (north-bottom)
Entries 2.720111e+07
ADC
channel

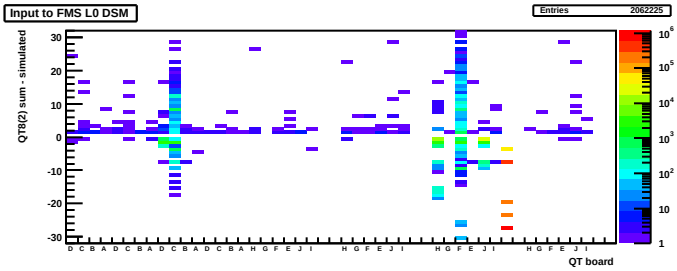

Input to FMS L0 DSM
Entries 1.368497e+08
QT8(0) sum
QT board

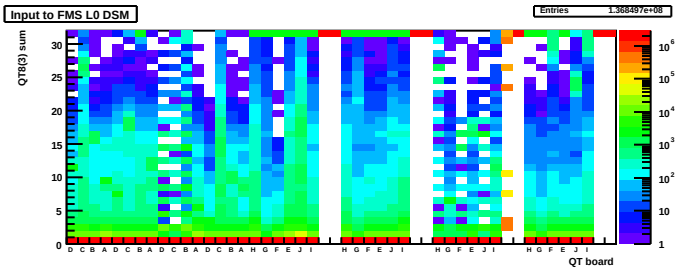

Input to FMS L0 DSM
Entries 55884
QT8(0) sum - simulated
QT board

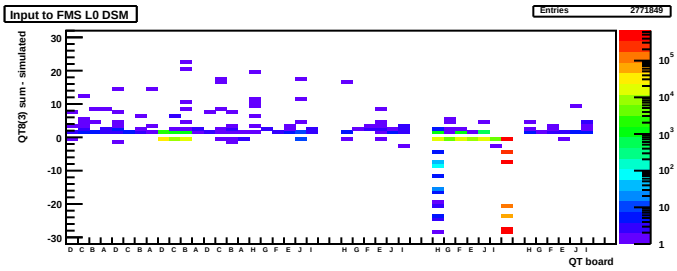

Input to FMS L0 DSM
Entries 1.368497e+08
QT8(1) sum
QT board

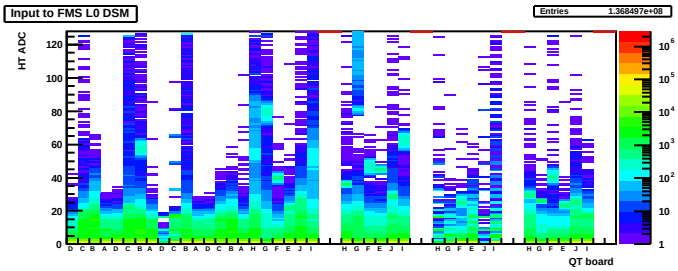

Input to FMS L0 DSM
Entries 64780
QT8(1) sum - simulated
QT board

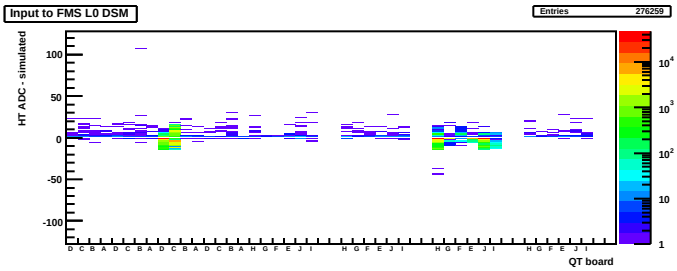

Input to FMS L0 DSM
Entries 1.368497e+08
QT8(2) sum
QT board

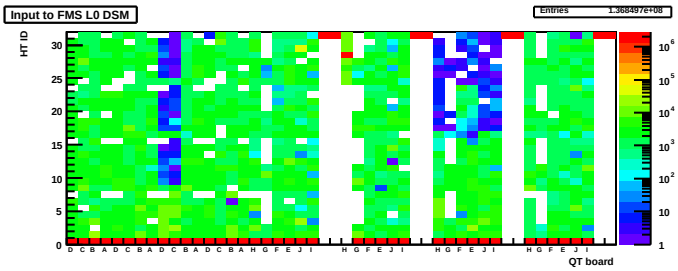

Input to FMS L0 DSM
Entries 2062225
QT8(2) sum - simulated
QT board

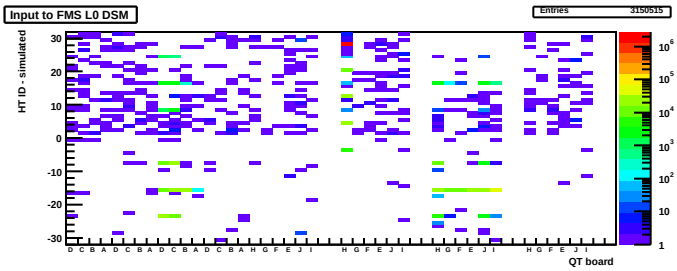

Input to FMS L0 DSM
Entries 1.368497e+08
QT8(3) sum
QT board

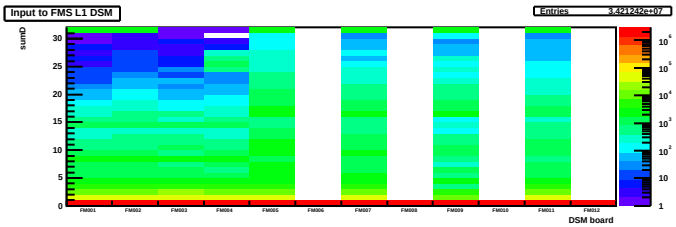

Input to FMS L0 DSM
Entries 2771849
QT8(3) sum - simulated
QT board

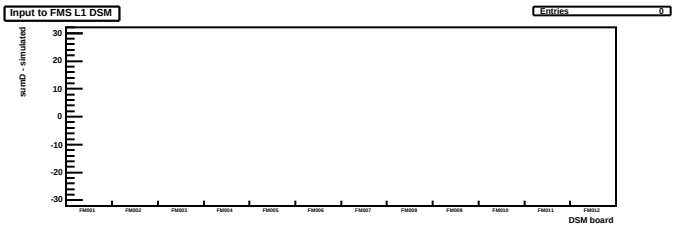

Input to FMS L0 DSM
Entries 1.368497e+08
HT ADC
QT board

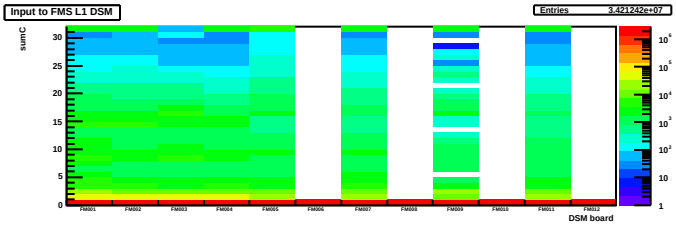

Input to FMS L0 DSM
Entries 276259
HT ADC - simulated
QT board

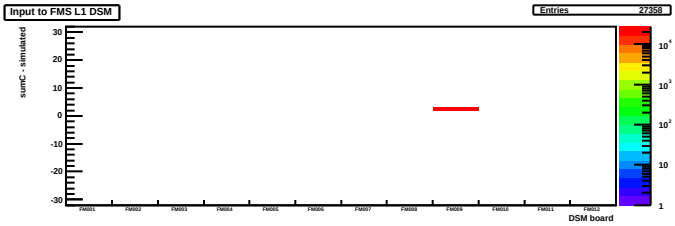

Input to FMS L0 DSM
Entries 1.368497e+08
HT ID
QT board

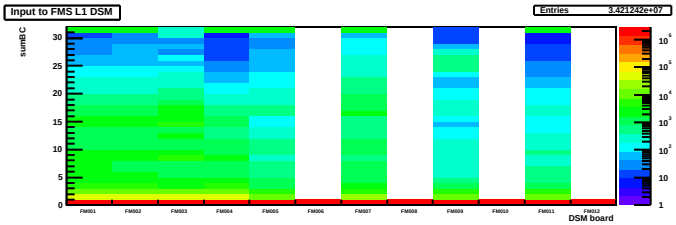

Input to FMS L0 DSM
Entries 3150515
HT ID - simulated
QT board

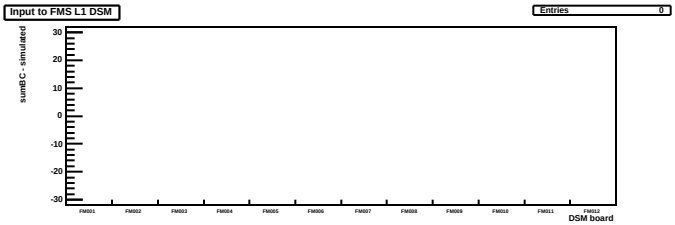

Input to FMS L1 DSM
Entries 3.421242e+07
sumD
DSM board

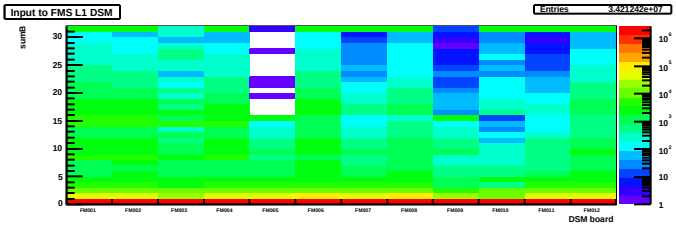

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

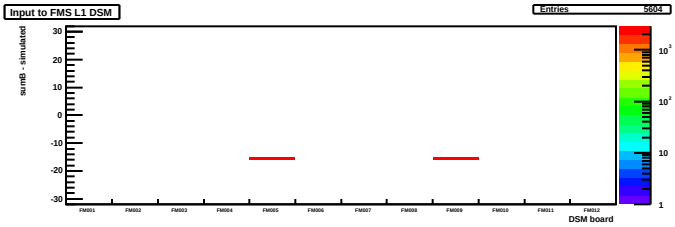

Input to FMS L1 DSM
Entries 3.421242e+07
sumC
DSM board

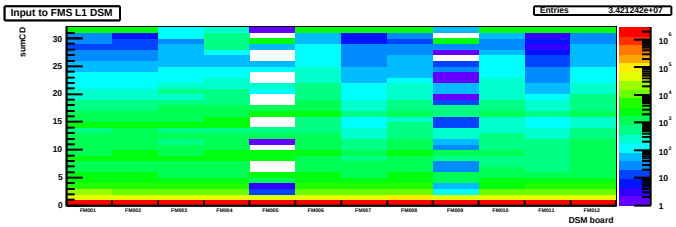

Input to FMS L1 DSM
Entries 27358
sumC - simulated
DSM board

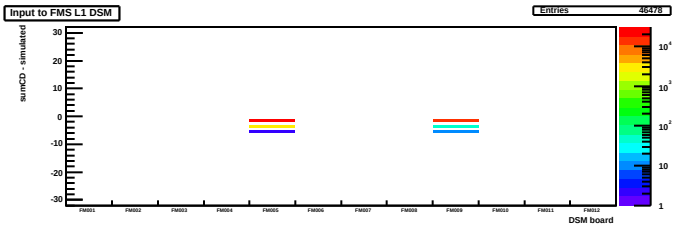

Input to FMS L1 DSM
Entries 3.421242e+07
sumBC
DSM board

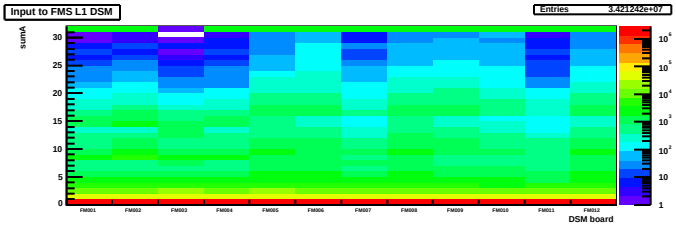

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

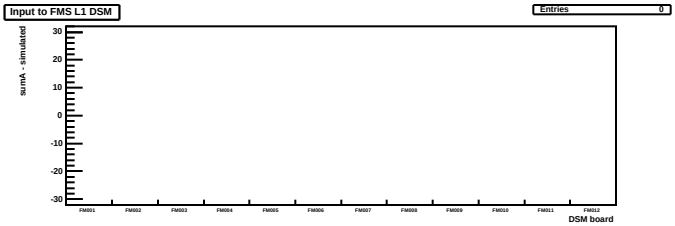

Input to FMS L1 DSM
Entries 3.421242e+07
sumB
DSM board

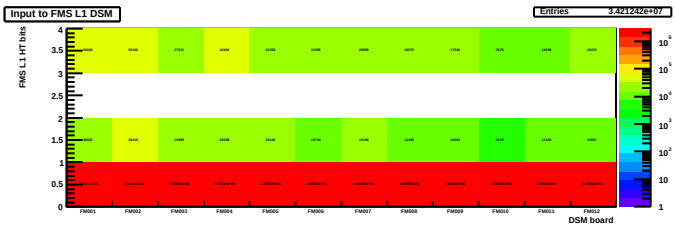

Input to FMS L1 DSM
Entries 5604
sumB - simulated
DSM board

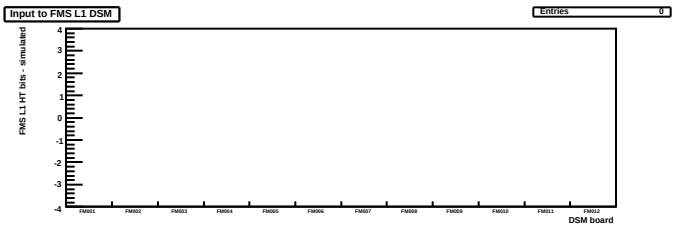

Input to FMS L1 DSM
Entries 3.421242e+07
sumCD
DSM board

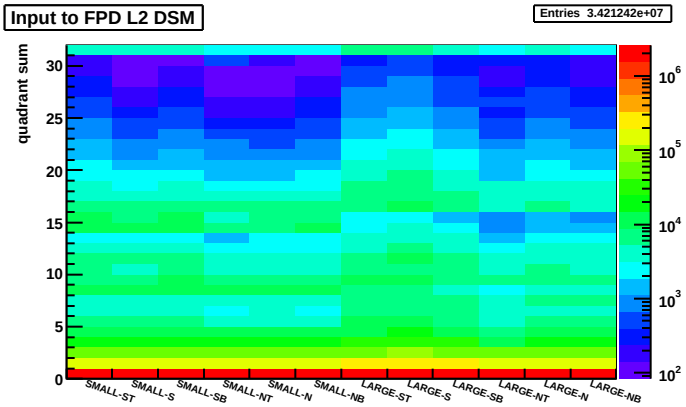

Input to FMS L1 DSM
Entries 46478
sumCD - simulated
DSM board

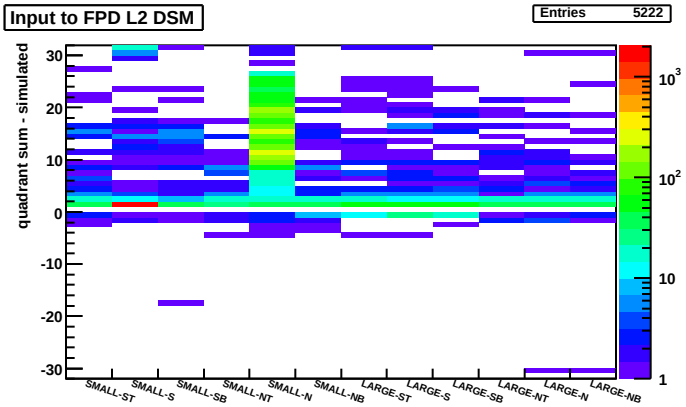

Input to FMS L1 DSM
Entries 3.421242e+07
sumA
DSM board

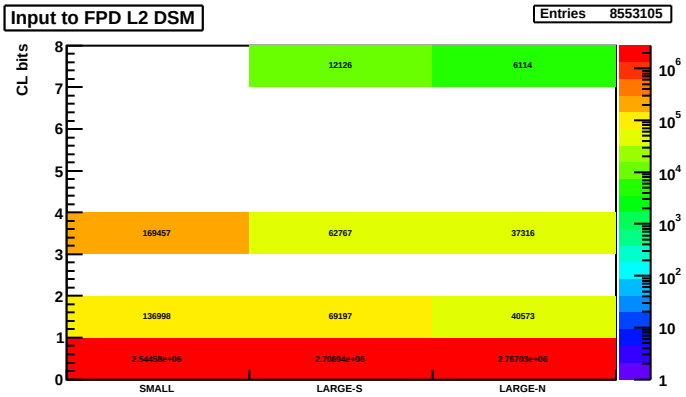

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

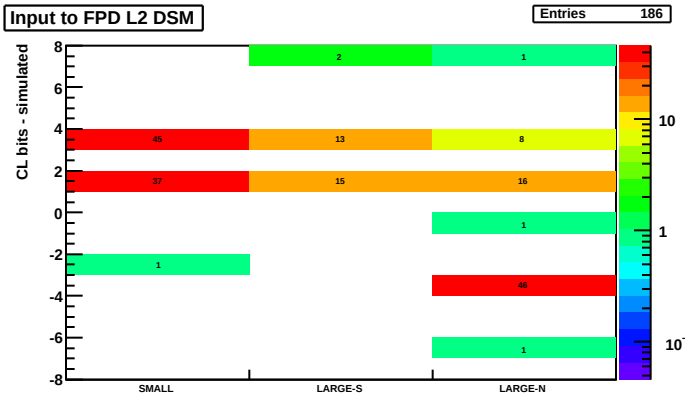

Input to FMS L1 DSM
Entries 3.421242e+07
FMS L1 HT bits
DSM board

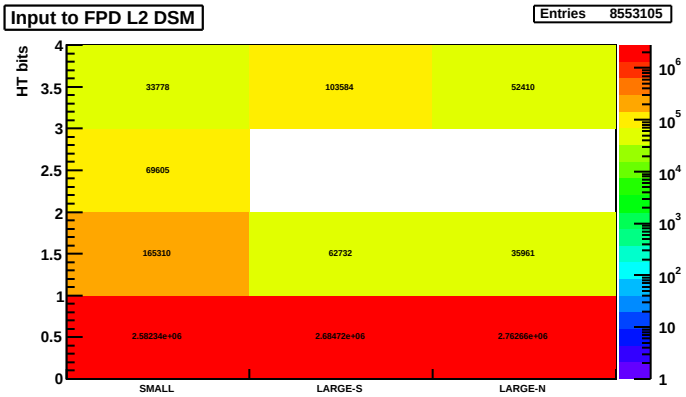

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

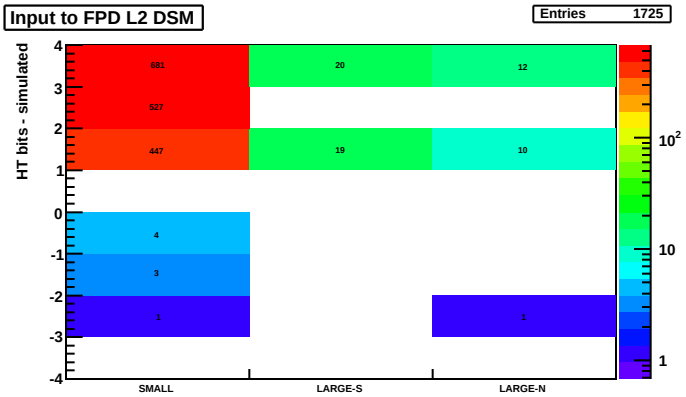

Input to FPD L2 DSM
Entries 3.421242e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

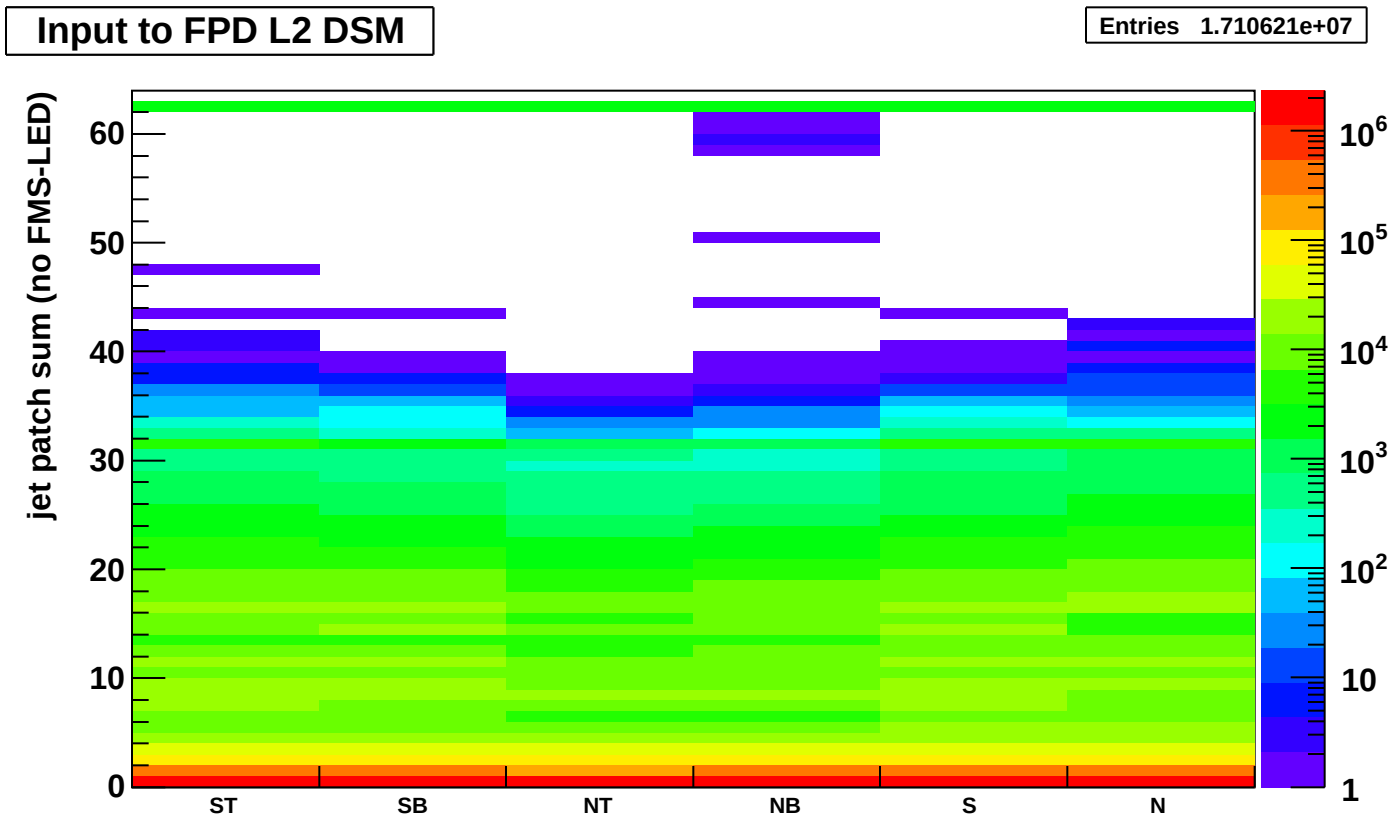

Input to FPD L2 DSM
Entries 5222
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

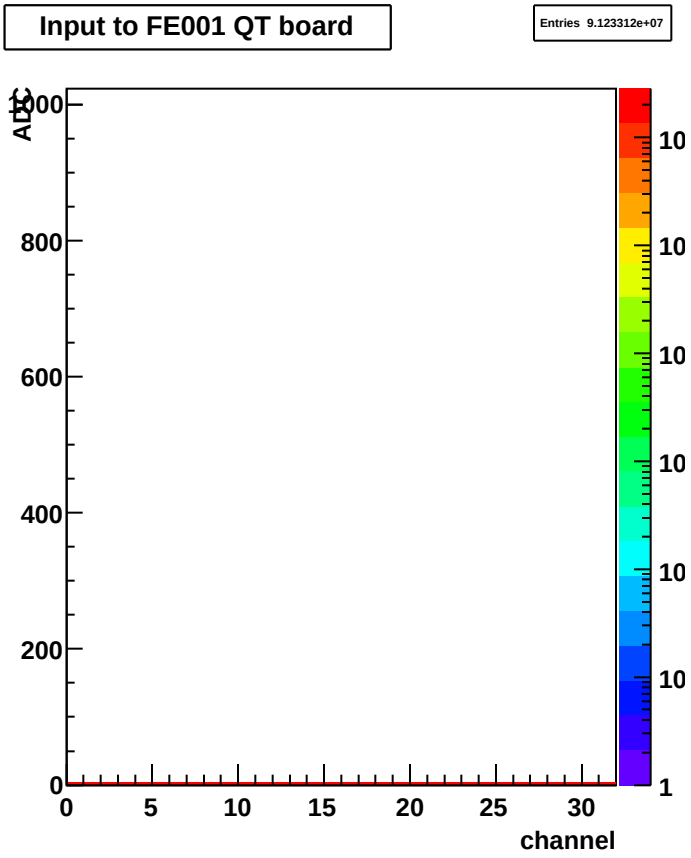

Input to FPD L2 DSM
Entries 8553105
CL bits
12126
6114
169457
62767
37316
136998
69197
40573
2.54458e+06
2.70694e+06
2.76703e+06
SMALL
LARGE-S
LARGE-N

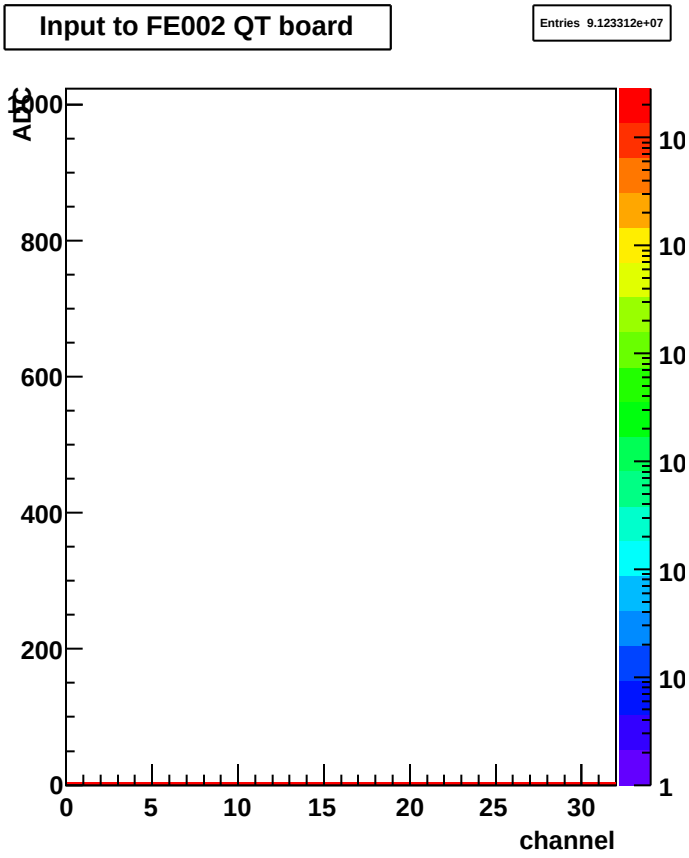

Input to FPD L2 DSM
Entries 186
CL bits - simulated
2
1
45
13
8
37
15
16
1
1
46
1
SMALL
LARGE-S
LARGE-N

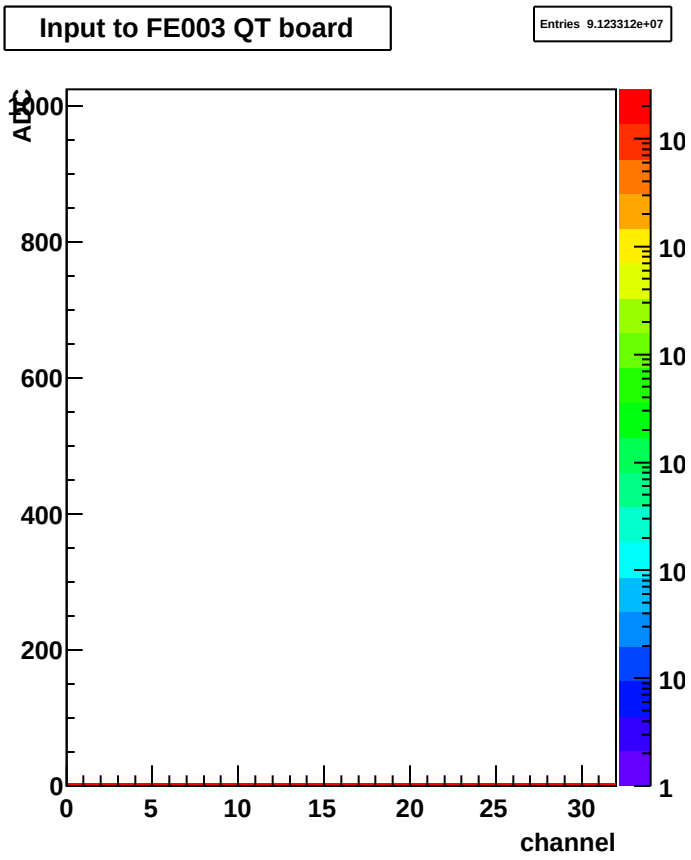

Input to FPD L2 DSM
Entries 8553105
HT bits
33778
103584
52410
69605
165310
62732
35961
2.58234e+06
2.68472e+06
2.76266e+06
SMALL
LARGE-S
LARGE-N

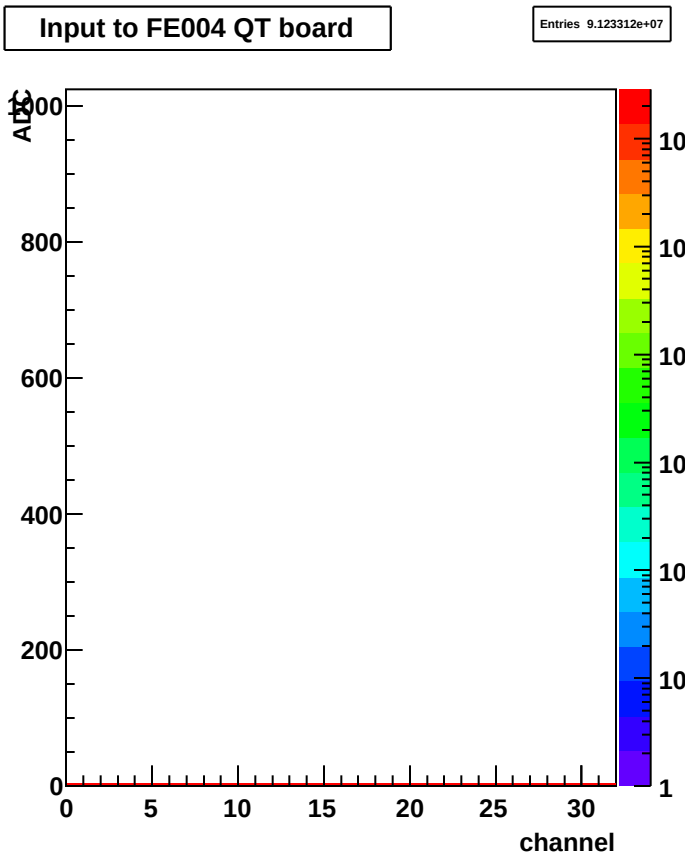

Input to FPD L2 DSM
Entries 1725
HT bits - simulated
681
20
12
527
447
19
10
4
3
1
1
SMALL
LARGE-S
LARGE-N

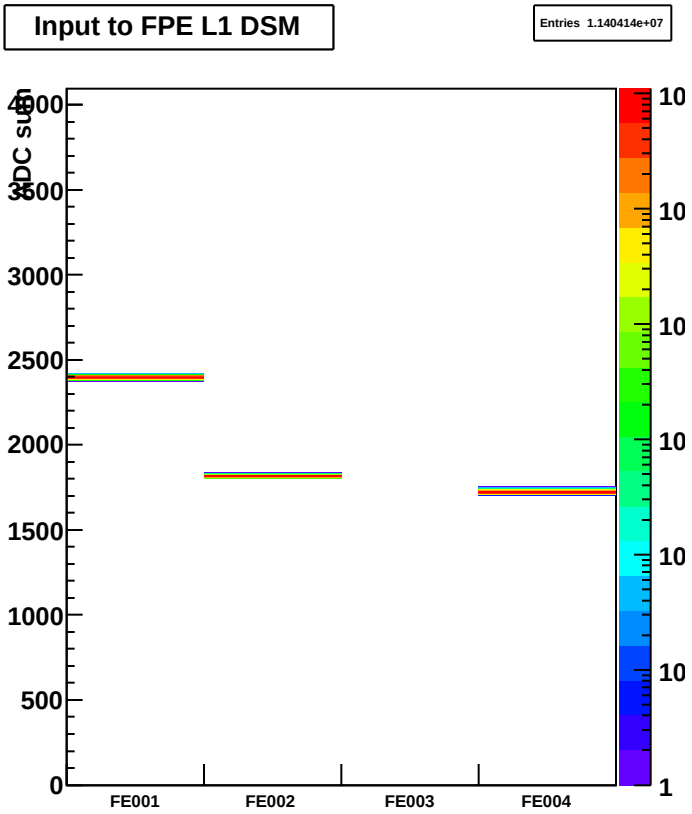

Input to FPD L2 DSM
Entries 1.710621e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 9.123312e+07
ADC
channel

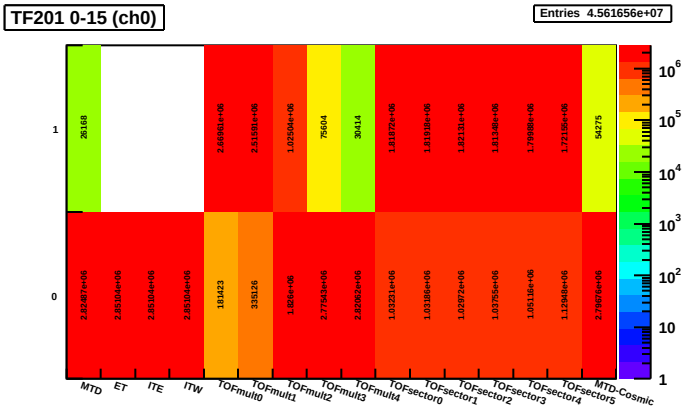

Input to FE002 QT board
Entries 9.123312e+07
ADC
channel


Input to FE003 QT board
Entries 9.123312e+07
ADC
channel

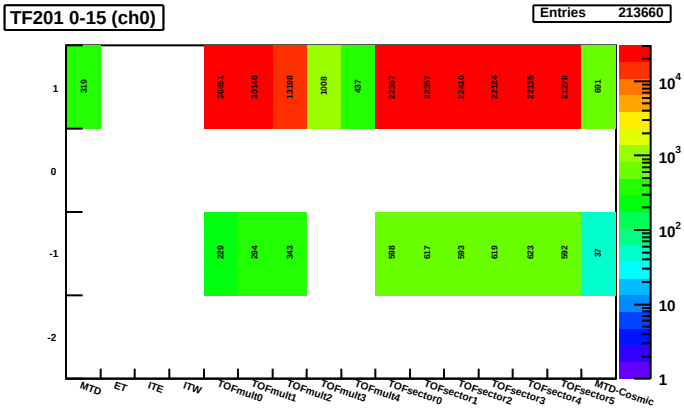

Input to FE004 QT board
Entries 9.123312e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.140414e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

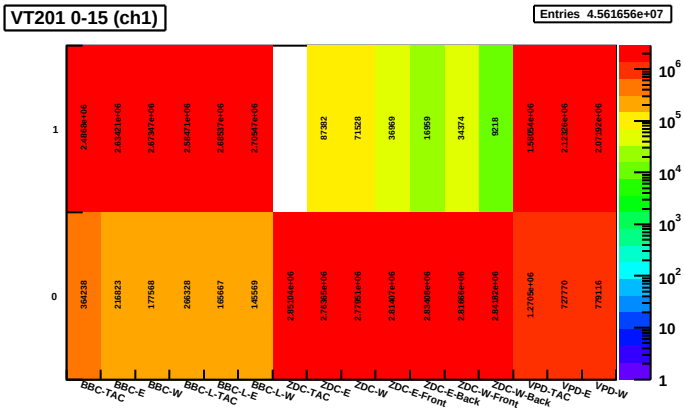
TF201 0-15 (ch0)



TF201 0-15 (ch0)



VT201 0-15 (ch1)



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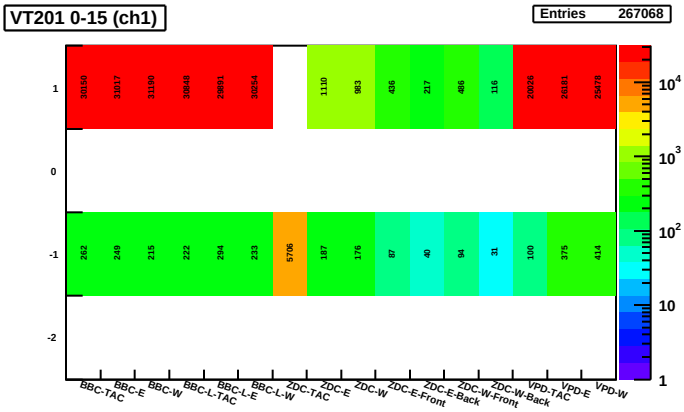

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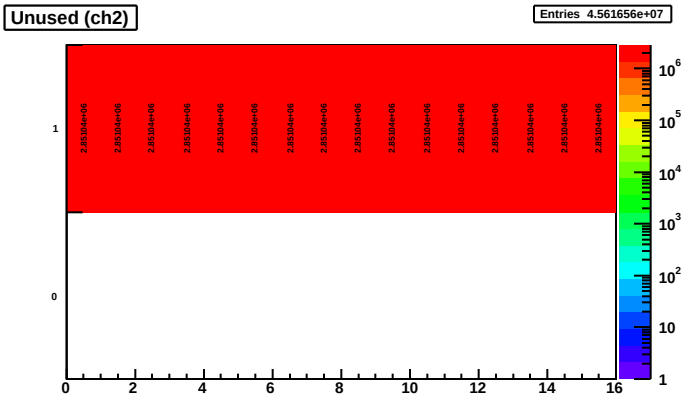

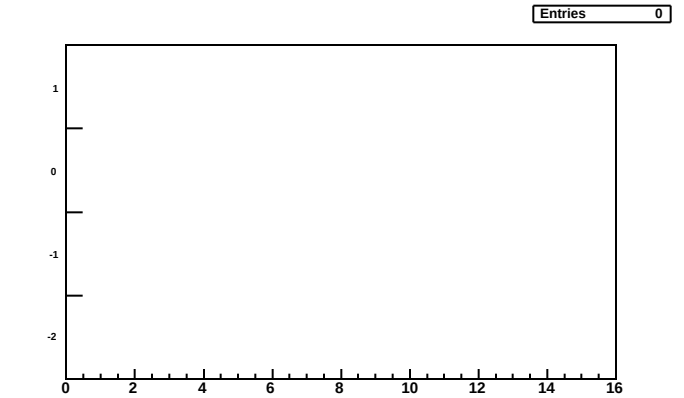

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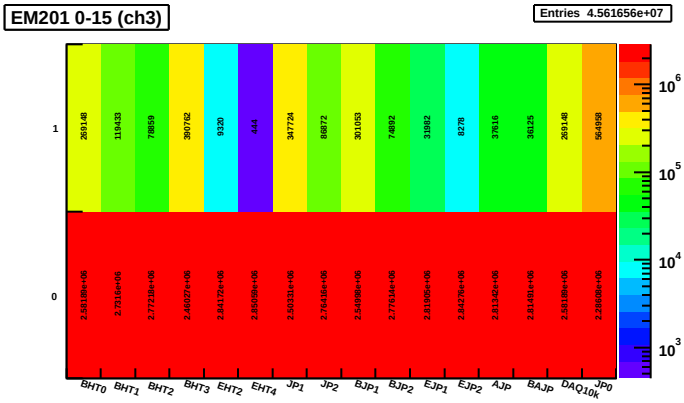

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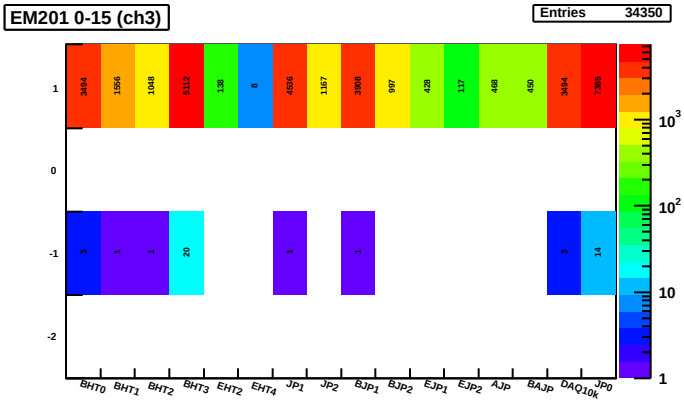
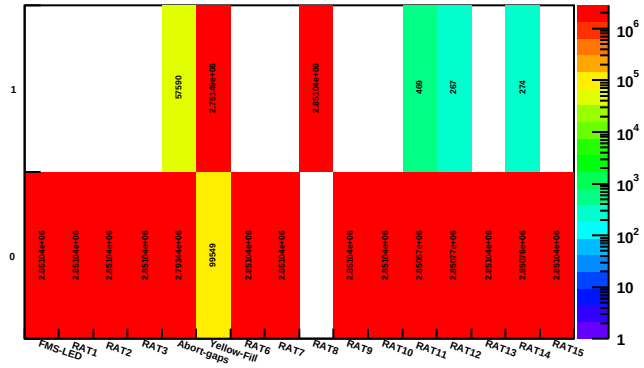
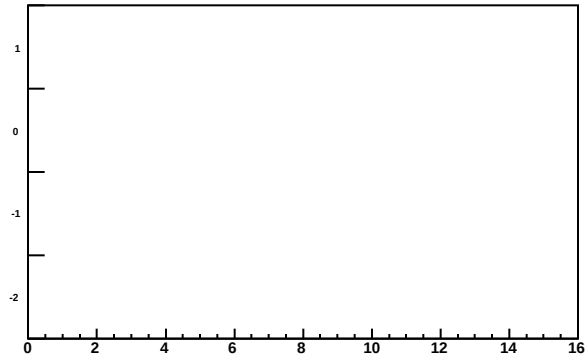
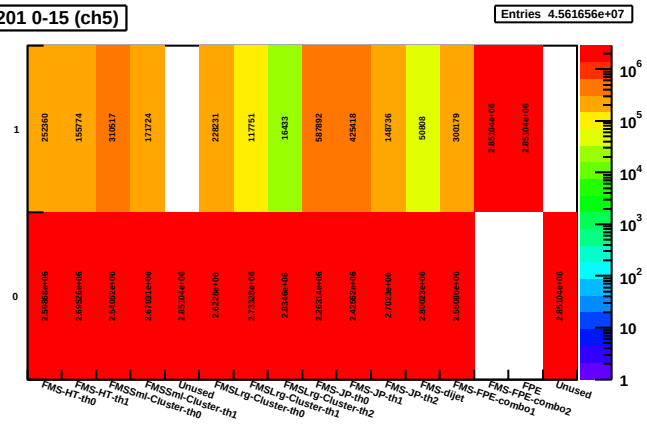


RAT board (ch4)





FP201 0-15 (ch5)



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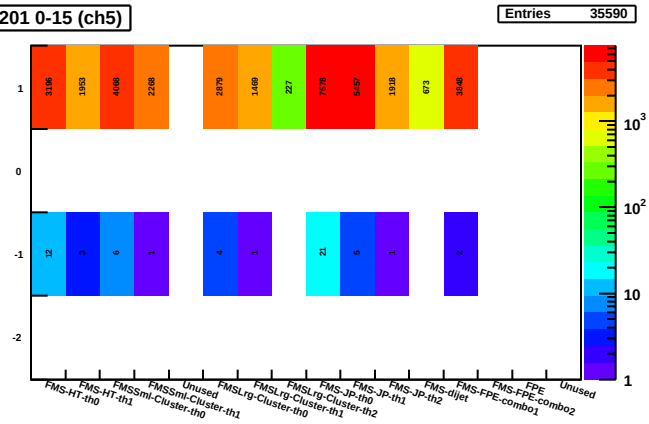

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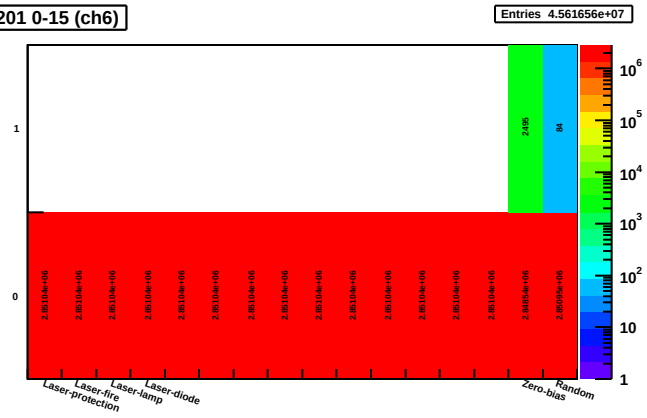

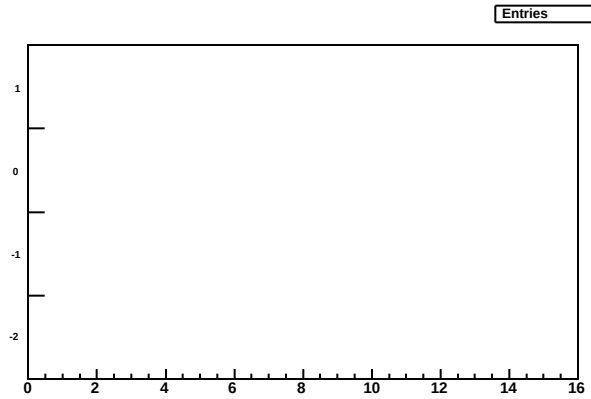
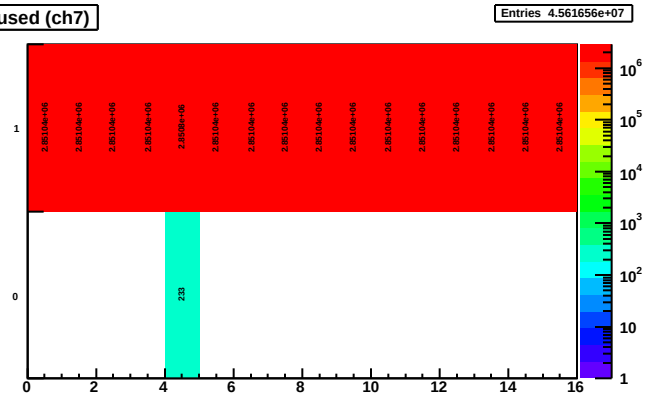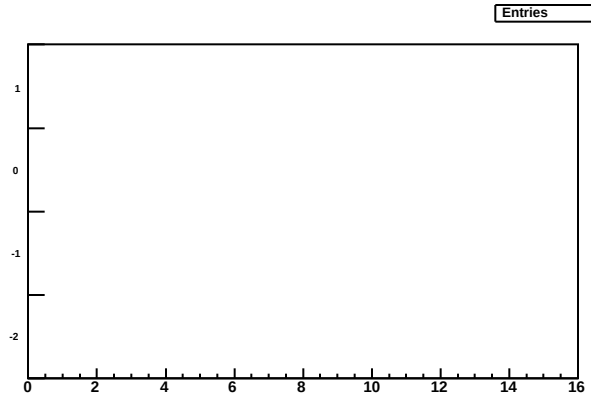
Unused (ch7)