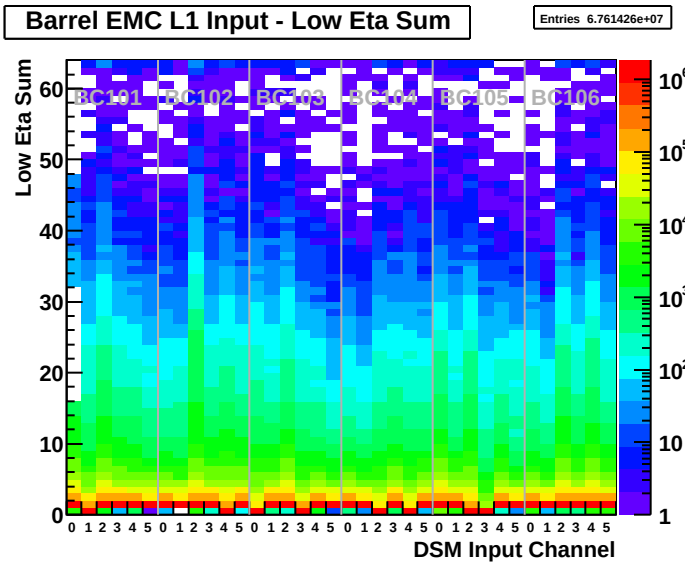
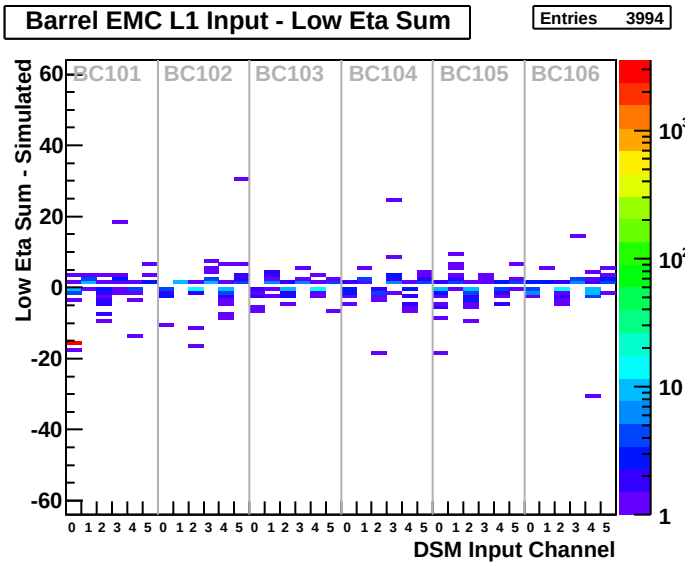
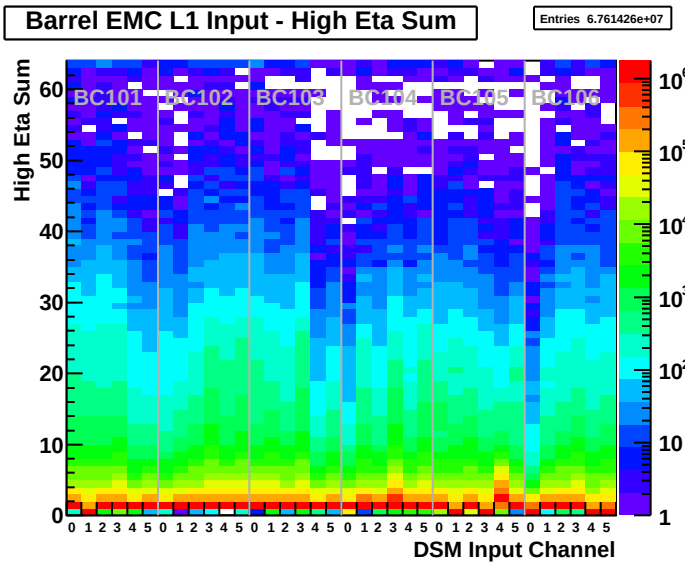
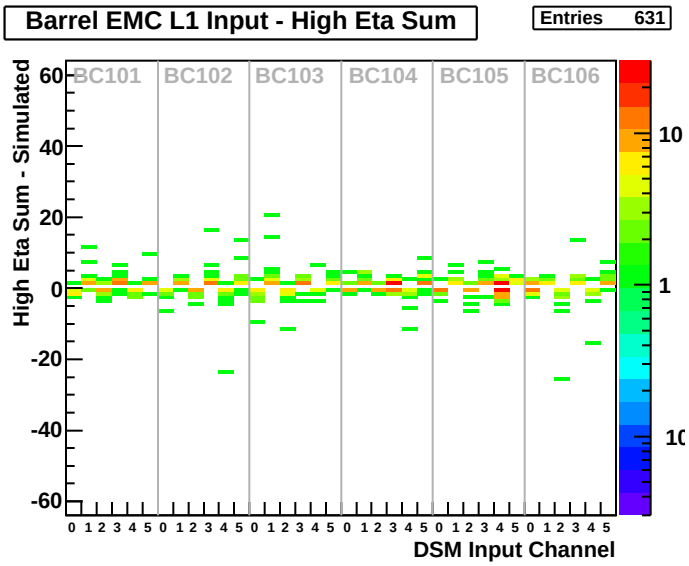
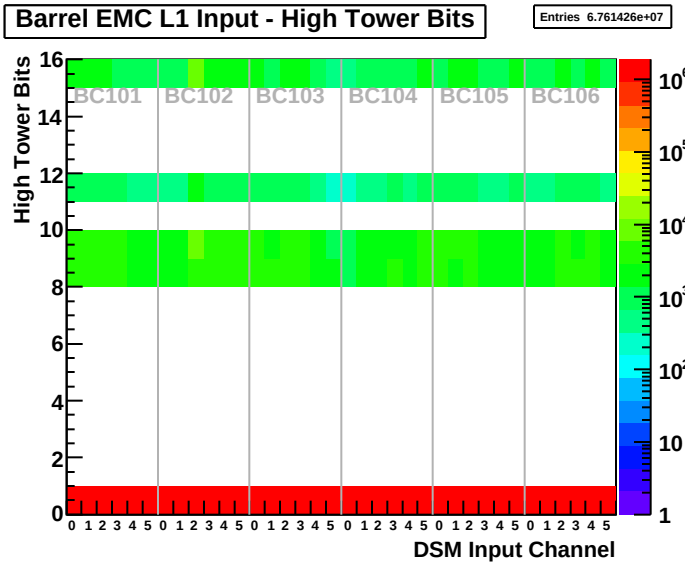
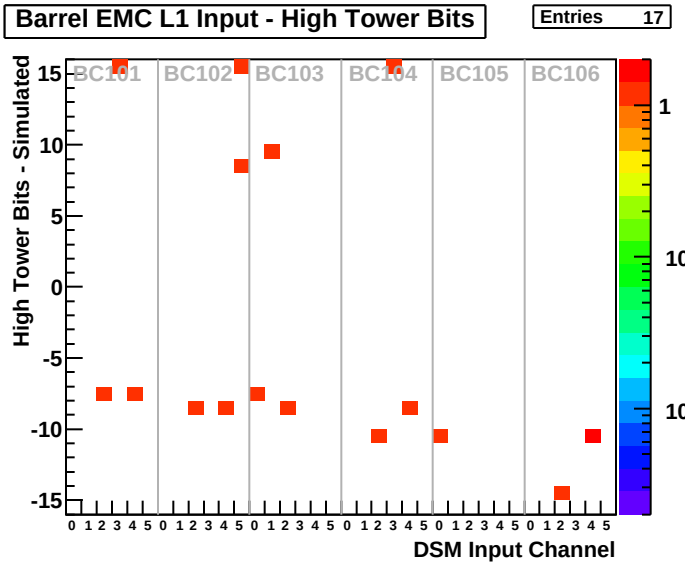

Barrel EMC L1 Input - Low Eta Sum
Entries 6.761426e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

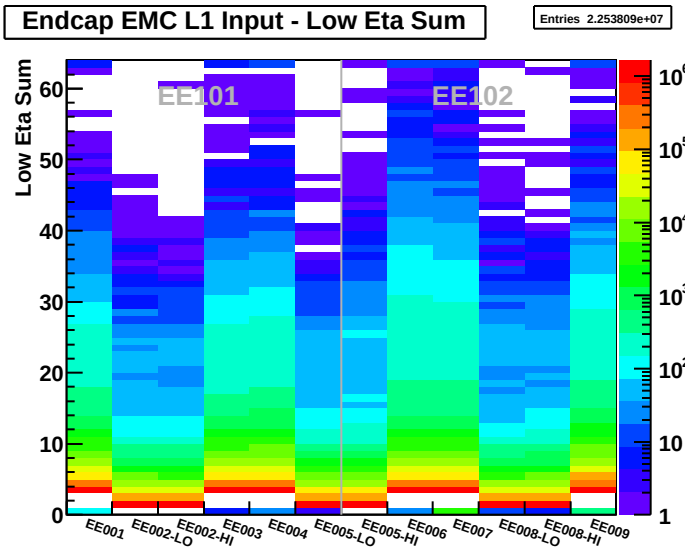

Barrel EMC L1 Input - Low Eta Sum
Entries 3994
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

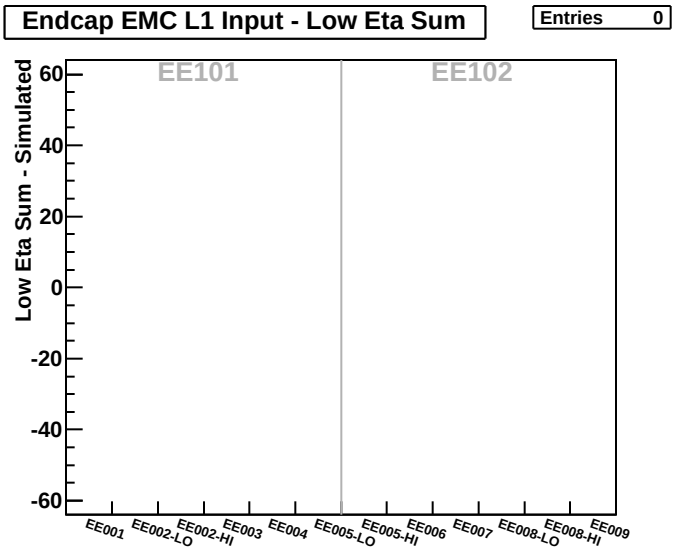

Barrel EMC L1 Input - High Eta Sum
Entries 6.761426e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

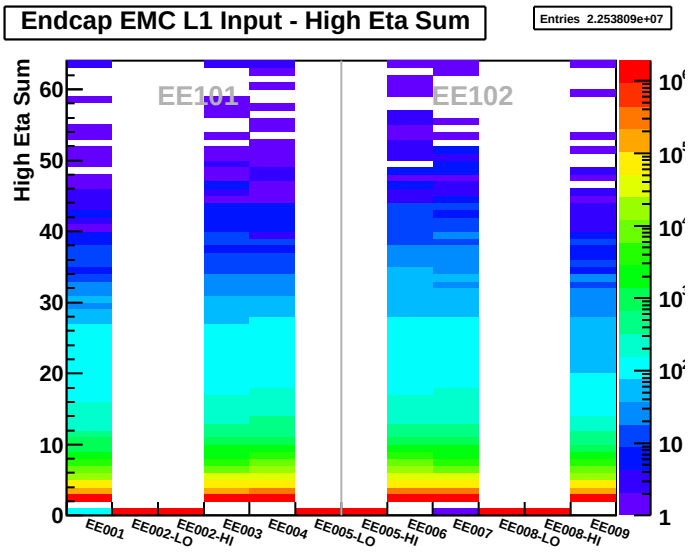

Barrel EMC L1 Input - High Eta Sum
Entries 631
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

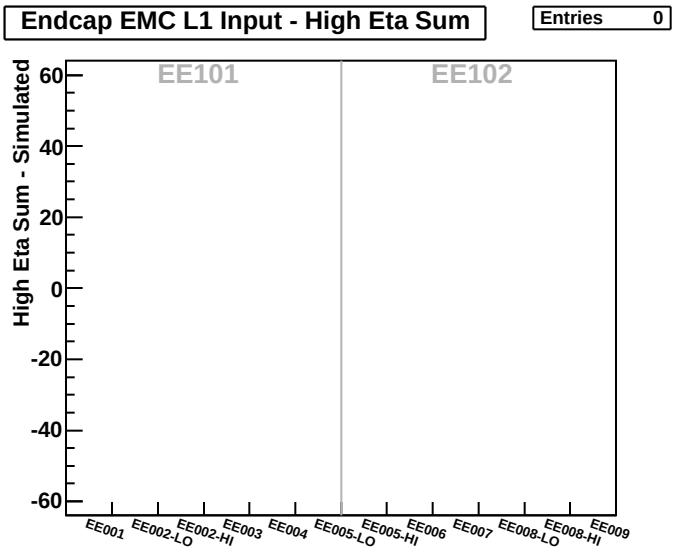

Barrel EMC L1 Input - High Tower Bits
Entries 6.761426e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

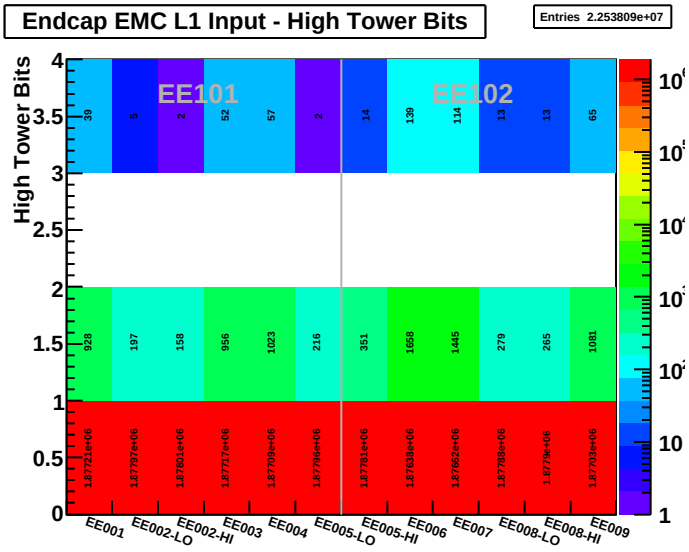

Barrel EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

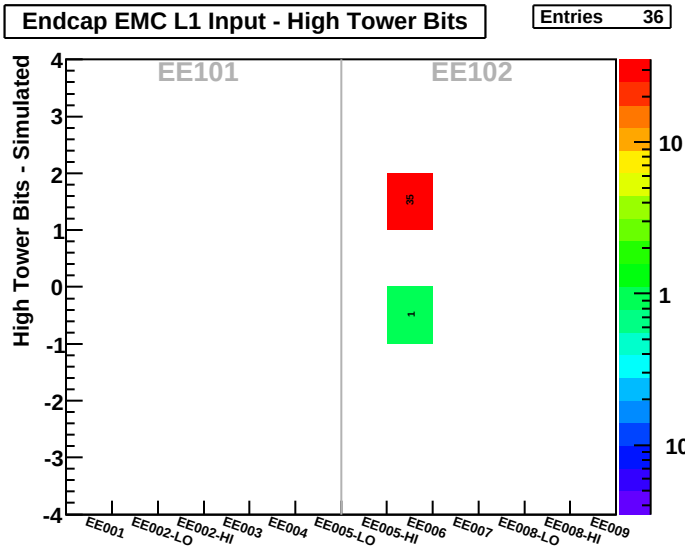

Endcap EMC L1 Input - Low Eta Sum
Entries 2.253809e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

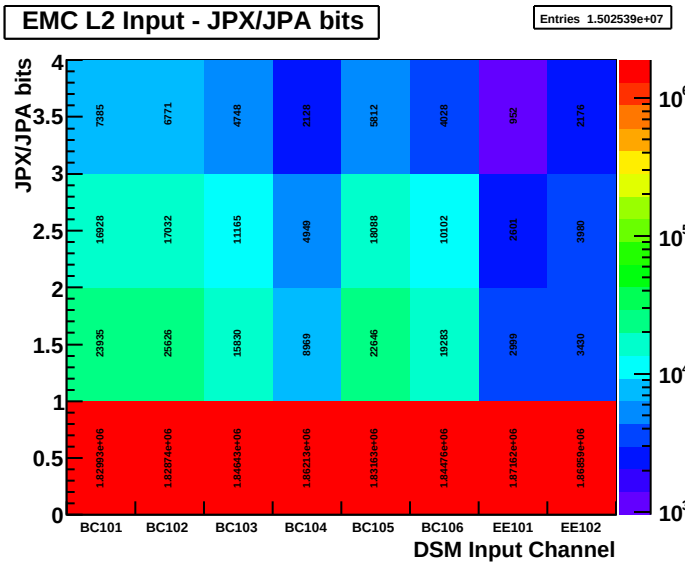

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

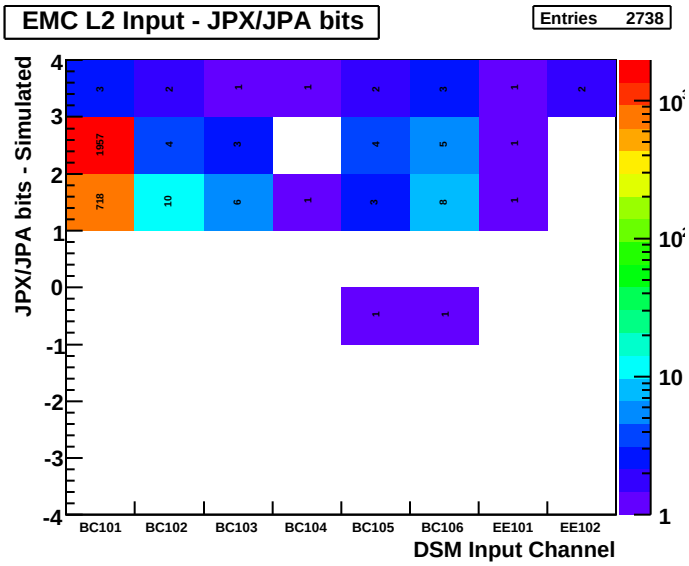

Endcap EMC L1 Input - High Eta Sum
Entries 2.253809e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

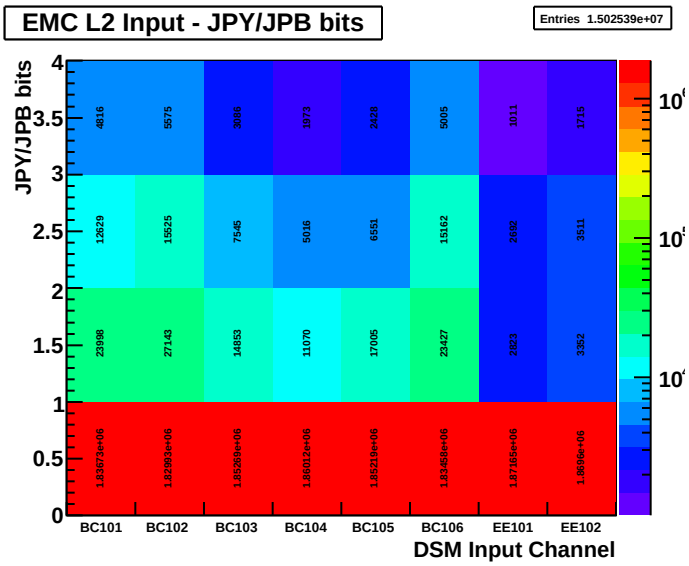

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

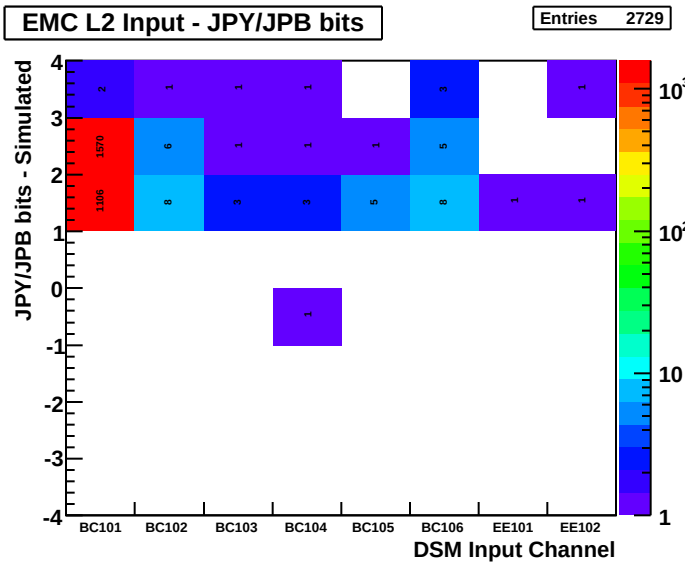

Endcap EMC L1 Input - High Tower Bits
Entries 2.253809e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

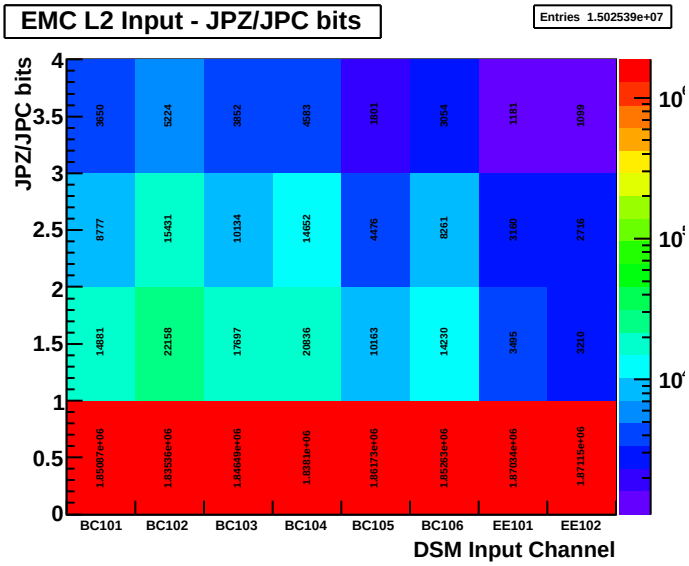

Endcap EMC L1 Input - High Tower Bits
Entries 36
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

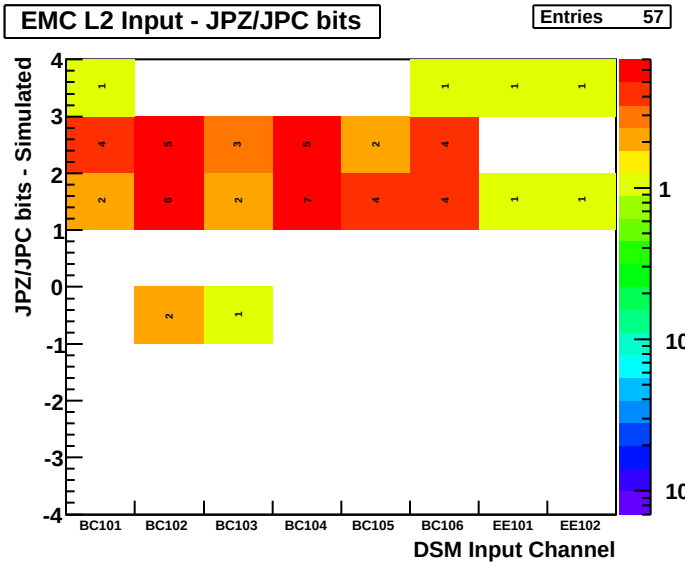

EMC L2 Input - JPX/JPA bits
Entries 1.502539e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

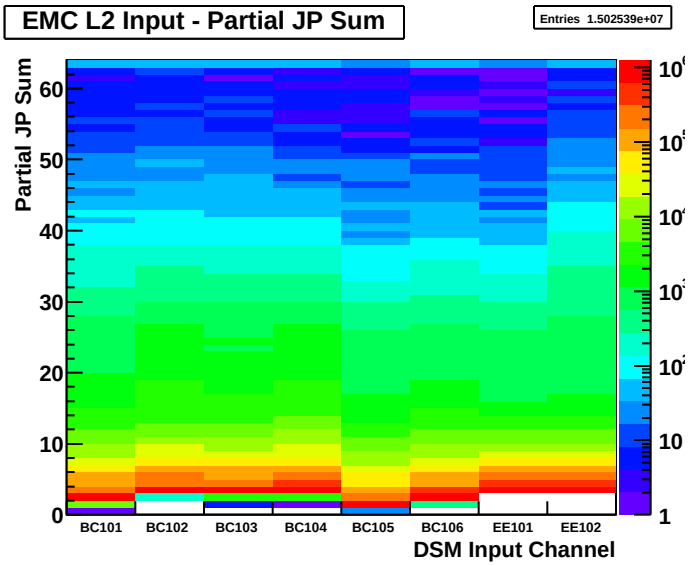

EMC L2 Input - JPX/JPA bits
Entries 2738
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

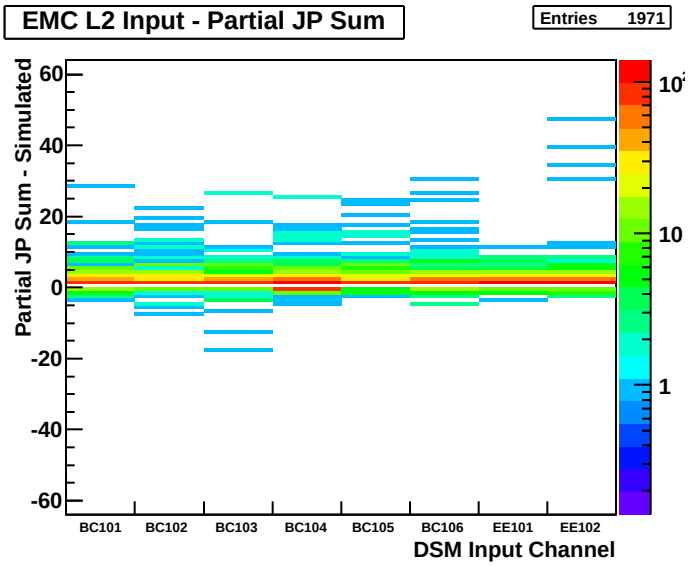

EMC L2 Input - JPY/JPB bits
Entries 1.502539e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

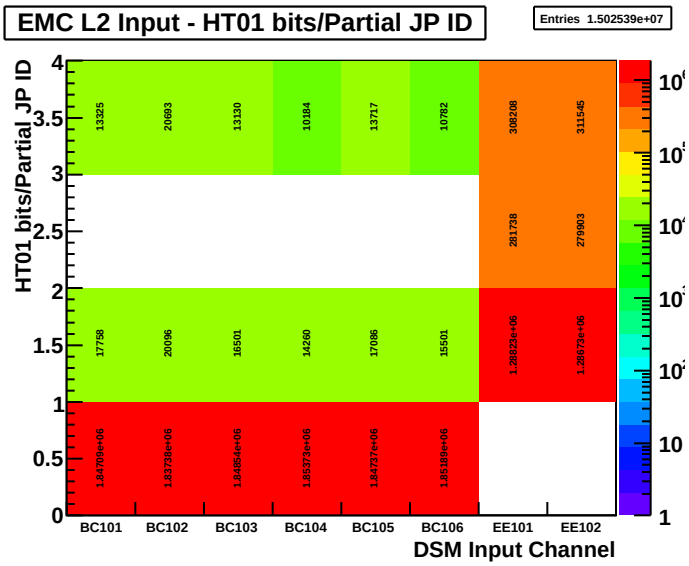

EMC L2 Input - JPY/JPB bits
Entries 2729
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

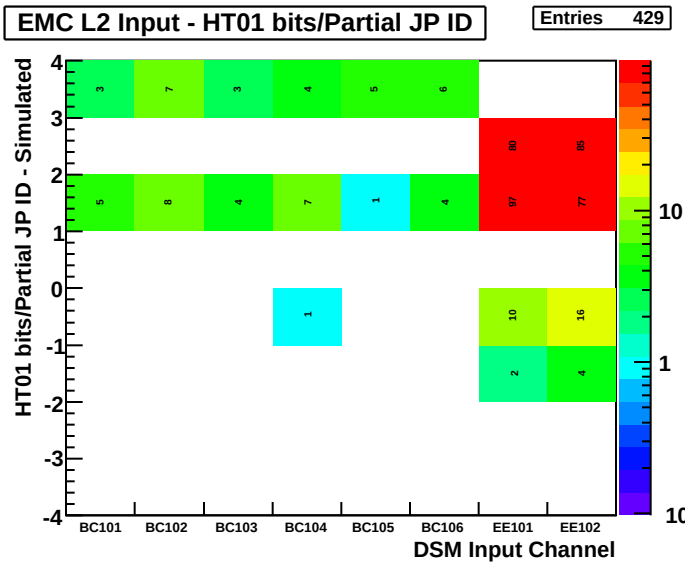

EMC L2 Input - JPZ/JPC bits
Entries 1.502539e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

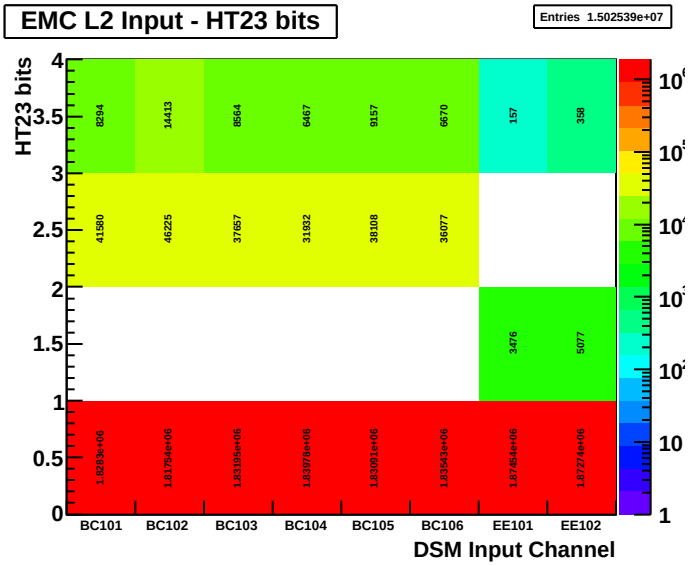

EMC L2 Input - JPZ/JPC bits
Entries 57
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

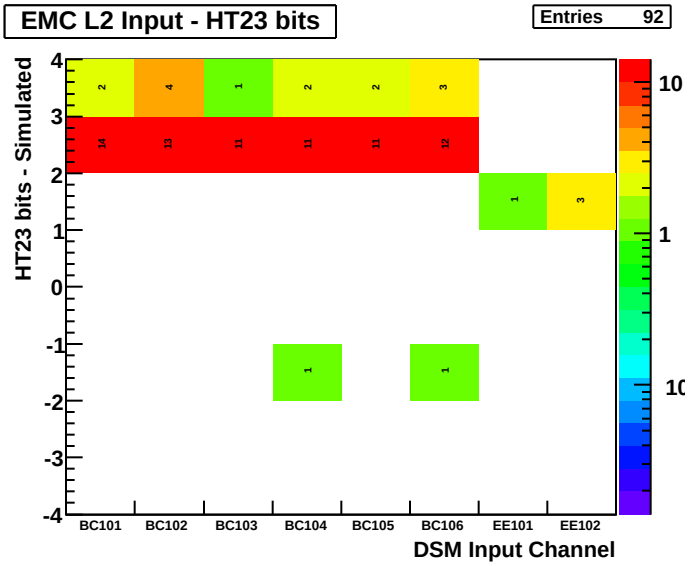

EMC L2 Input - Partial JP Sum
Entries 1.502539e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

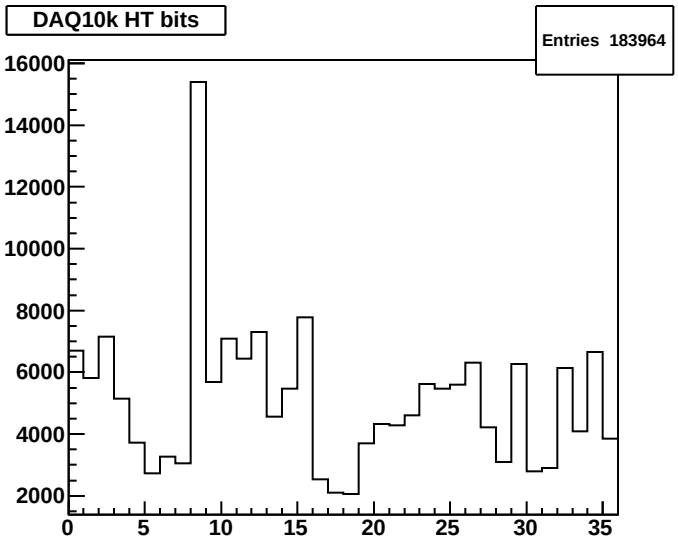

EMC L2 Input - Partial JP Sum
Entries 1971
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

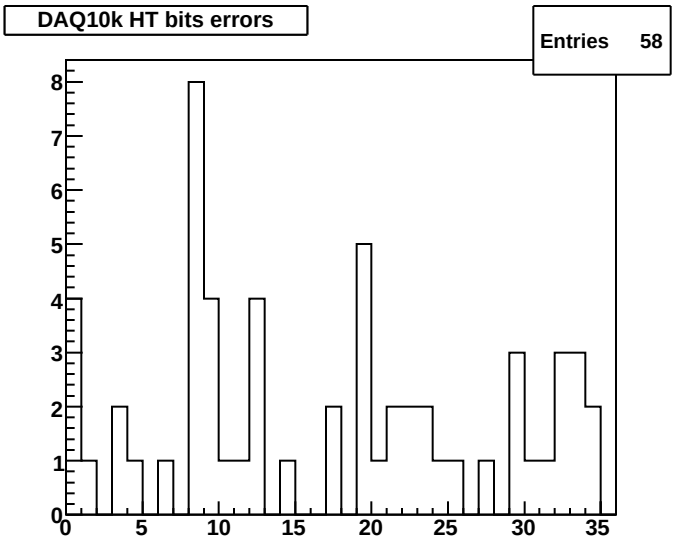

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.502539e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

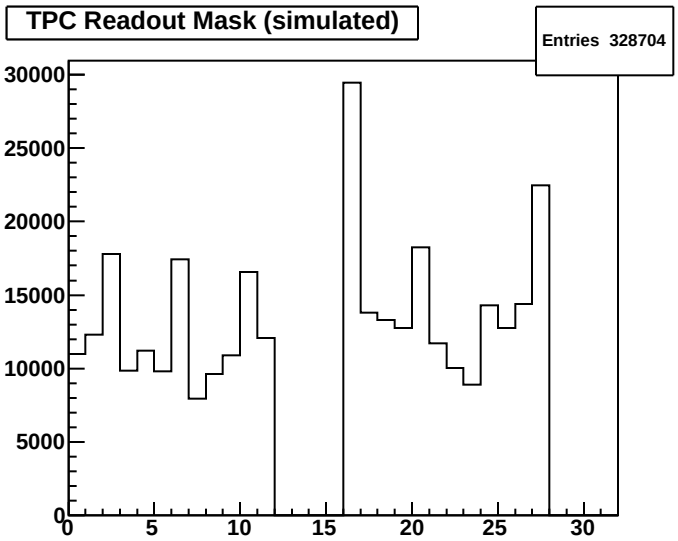

EMC L2 Input - HT01 bits/Partial JP ID
Entries 429
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

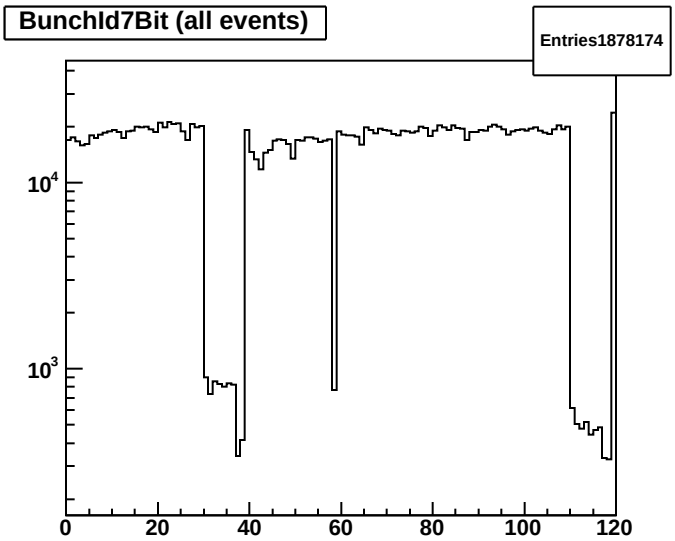

EMC L2 Input - HT23 bits
Entries 1.502539e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

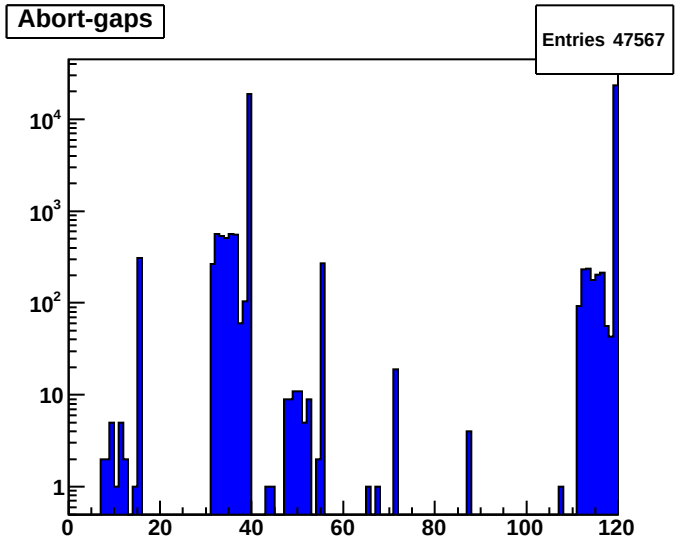

EMC L2 Input - HT23 bits
Entries 92
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

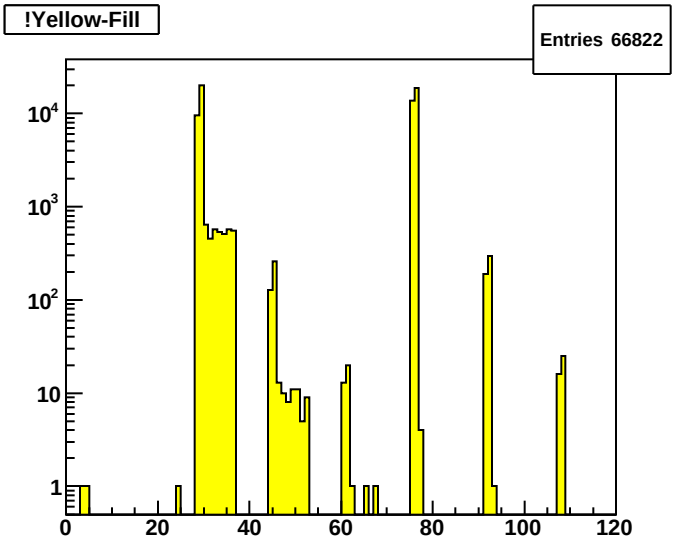

DAQ10k HT bits
Entries 183964

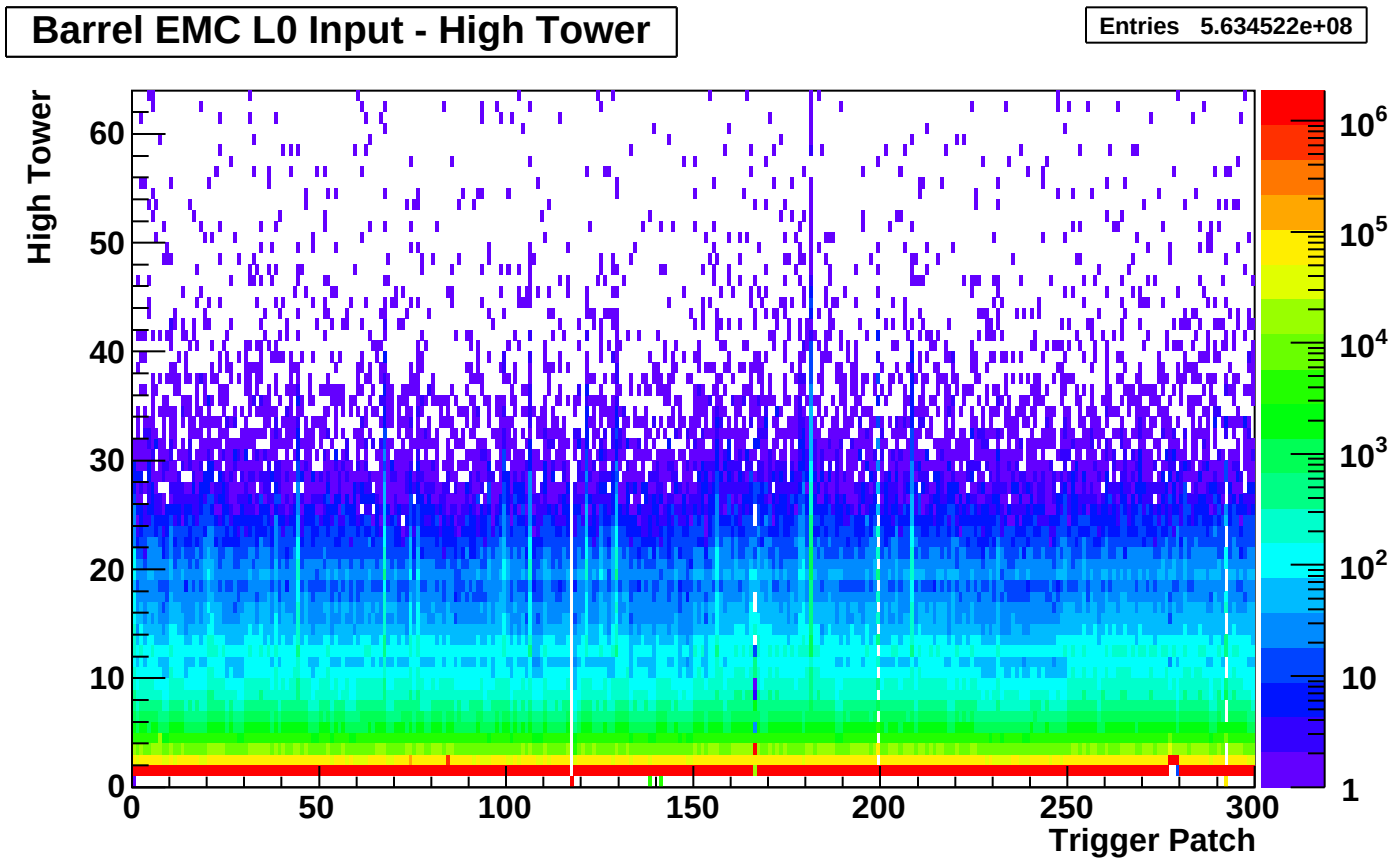

DAQ10k HT bits errors
Entries 58

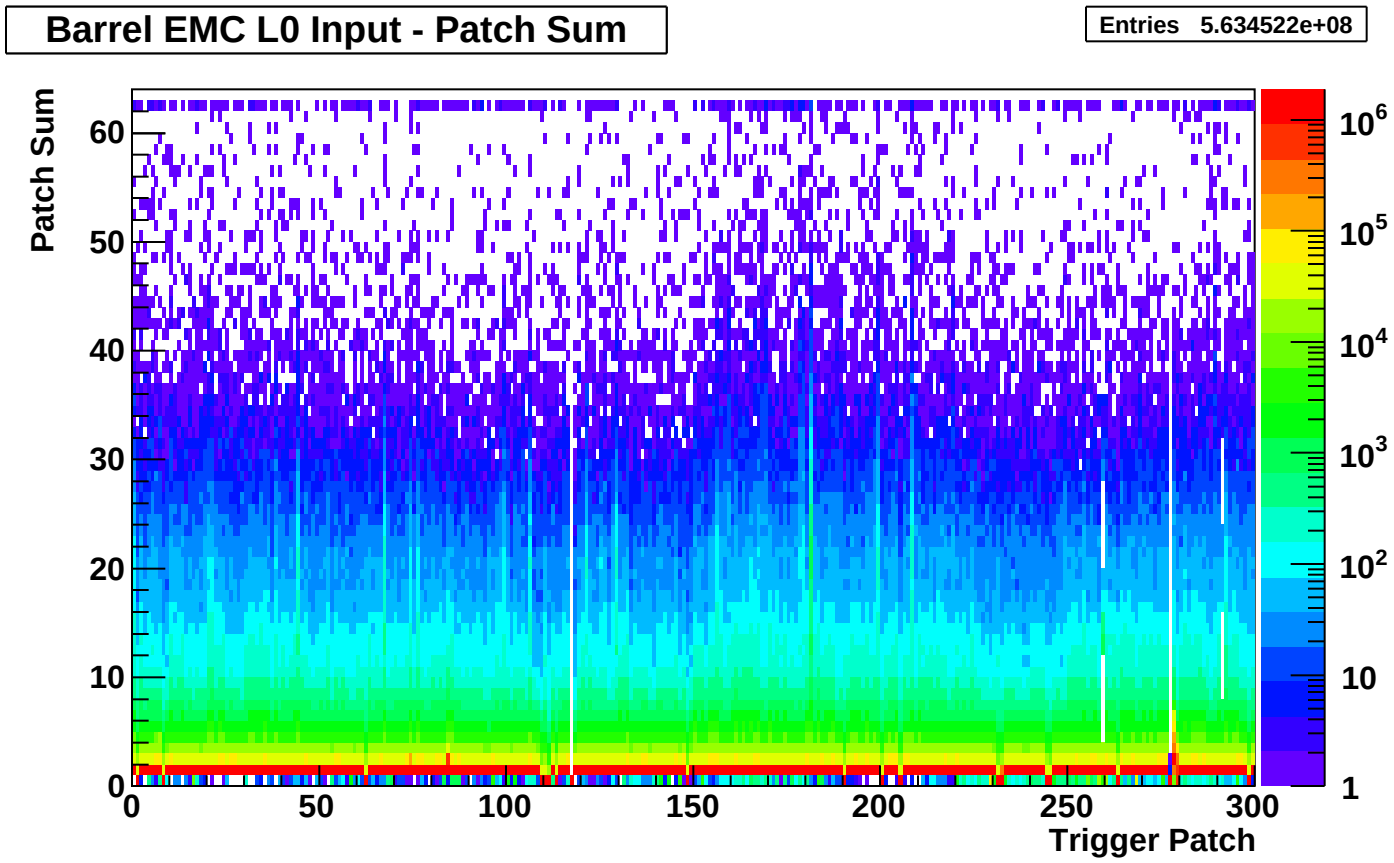

TPC Readout Mask (simulated)
Entries 328704

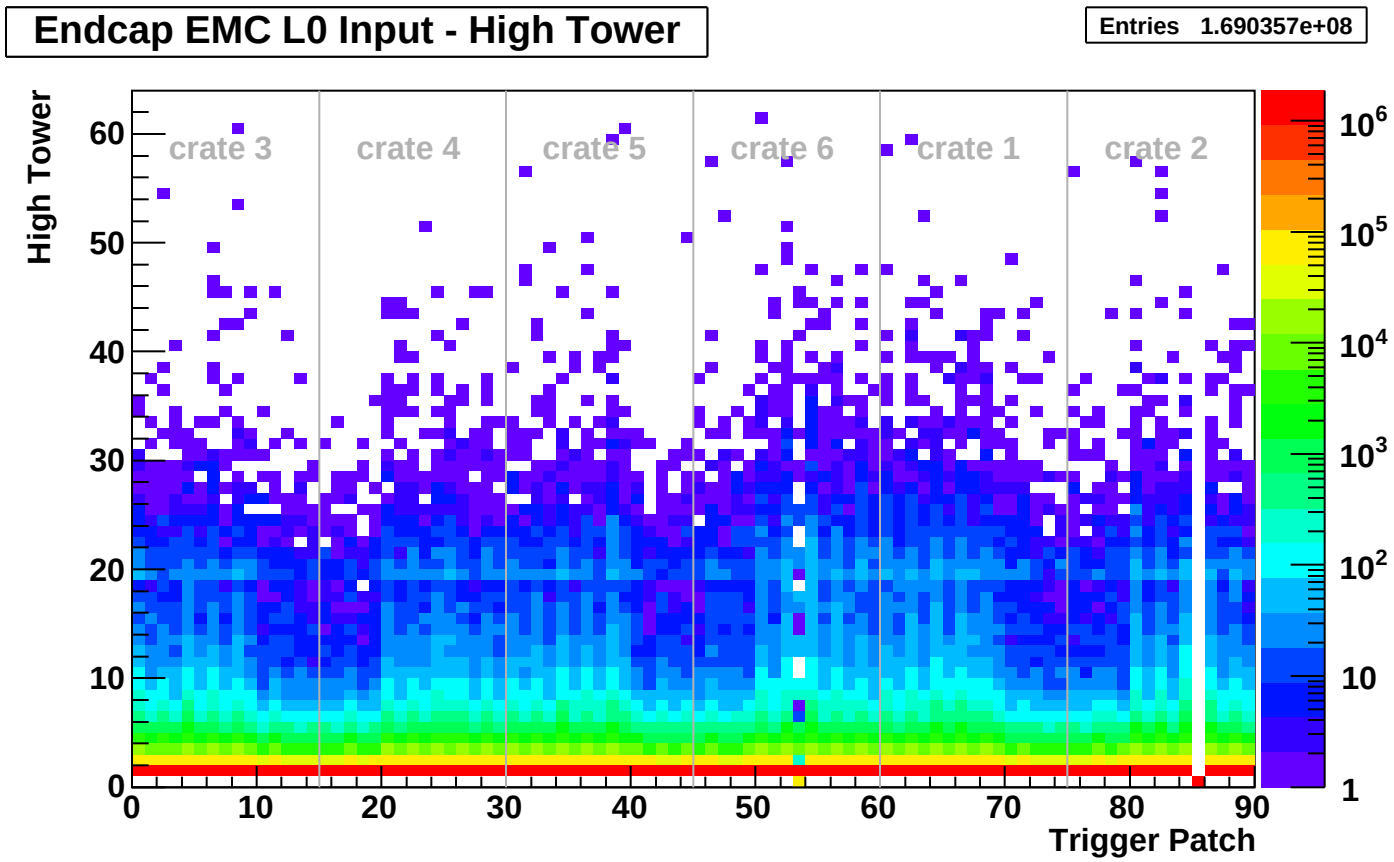

BunchId7Bit (all events)
Entries1878174

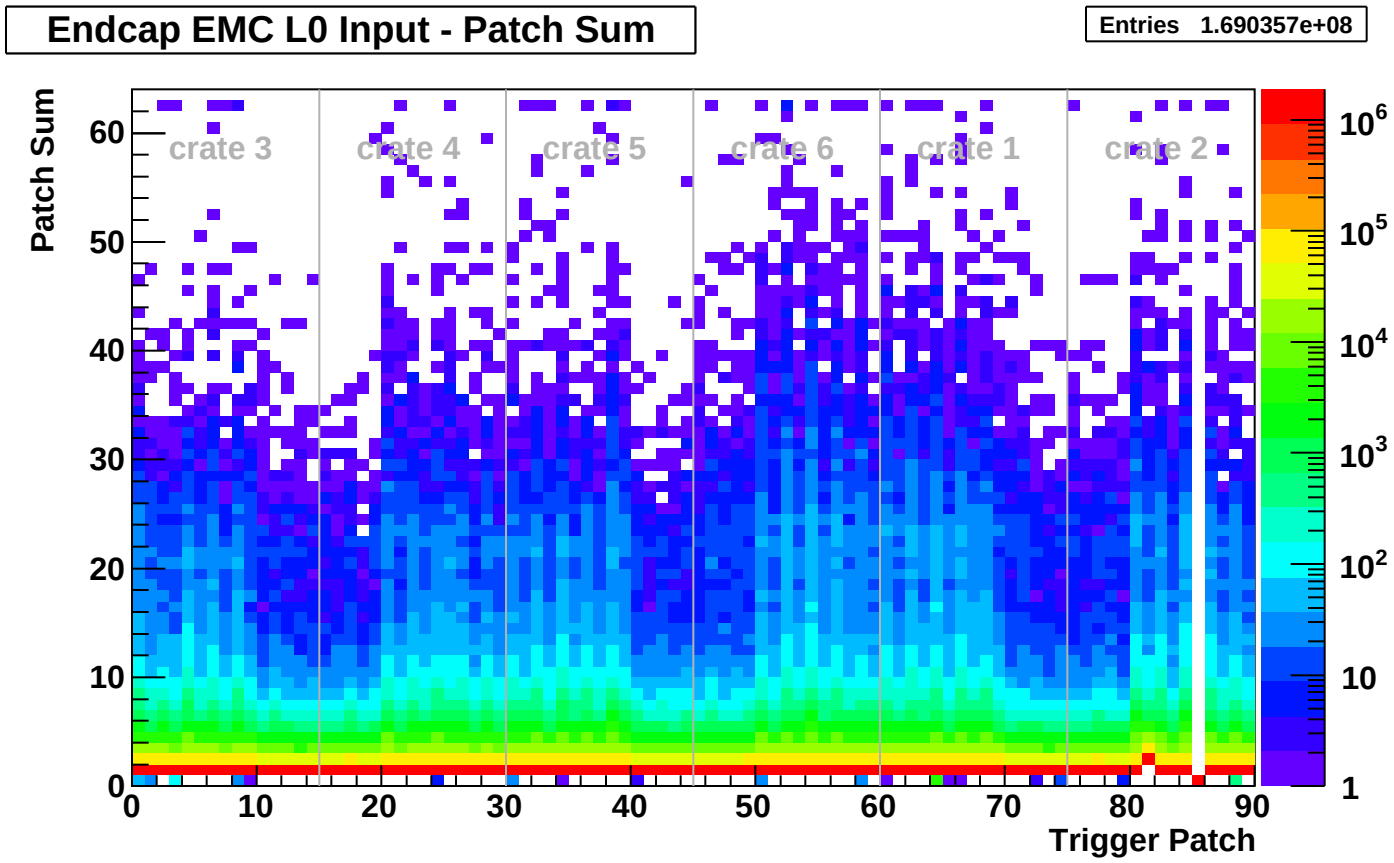

Abort-gaps
Entries 47567

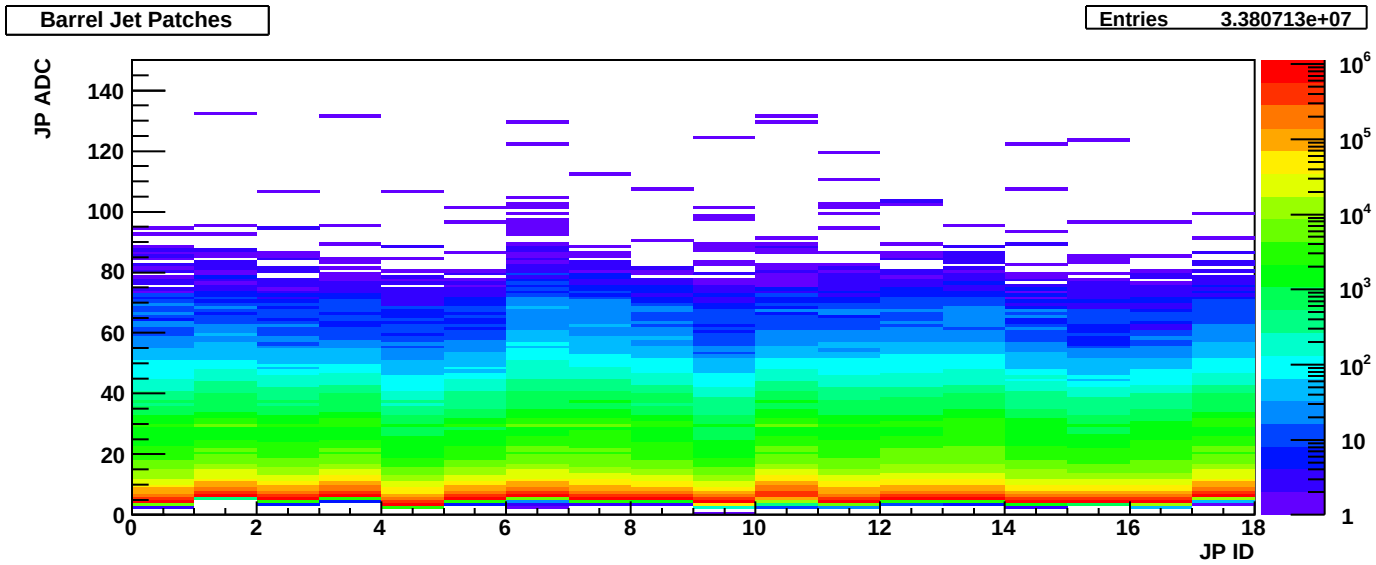

!Yellow-Fill
Entries 66822

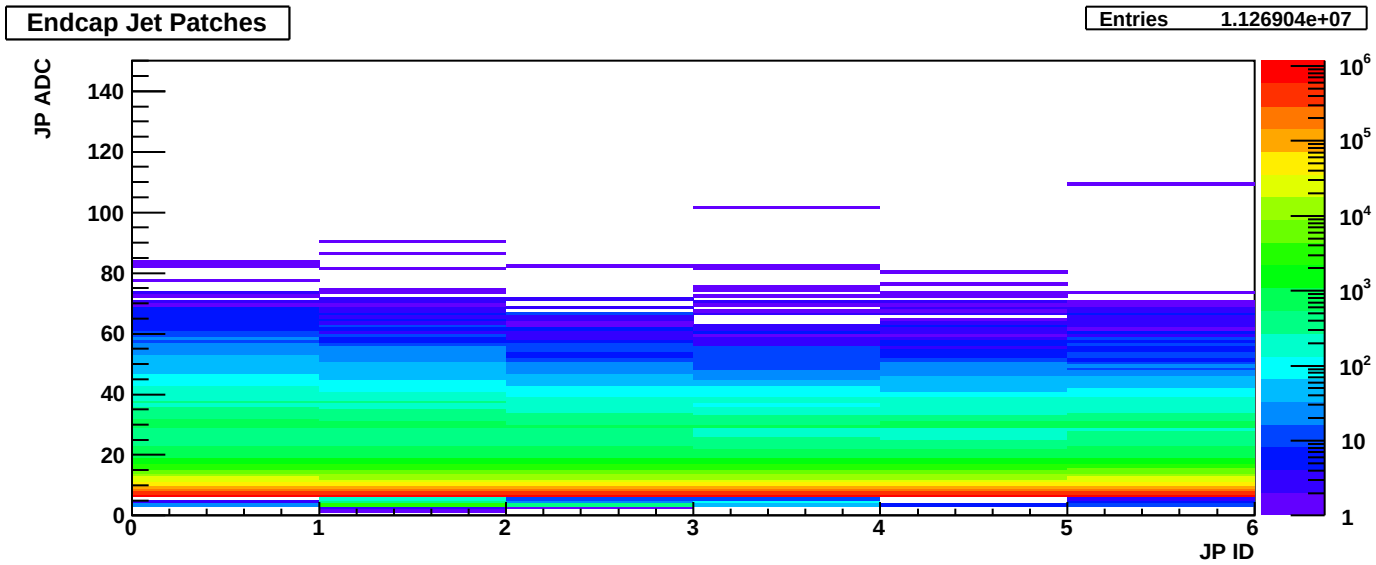

Barrel EMC L0 Input - High Tower
Entries 5.634522e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

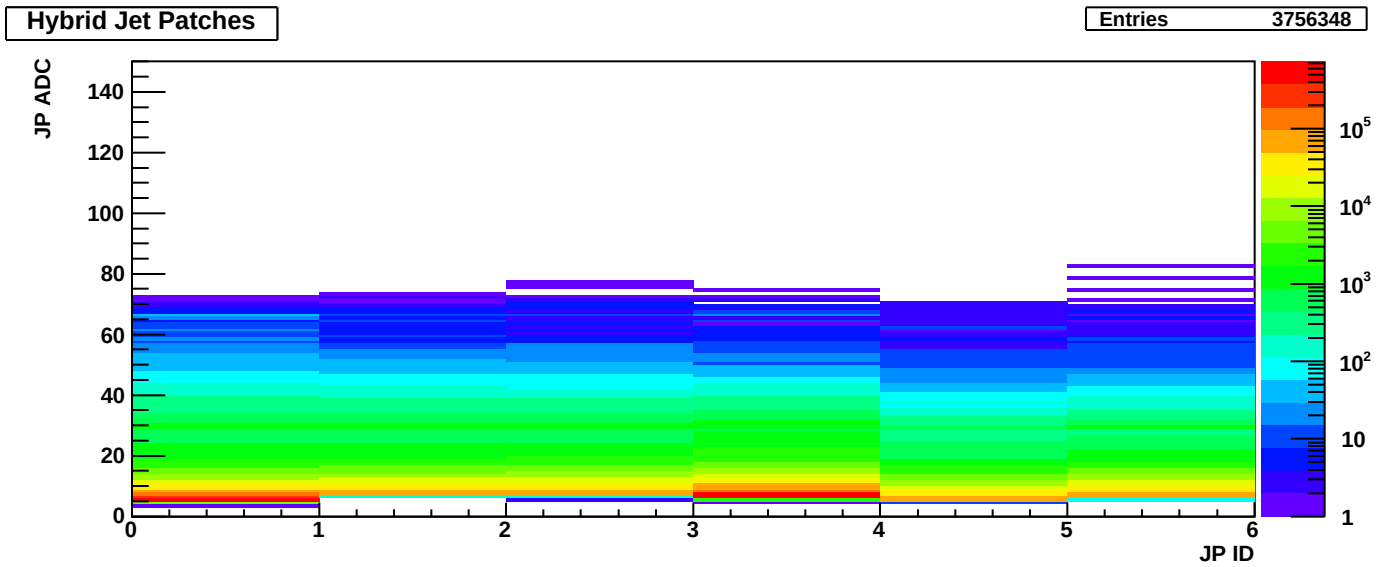

Barrel EMC L0 Input - Patch Sum
Entries 5.634522e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

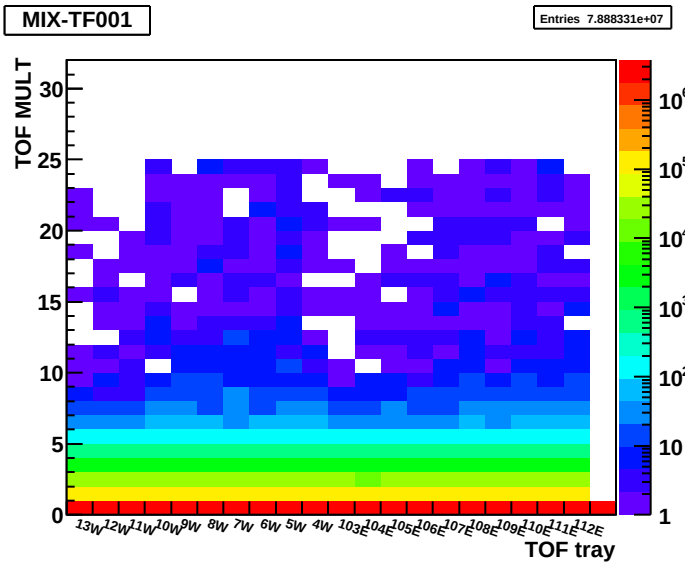

Endcap EMC L0 Input - High Tower
Entries 1.690357e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

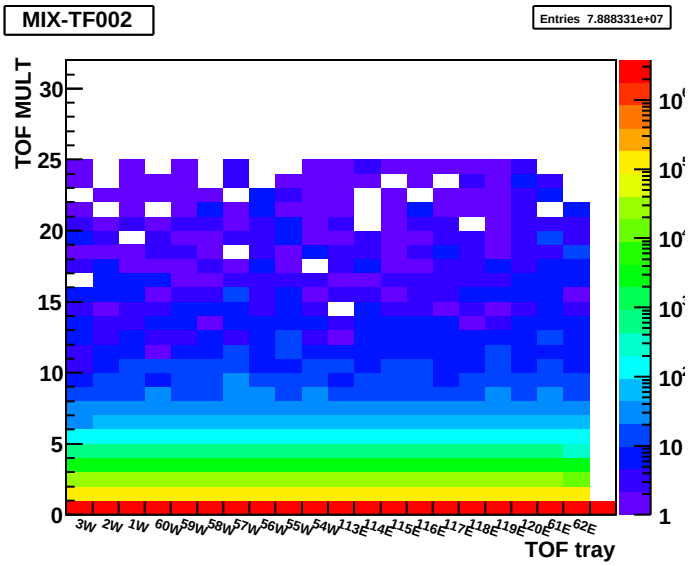

Endcap EMC L0 Input - Patch Sum
Entries 1.690357e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

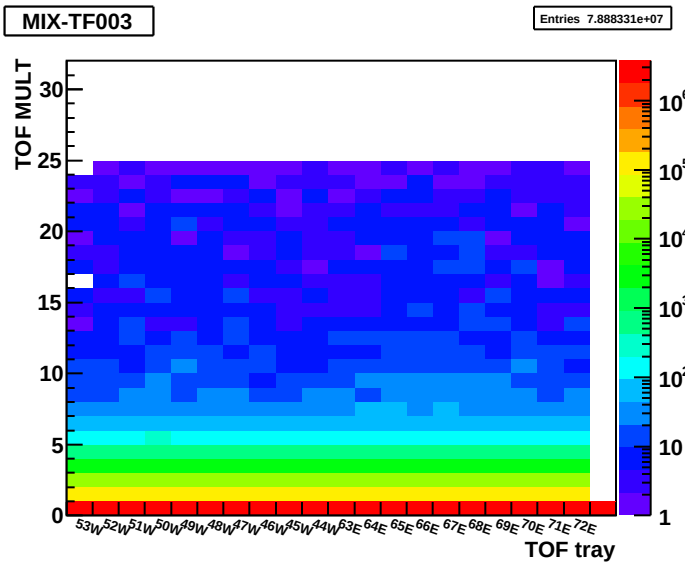

Barrel Jet Patches
Entries 3.380713e+07
JP ADC
JP ID

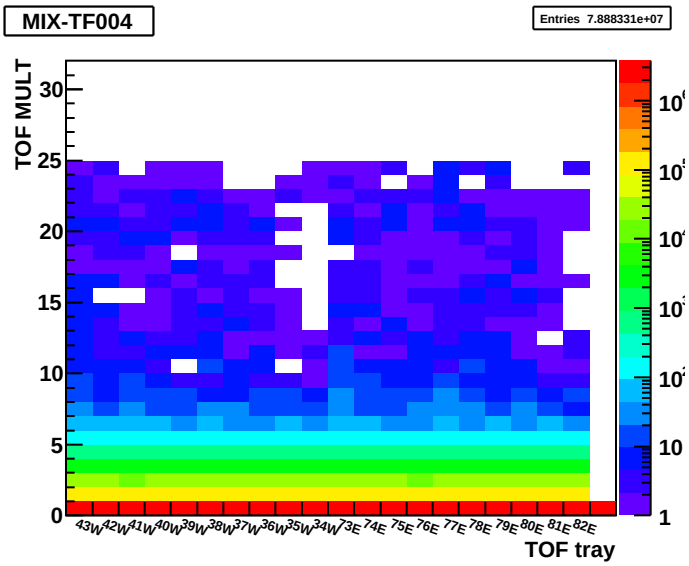

Endcap Jet Patches
Entries 1.126904e+07
JP ADC
JP ID

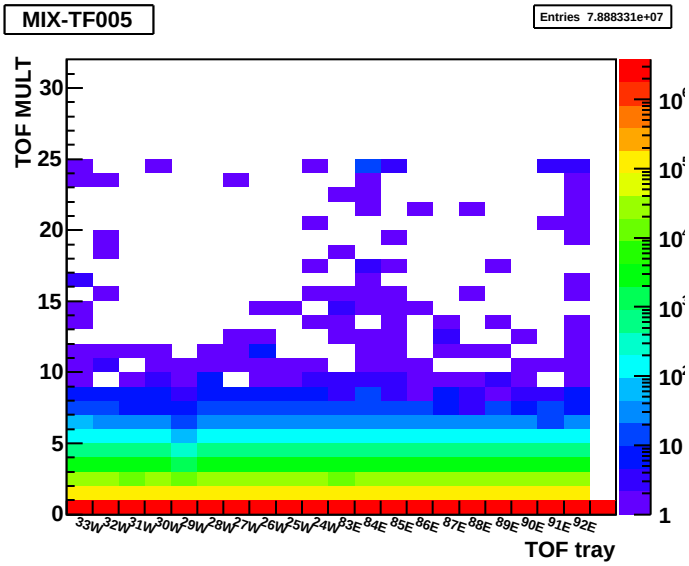

Hybrid Jet Patches
Entries 3756348
JP ADC
JP ID

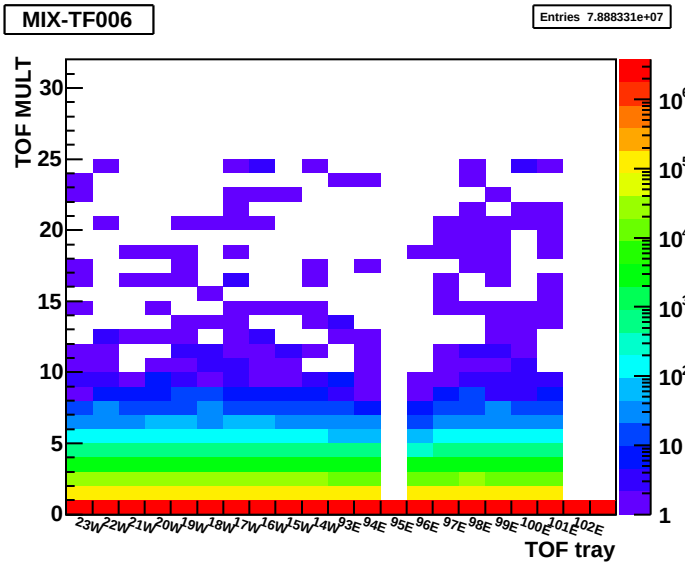

MIX-TF001
Entries 7.888331e+07
TOF MULT
TOF tray

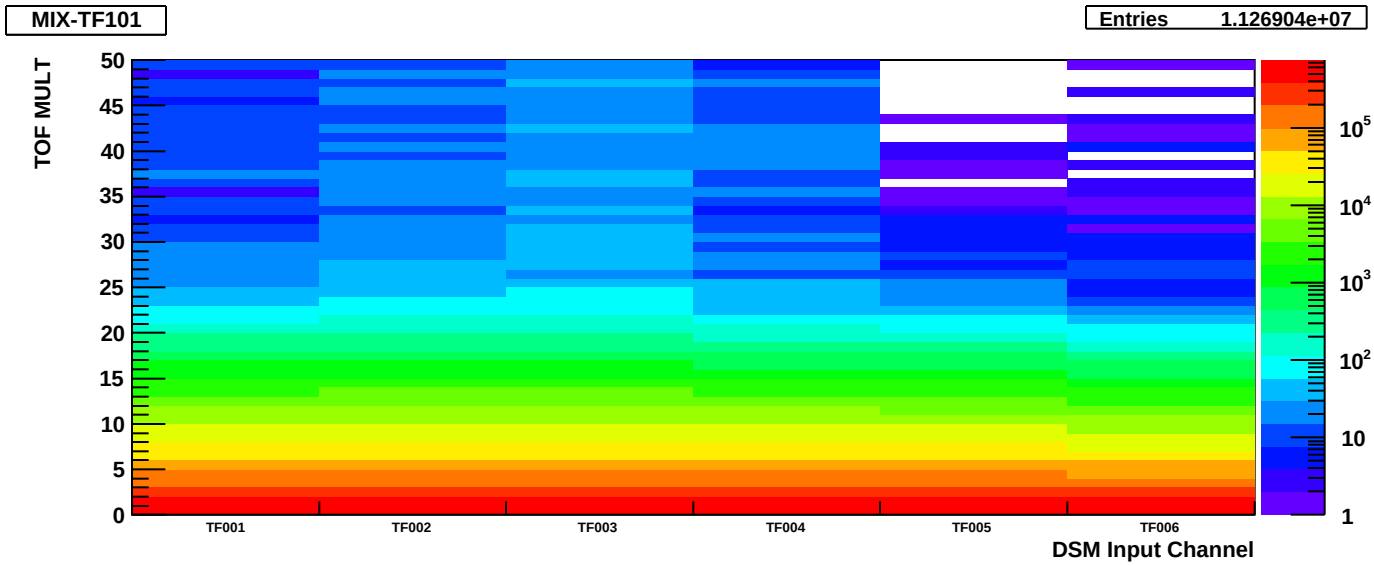

MIX-TF002
Entries 7.888331e+07
TOF MULT
TOF tray

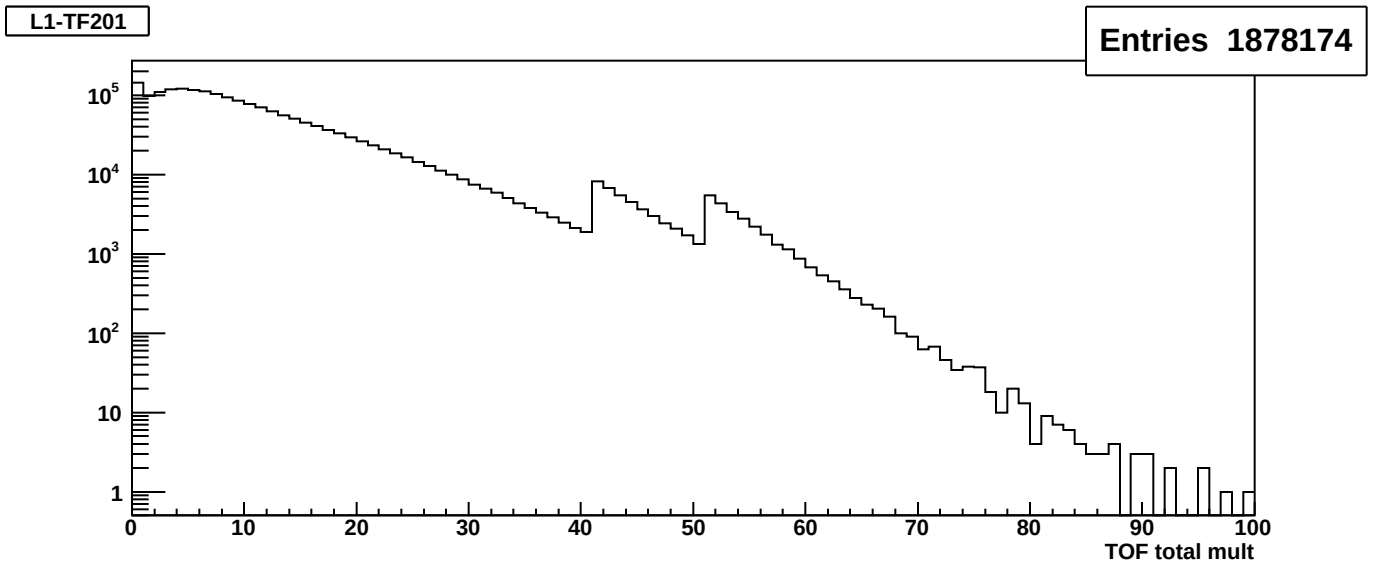

MIX-TF003
Entries 7.888331e+07
TOF MULT
TOF tray

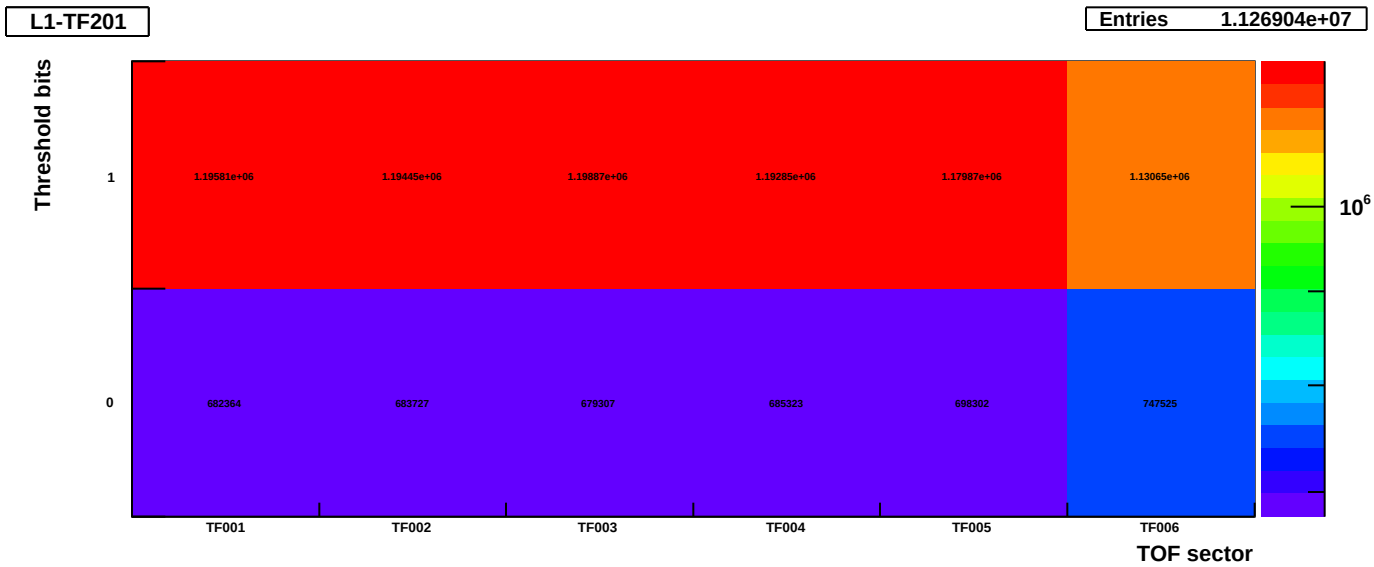

MIX-TF004
Entries 7.888331e+07
TOF MULT
TOF tray

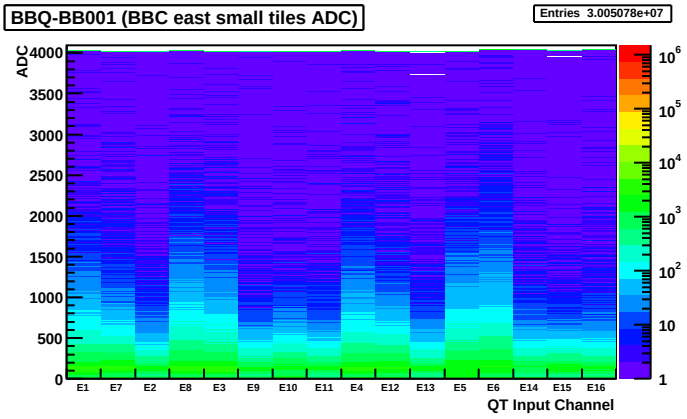

MIX-TF005
Entries 7.888331e+07
TOF MULT
TOF tray

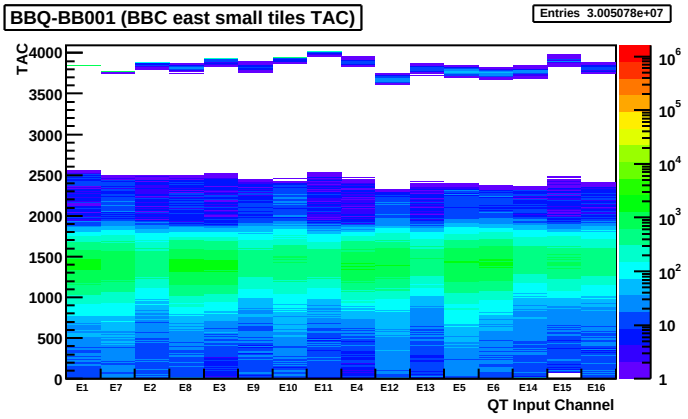

MIX-TF006
Entries 7.888331e+07
TOF MULT
TOF tray

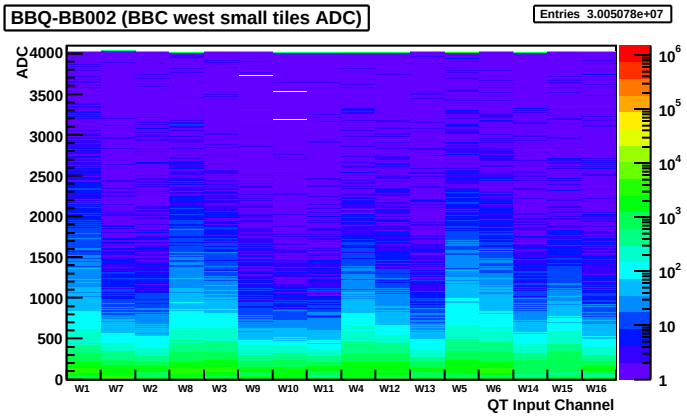

MIX-TF101
Entries 1.126904e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

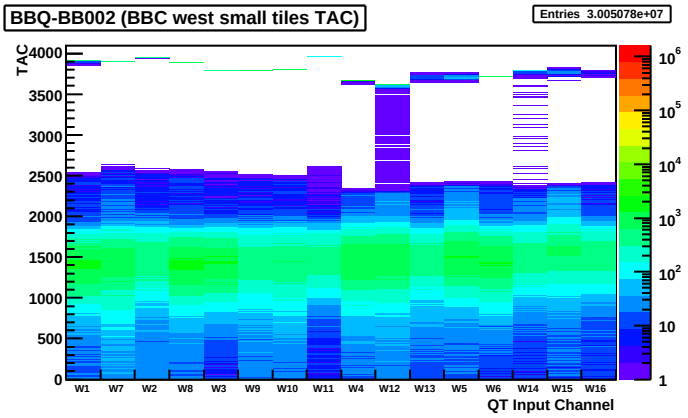

L1-TF201
Entries 1878174
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

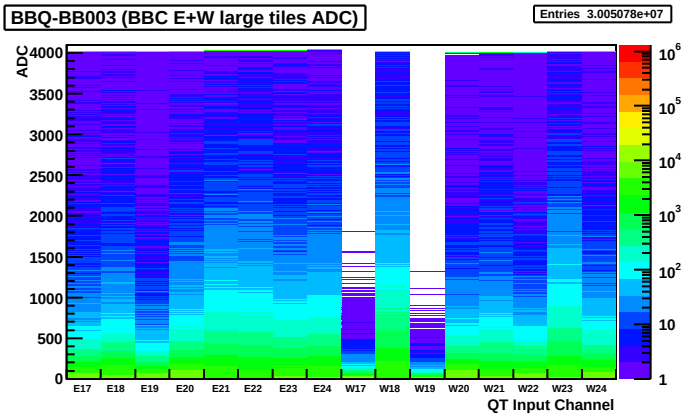

L1-TF201
Entries 1.126904e+07
Threshold bits
1
0
1.19581e+06
1.19445e+06
1.19887e+06
1.19285e+06
1.17987e+06
1.13065e+06
682364
683727
679307
685323
698302
747525
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

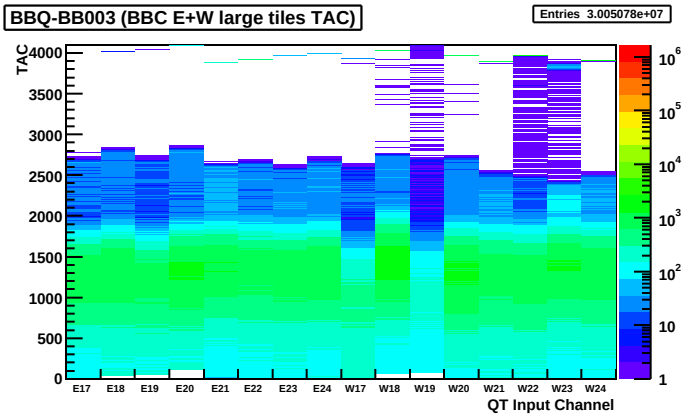

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.005078e+07
ADC
QT Input Channel

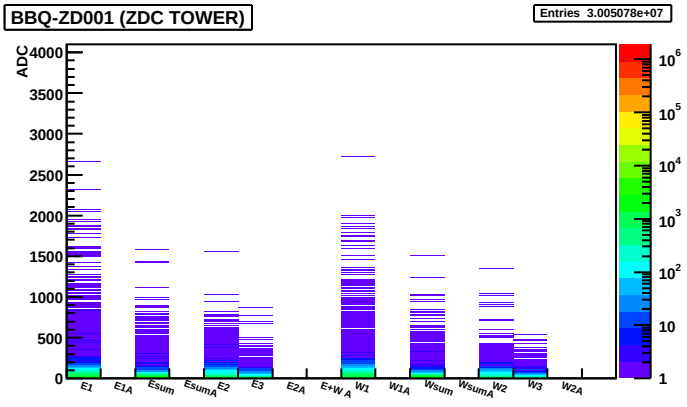

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.005078e+07
TAC
QT Input Channel

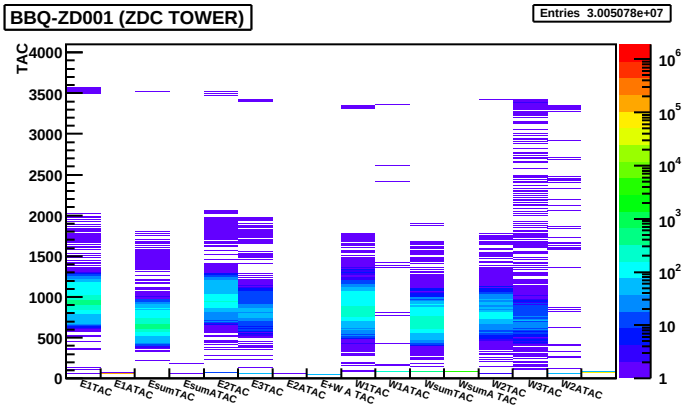

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.005078e+07
ADC
QT Input Channel

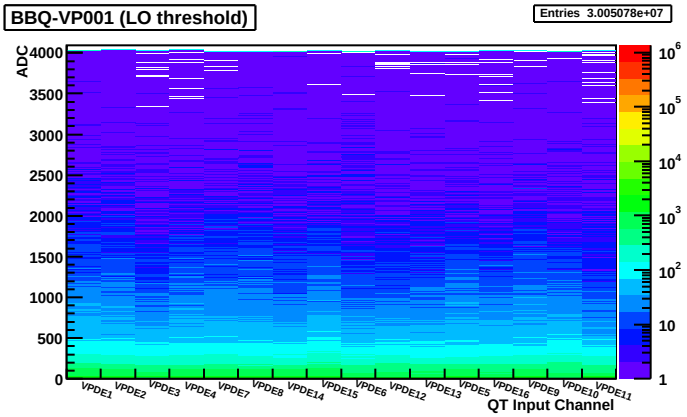

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.005078e+07
TAC
QT Input Channel

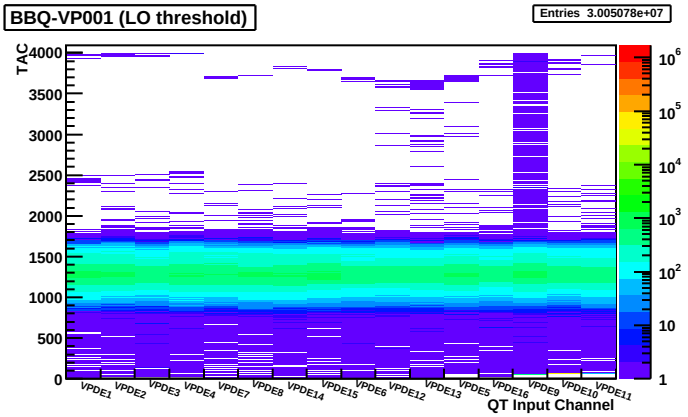

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.005078e+07
ADC
QT Input Channel

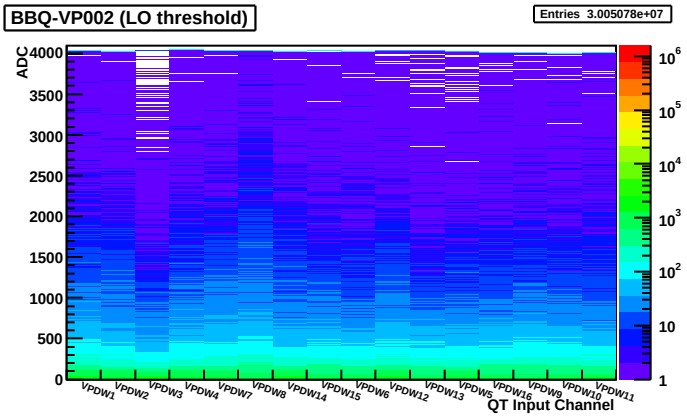

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.005078e+07
TAC
QT Input Channel

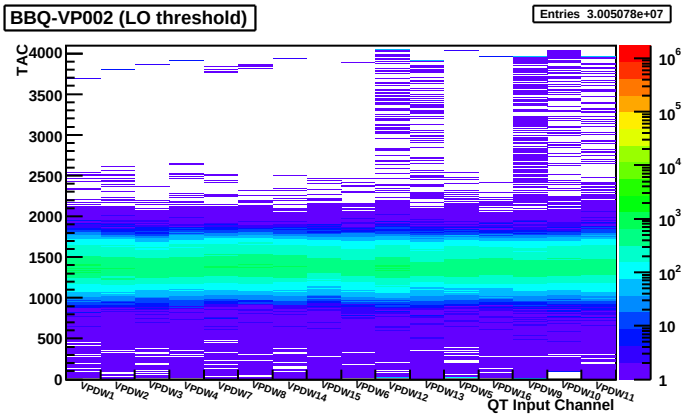

BBQ-ZD001 (ZDC TOWER)
Entries 3.005078e+07
ADC

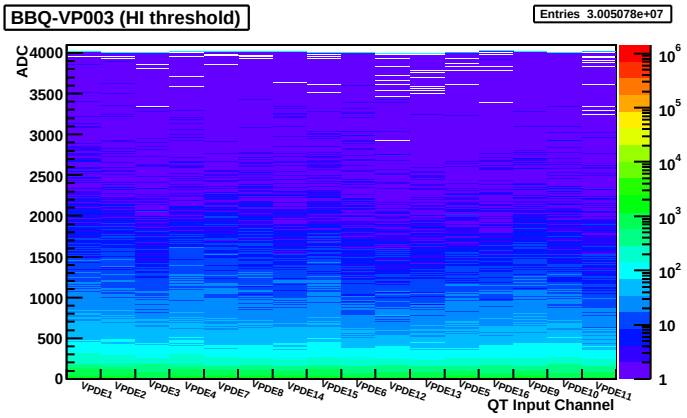

BBQ-ZD001 (ZDC TOWER)
Entries 3.005078e+07
TAC

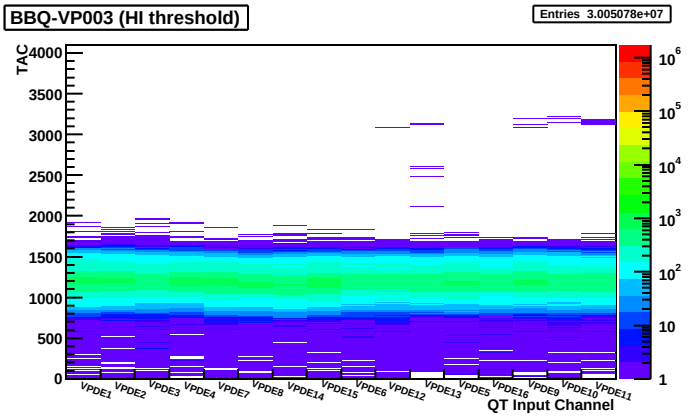

BBQ-VP001 (LO threshold)
Entries 3.005078e+07
ADC
QT Input Channel

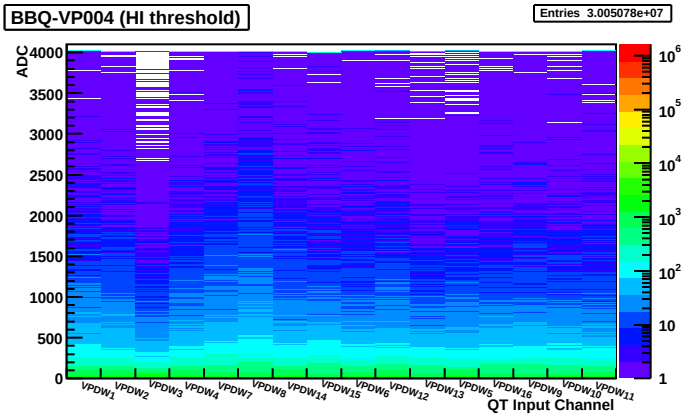

BBQ-VP001 (LO threshold)
Entries 3.005078e+07
TAC
QT Input Channel

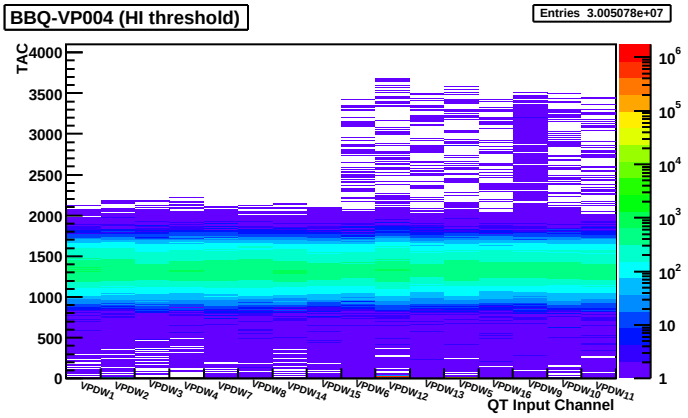

BBQ-VP002 (LO threshold)
Entries 3.005078e+07
ADC
QT Input Channel

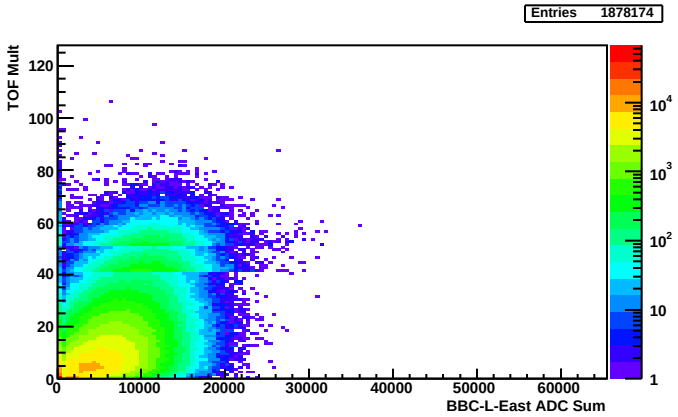

BBQ-VP002 (LO threshold)
Entries 3.005078e+07
TAC
QT Input Channel

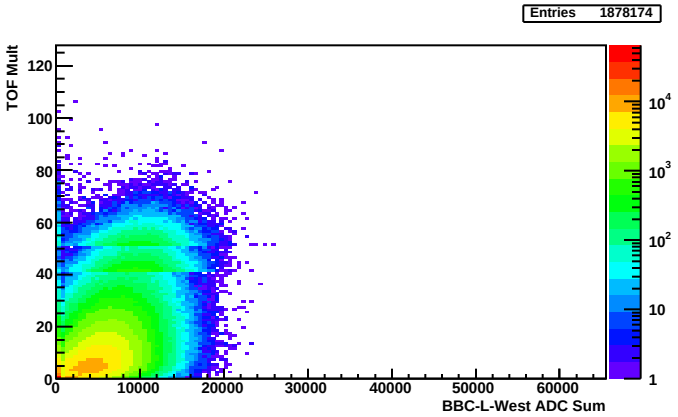

BBQ-VP003 (HI threshold)
Entries 3.005078e+07
ADC
QT Input Channel

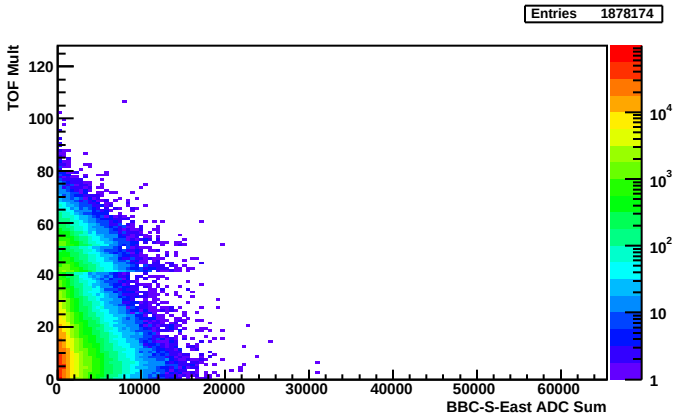

BBQ-VP003 (HI threshold)
Entries 3.005078e+07
TAC
QT Input Channel

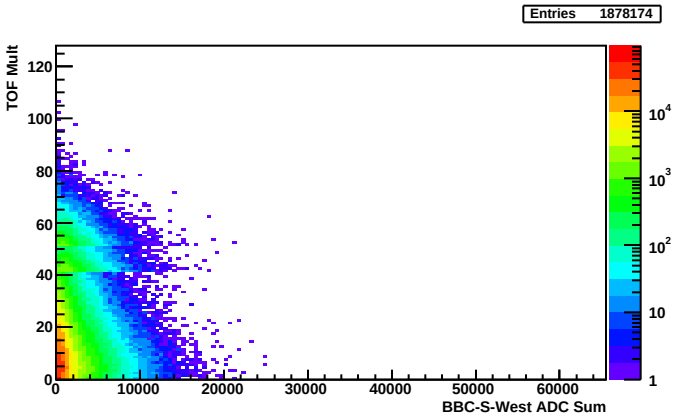

BBQ-VP004 (HI threshold)
Entries 3.005078e+07
ADC
QT Input Channel

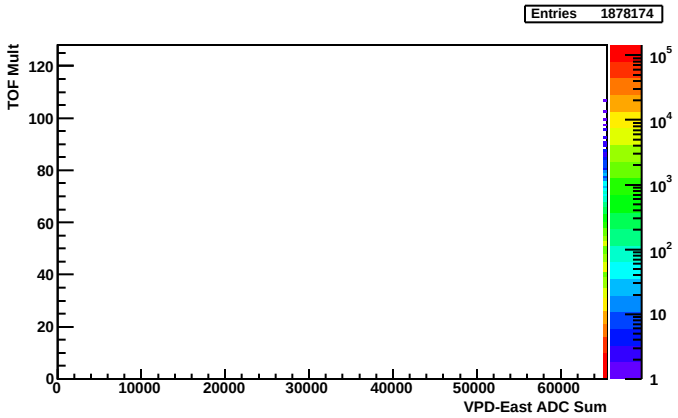

BBQ-VP004 (HI threshold)
Entries 3.005078e+07
TAC
QT Input Channel

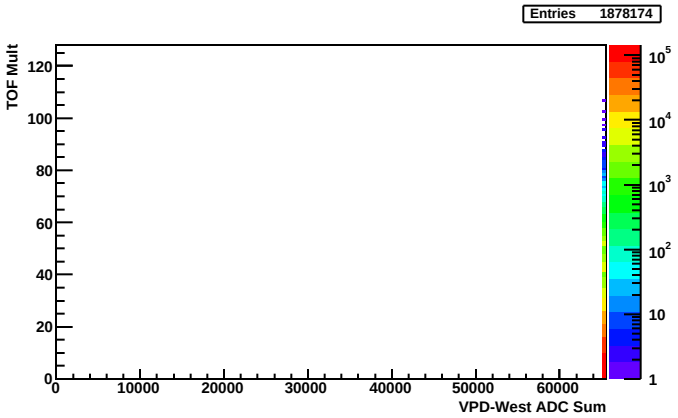

Entries 1878174
TOF Mult
BBC-L-East ADC Sum

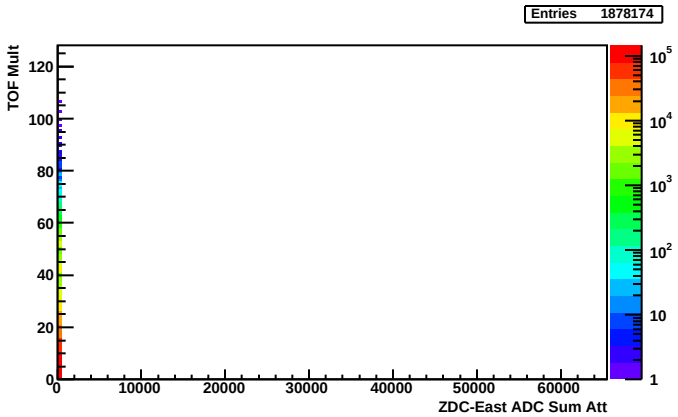

Entries 1878174
TOF Mult
BBC-L-West ADC Sum

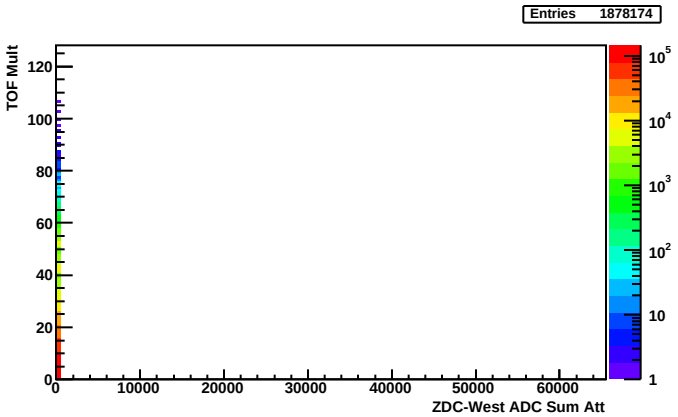

Entries 1878174
TOF Mult
BBC-S-East ADC Sum

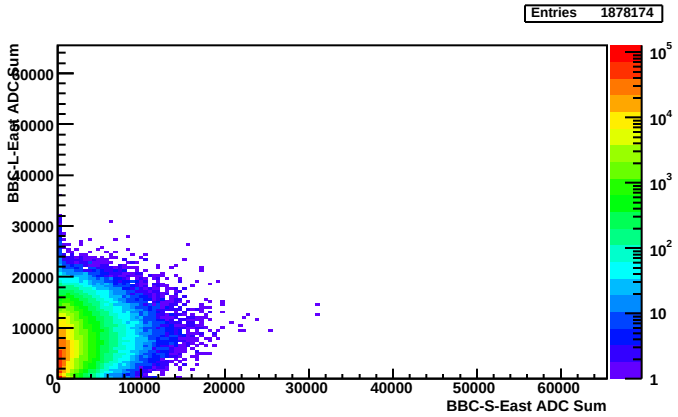

Entries 1878174
TOF Mult
BBC-S-West ADC Sum

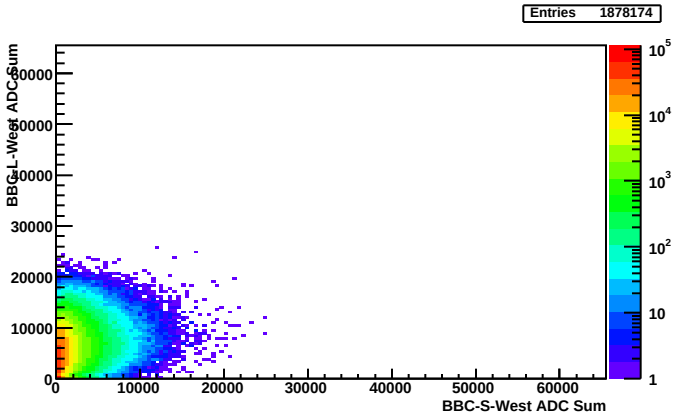

Entries 1878174
TOF Mult
VPD-East ADC Sum

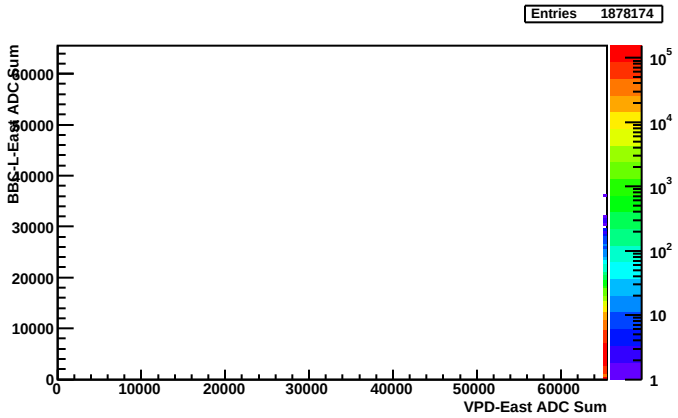

Entries 1878174
TOF Mult
VPD-West ADC Sum

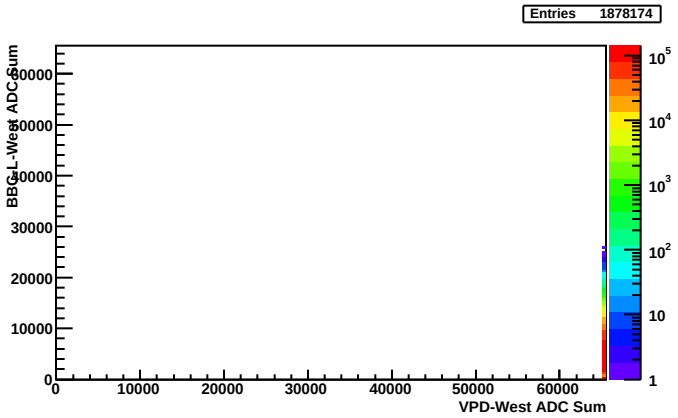

Entries 1878174
TOF Mult
ZDC-East ADC Sum Att


Entries 1878174
TOF Mult
ZDC-West ADC Sum Att


Entries 1878174
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1878174
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1878174
BBC-L-East ADC Sum
VPD-East ADC Sum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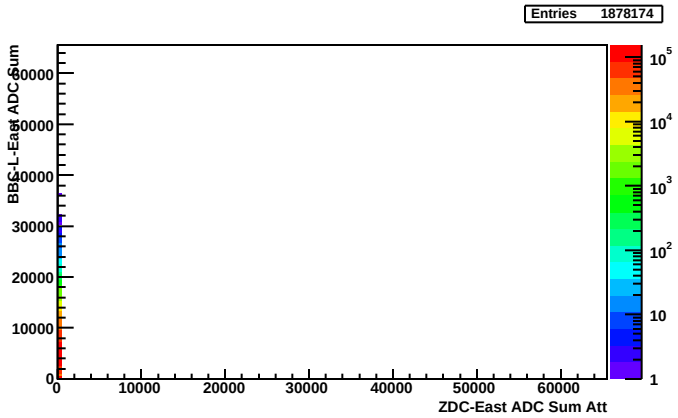
BBC-L-West ADC Sum
VPD-West ADC Sum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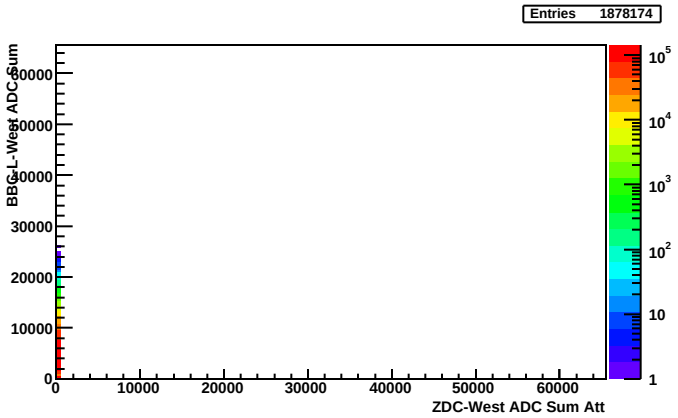
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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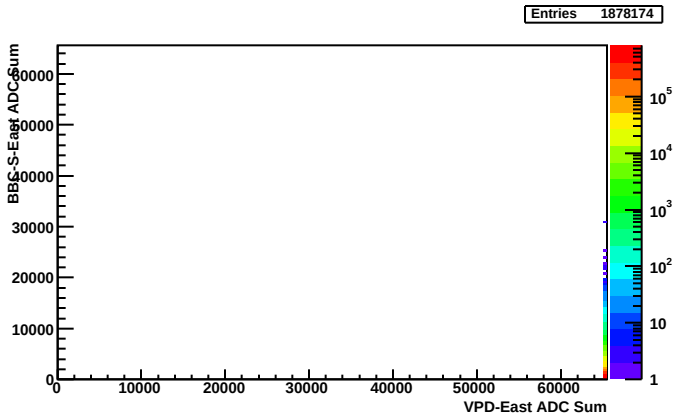
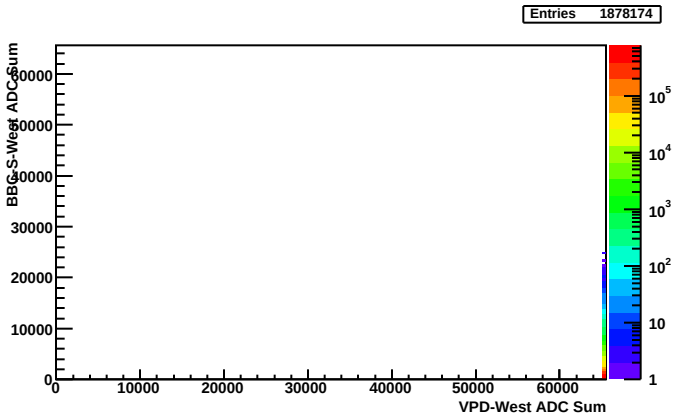
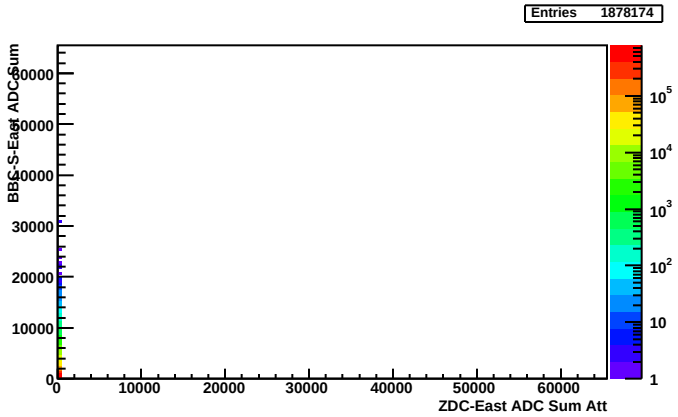
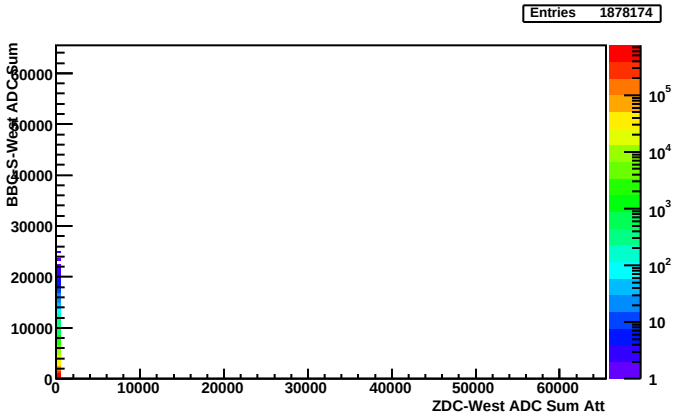
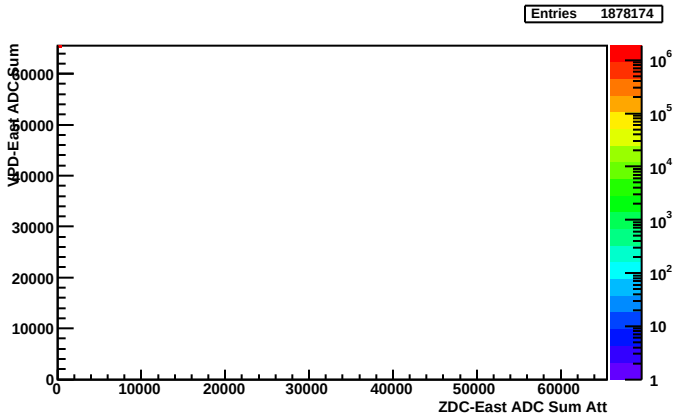
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

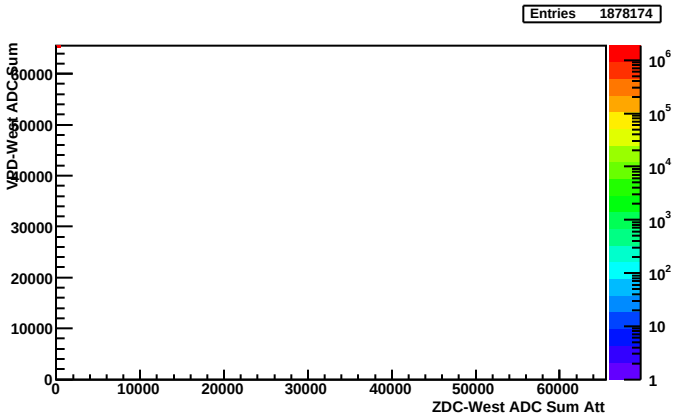

Entries 1878174
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1878174
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1878174
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1878174
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1878174
VPD-East ADC Sum
ZDC-East ADC Sum Att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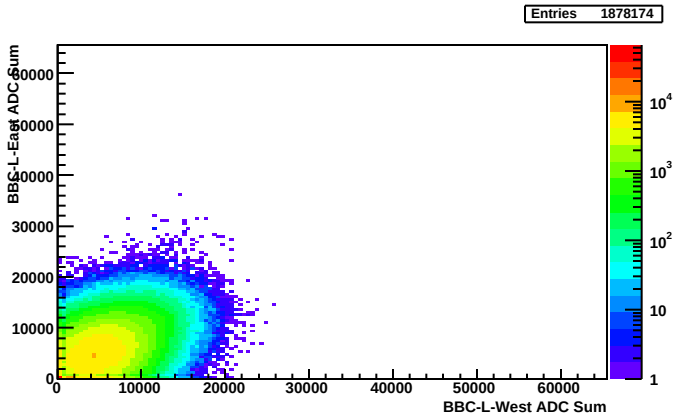
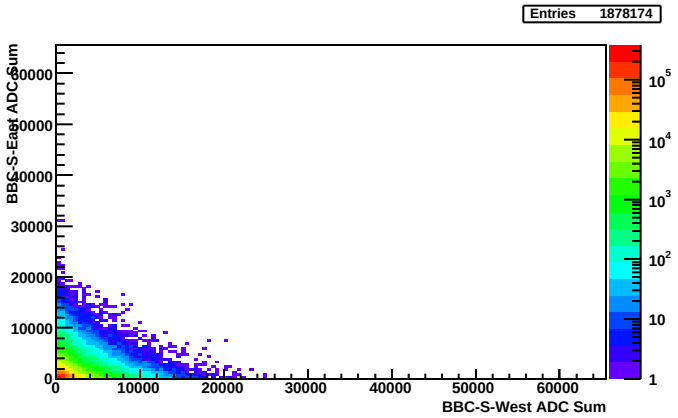
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 1878174
BBC-L-East ADC Sum
BBC-L-West ADC Sum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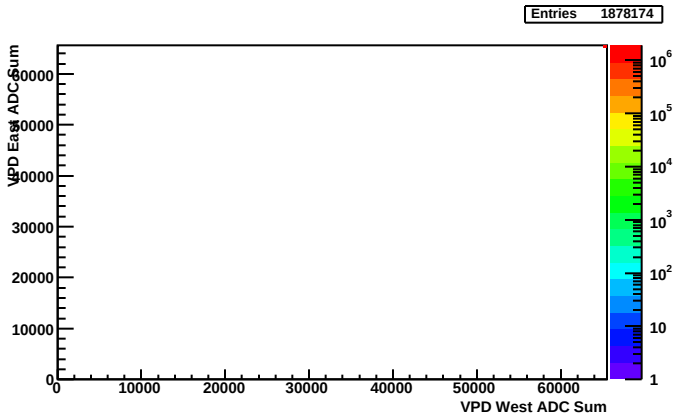
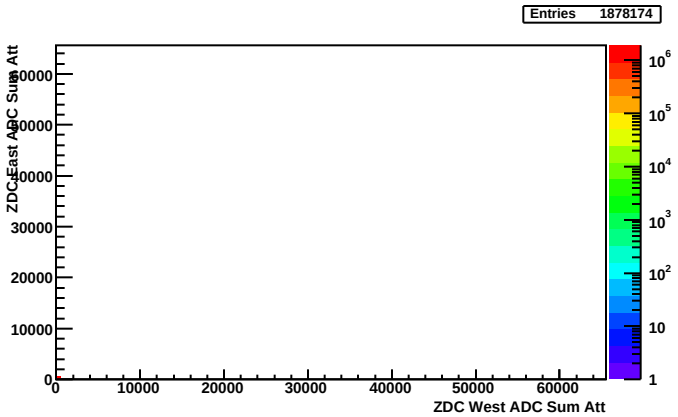
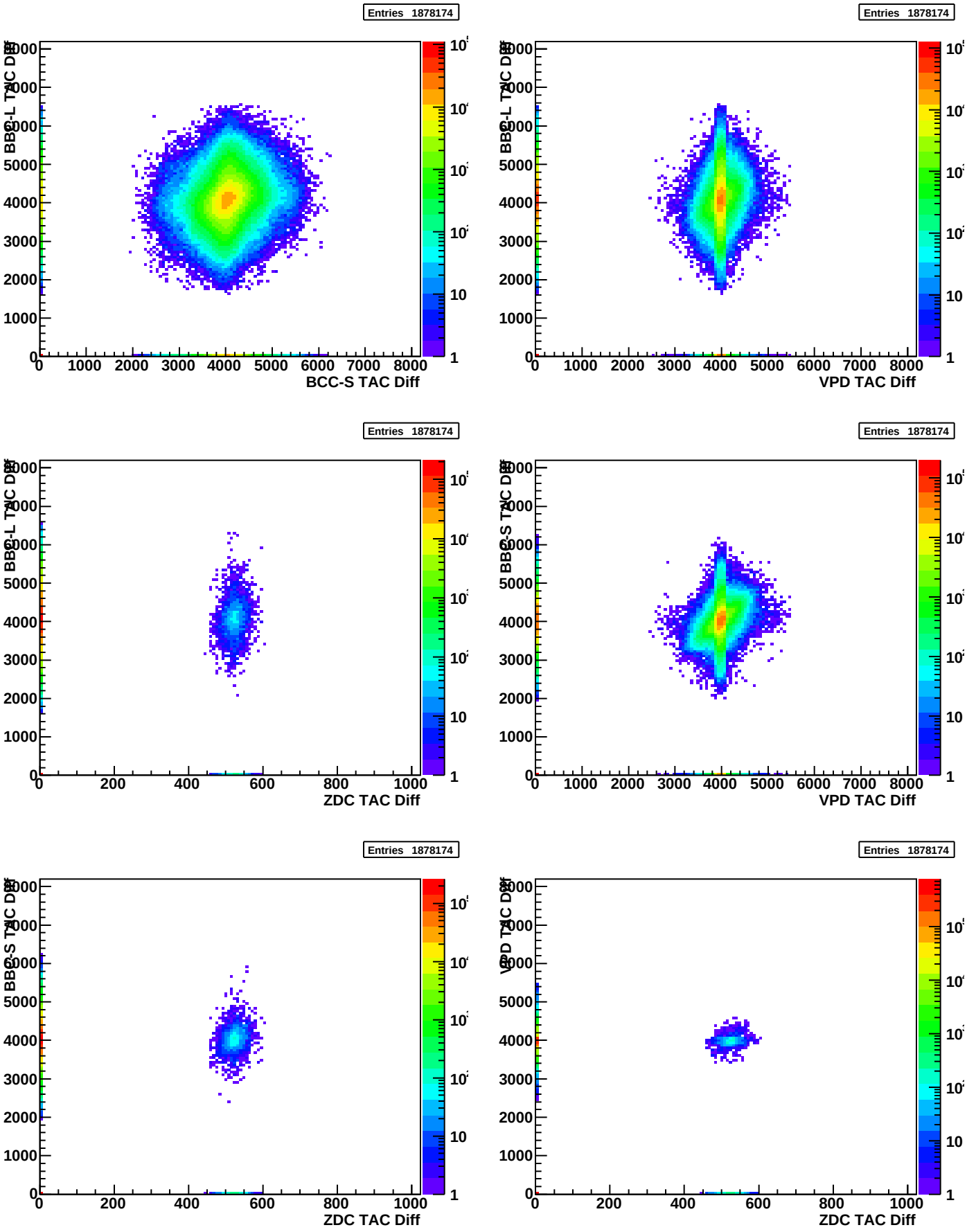
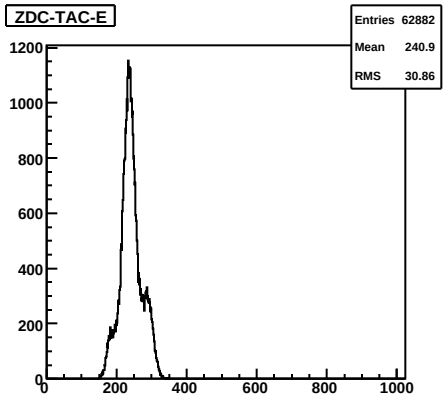
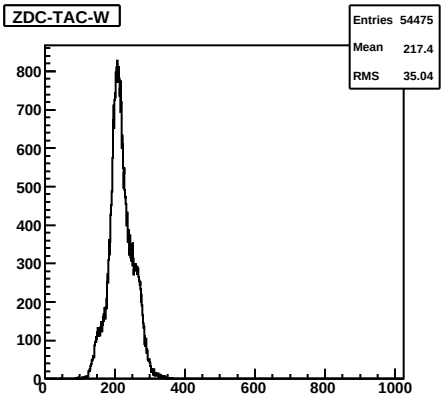
BBC-S-East ADC Sum
BBC-S-West ADC Sum

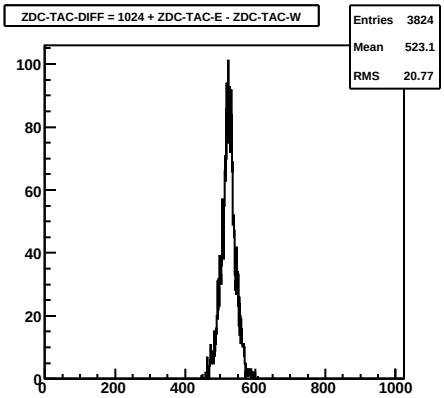

Entries 1878174
VPD East ADC Sum
VPD West ADC Sum

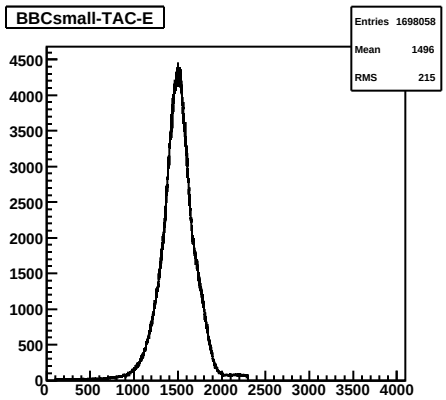

Entries 1878174
ZDC East ADC Sum Att
ZDC West ADC Sum Att

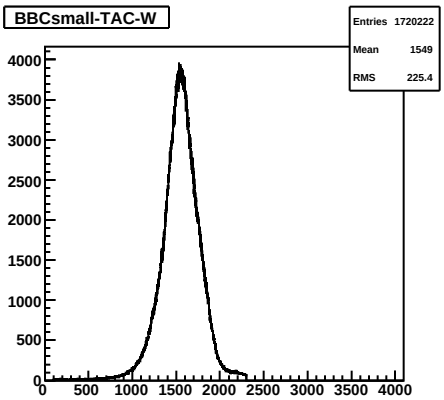

Entries 1878174
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1878174
BBC-L TAC Diff
VPD TAC Diff
Entries 1878174
BBC-L TAC Diff
ZDC TAC Diff
Entries 1878174
BBC-S TAC Diff
VPD TAC Diff
Entries 1878174
BBC-S TAC Diff
ZDC TAC Diff
Entries 1878174
VPD TAC Diff
ZDC TAC Diff

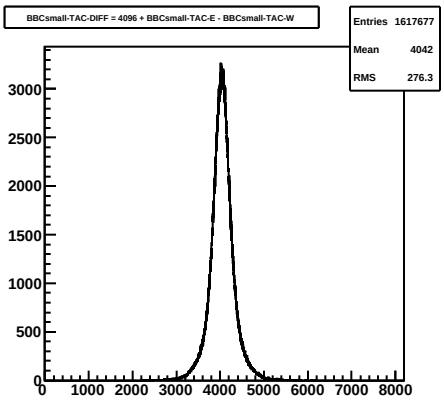

ZDC-TAC-E
Entries 62882
Mean 240.9
RMS 30.86

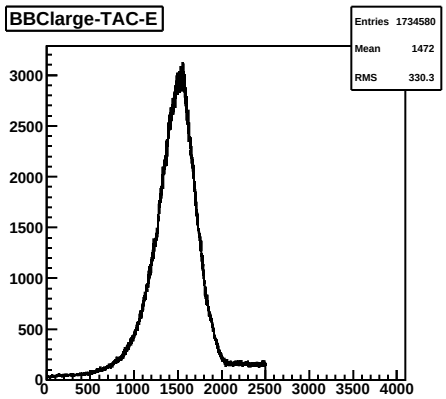

ZDC-TAC-W
Entries 54475
Mean 217.4
RMS 35.04

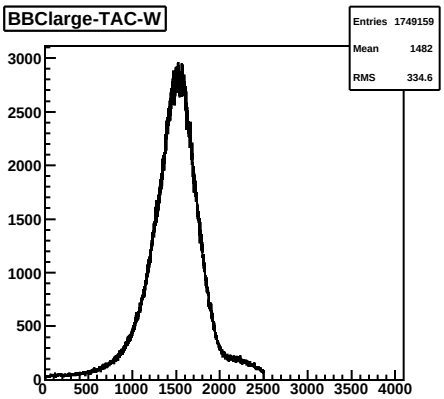

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3824
Mean 523.1
RMS 20.77

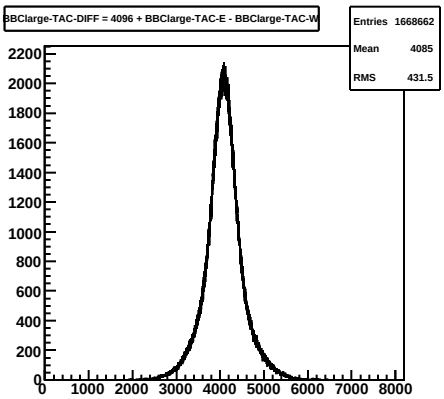

BBCsmall-TAC-E
Entries 1698058
Mean 1496
RMS 215

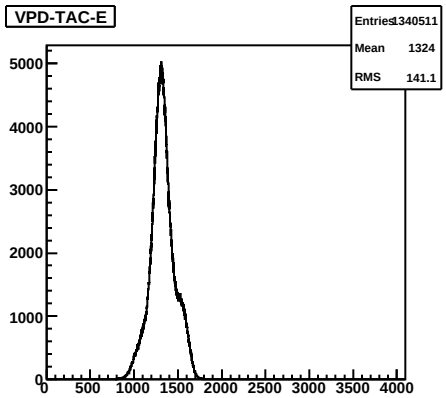

BBCsmall-TAC-W
Entries 1720222
Mean 1549
RMS 225.4

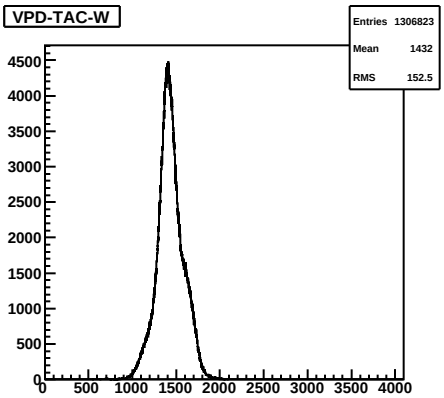

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1617677
Mean 4042
RMS 276.3

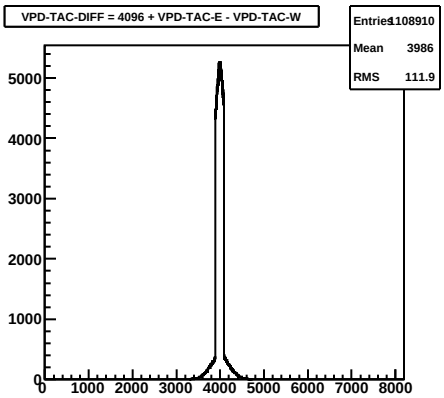

BBClarge-TAC-E
Entries 1734580
Mean 1472
RMS 330.3

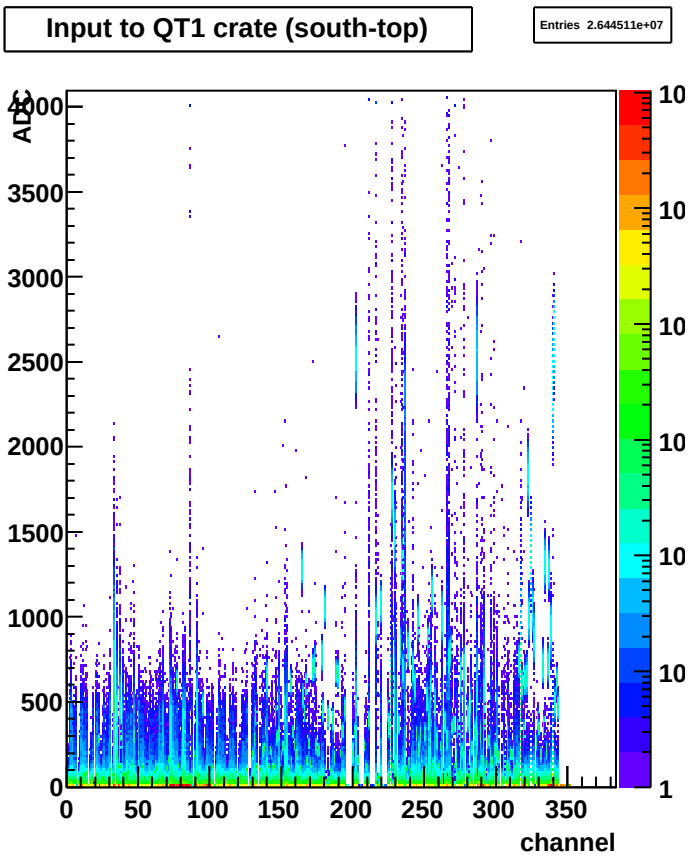

BBClarge-TAC-W
Entries 1749159
Mean 1482
RMS 334.6

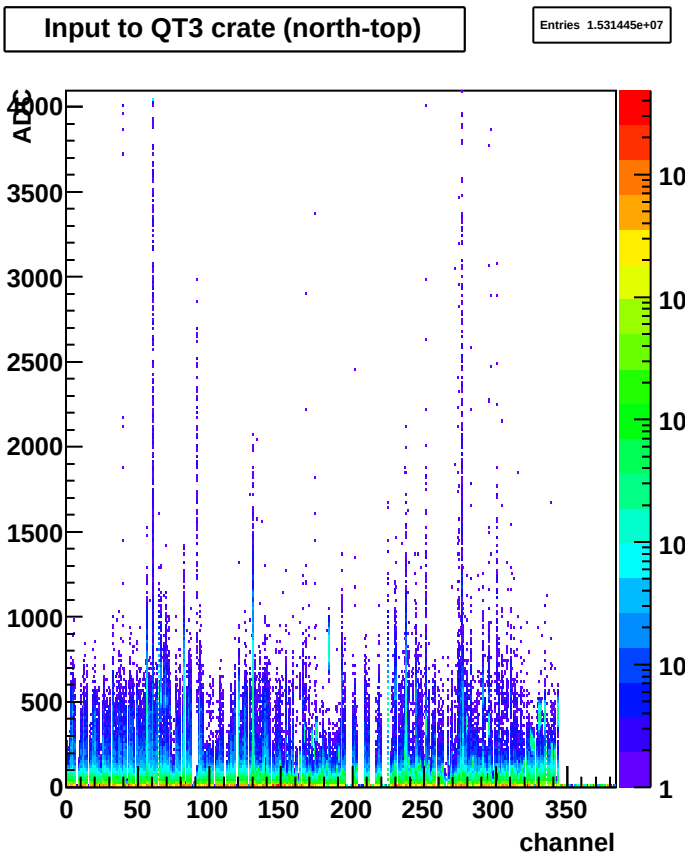

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1668662
Mean 4085
RMS 431.5

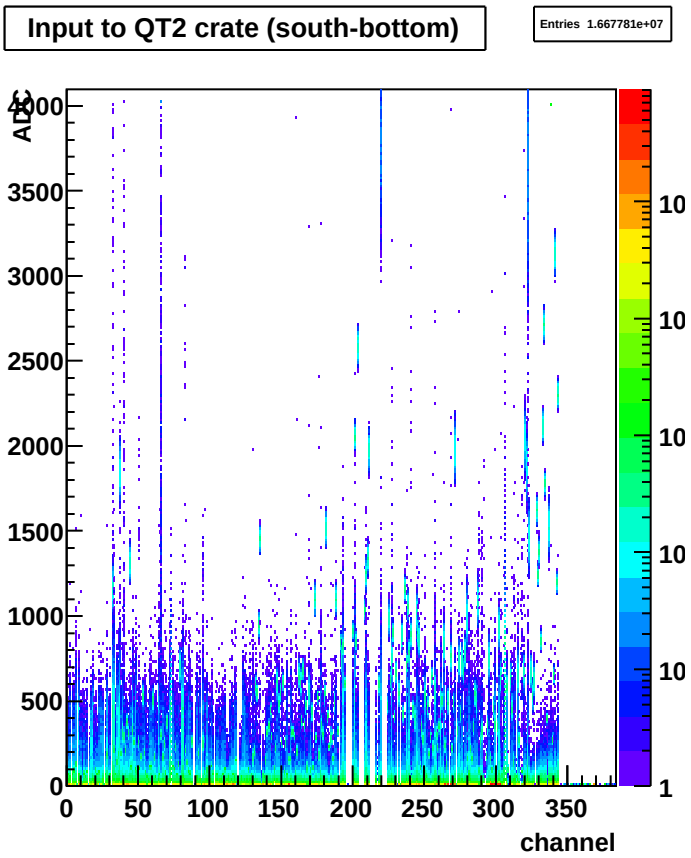

VPD-TAC-E
Entries 1340511
Mean 1324
RMS 141.1

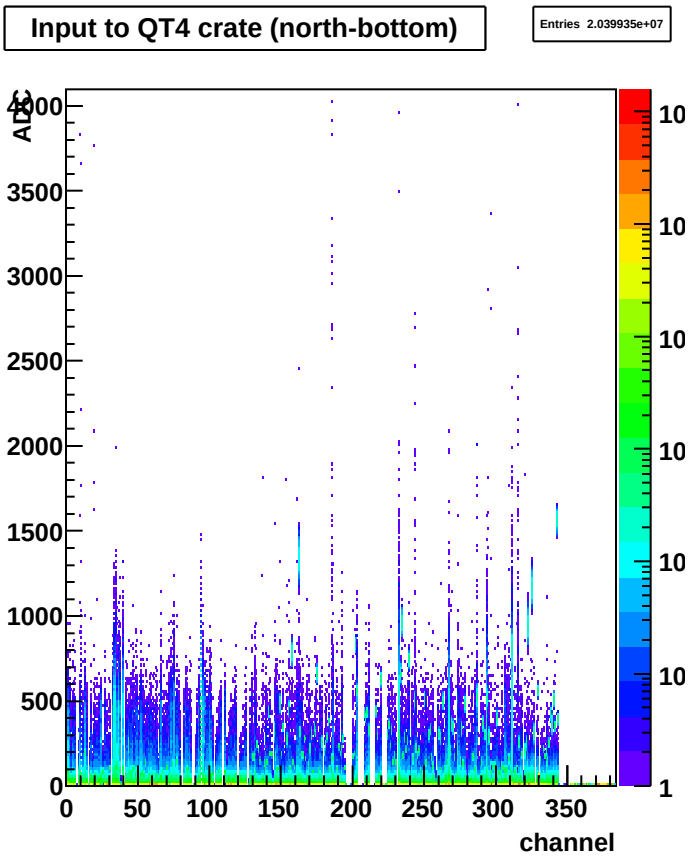

VPD-TAC-W
Entries 1306823
Mean 1432
RMS 152.5

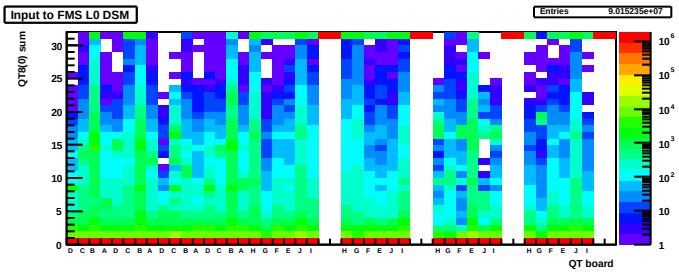

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1108910
Mean 3986
RMS 111.9

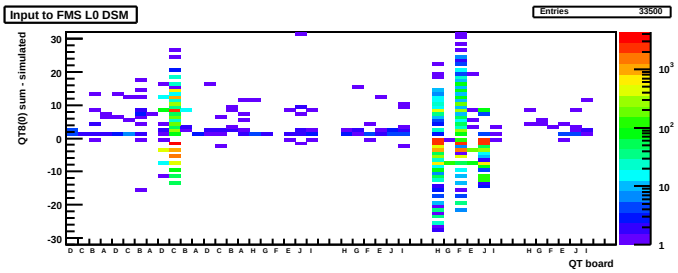

Input to QT1 crate (south-top)
Entries 2.644511e+07
ADC
channel

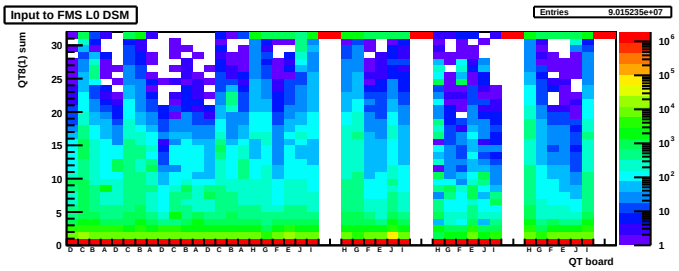

Input to QT3 crate (north-top)
Entries 1.531445e+07
ADC
channel

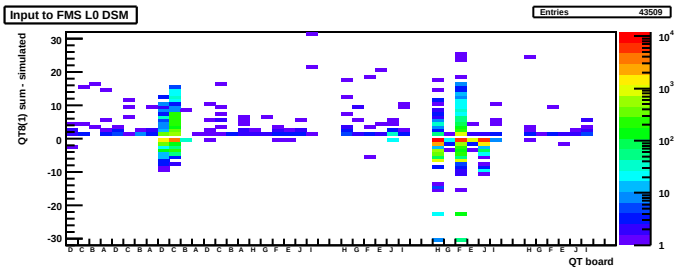

Input to QT2 crate (south-bottom)
Entries 1.667781e+07
ADC
channel

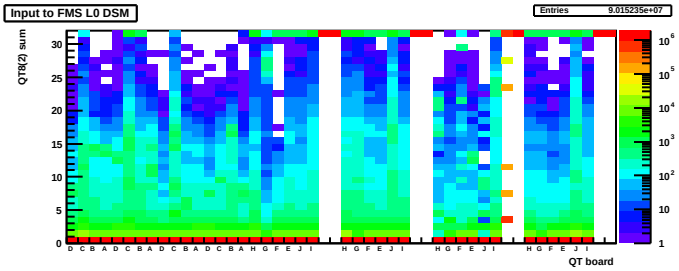

Input to QT4 crate (north-bottom)
Entries 2.039935e+07
ADC
channel

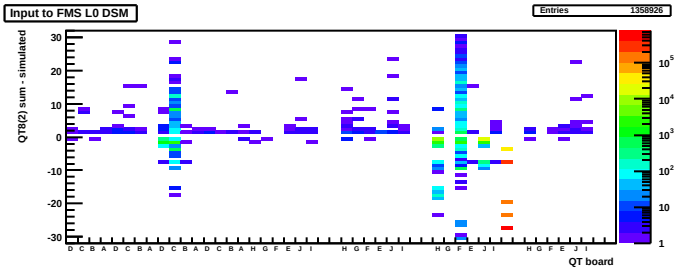

Input to FMS L0 DSM
Entries 9.015235e+07
QT8(0) sum
QT board

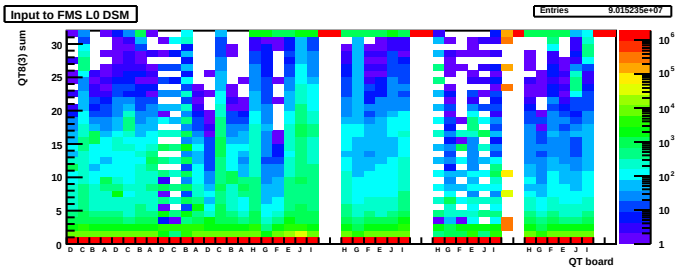

Input to FMS L0 DSM
Entries 33500
QT8(0) sum - simulated
QT board

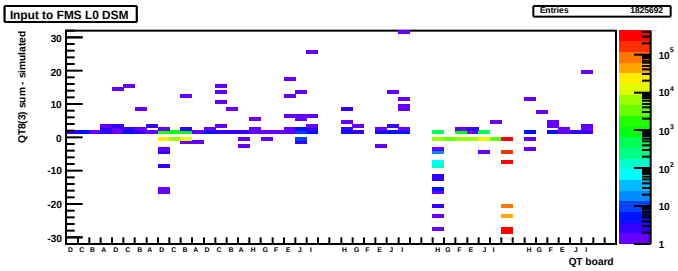

Input to FMS L0 DSM
Entries 9.015235e+07
QT8(1) sum
QT board

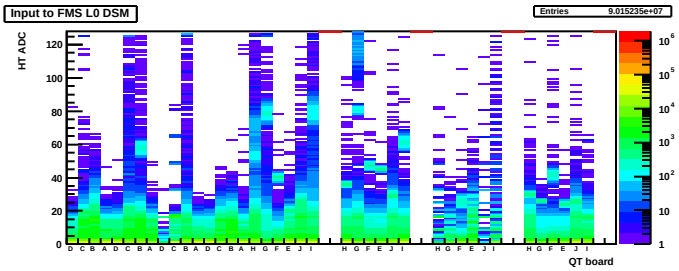

Input to FMS L0 DSM
Entries 43509
QT8(1) sum - simulated
QT board

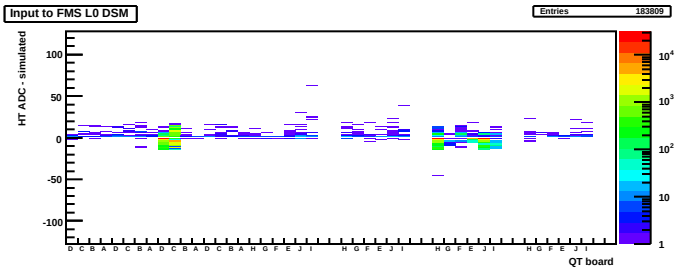

Input to FMS L0 DSM
Entries 9.015235e+07
QT8(2) sum
QT board

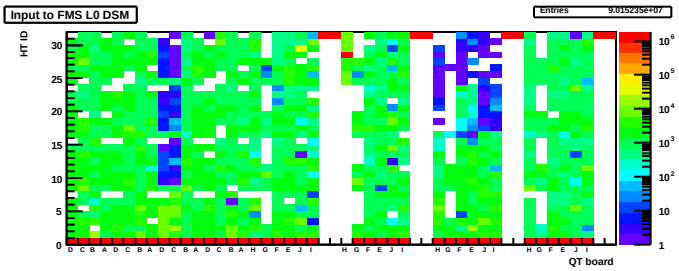

Input to FMS L0 DSM
Entries 1358926
QT8(2) sum - simulated
QT board

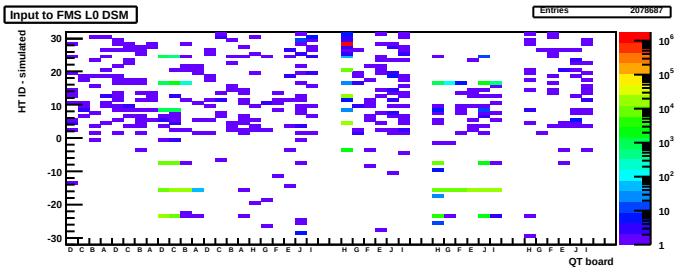

Input to FMS L0 DSM
Entries 9.015235e+07
QT8(3) sum
QT board

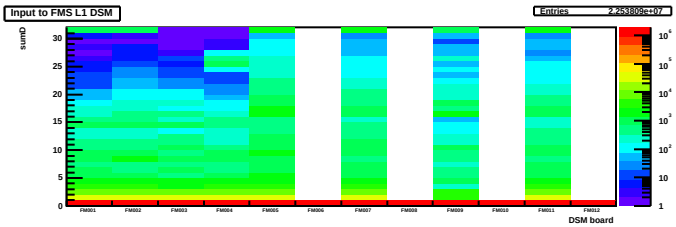

Input to FMS L0 DSM
Entries 1825692
QT8(3) sum - simulated
QT board

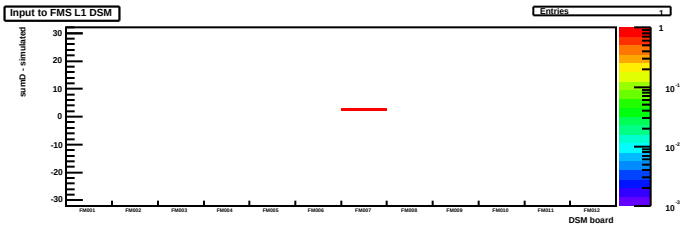

Input to FMS L0 DSM
Entries 9.015235e+07
HT ADC
QT board

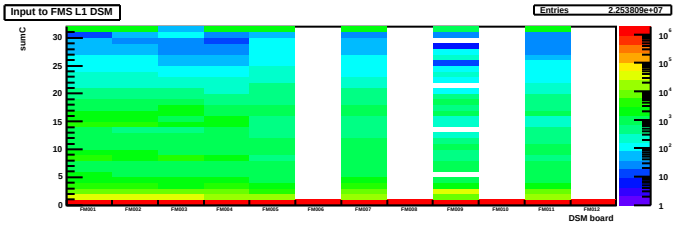

Input to FMS L0 DSM
Entries 183809
HT ADC - simulated
QT board

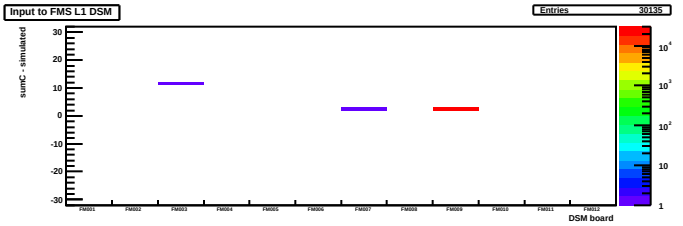

Input to FMS L0 DSM
Entries 9.015235e+07
HT ID
QT board

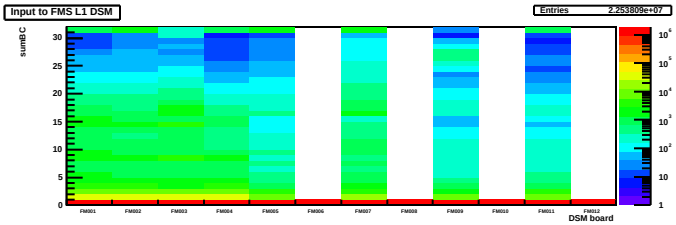

Input to FMS L0 DSM
Entries 2078687
HT ID - simulated
QT board

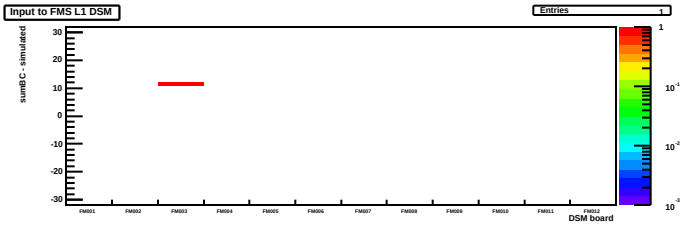

Input to FMS L1 DSM
Entries 2.253809e+07
sumD
DSM board

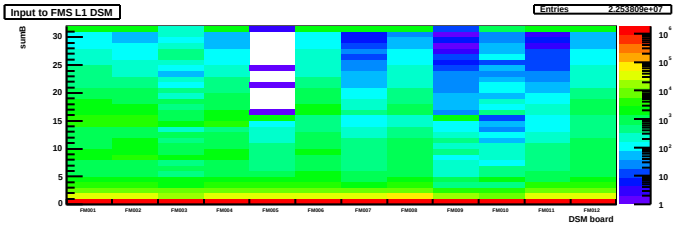

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

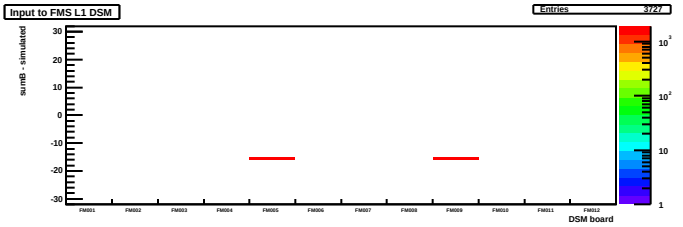

Input to FMS L1 DSM
Entries 2.253809e+07
sumC
DSM board

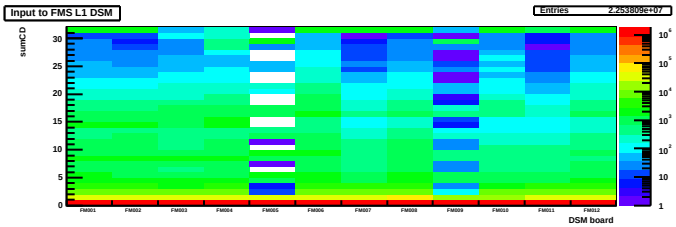

Input to FMS L1 DSM
Entries 30135
sumC - simulated
DSM board

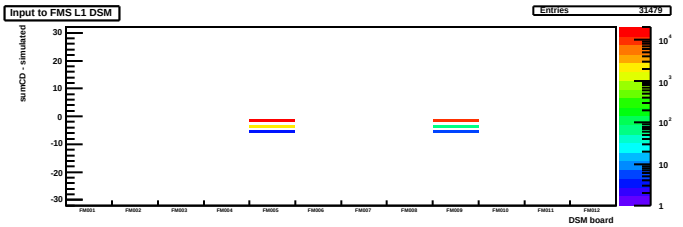

Input to FMS L1 DSM
Entries 2.253809e+07
sumBC
DSM board

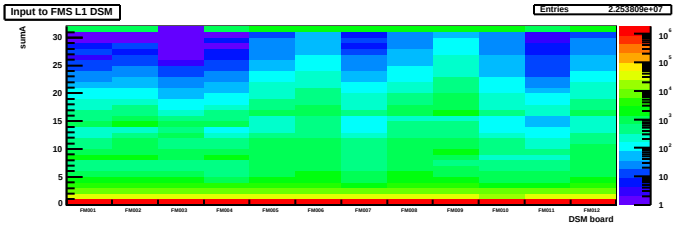

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

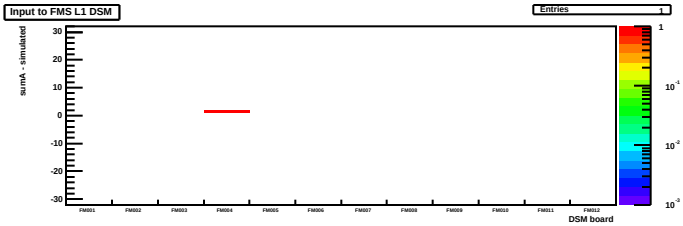

Input to FMS L1 DSM
Entries 2.253809e+07
sumB
DSM board

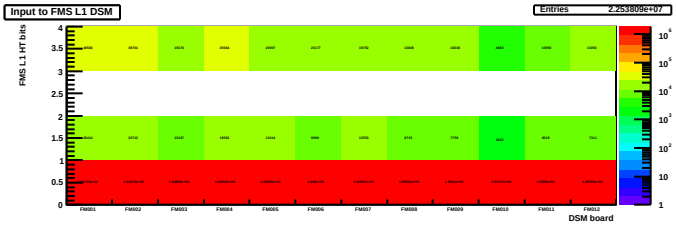

Input to FMS L1 DSM
Entries 3727
sumB - simulated
DSM board

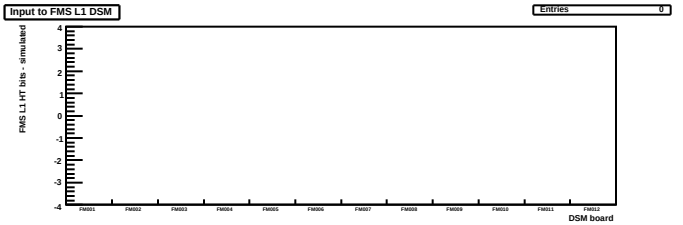

Input to FMS L1 DSM
Entries 2.253809e+07
sumCD
DSM board

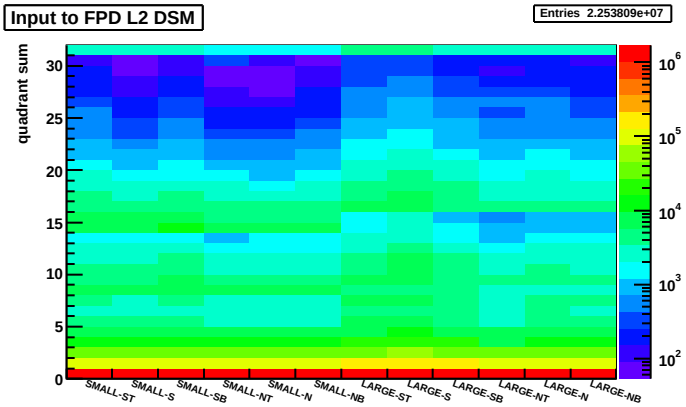

Input to FMS L1 DSM
Entries 31479
sumCD - simulated
DSM board

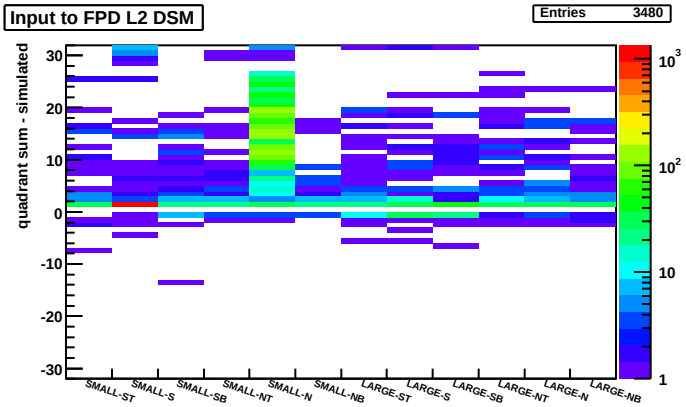

Input to FMS L1 DSM
Entries 2.253809e+07
sumA
DSM board

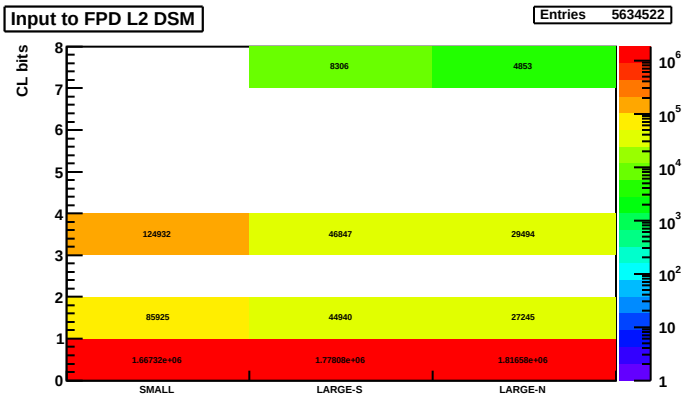

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

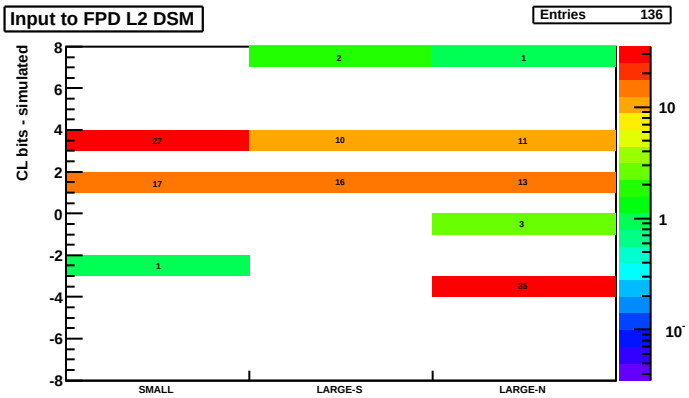

Input to FMS L1 DSM
Entries 2.253809e+07
FMS L1 HT bits
DSM board

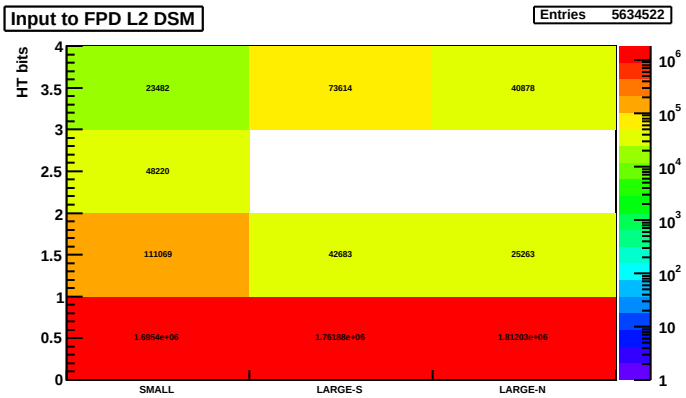

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

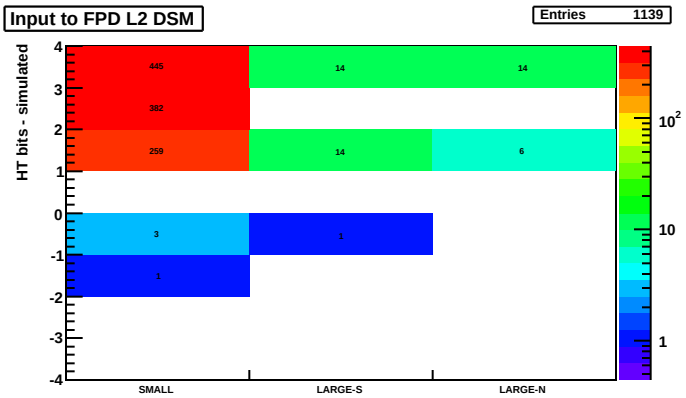

Input to FPD L2 DSM
Entries 2.253809e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

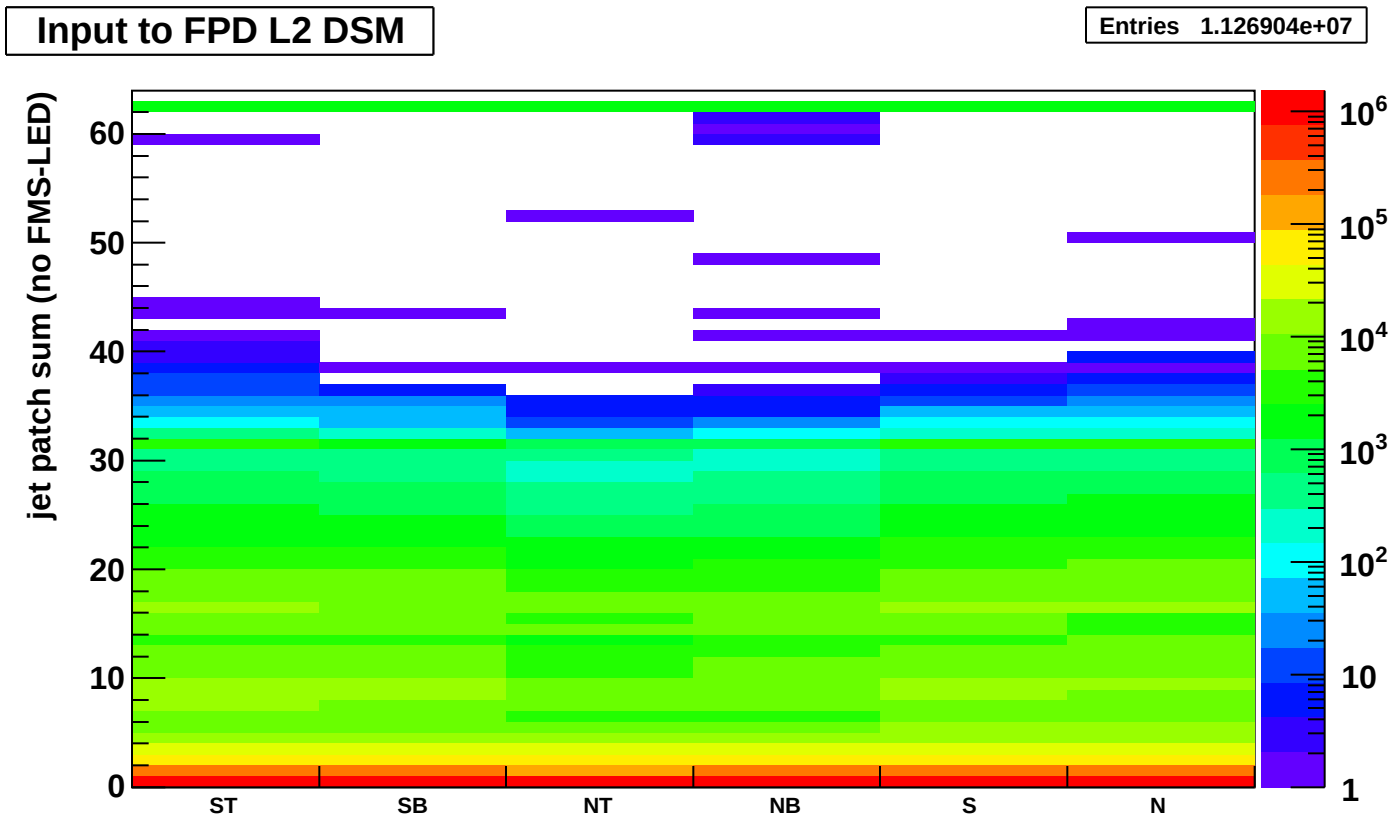

Input to FPD L2 DSM
Entries 3480
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

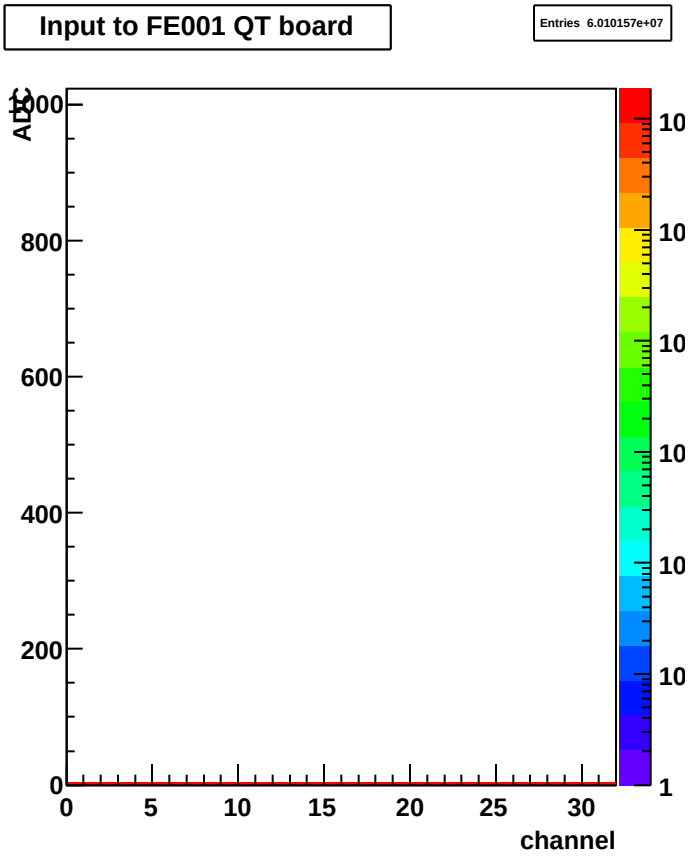

Input to FPD L2 DSM
Entries 5634522
CL bits
8306
4853
124932
46847
29494
85925
44940
27245
1.66732e+06
1.77808e+06
1.81658e+06
SMALL
LARGE-S
LARGE-N

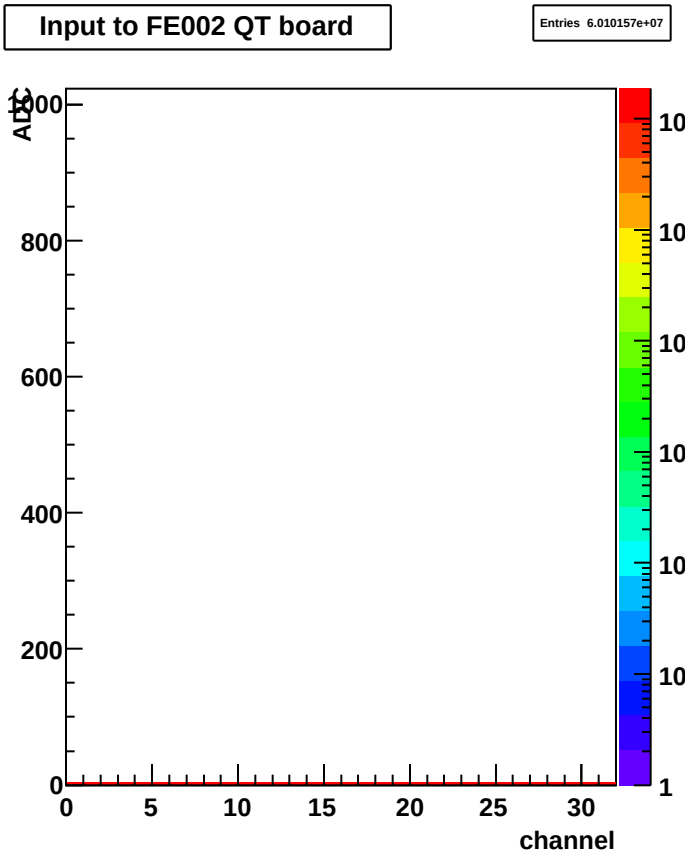

Input to FPD L2 DSM
Entries 136
CL bits - simulated
2
1
27
10
11
17
16
13
3
1
35
SMALL
LARGE-S
LARGE-N

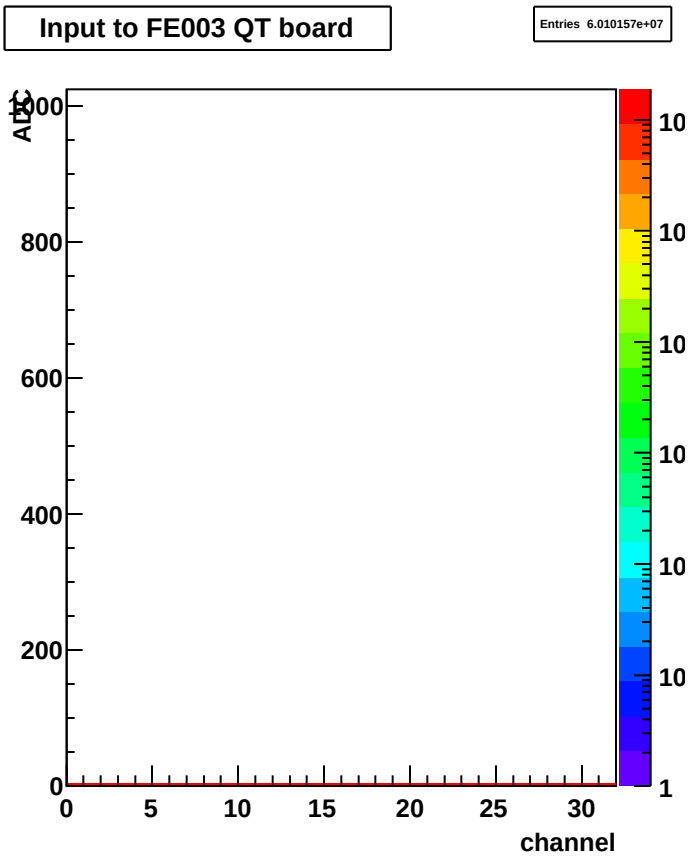

Input to FPD L2 DSM
Entries 5634522
HT bits
23482
73614
40878
48220
111069
42683
25263
1.6954e+06
1.76188e+06
1.81203e+06
SMALL
LARGE-S
LARGE-N

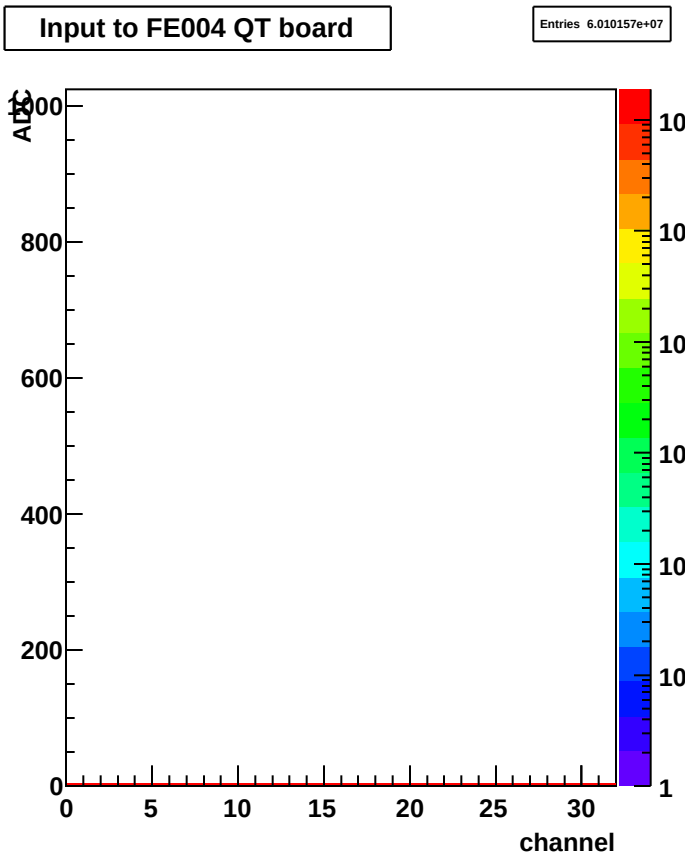

Input to FPD L2 DSM
Entries 1139
HT bits - simulated
445
14
14
382
259
14
6
3
1
1
SMALL
LARGE-S
LARGE-N

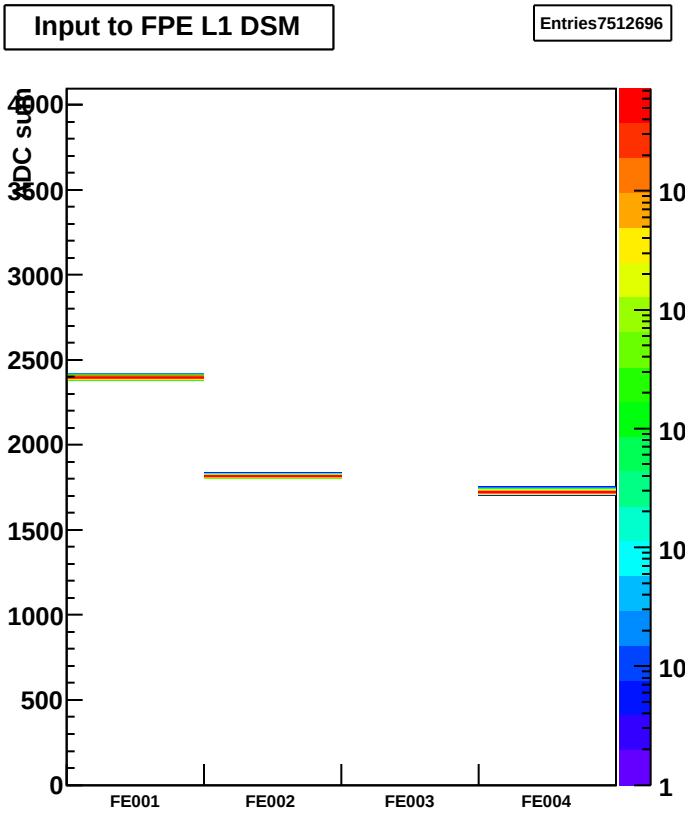

Input to FPD L2 DSM
Entries 1.126904e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 6.010157e+07
ADC
channel

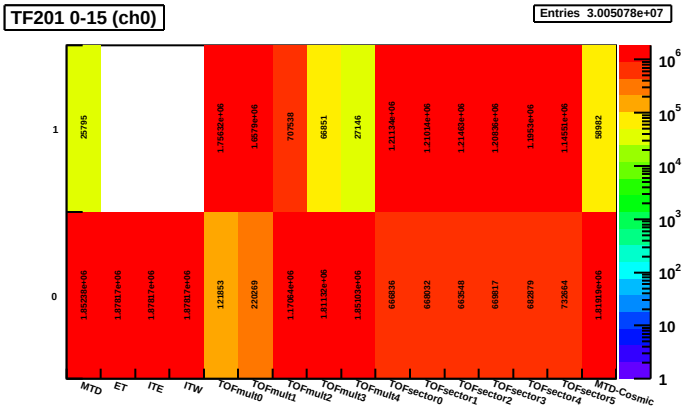

Input to FE002 QT board
Entries 6.010157e+07
ADC
channel


Input to FE003 QT board
Entries 6.010157e+07
ADC
channel

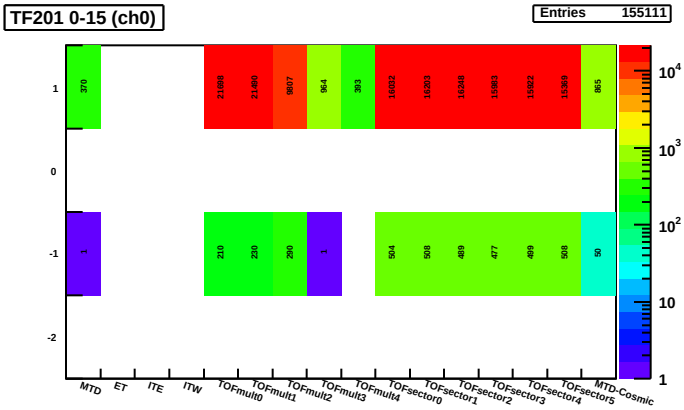

Input to FE004 QT board
Entries 6.010157e+07
ADC
channel


Input to FPE L1 DSM
Entries7512696
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

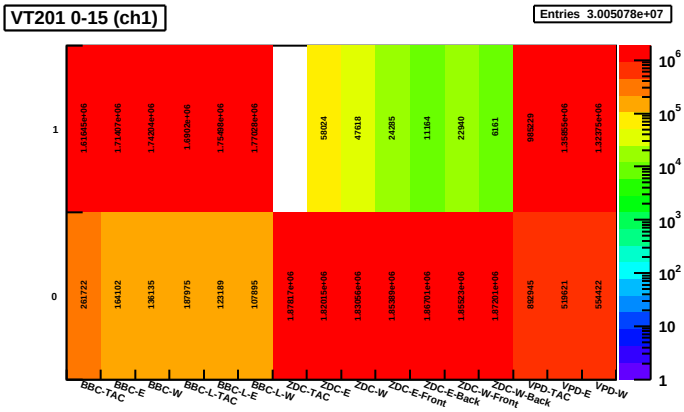
TF201 0-15 (ch0)



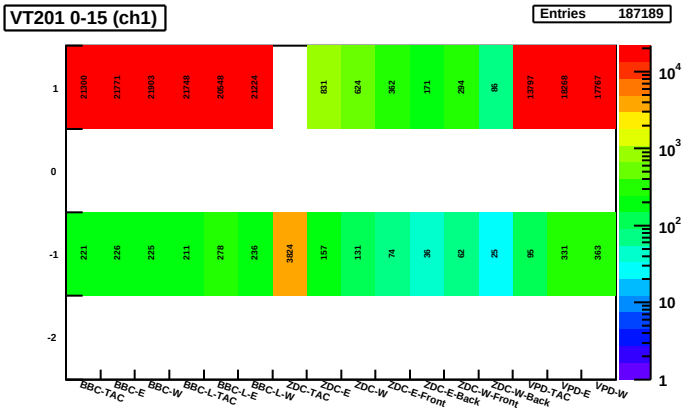
TF201 0-15 (ch0)



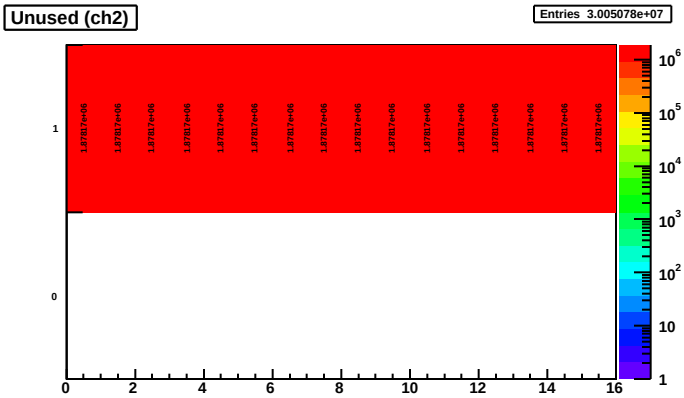
VT201 0-15 (ch1)

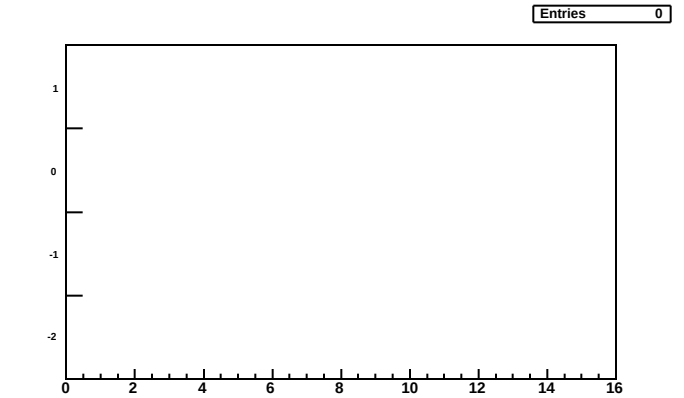


VT201 0-15 (ch1)

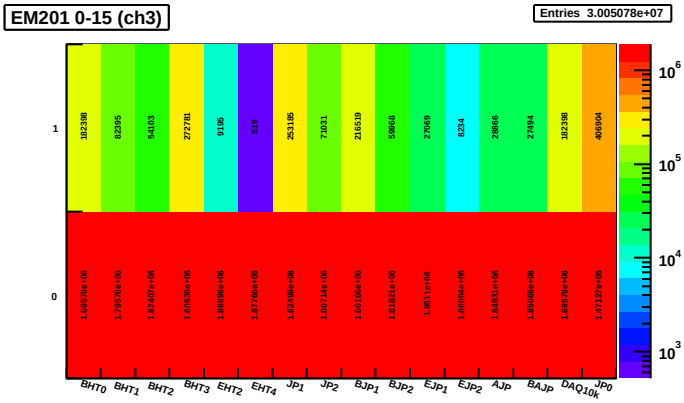


Unused (ch2)





EM201 0-15 (ch3)



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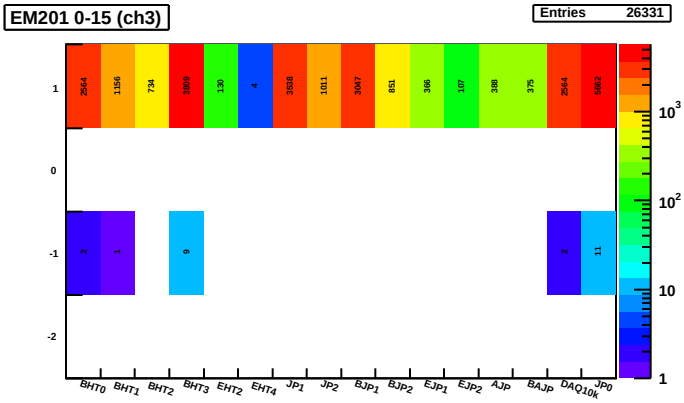
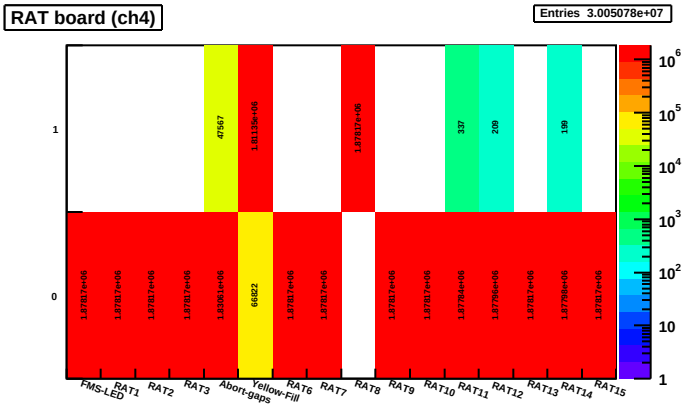
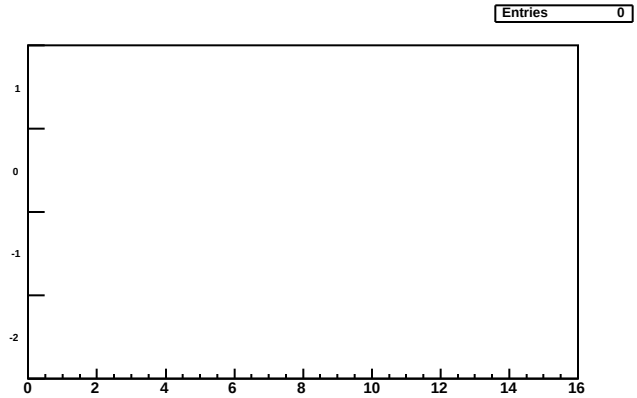


RAT board (ch4)





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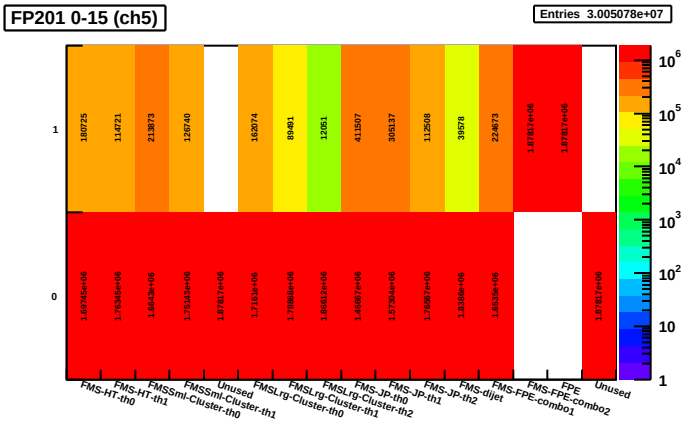

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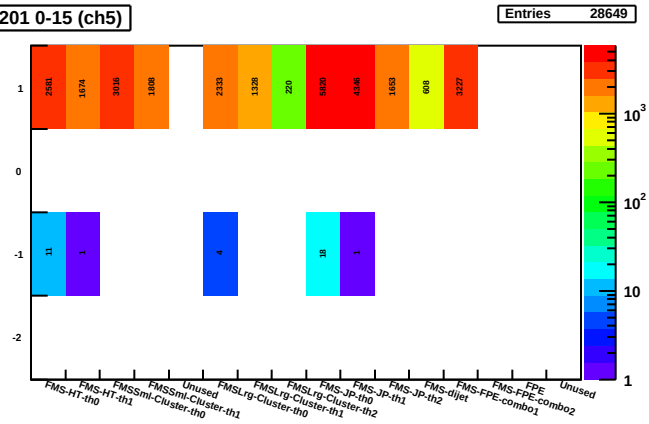

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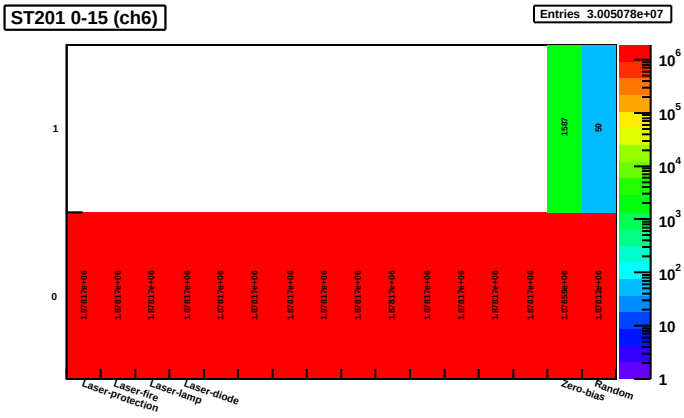

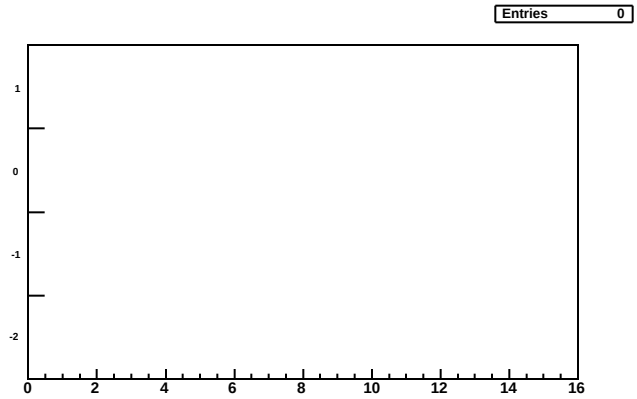
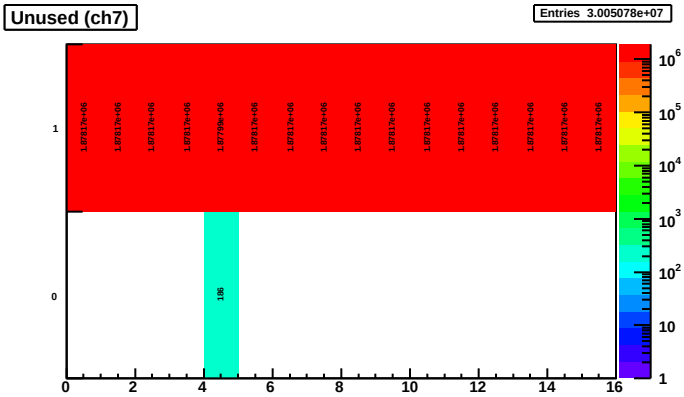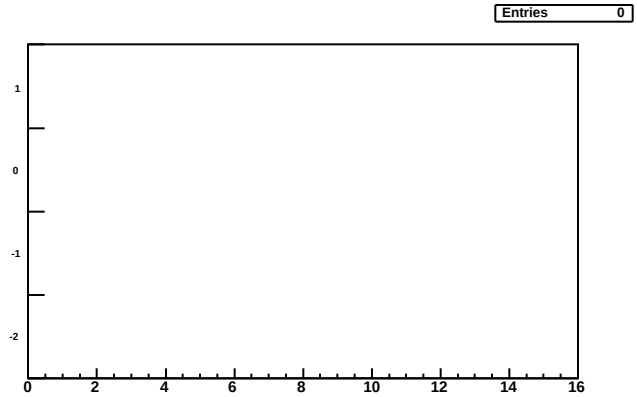
Unused (ch7)