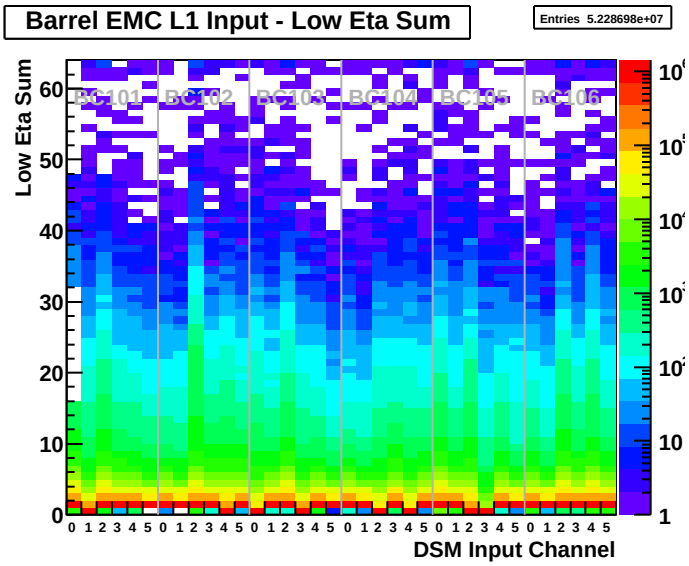
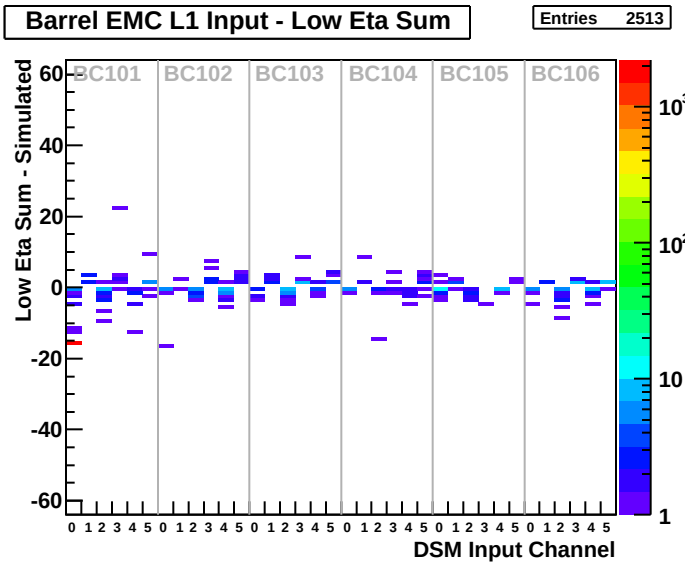
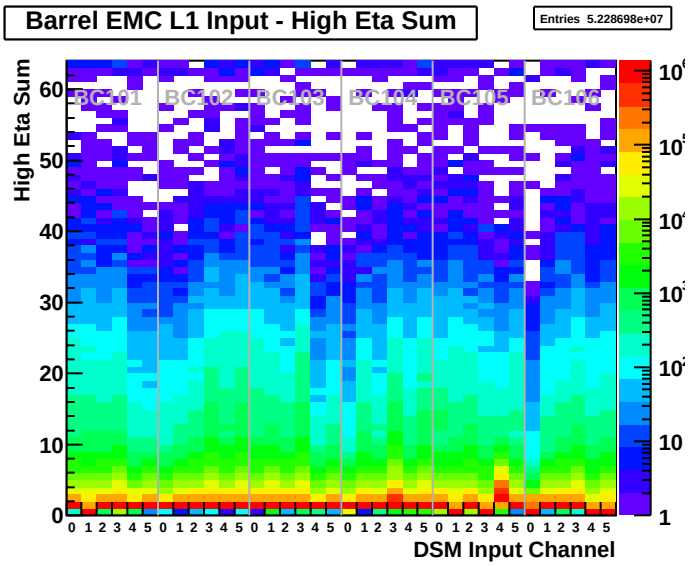
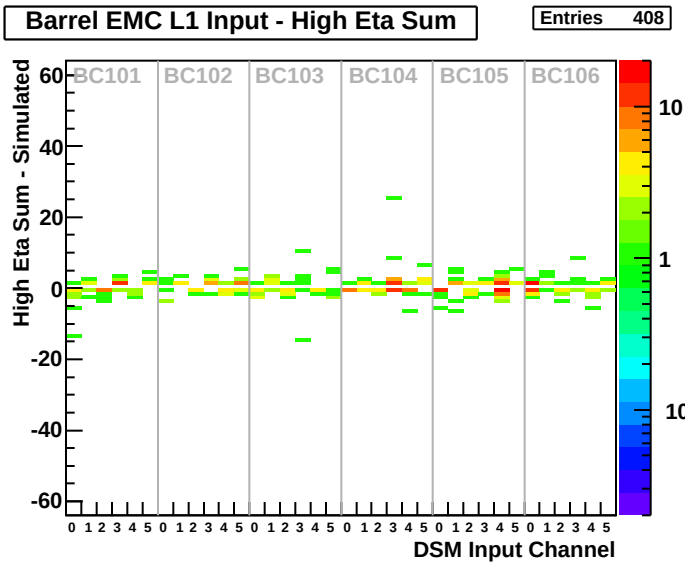
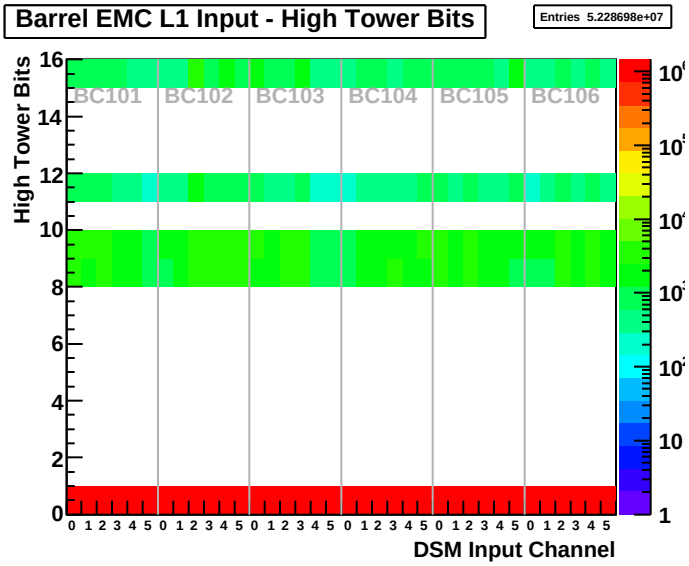
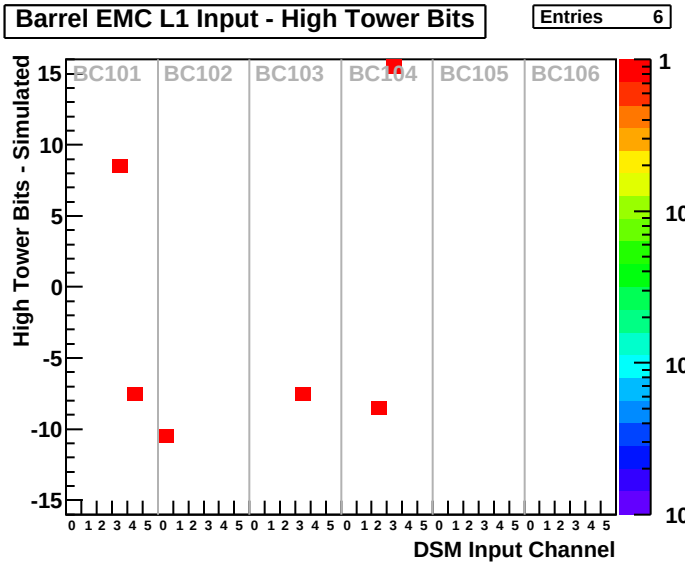

Barrel EMC L1 Input - Low Eta Sum
Entries 5.228698e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

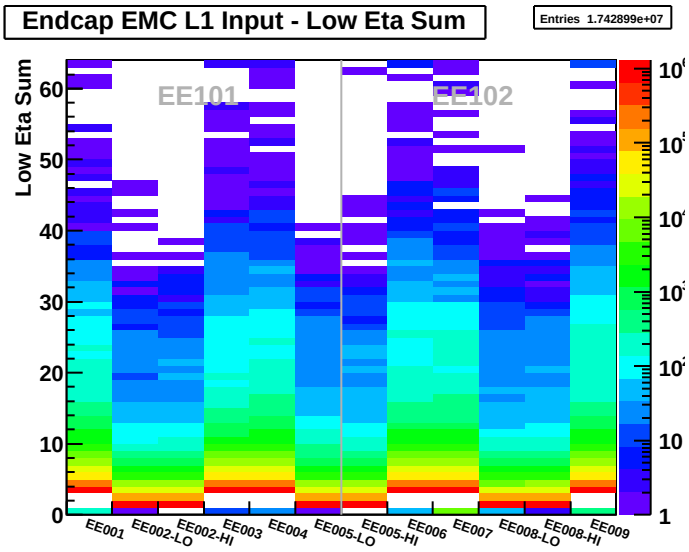

Barrel EMC L1 Input - Low Eta Sum
Entries 2513
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

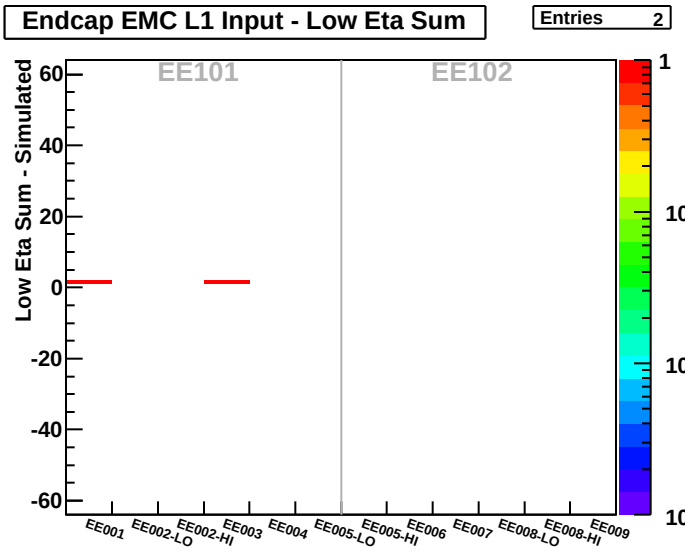

Barrel EMC L1 Input - High Eta Sum
Entries 5.228698e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

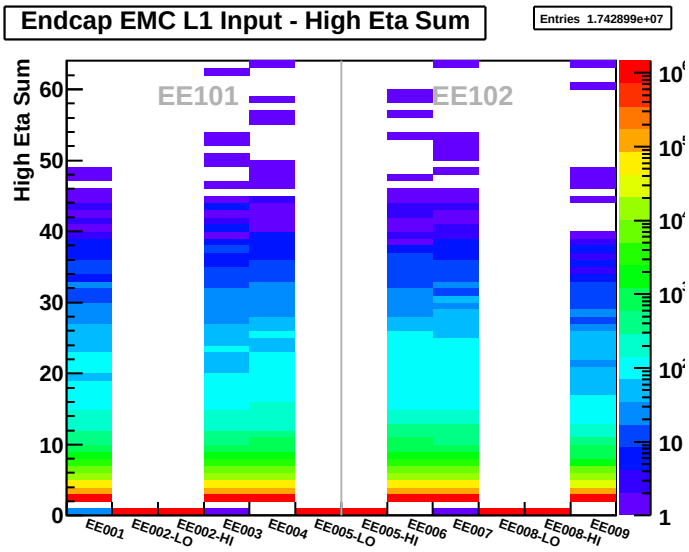

Barrel EMC L1 Input - High Eta Sum
Entries 408
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

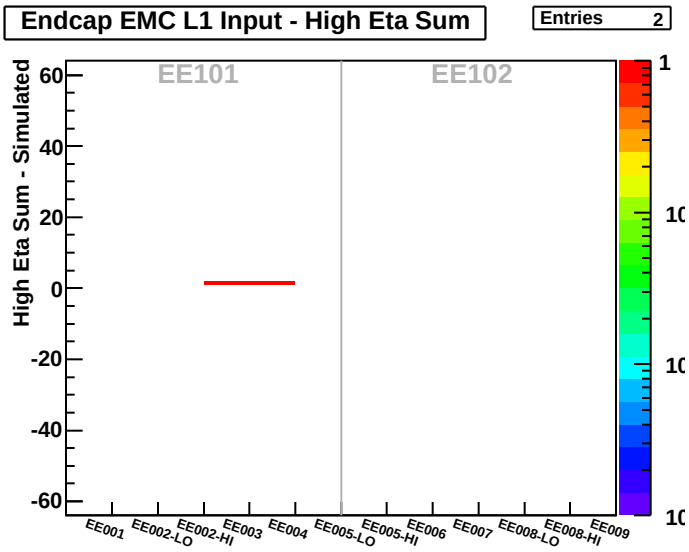

Barrel EMC L1 Input - High Tower Bits
Entries 5.228698e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

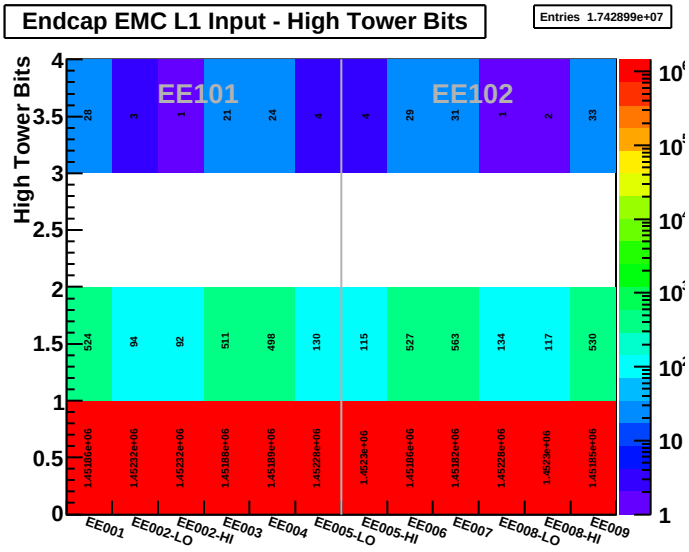

Barrel EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

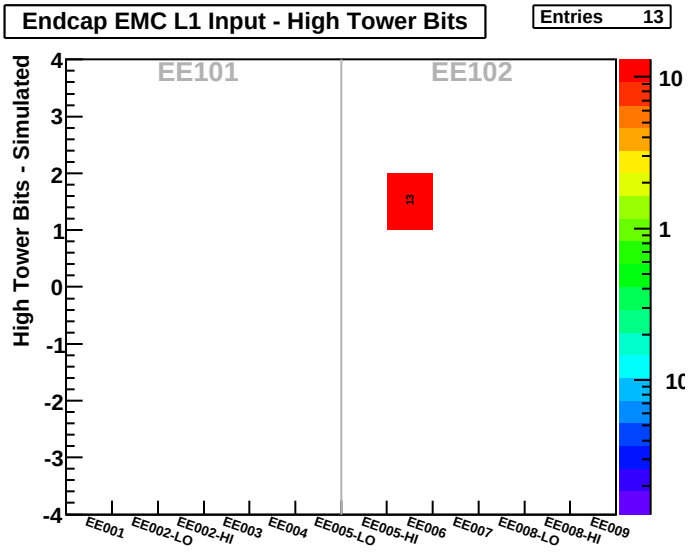

Endcap EMC L1 Input - Low Eta Sum
Entries 1.742899e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

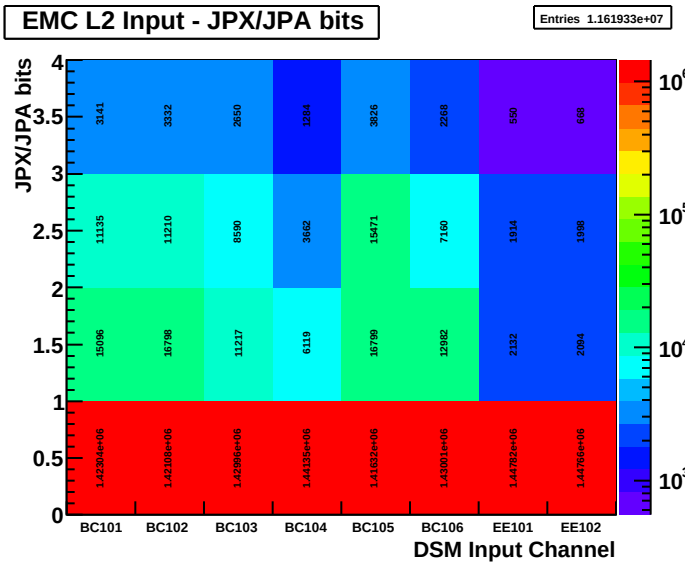

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

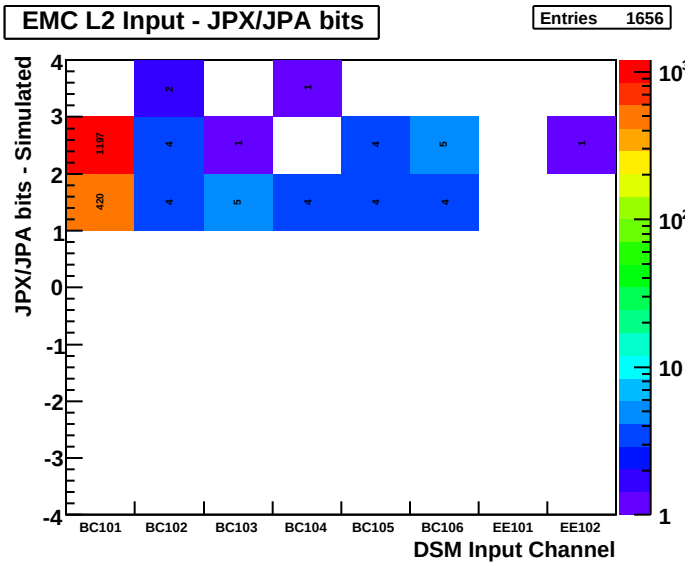

Endcap EMC L1 Input - High Eta Sum
Entries 1.742899e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

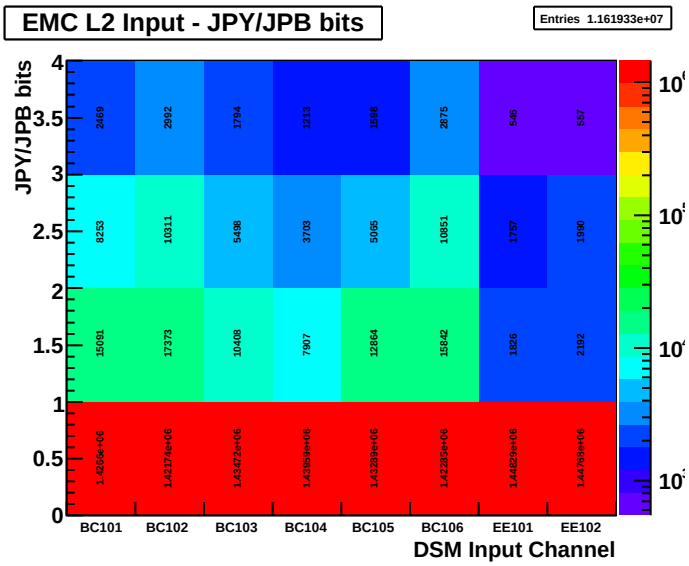

Endcap EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

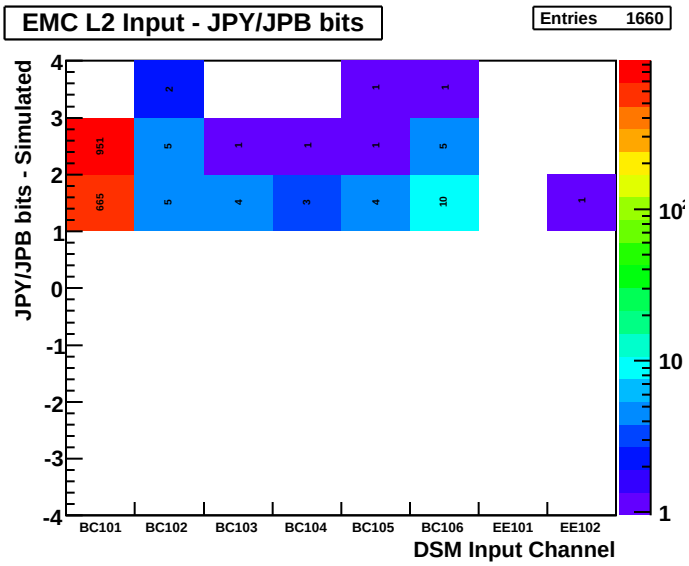

Endcap EMC L1 Input - High Tower Bits
Entries 1.742899e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

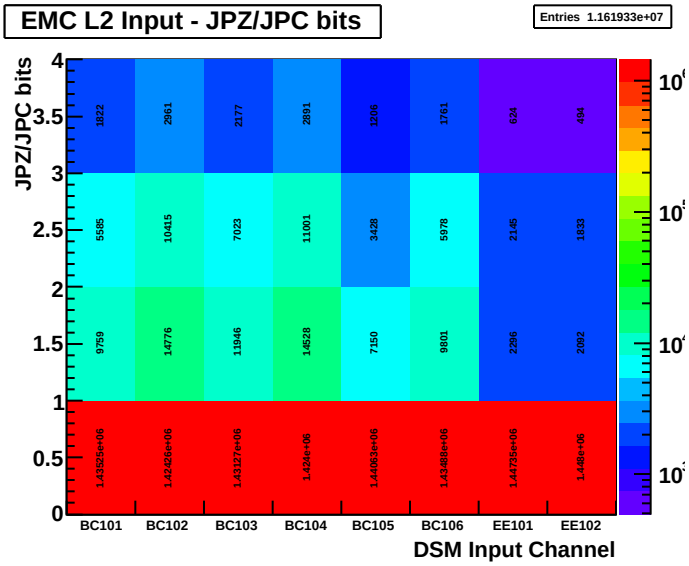

Endcap EMC L1 Input - High Tower Bits
Entries 13
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

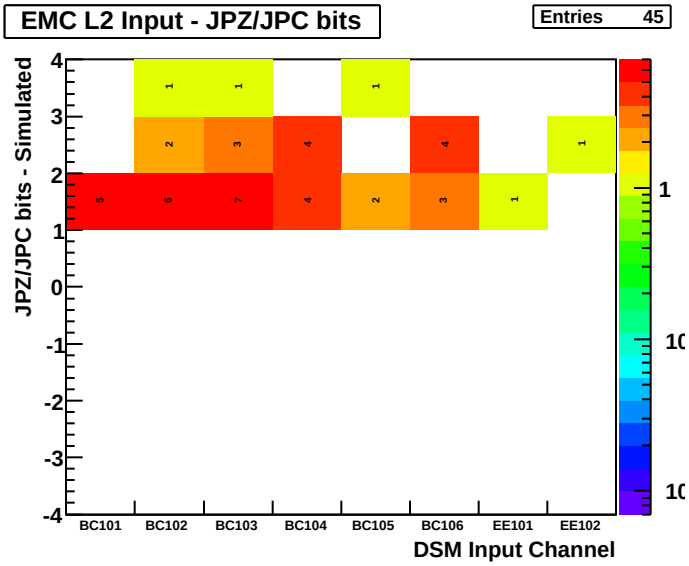

EMC L2 Input - JPX/JPA bits
Entries 1.161933e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3141 3332 2650 1284 3826 2268 550 668
11135 11210 8590 3662 15471 7160 1914 1998
15096 16798 11217 6119 16799 12962 2132 2094
1.42304e+06 1.42108e+06 1.42906e+06 1.44135e+06 1.41632e+06 1.43001e+06 1.44782e+06 1.44766e+06

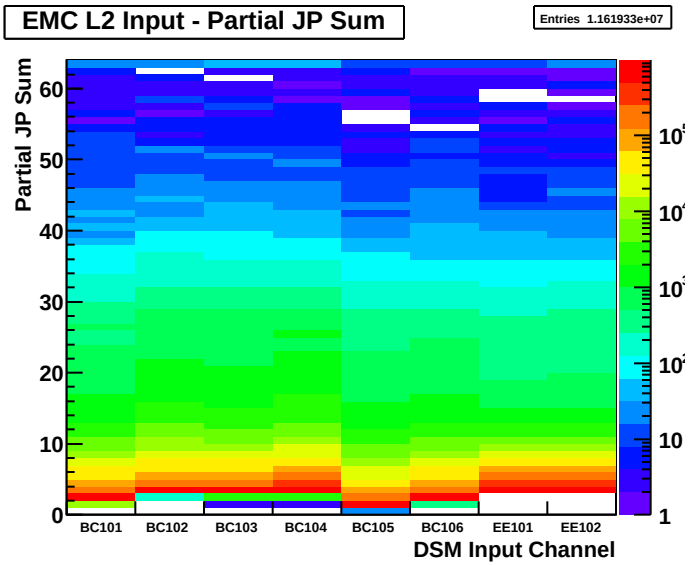

EMC L2 Input - JPX/JPA bits
Entries 1656
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1197 2 1 4 1 4 5 1
420 4 5 4 4 4

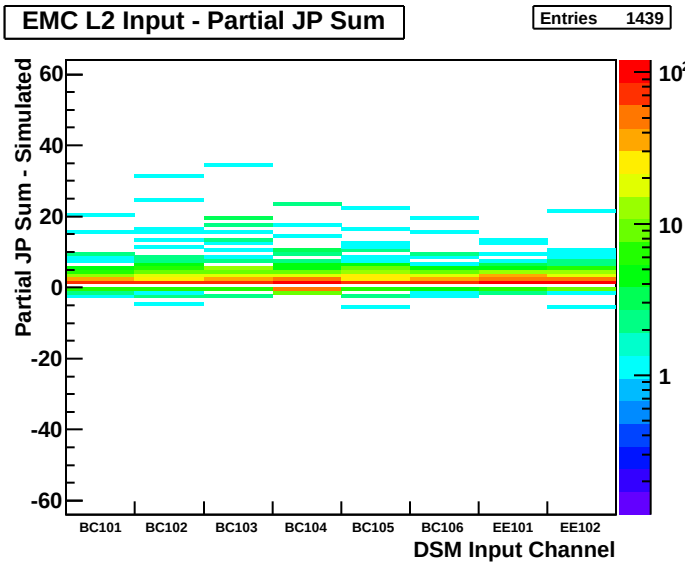

EMC L2 Input - JPY/JPB bits
Entries 1.161933e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2469 2992 1794 1213 1598 2875 546 557
8253 10311 5498 3703 5065 10851 1757 1990
15091 17373 10408 7907 12864 15842 1826 2192
1.4266e+06 1.42174e+06 1.42472e+06 1.43959e+06 1.43289e+06 1.42295e+06 1.44829e+06 1.44768e+06

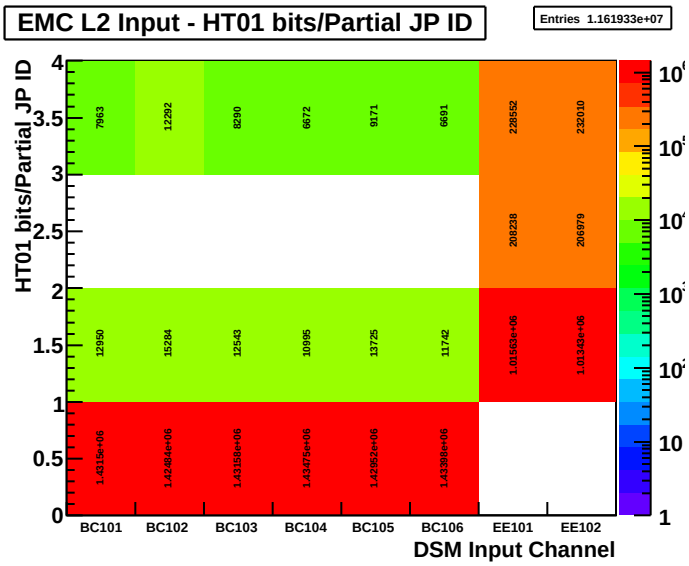

EMC L2 Input - JPY/JPB bits
Entries 1660
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
951 2 1 1 1 1
665 5 5 1 1 1 5
5 4 3 4 10 1

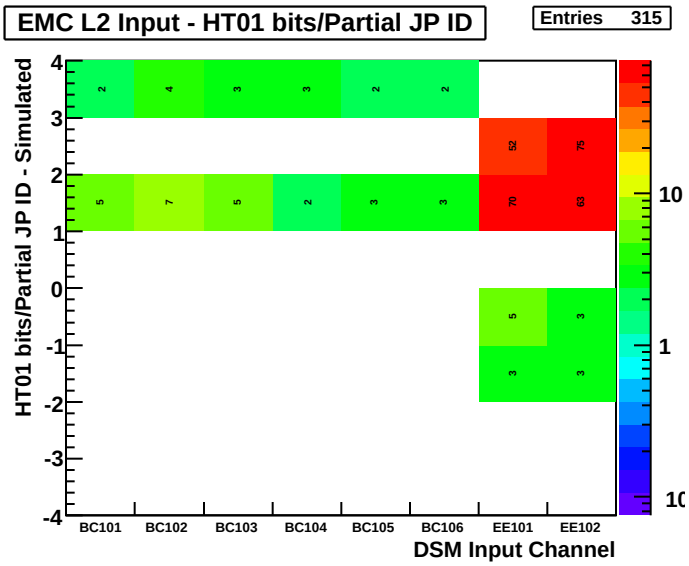

EMC L2 Input - JPZ/JPC bits
Entries 1.161933e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1822 2961 2177 2891 1206 1761 624 494
5585 10415 7023 11001 3428 5978 2145 1833
9759 14776 11946 14628 7150 9801 2236 2092
1.43525e+06 1.42426e+06 1.43127e+06 1.424e+06 1.44063e+06 1.43488e+06 1.44735e+06 1.448e+06

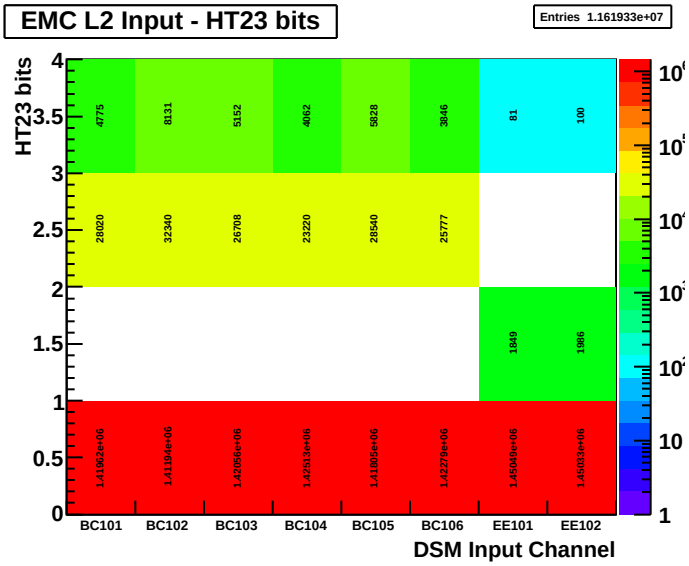

EMC L2 Input - JPZ/JPC bits
Entries 45
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1
2 3 4 4 1
5 6 7 4 2 3 1

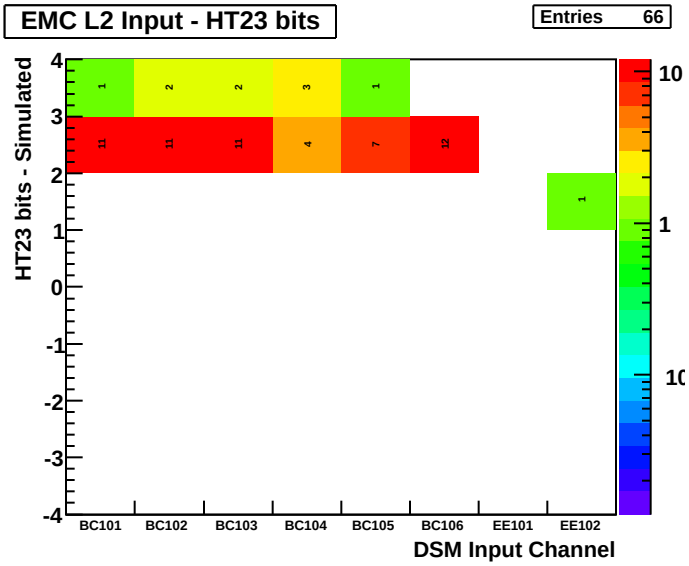

EMC L2 Input - Partial JP Sum
Entries 1.161933e+07
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

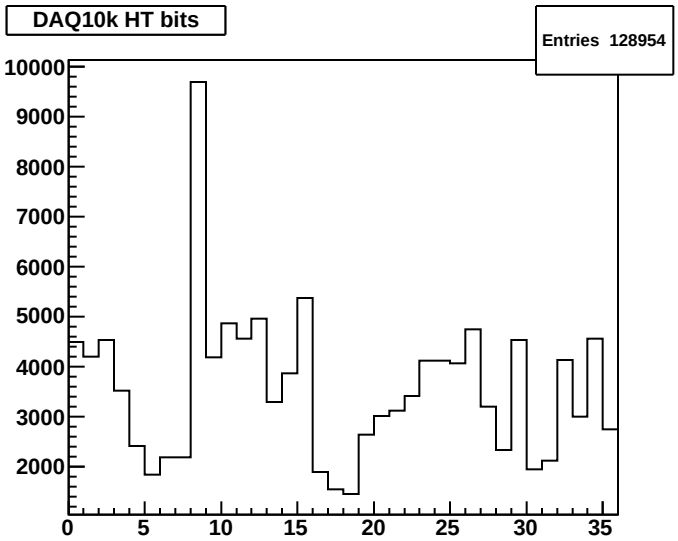

EMC L2 Input - Partial JP Sum
Entries 1439
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

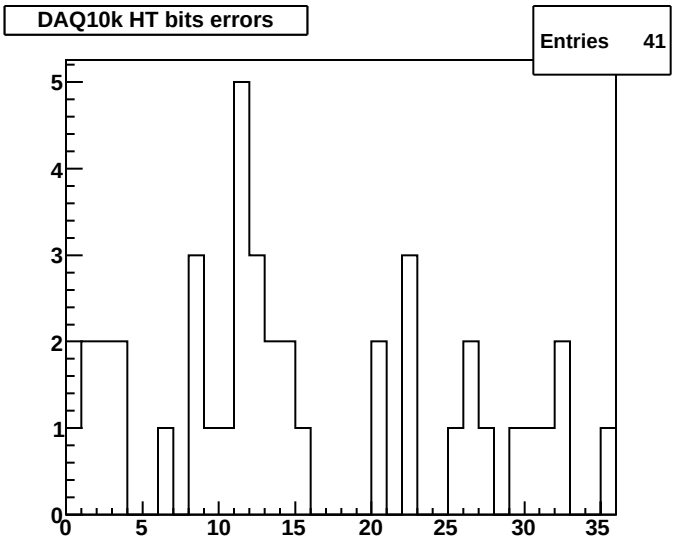

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.161933e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

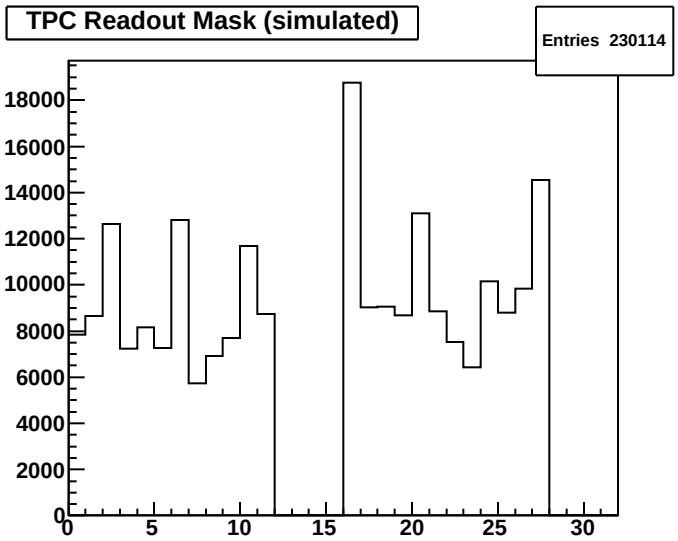

EMC L2 Input - HT01 bits/Partial JP ID
Entries 315
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

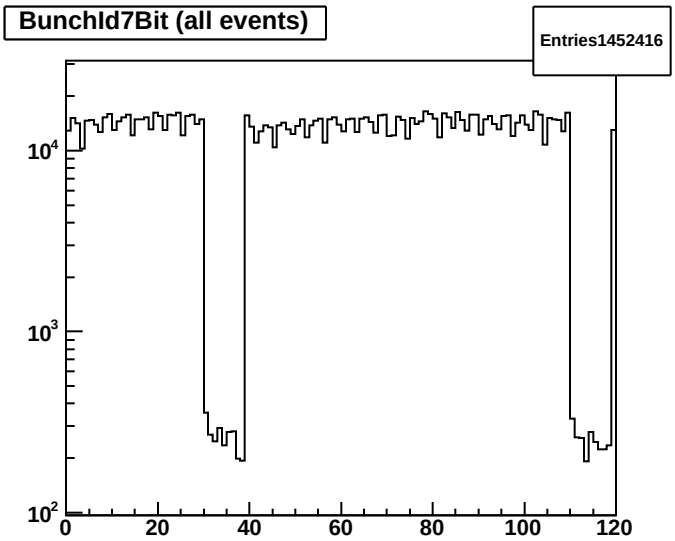

EMC L2 Input - HT23 bits
Entries 1.161933e+07
HT23 bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

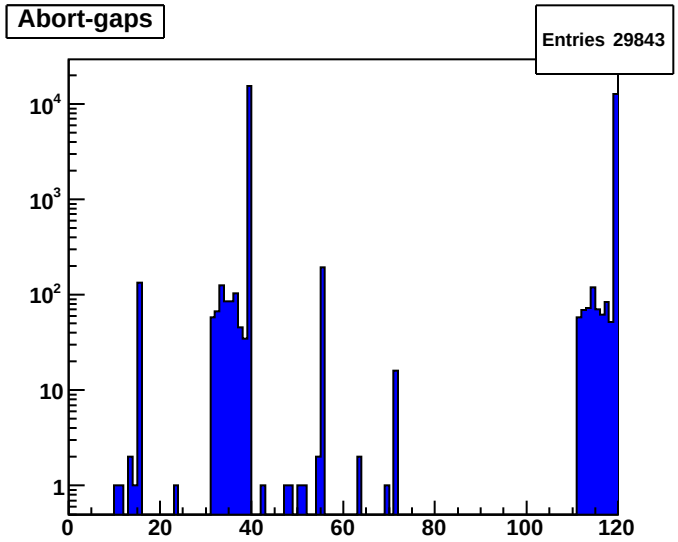

EMC L2 Input - HT23 bits
Entries 66
HT23 bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

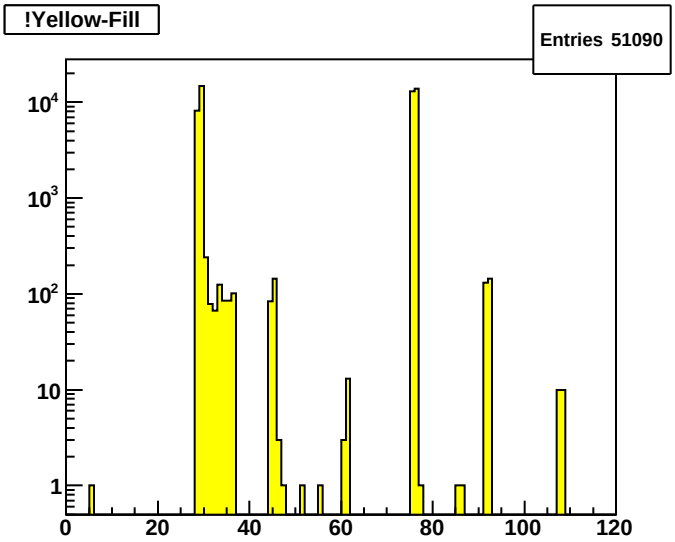

DAQ10k HT bits
Entries 128954

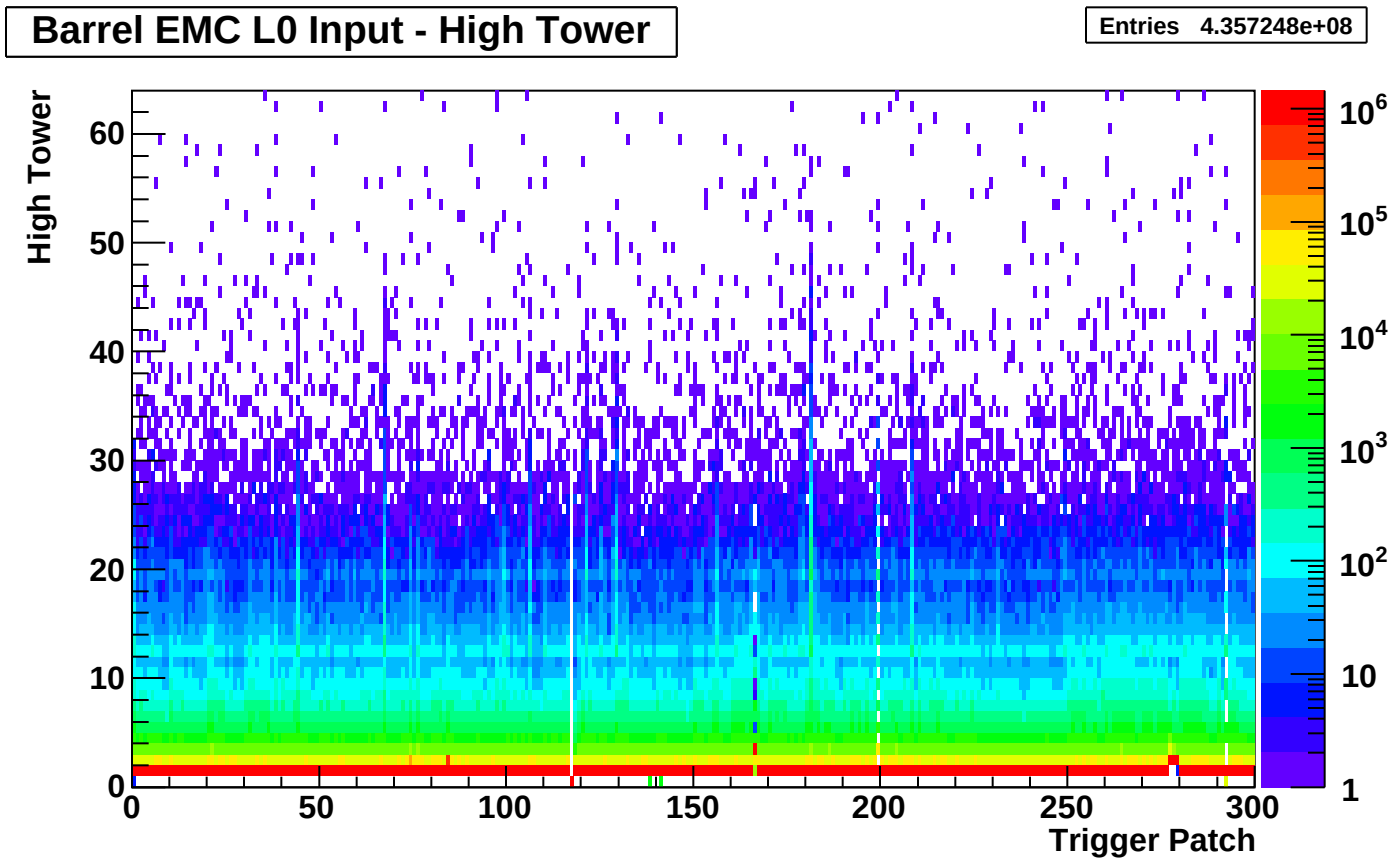

DAQ10k HT bits errors
Entries 41

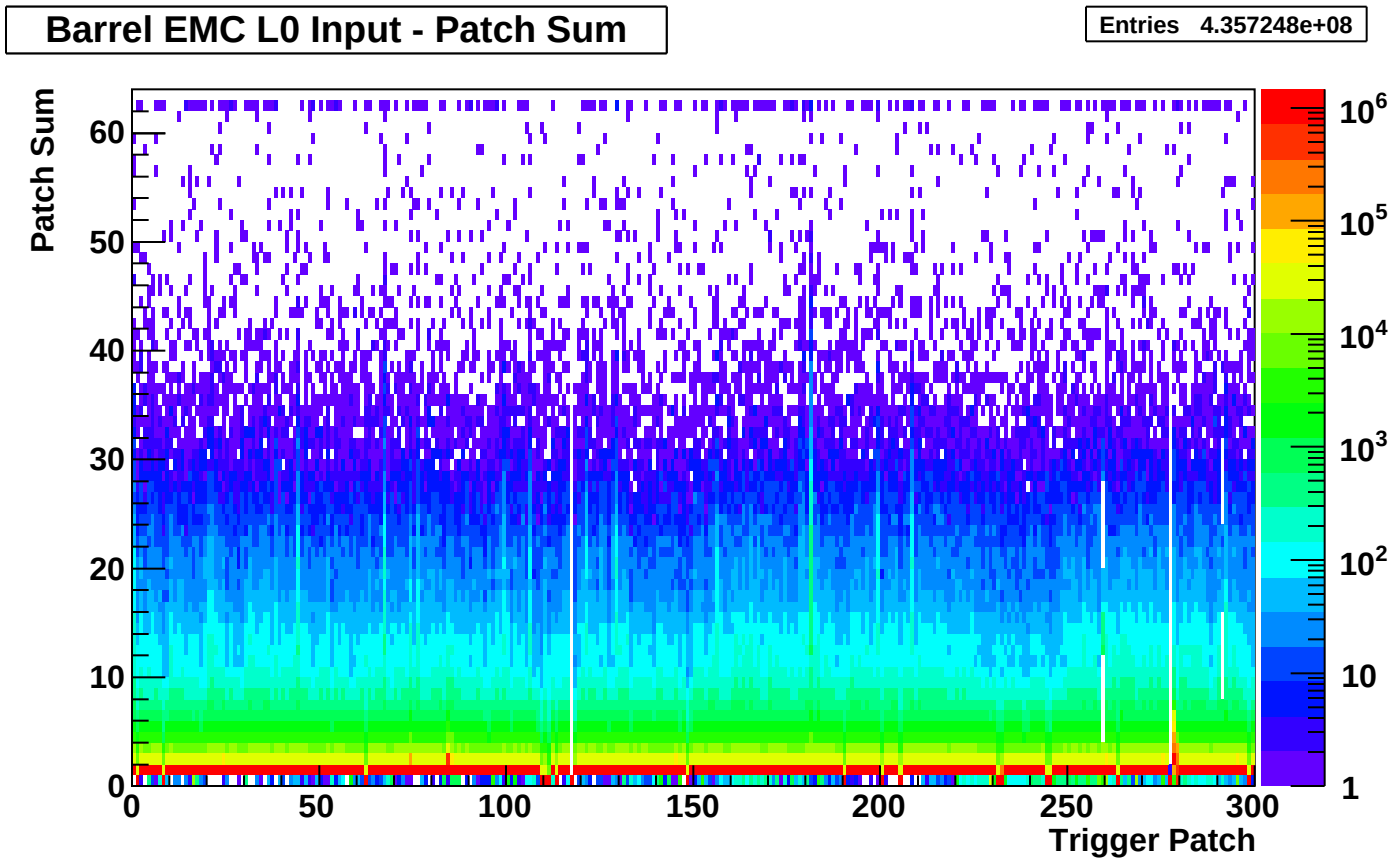

TPC Readout Mask (simulated)
Entries 230114

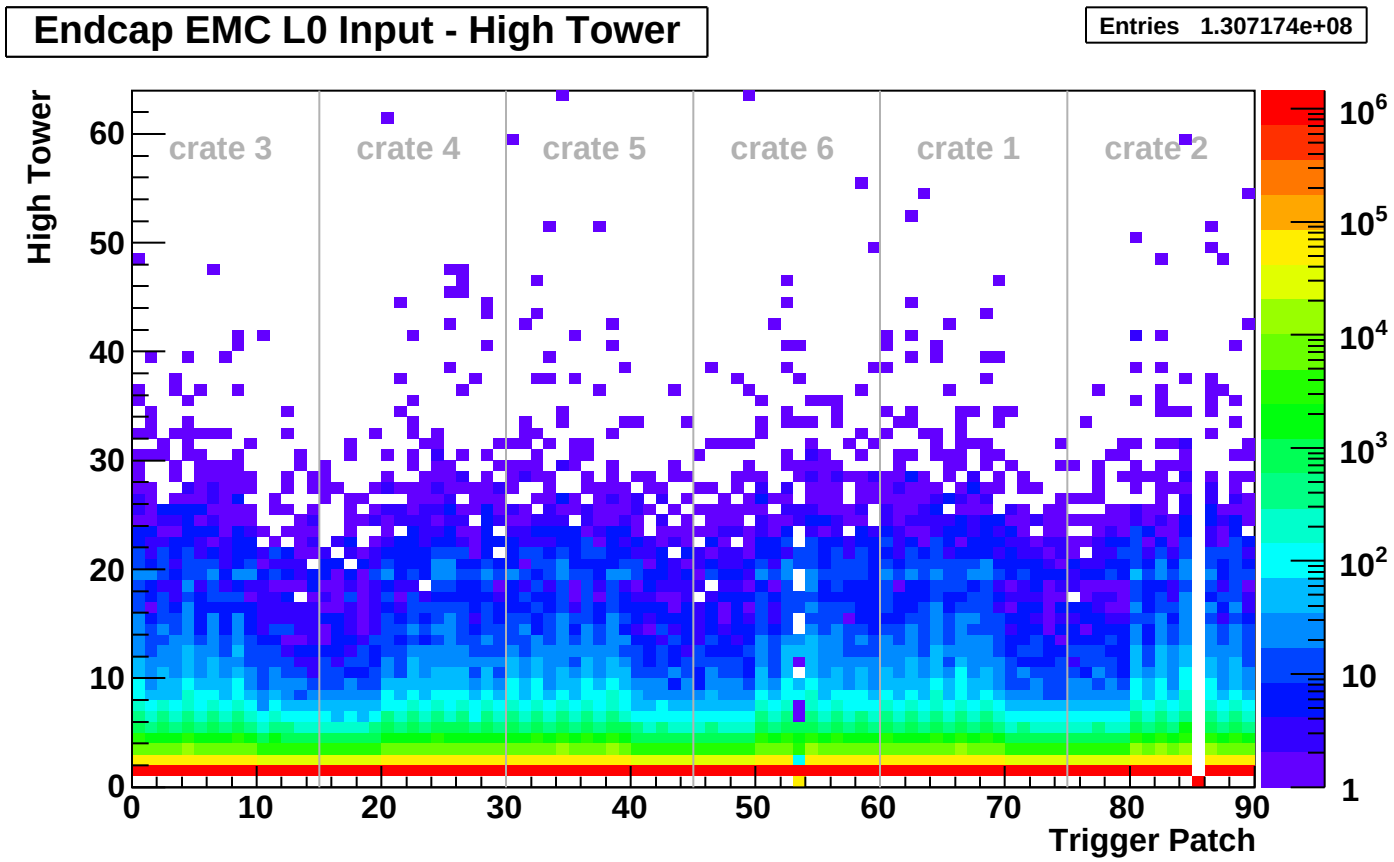

BunchId7Bit (all events)
Entries1452416

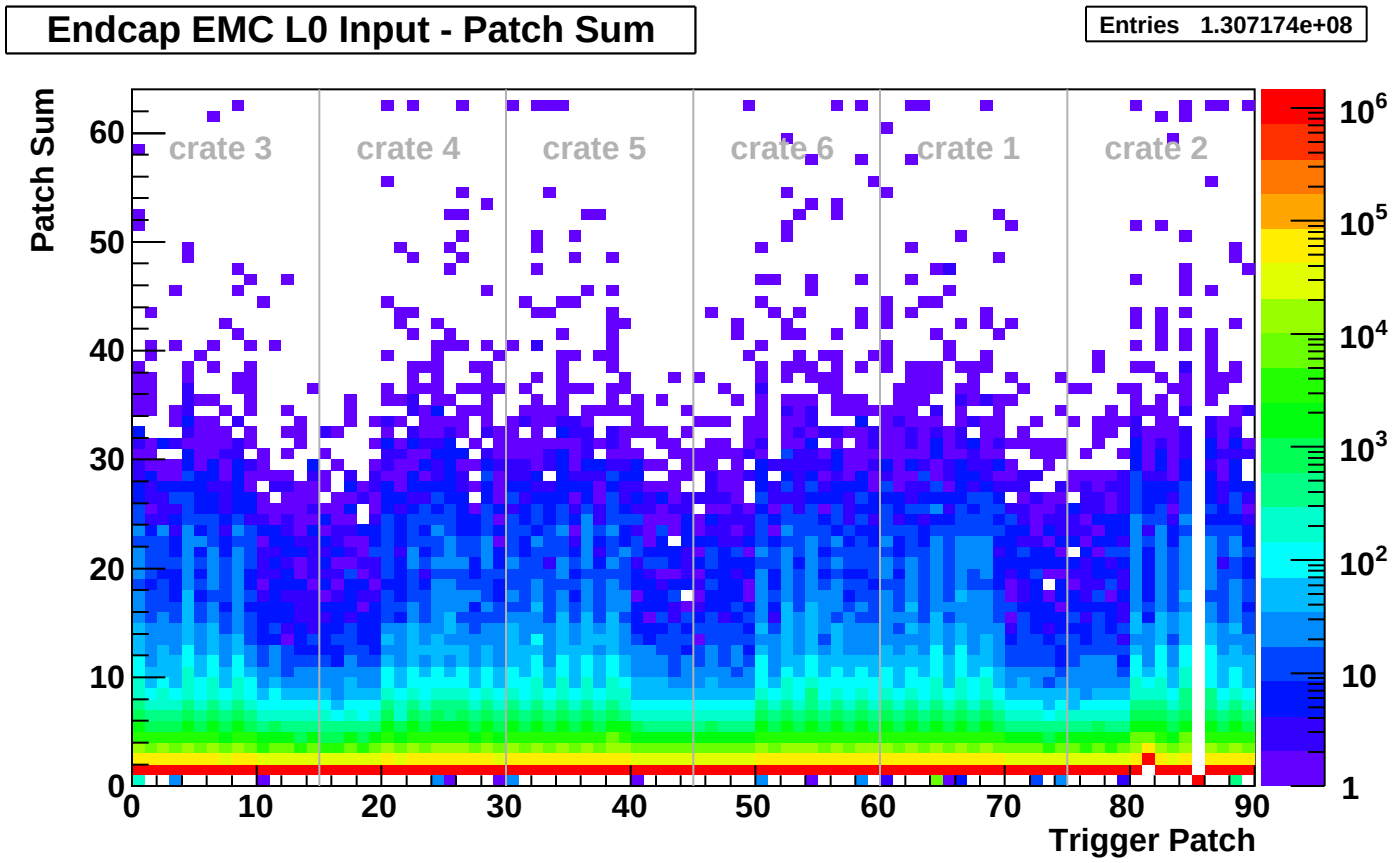

Abort-gaps
Entries 29843

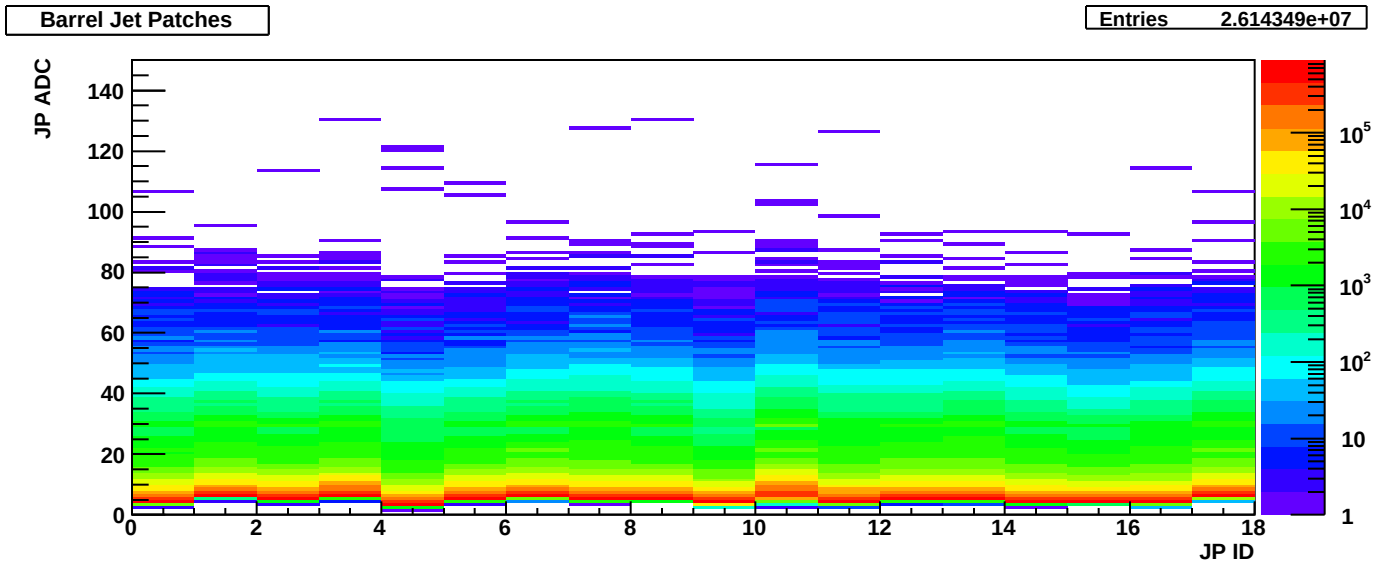

!Yellow-Fill
Entries 51090

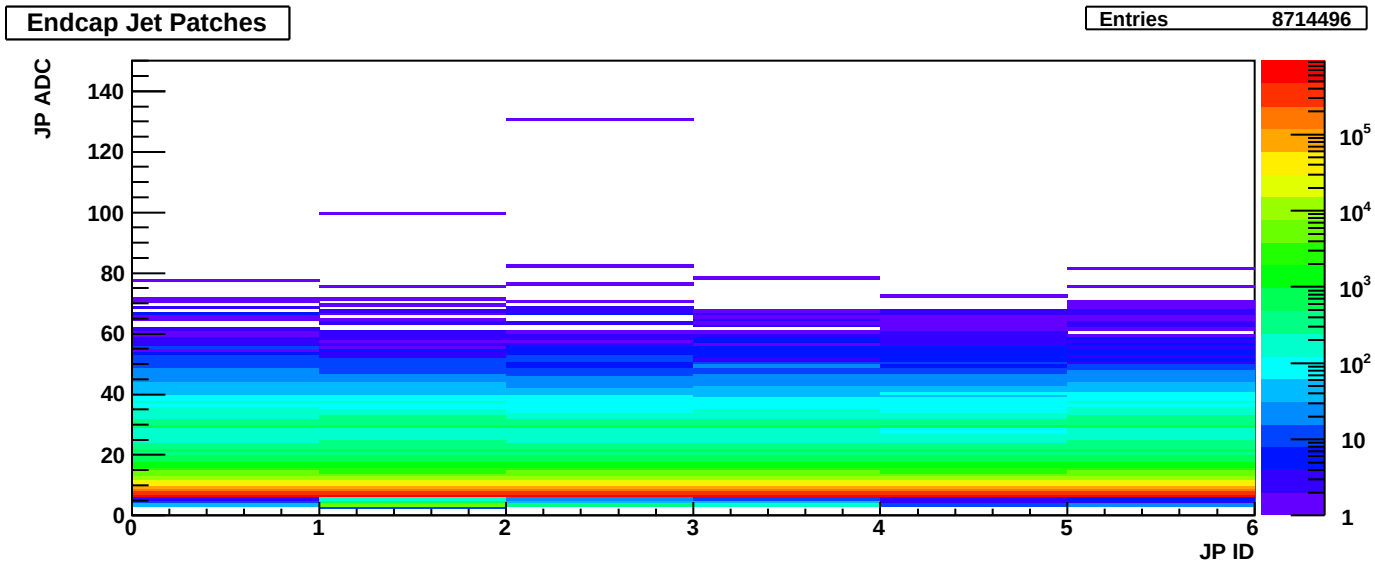

Barrel EMC L0 Input - High Tower
Entries 4.357248e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

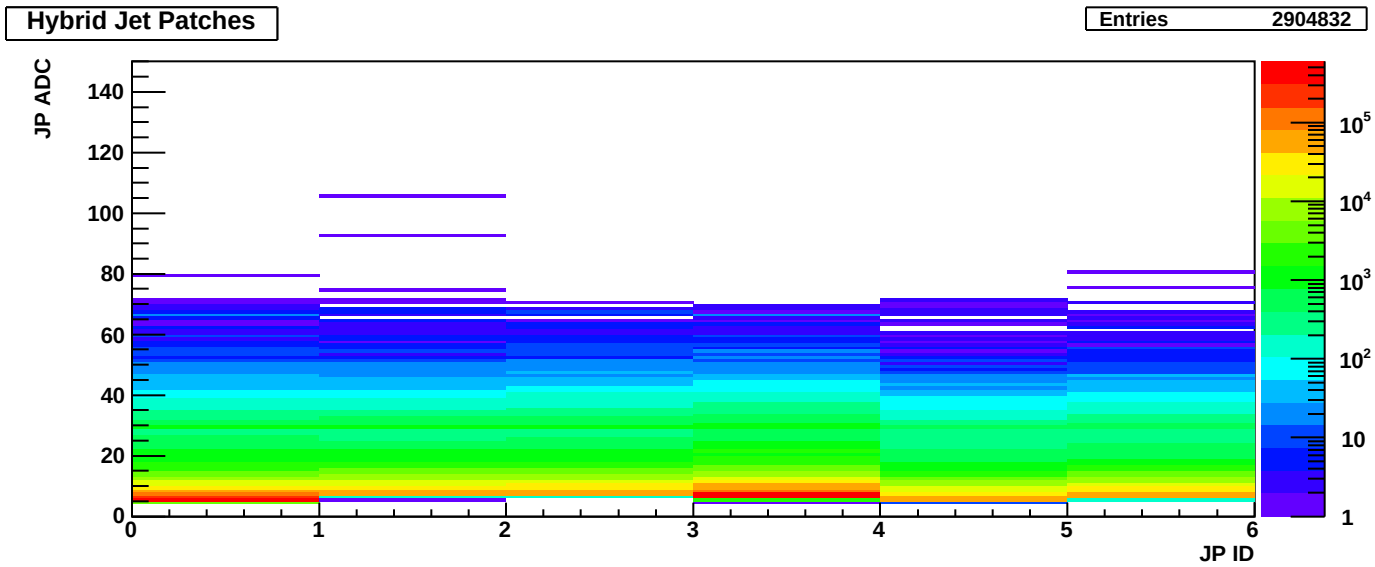

Barrel EMC L0 Input - Patch Sum
Entries 4.357248e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

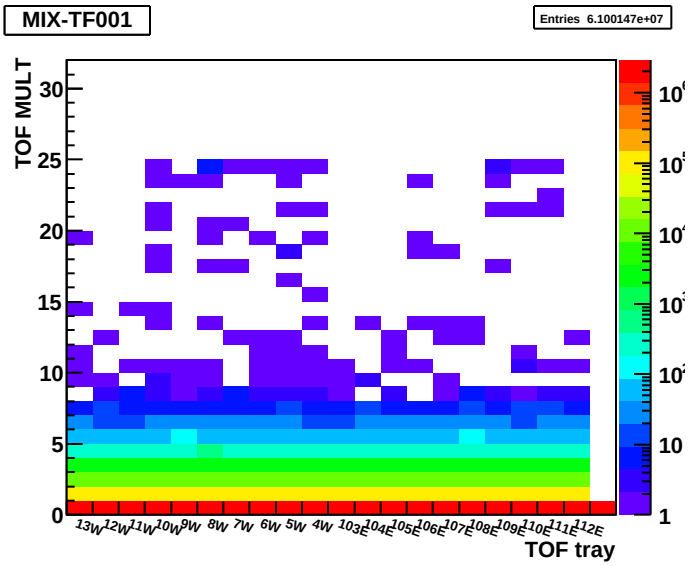

Endcap EMC L0 Input - High Tower
Entries 1.307174e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

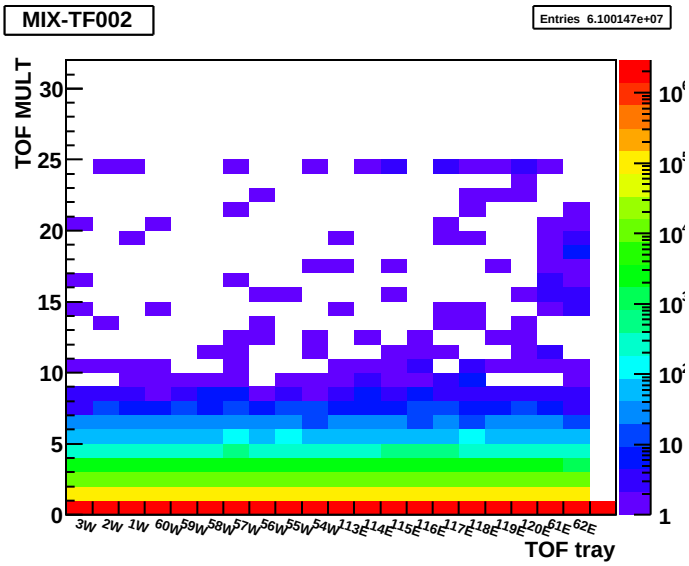

Endcap EMC L0 Input - Patch Sum
Entries 1.307174e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

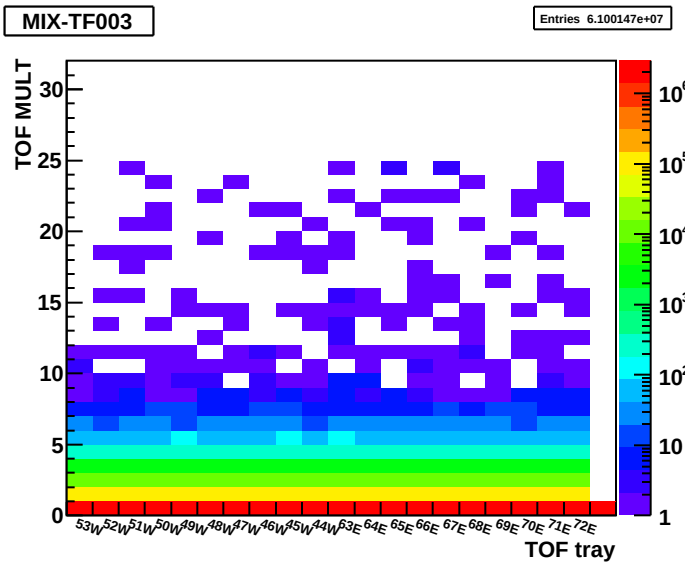

Barrel Jet Patches
Entries 2.614349e+07
JP ADC
JP ID

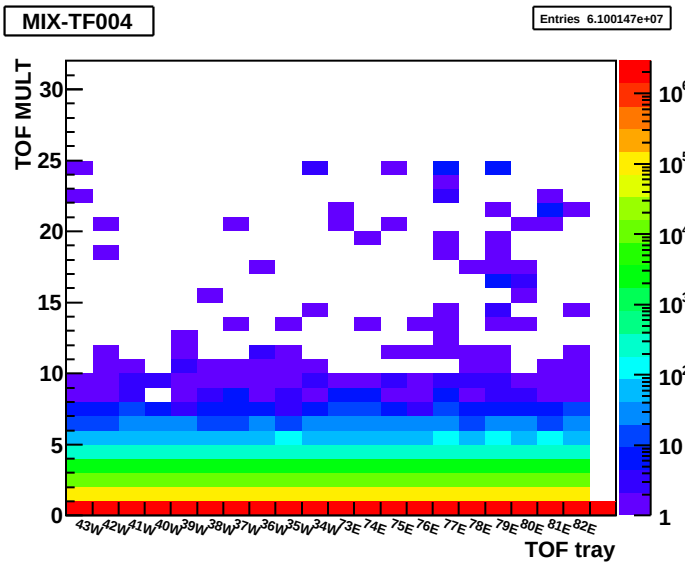

Endcap Jet Patches
Entries 8714496
JP ADC
JP ID

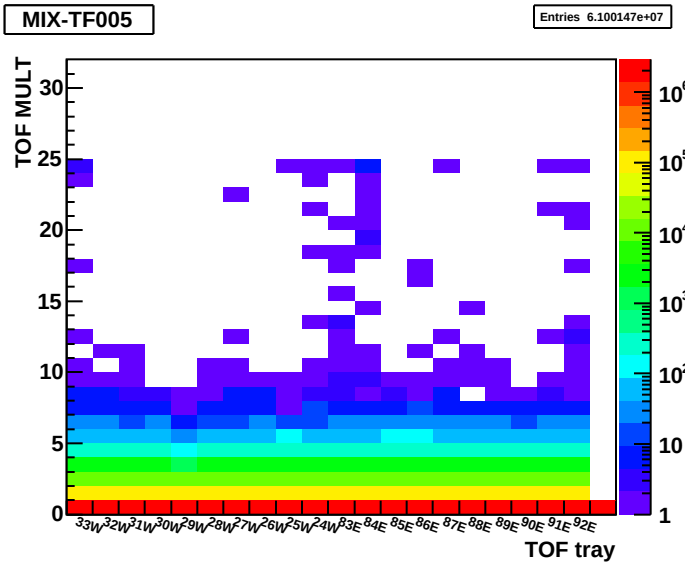

Hybrid Jet Patches
Entries 2904832
JP ADC
JP ID

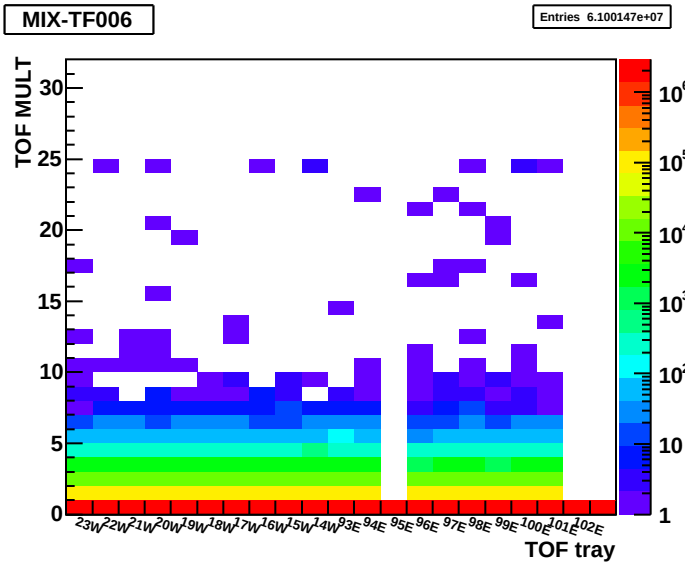

MIX-TF001
Entries 6.100147e+07
TOF MULT
TOF tray

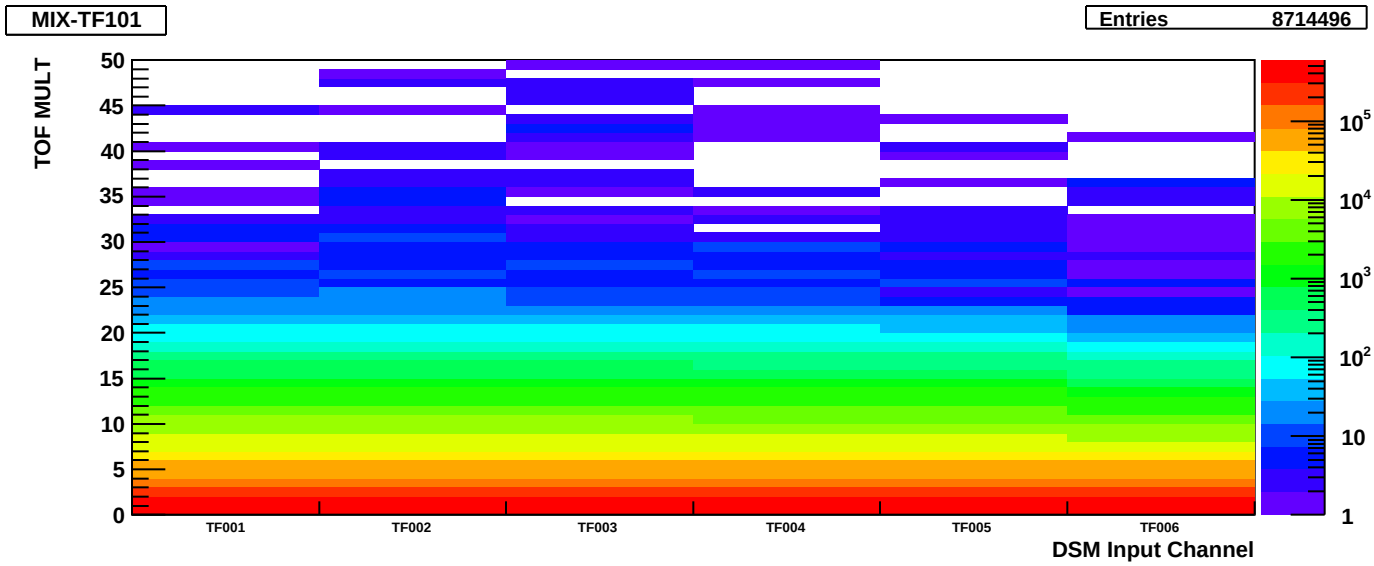

MIX-TF002
Entries 6.100147e+07
TOF MULT
TOF tray

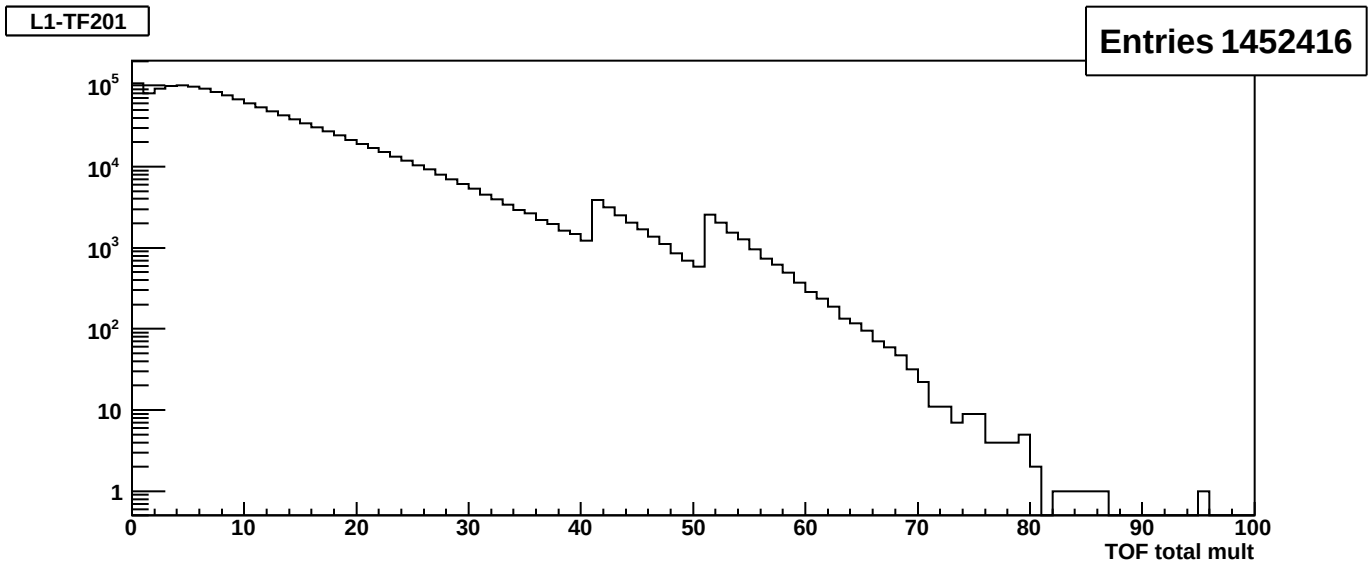

MIX-TF003
Entries 6.100147e+07
TOF MULT
TOF tray

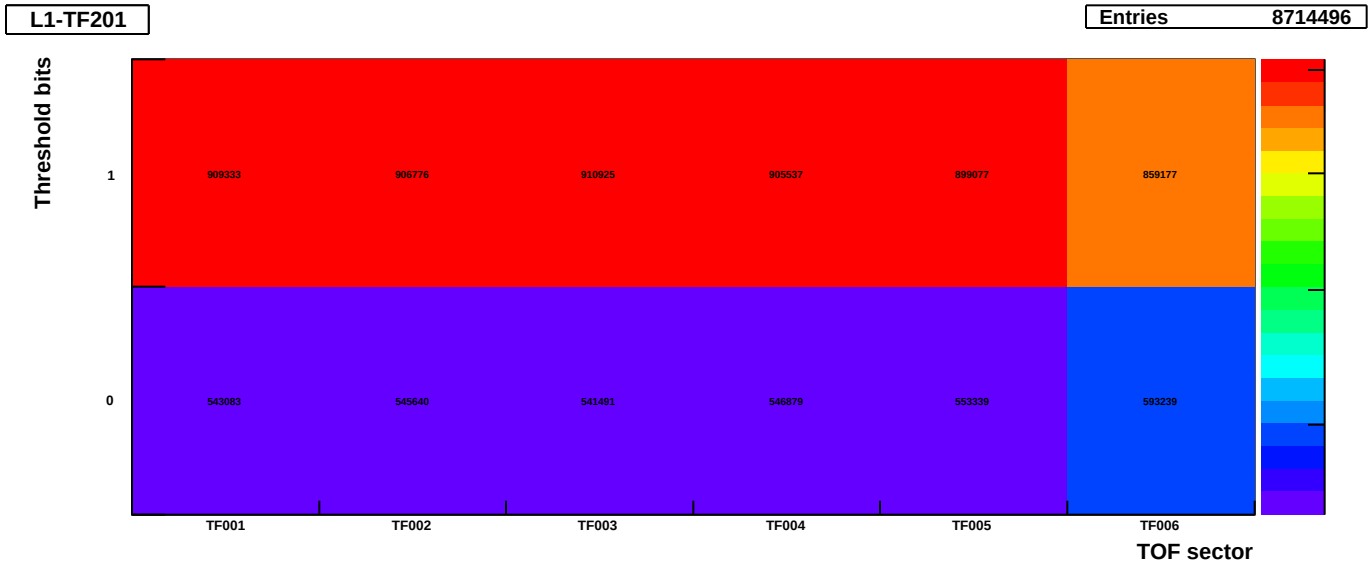

MIX-TF004
Entries 6.100147e+07
TOF MULT
TOF tray

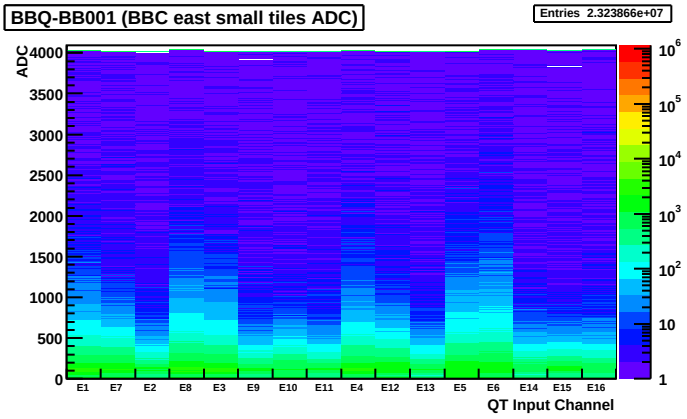

MIX-TF005
Entries 6.100147e+07
TOF MULT
TOF tray

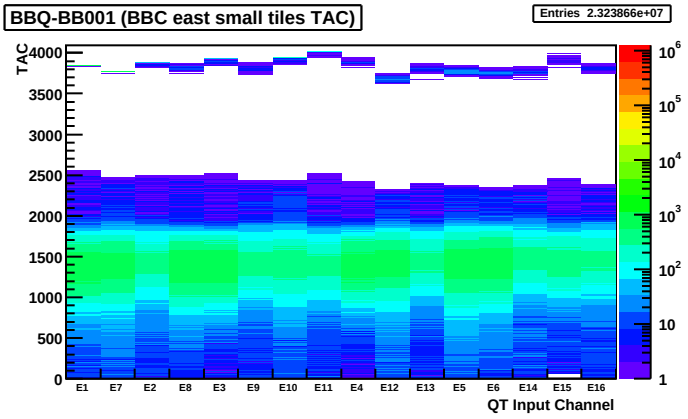

MIX-TF006
Entries 6.100147e+07
TOF MULT
TOF tray

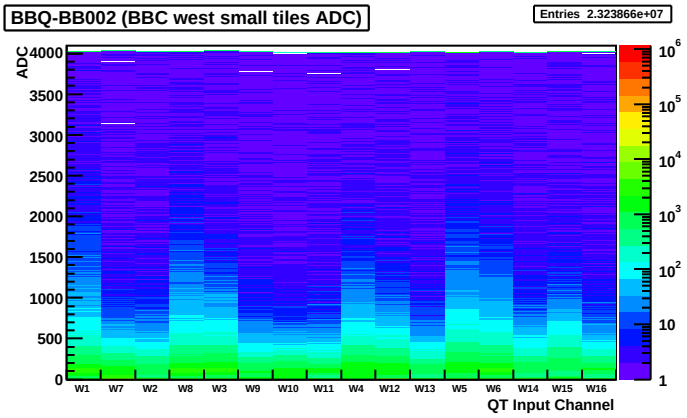

MIX-TF101
Entries 8714496
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

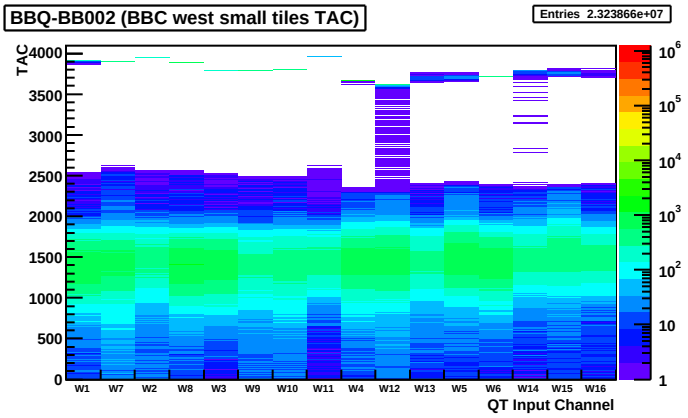

L1-TF201
Entries 1452416
TOF total mult

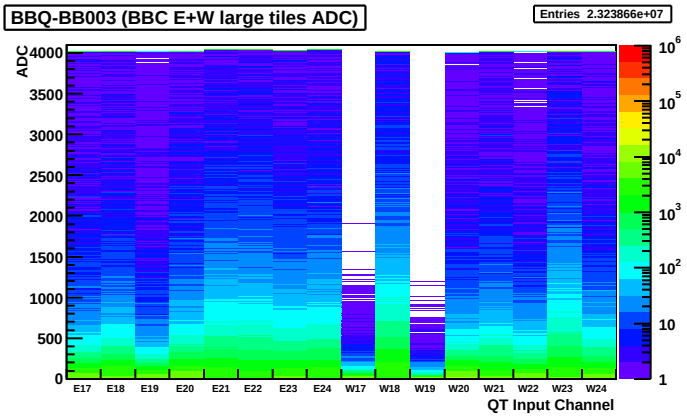

L1-TF201
Entries 8714496
Threshold bits
909333
906776
910925
905537
899077
859177
543083
545640
541491
546879
553339
593239
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

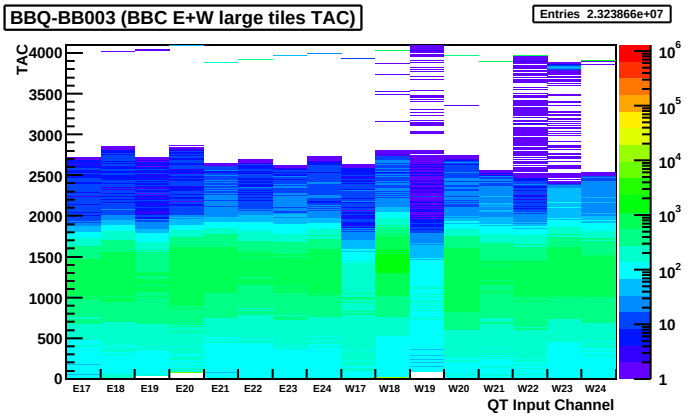

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.323866e+07
ADC
QT Input Channel

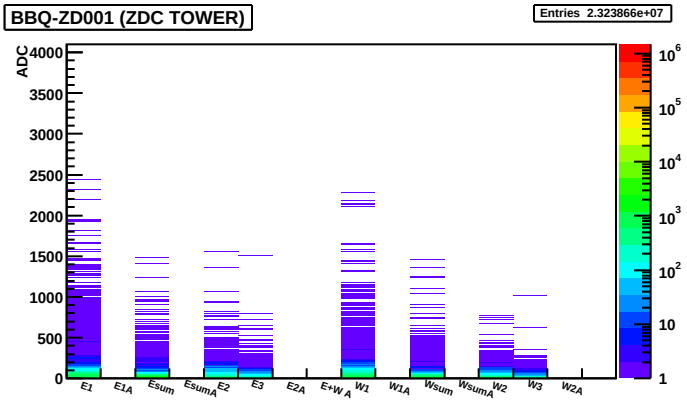

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.323866e+07
TAC
QT Input Channel

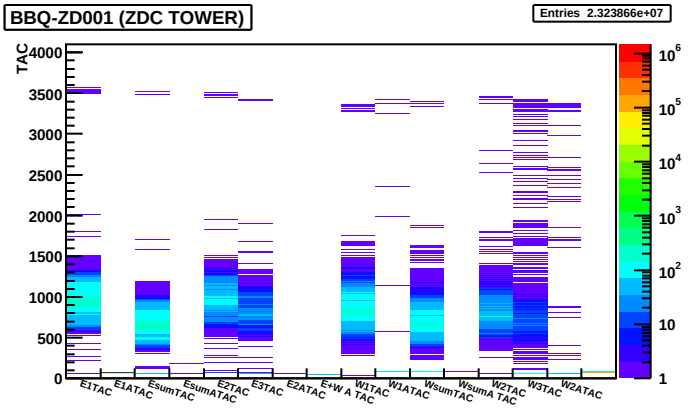

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.323866e+07
ADC
QT Input Channel

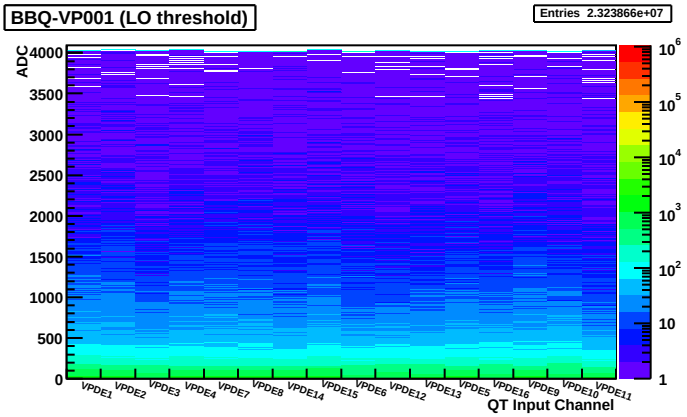

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.323866e+07
TAC
QT Input Channel

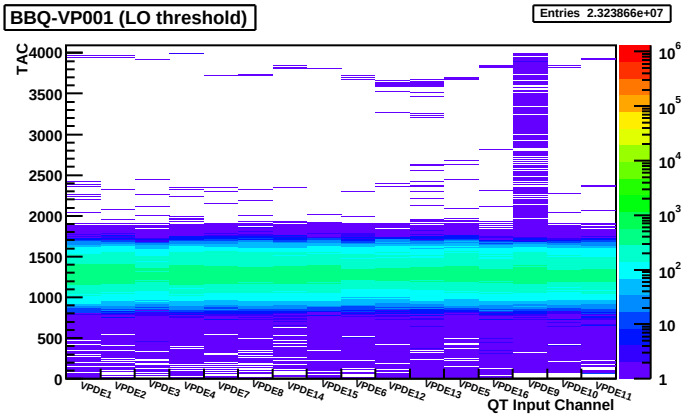

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.323866e+07
ADC
QT Input Channel

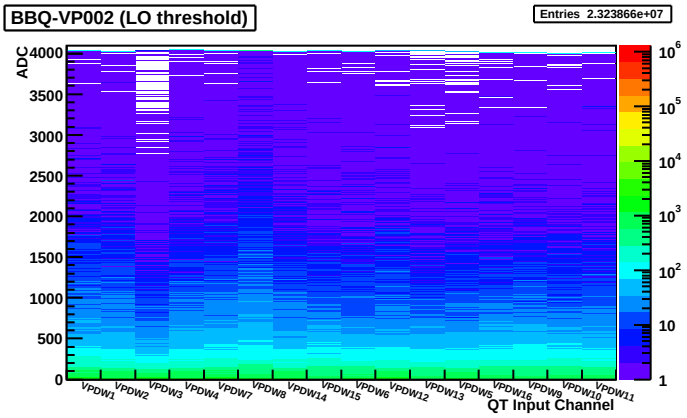

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.323866e+07
TAC
QT Input Channel

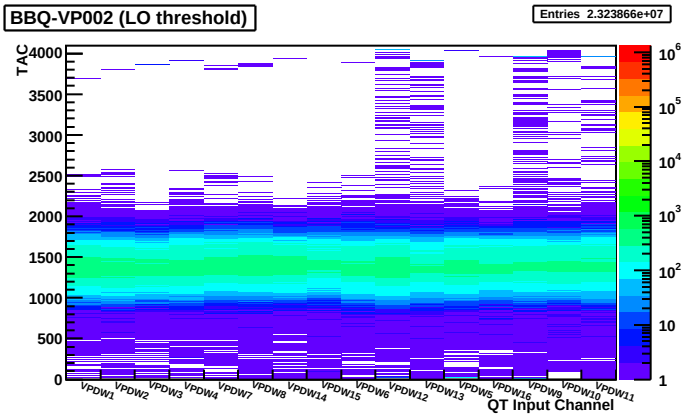

BBQ-ZD001 (ZDC TOWER)
Entries 2.323866e+07
ADC

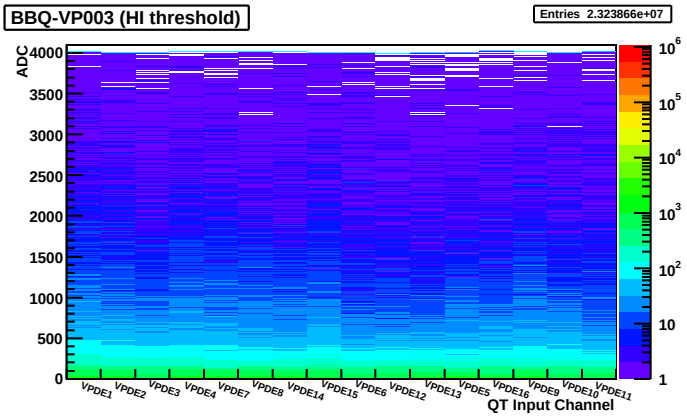

BBQ-ZD001 (ZDC TOWER)
Entries 2.323866e+07
TAC

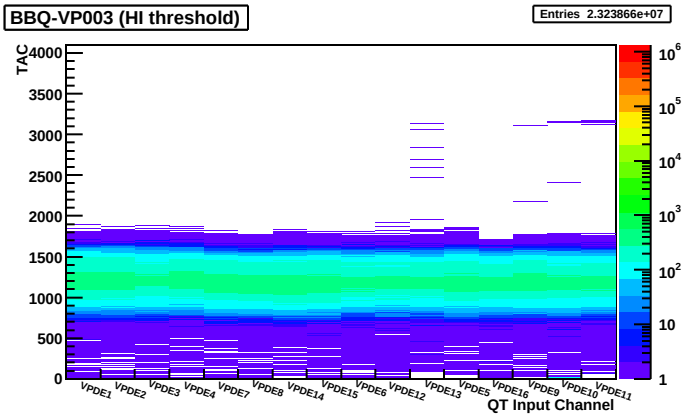

BBQ-VP001 (LO threshold)
Entries 2.323866e+07
ADC
QT Input Channel

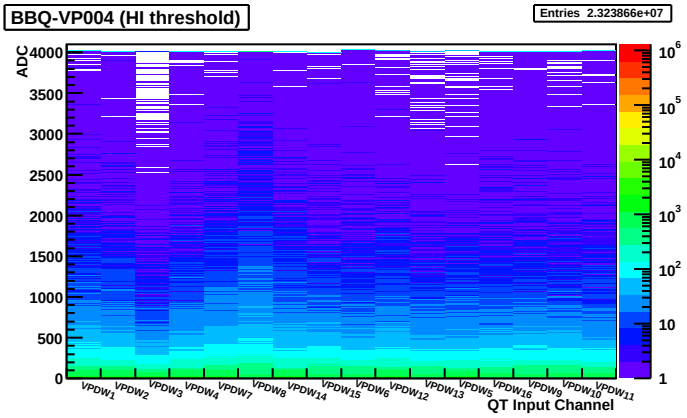

BBQ-VP001 (LO threshold)
Entries 2.323866e+07
TAC
QT Input Channel

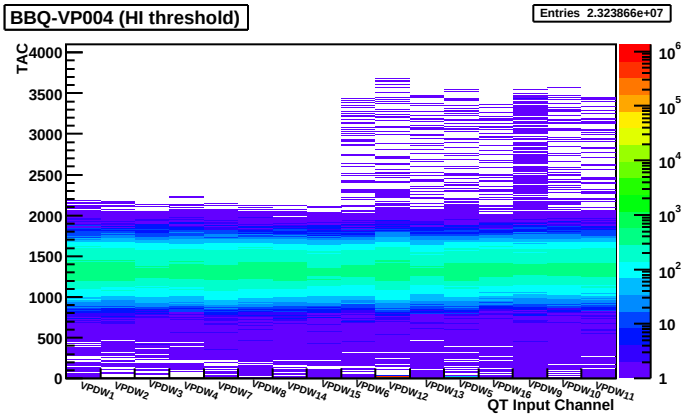

BBQ-VP002 (LO threshold)
Entries 2.323866e+07
ADC
QT Input Channel

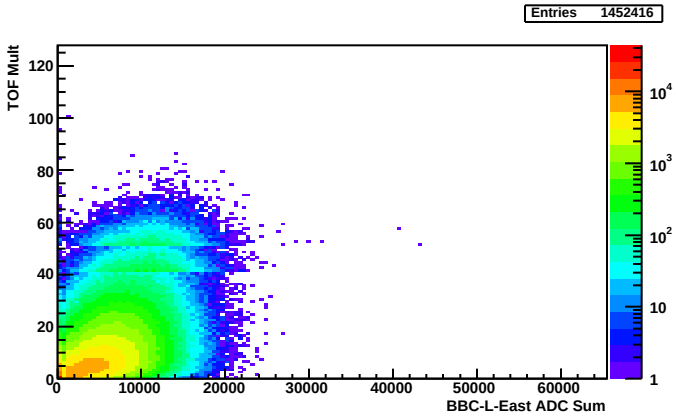

BBQ-VP002 (LO threshold)
Entries 2.323866e+07
TAC
QT Input Channel

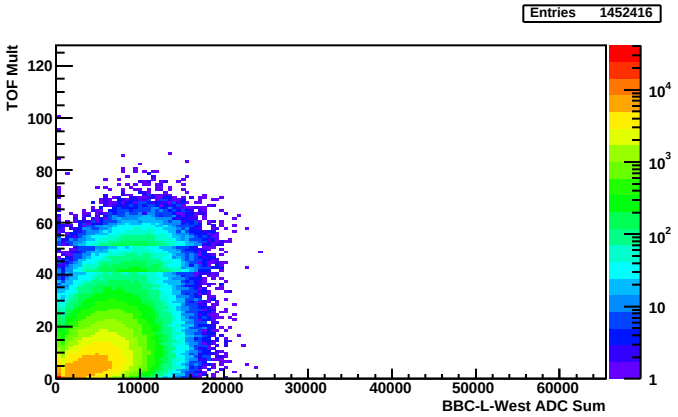

BBQ-VP003 (HI threshold)
Entries 2.323866e+07
ADC
QT Input Channel

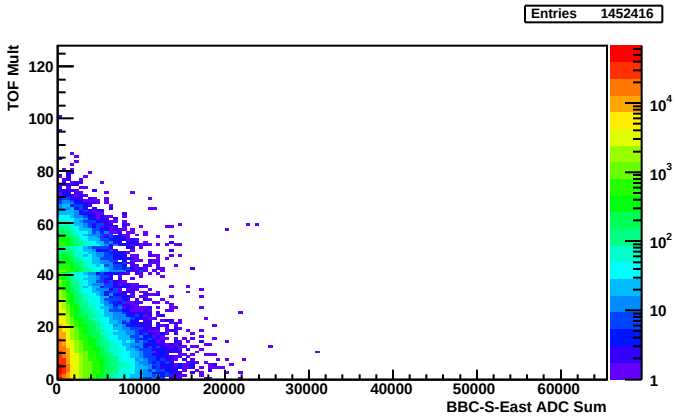

BBQ-VP003 (HI threshold)
Entries 2.323866e+07
TAC
QT Input Channel

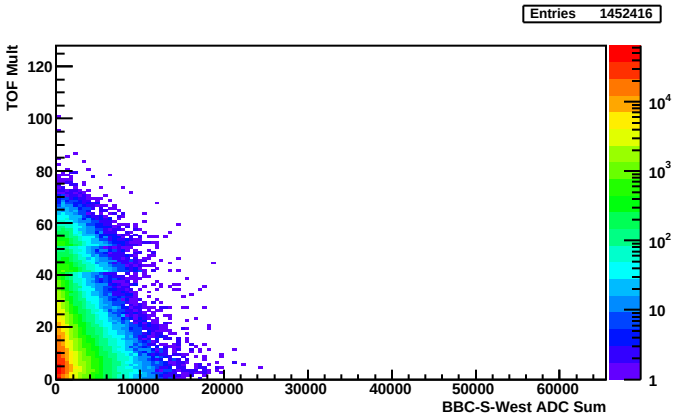

BBQ-VP004 (HI threshold)
Entries 2.323866e+07
ADC
QT Input Channel

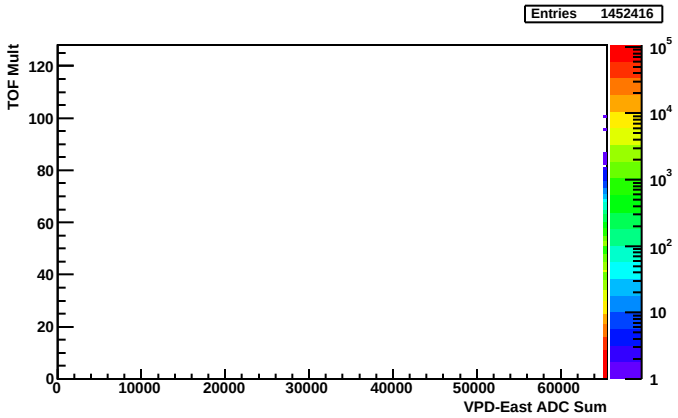

BBQ-VP004 (HI threshold)
Entries 2.323866e+07
TAC
QT Input Channel

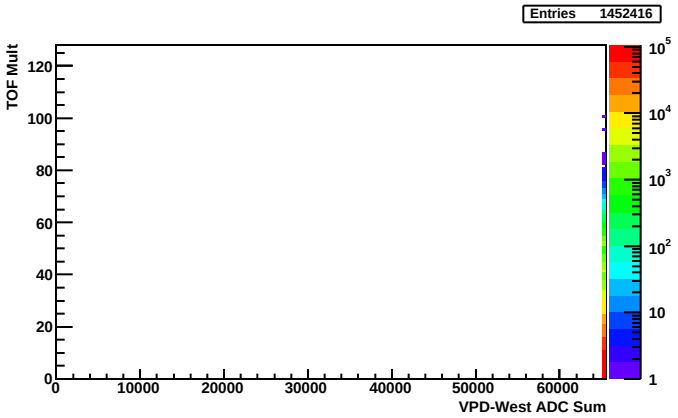

Entries 1452416
TOF Mult
BBC-L-East ADC Sum

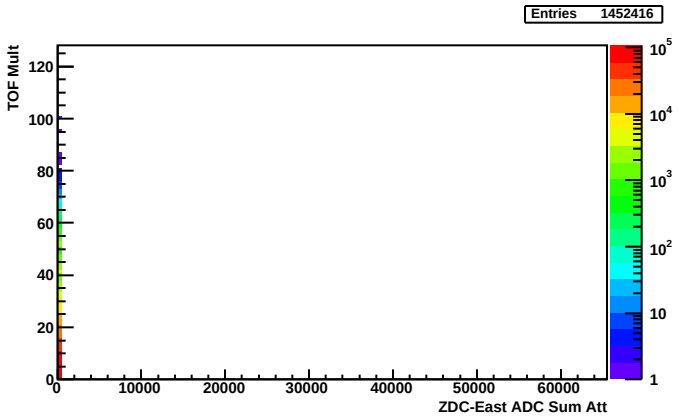

Entries 1452416
TOF Mult
BBC-L-West ADC Sum

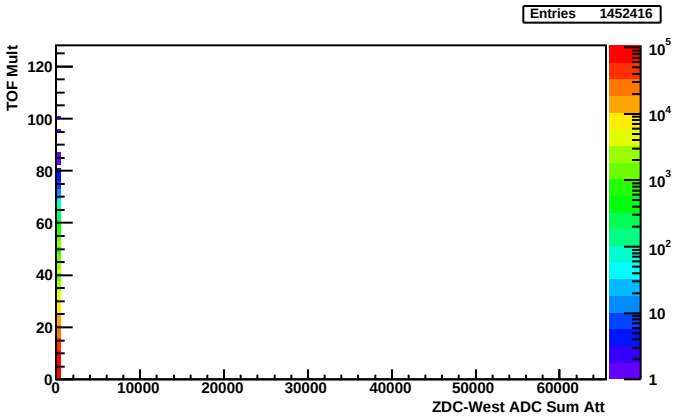

Entries 1452416
TOF Mult
BBC-S-East ADC Sum

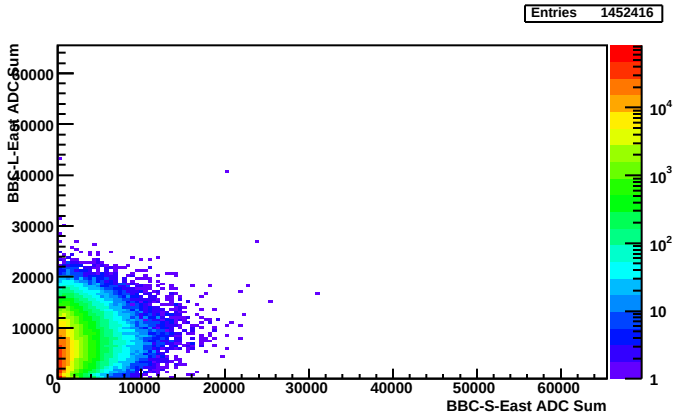

Entries 1452416
TOF Mult
BBC-S-West ADC Sum

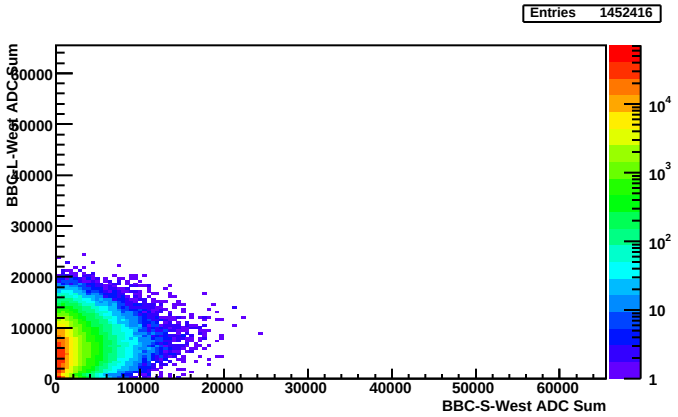

Entries 1452416
TOF Mult
VPD-East ADC Sum

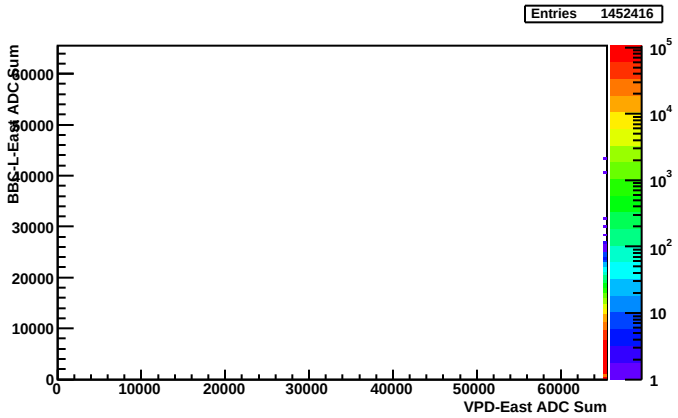

Entries 1452416
TOF Mult
VPD-West ADC Sum

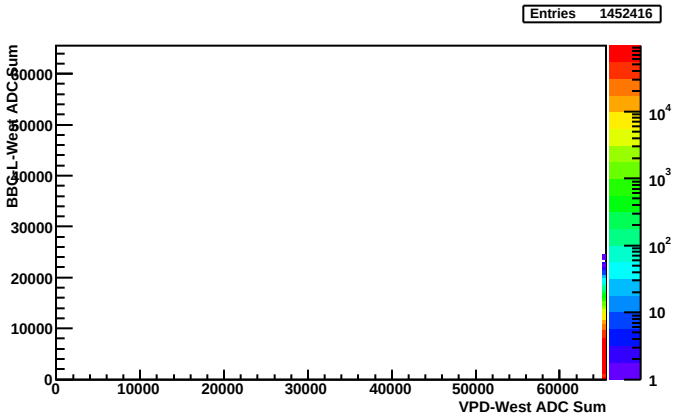

Entries 1452416
TOF Mult
ZDC-East ADC Sum Att

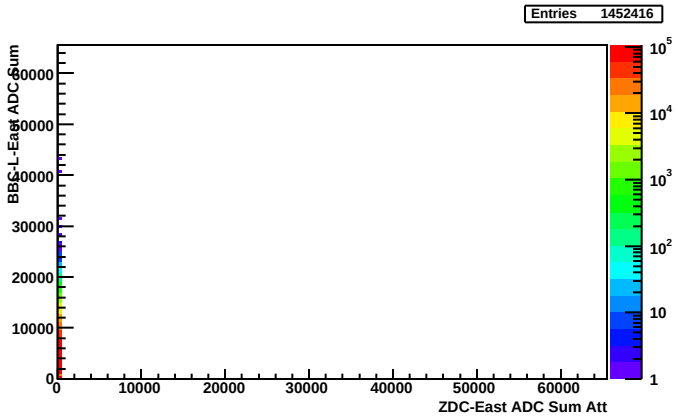

Entries 1452416
TOF Mult
ZDC-West ADC Sum Att

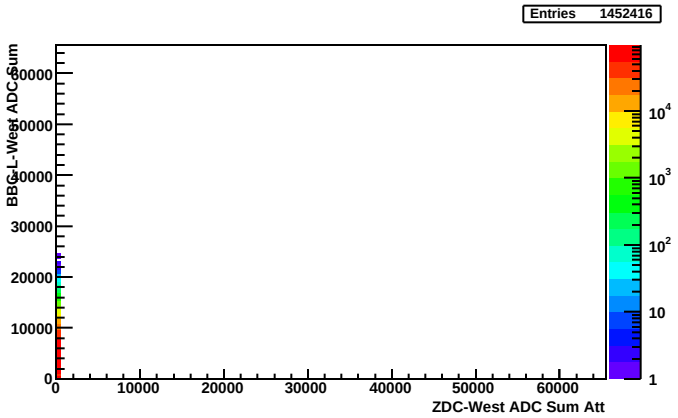

Entries 1452416
BBC-L-East ADC Sum
BBC-S-East ADC Sum

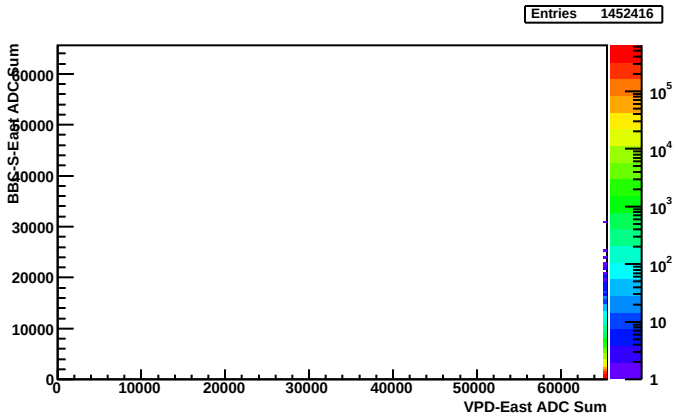

Entries 1452416
BBC-L-West ADC Sum
BBC-S-West ADC Sum

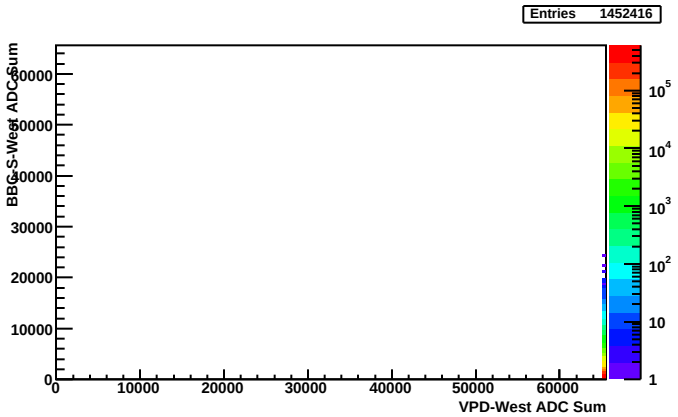

Entries 1452416
BBC-L-East ADC Sum
VPD-East ADC Sum

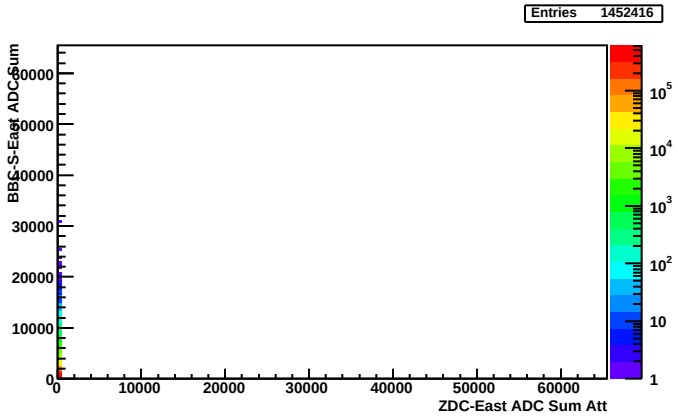

Entries 1452416
BBC-L-West ADC Sum
VPD-West ADC Sum

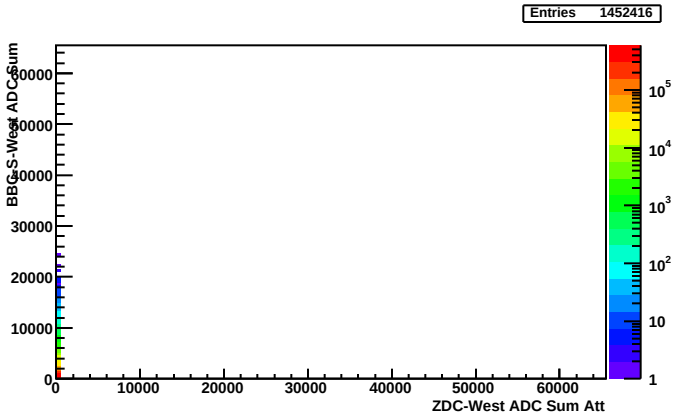

Entries 1452416
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

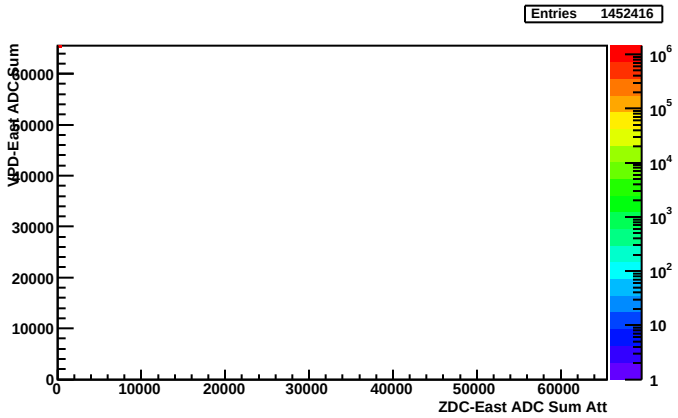

Entries 1452416
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

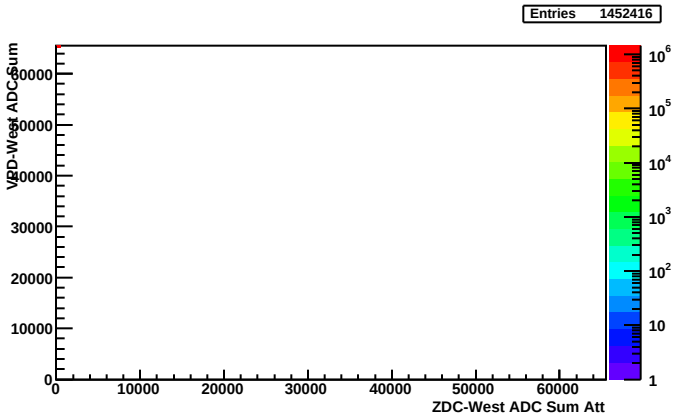

Entries 1452416
BBC-S-East ADC Sum
VPD-East ADC Sum

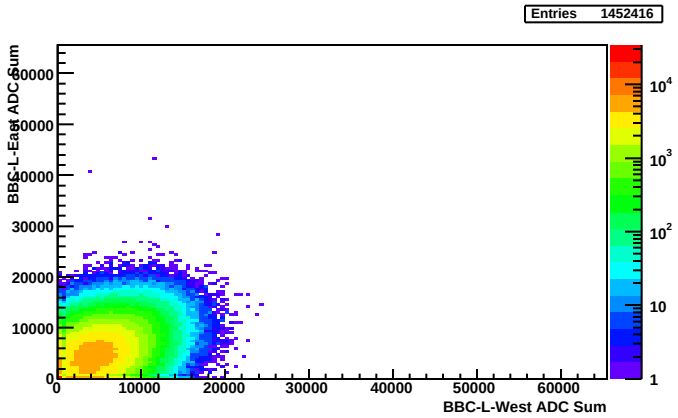

Entries 1452416
BBC-S-West ADC Sum
VPD-West ADC Sum

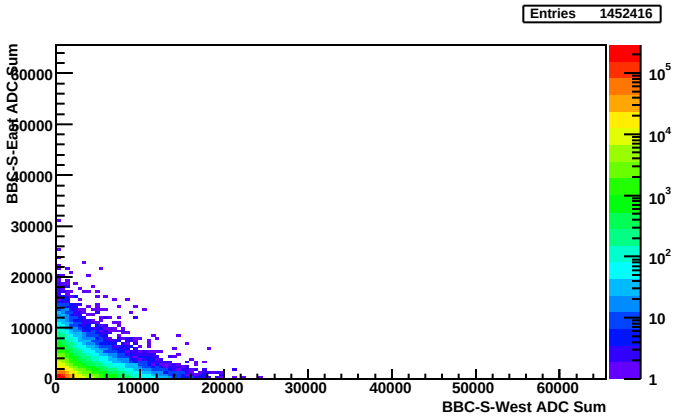

Entries 1452416
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

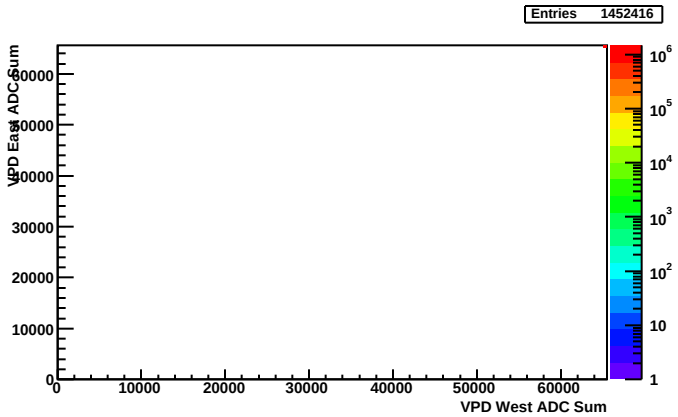

Entries 1452416
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

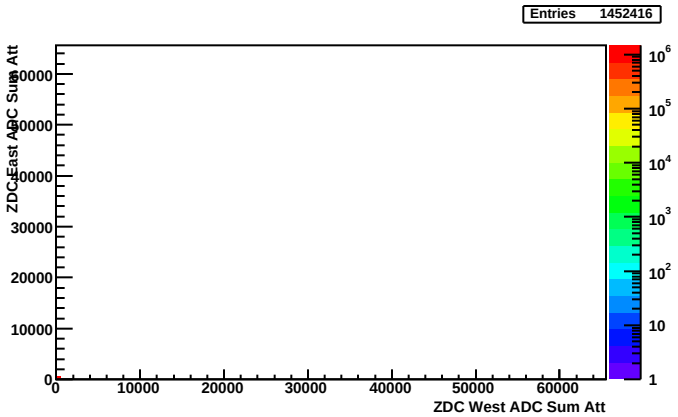

Entries 1452416
VPD-East ADC Sum
ZDC-East ADC Sum Att

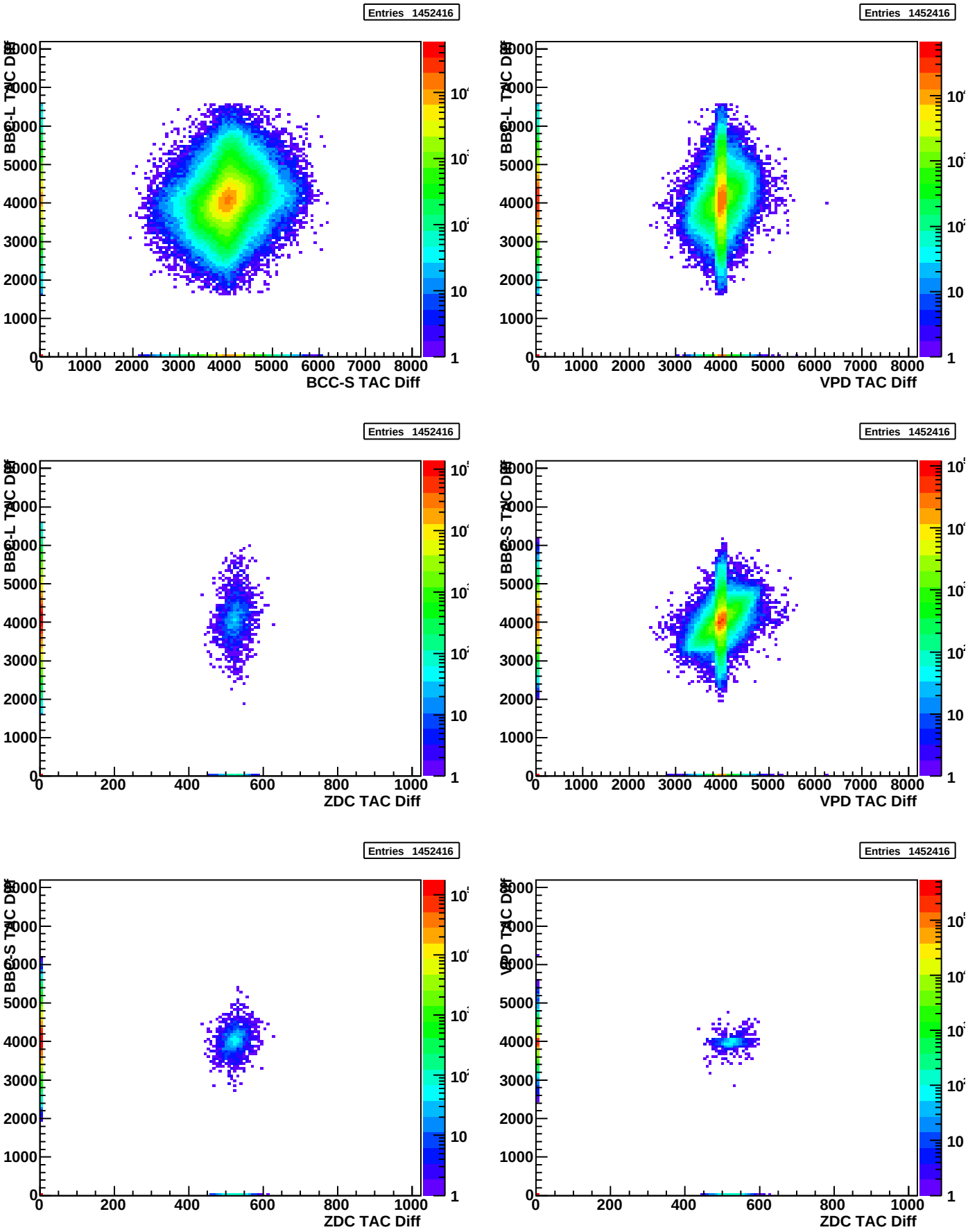

Entries 1452416
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 1452416
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 1452416
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 1452416
VPD East ADC Sum
VPD West ADC Sum


Entries 1452416
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 1452416
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1452416
BBC-L TAC Diff
VPD TAC Diff
Entries 1452416
BBC-L TAC Diff
ZDC TAC Diff
Entries 1452416
BBC-S TAC Diff
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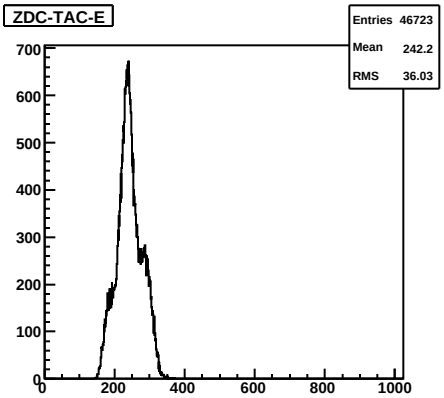
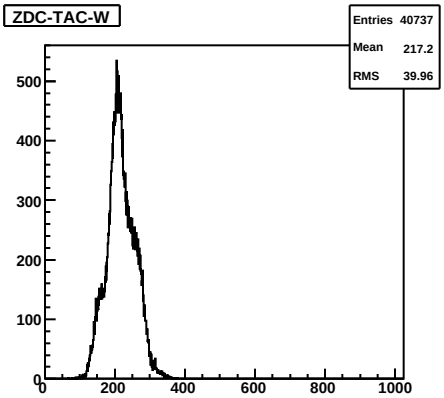
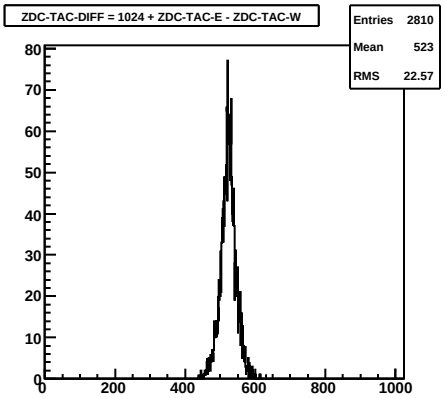
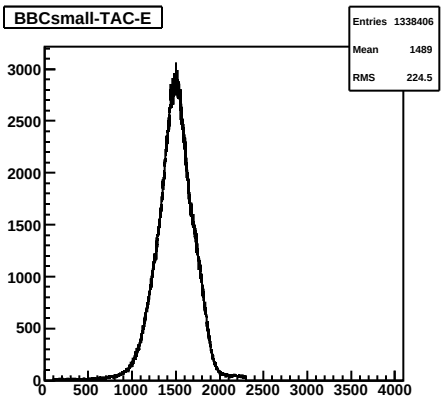
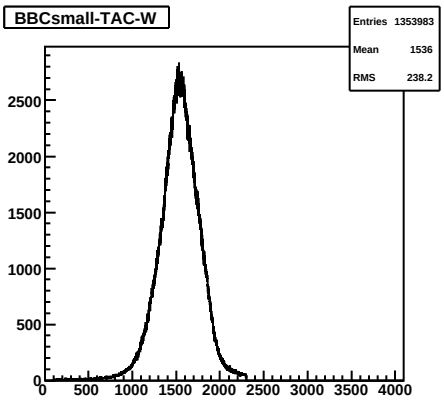
Entries 1452416
BBC-S TAC Diff
ZDC TAC Diff
Entries 1452416
VPD TAC Diff
ZDC TAC Diff

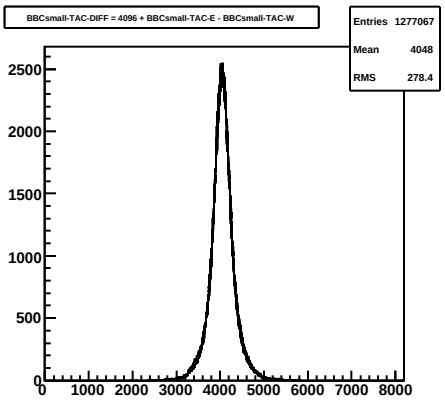

ZDC-TAC-E
Entries 46723
Mean 242.2
RMS 36.03

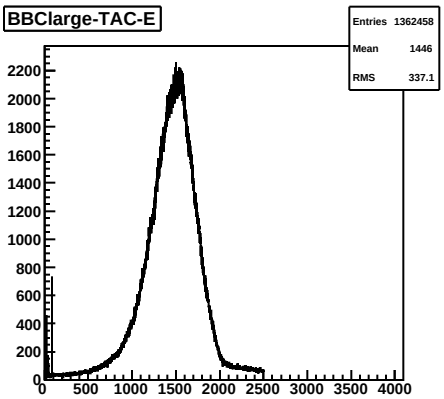

ZDC-TAC-W
Entries 40737
Mean 217.2
RMS 39.96

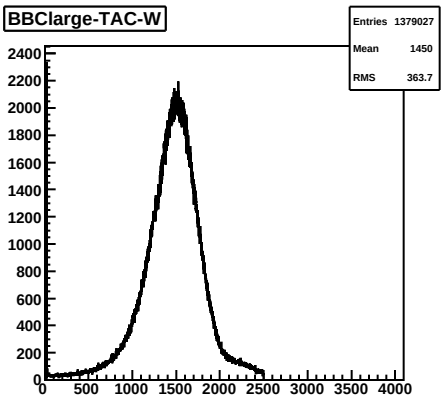

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2810
Mean 523
RMS 22.57

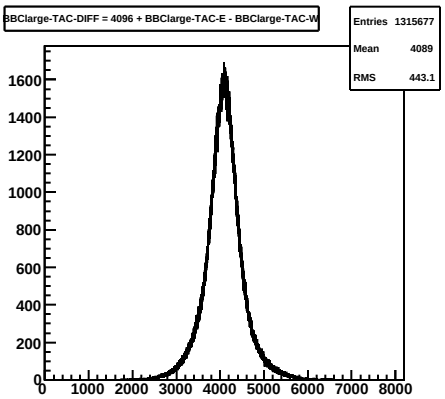

BBCsmall-TAC-E
Entries 1338406
Mean 1489
RMS 224.5

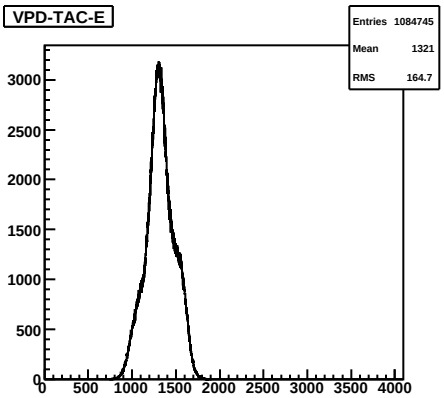

BBCsmall-TAC-W
Entries 1353983
Mean 1536
RMS 238.2

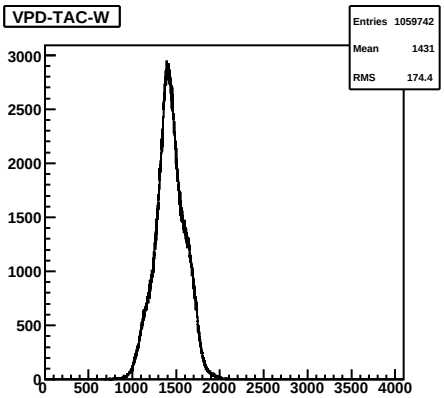

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1277067
Mean 4048
RMS 278.4

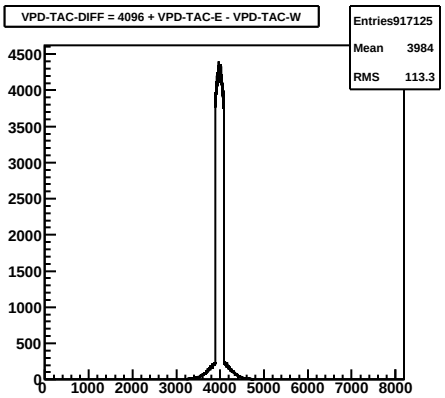

BBClarge-TAC-E
Entries 1362458
Mean 1446
RMS 337.1

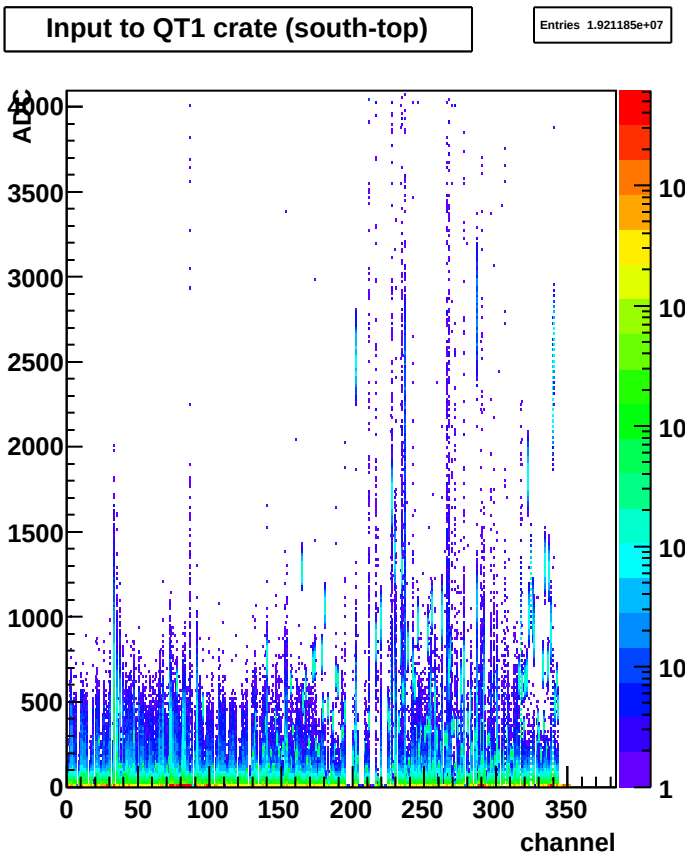

BBClarge-TAC-W
Entries 1379027
Mean 1450
RMS 363.7

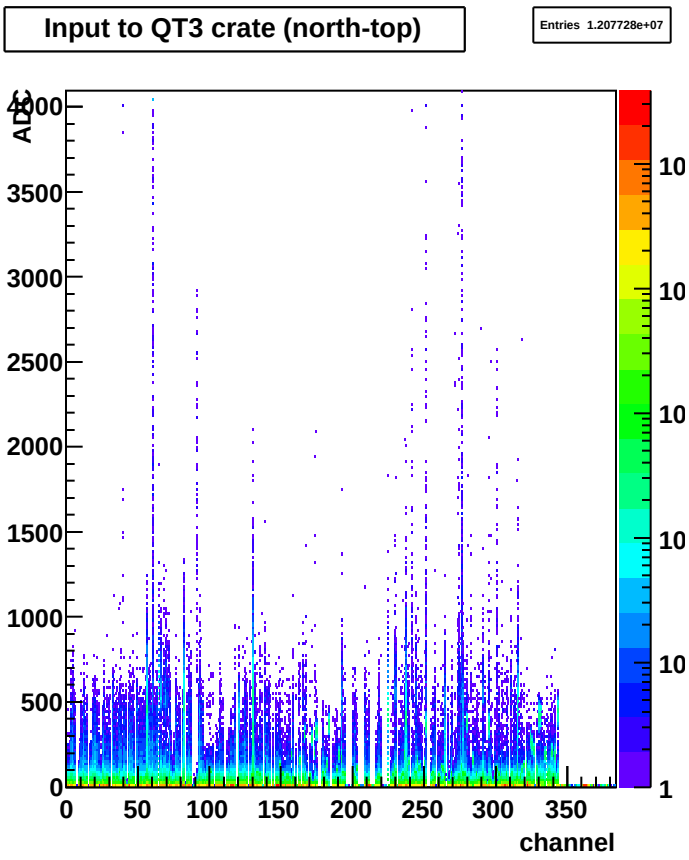

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1315677
Mean 4089
RMS 443.1

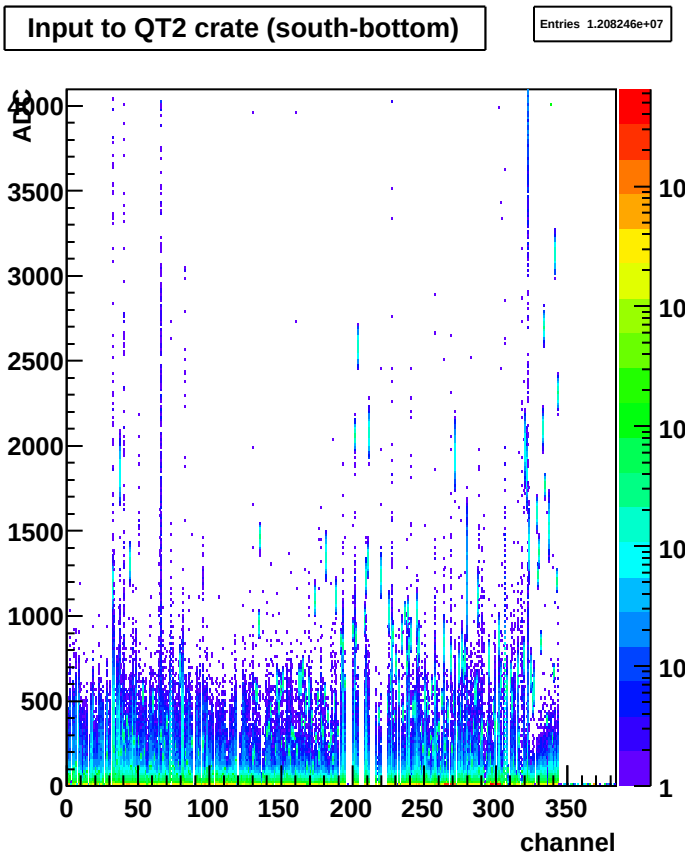

VPD-TAC-E
Entries 1084745
Mean 1321
RMS 164.7

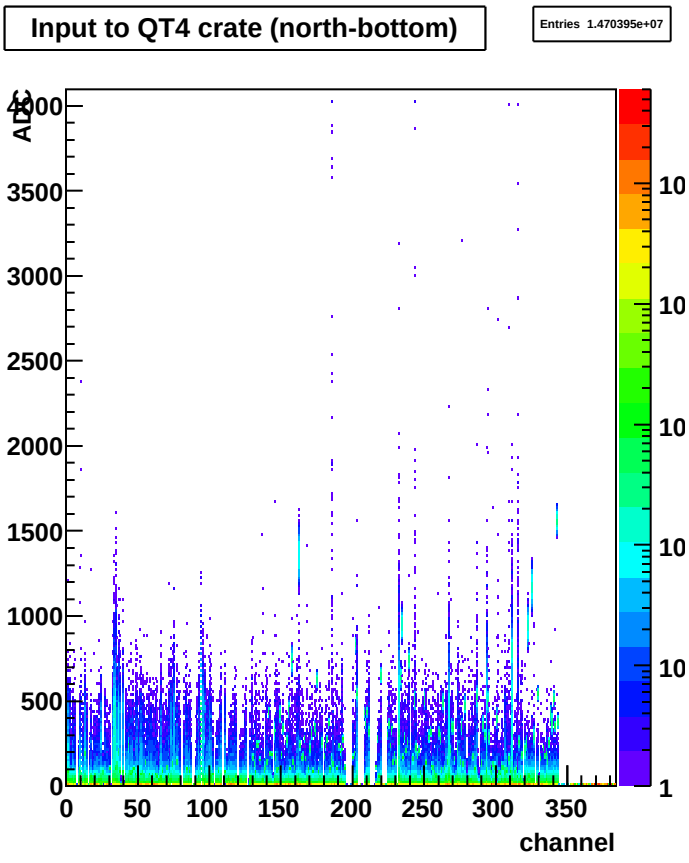

VPD-TAC-W
Entries 1059742
Mean 1431
RMS 174.4

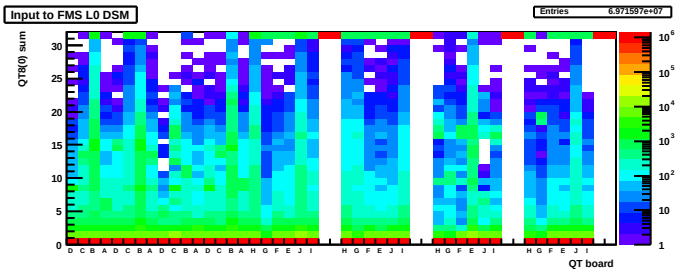

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries917125
Mean 3984
RMS 113.3

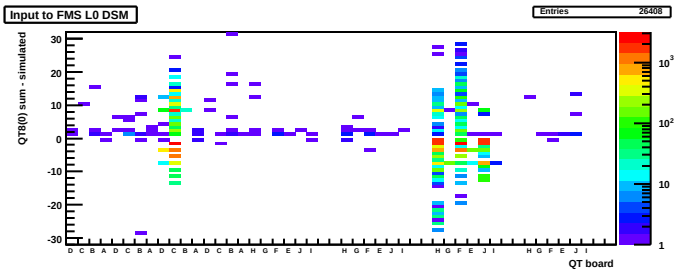

Input to QT1 crate (south-top)
Entries 1.921185e+07
ADC
channel

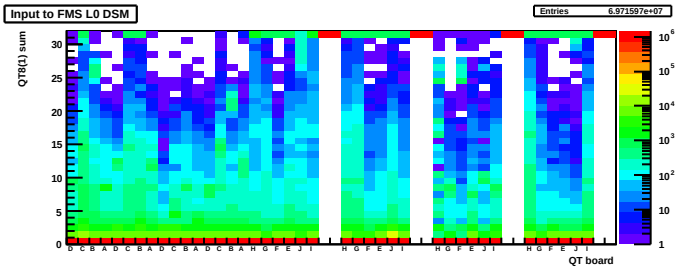

Input to QT3 crate (north-top)
Entries 1.207728e+07
ADC
channel

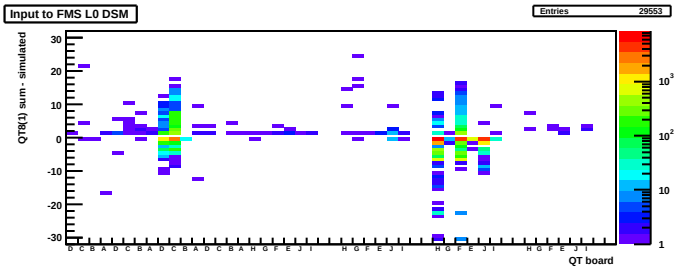

Input to QT2 crate (south-bottom)
Entries 1.208246e+07
ADC
channel

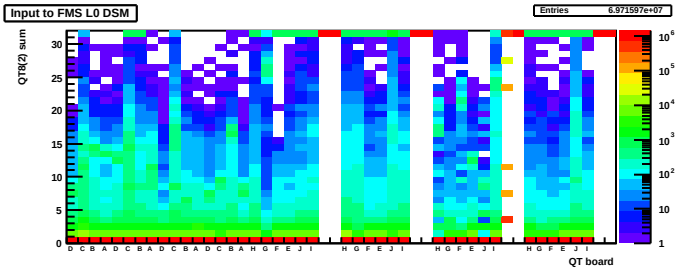

Input to QT4 crate (north-bottom)
Entries 1.470395e+07
ADC
channel

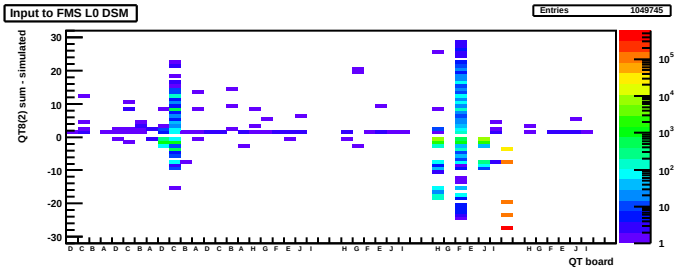

Input to FMS L0 DSM
Entries 6.971597e+07
QT8(0) sum
QT board

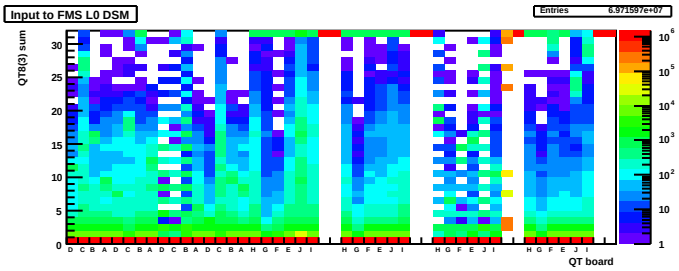

Input to FMS L0 DSM
Entries 26408
QT8(0) sum - simulated
QT board

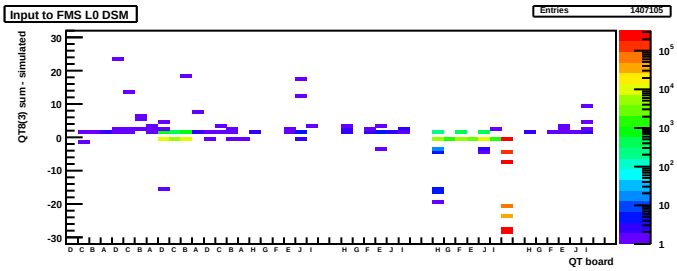

Input to FMS L0 DSM
Entries 6.971597e+07
QT8(1) sum
QT board

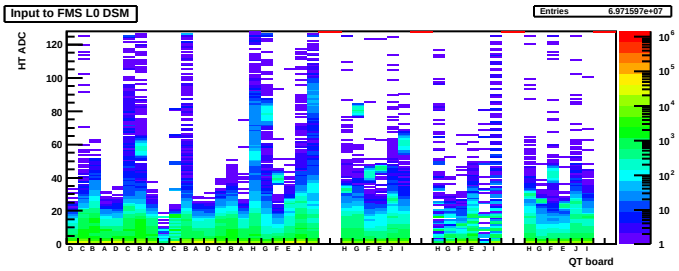

Input to FMS L0 DSM
Entries 29553
QT8(1) sum - simulated
QT board

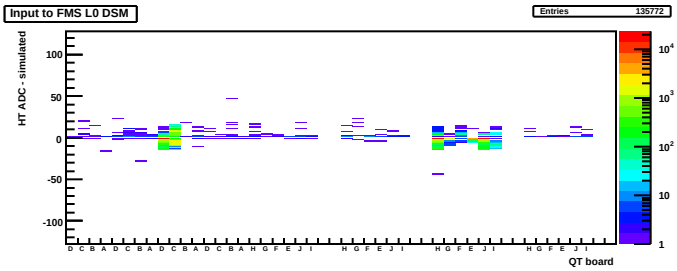

Input to FMS L0 DSM
Entries 6.971597e+07
QT8(2) sum
QT board

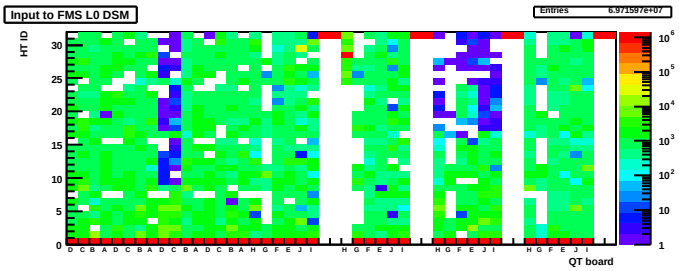

Input to FMS L0 DSM
Entries 1049745
QT8(2) sum - simulated
QT board

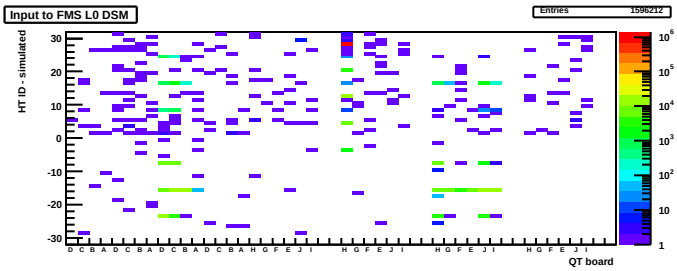

Input to FMS L0 DSM
Entries 6.971597e+07
QT8(3) sum
QT board

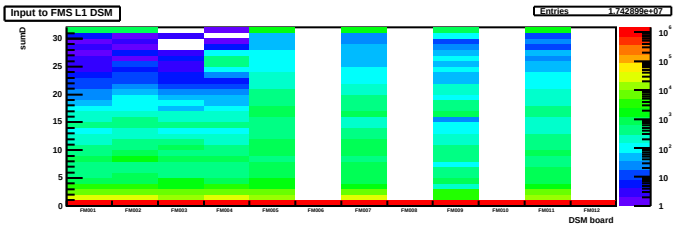

Input to FMS L0 DSM
Entries 1407105
QT8(3) sum - simulated
QT board

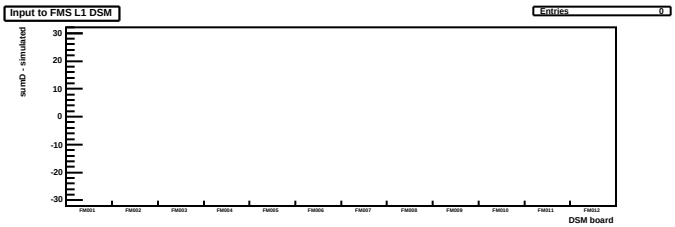

Input to FMS L0 DSM
Entries 6.971597e+07
HT ADC
QT board

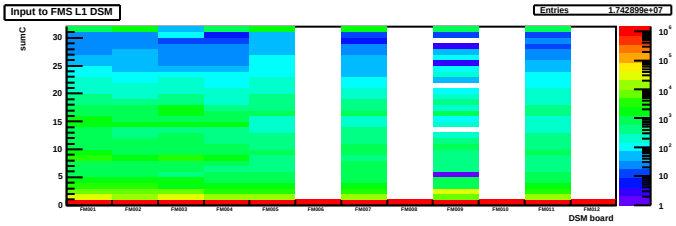

Input to FMS L0 DSM
Entries 135772
HT ADC - simulated
QT board

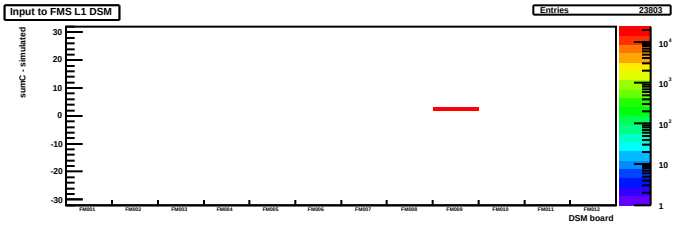

Input to FMS L0 DSM
Entries 6.971597e+07
HT ID
QT board

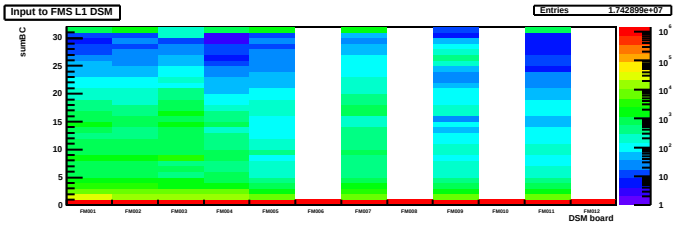

Input to FMS L0 DSM
Entries 1596212
HT ID - simulated
QT board

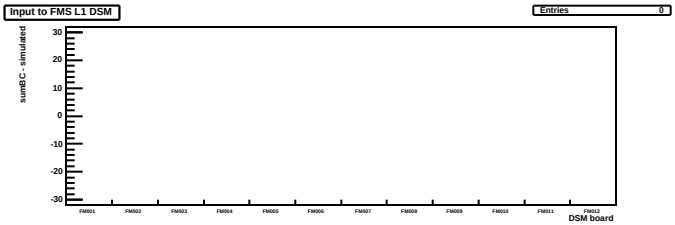

Input to FMS L1 DSM
Entries 1.742899e+07
sumD
DSM board

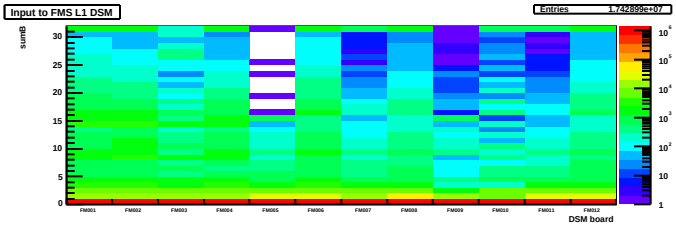

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

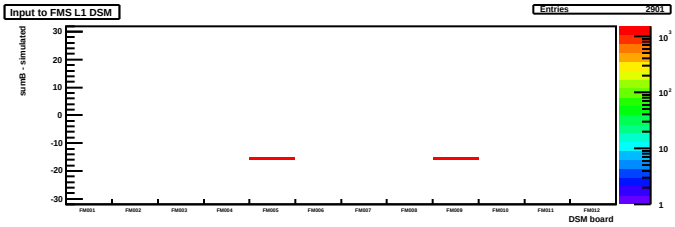

Input to FMS L1 DSM
Entries 1.742899e+07
sumC
DSM board

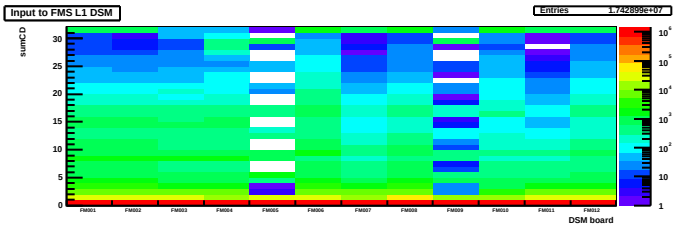

Input to FMS L1 DSM
Entries 23803
sumC - simulated
DSM board

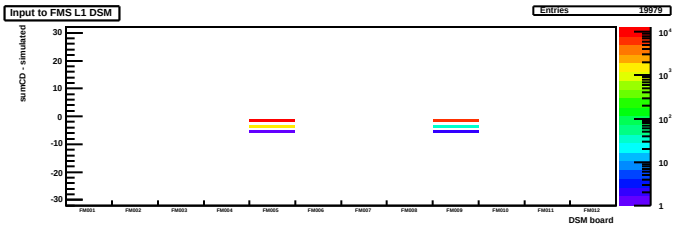

Input to FMS L1 DSM
Entries 1.742899e+07
sumBC
DSM board

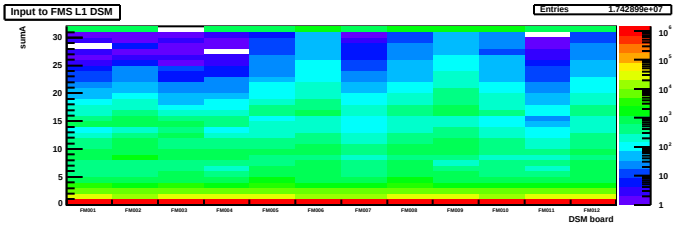

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

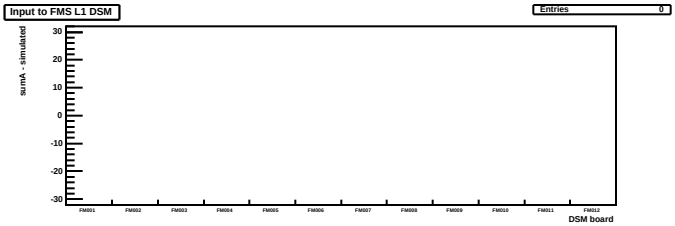

Input to FMS L1 DSM
Entries 1.742899e+07
sumB
DSM board

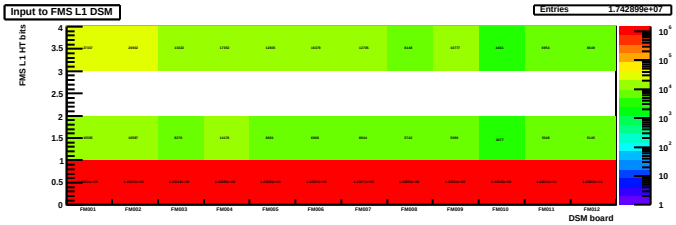

Input to FMS L1 DSM
Entries 2901
sumB - simulated
DSM board

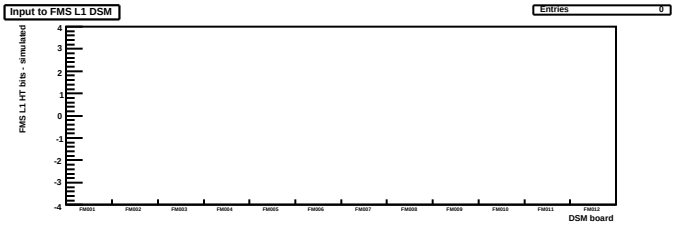

Input to FMS L1 DSM
Entries 1.742899e+07
sumCD
DSM board

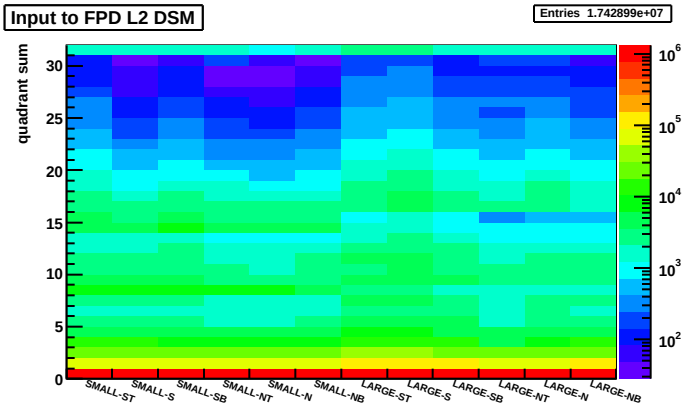

Input to FMS L1 DSM
Entries 19979
sumCD - simulated
DSM board

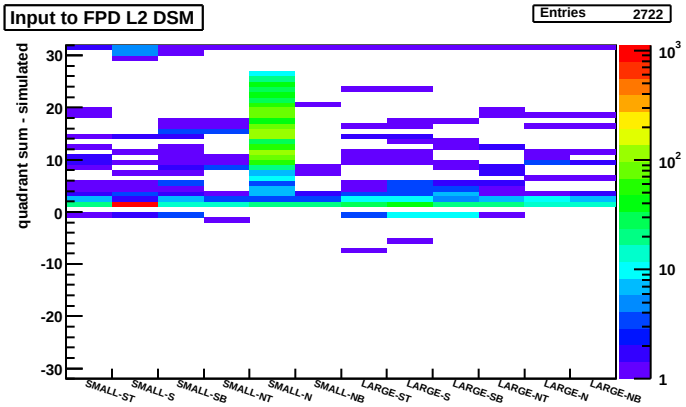

Input to FMS L1 DSM
Entries 1.742899e+07
sumA
DSM board

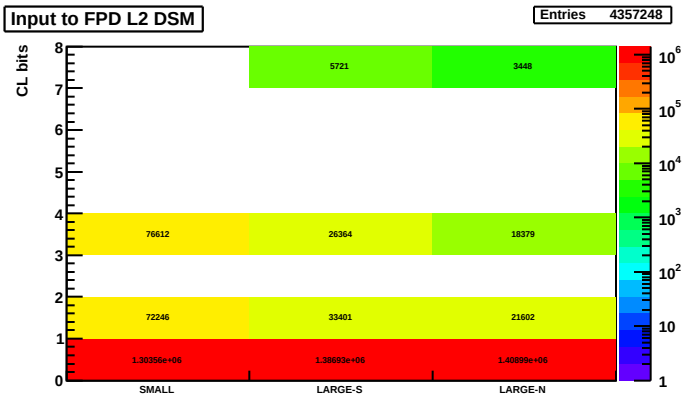

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

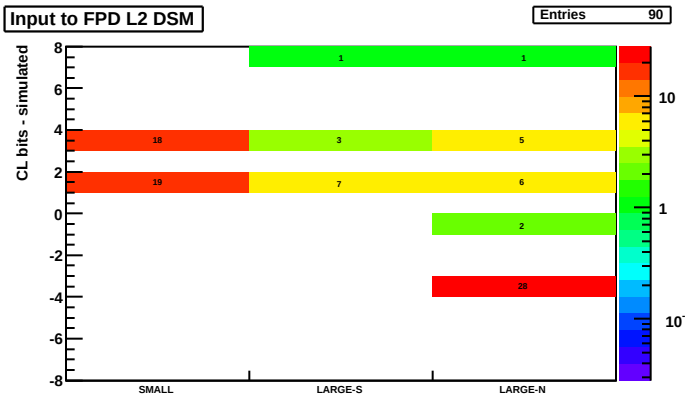

Input to FMS L1 DSM
Entries 1.742899e+07
FMS L1 HT bits
DSM board

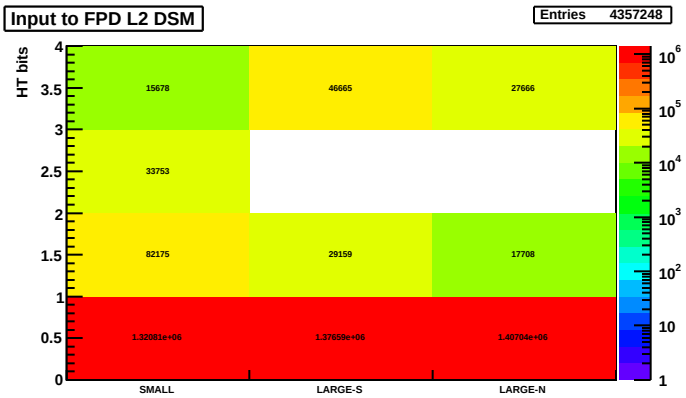

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

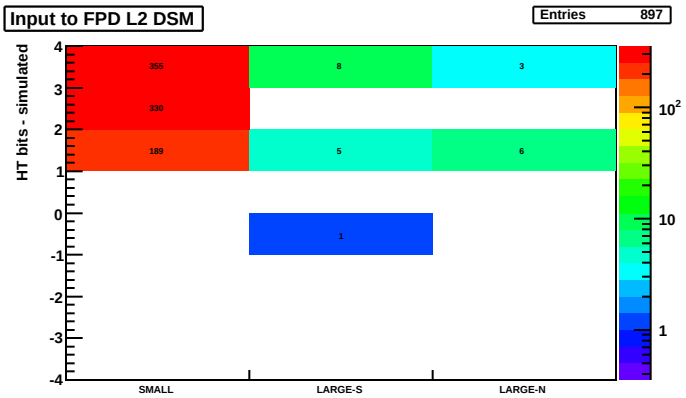

Input to FPD L2 DSM
Entries 1.742899e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

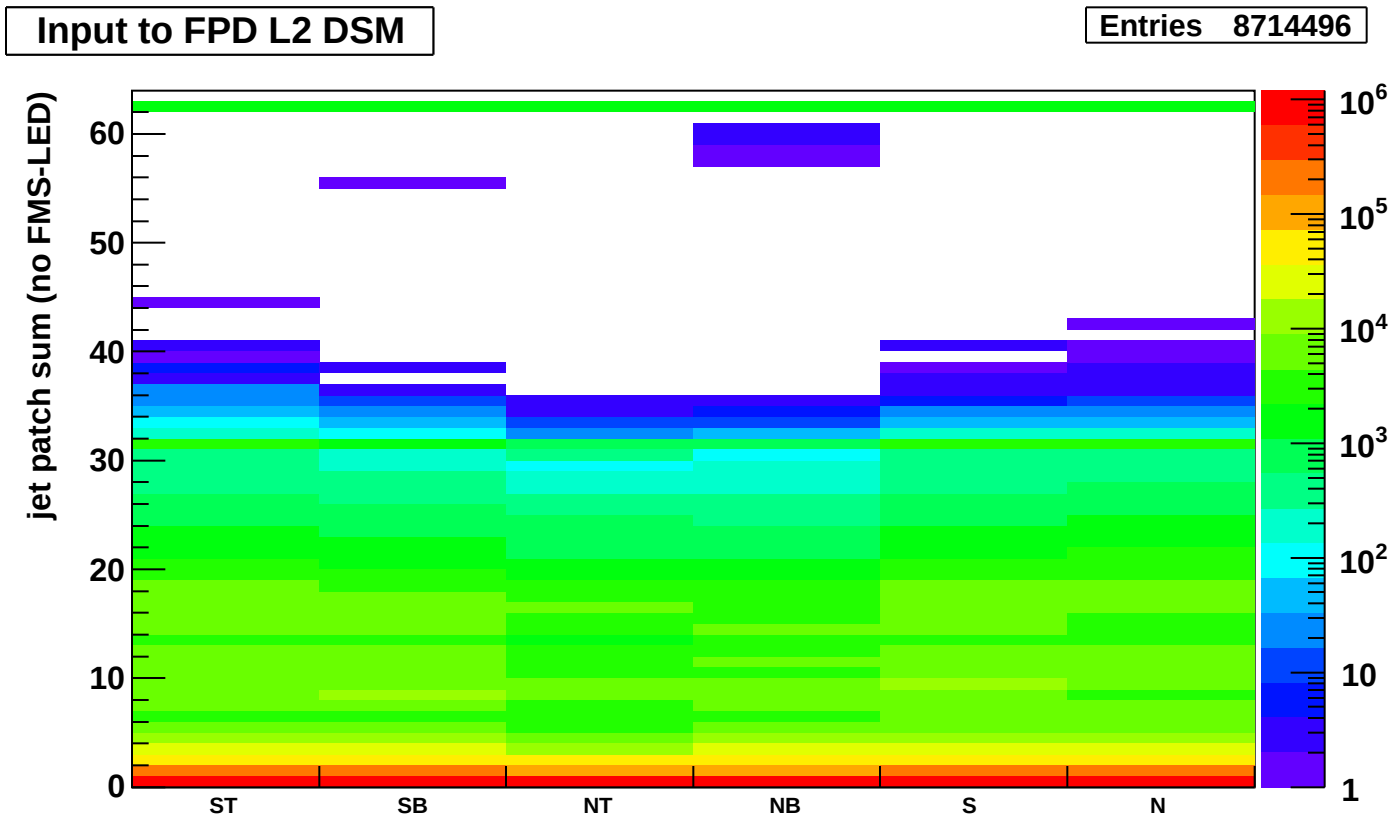

Input to FPD L2 DSM
Entries 2722
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

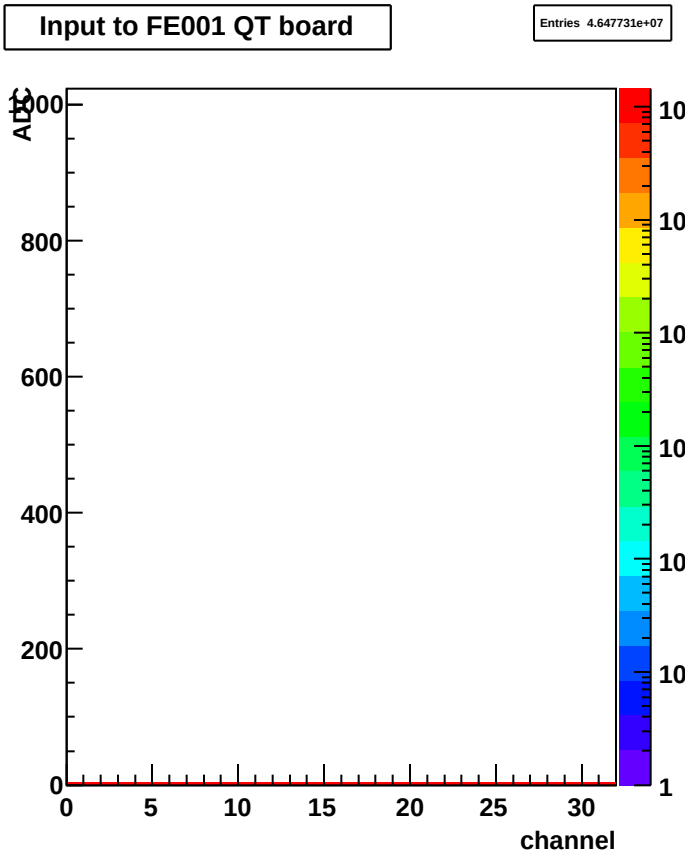

Input to FPD L2 DSM
Entries 4357248
CL bits
5721
3448
76612
26364
18379
72246
33401
21602
1.30356e+06
1.38693e+06
1.40899e+06
SMALL
LARGE-S
LARGE-N

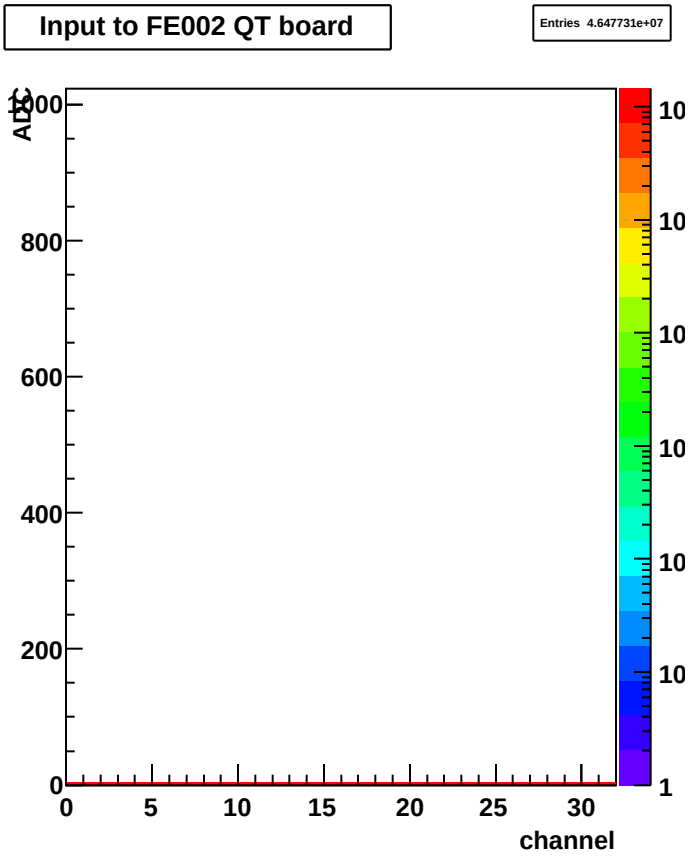

Input to FPD L2 DSM
Entries 90
CL bits - simulated
1
1
18
3
5
19
7
6
2
28
SMALL
LARGE-S
LARGE-N

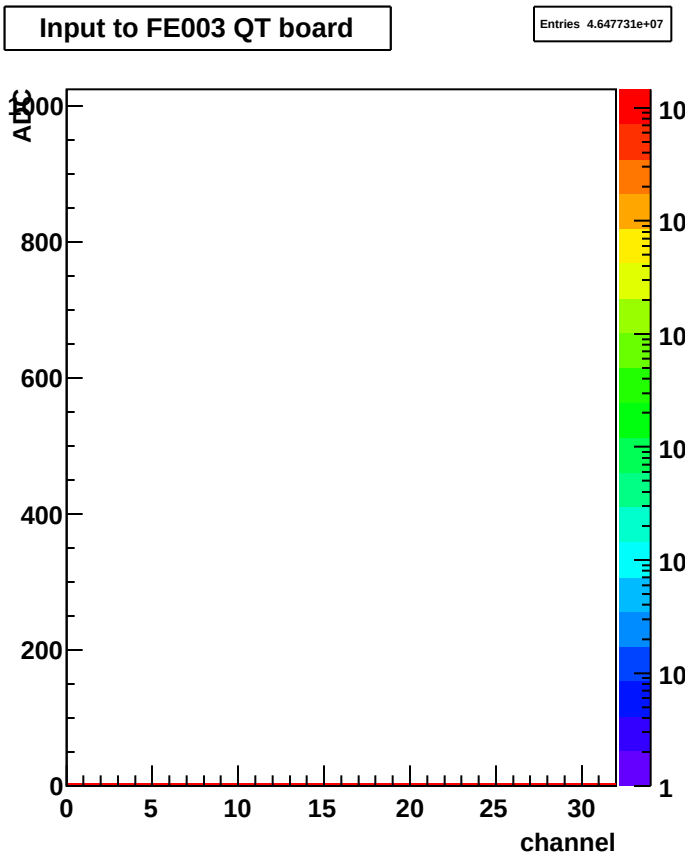

Input to FPD L2 DSM
Entries 4357248
HT bits
15678
46665
27666
33753
82175
29159
17708
1.32081e+06
1.37659e+06
1.40704e+06
SMALL
LARGE-S
LARGE-N

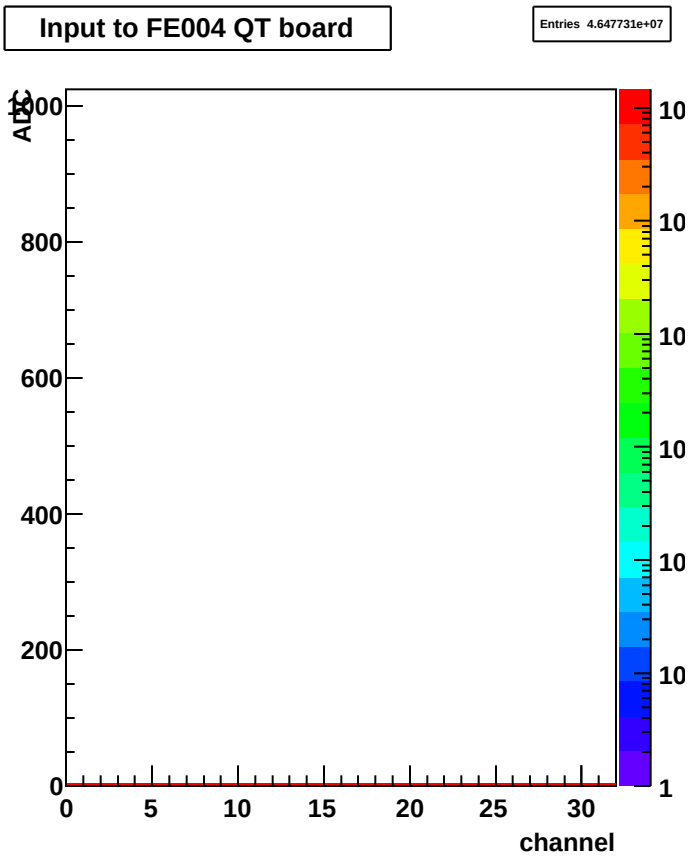

Input to FPD L2 DSM
Entries 897
HT bits - simulated
355
8
3
330
189
5
6
1
SMALL
LARGE-S
LARGE-N

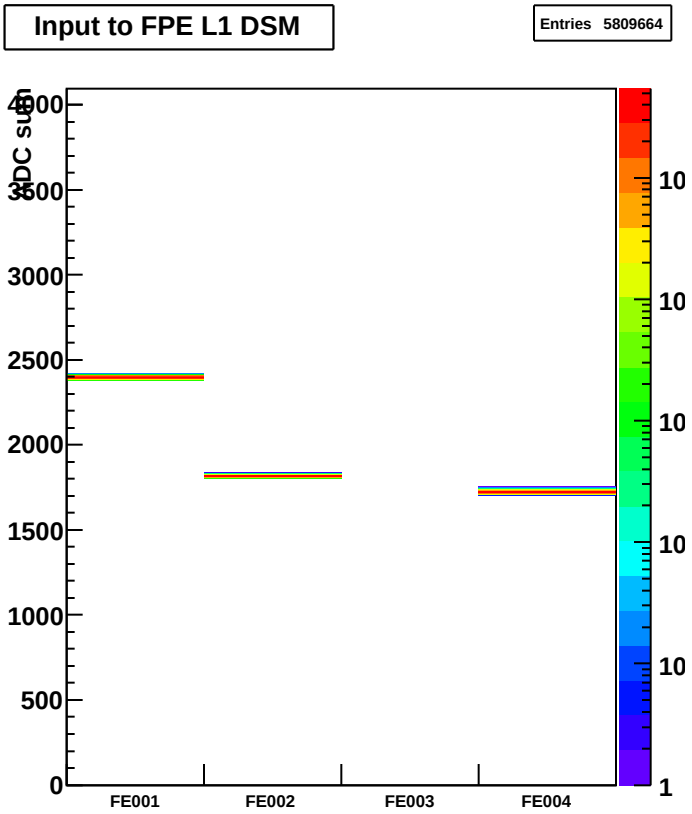

Input to FPD L2 DSM
Entries 8714496
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

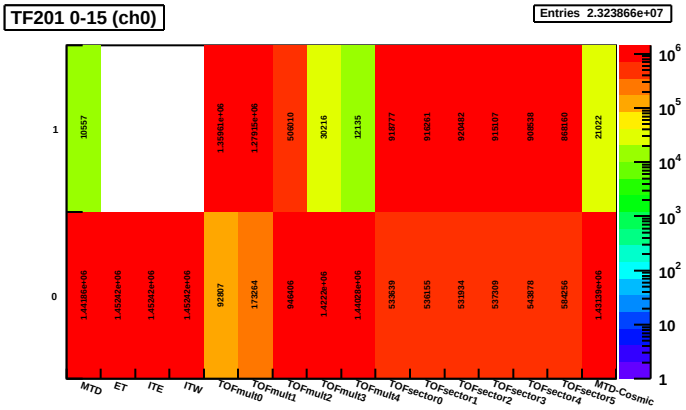

Input to FE001 QT board
Entries 4.647731e+07
ADC
channel

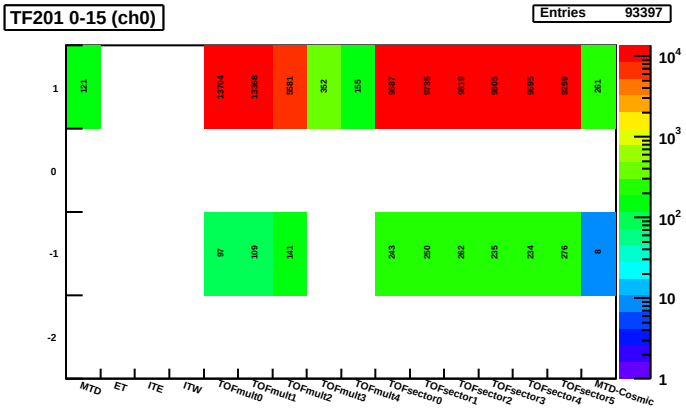

Input to FE002 QT board
Entries 4.647731e+07
ADC
channel


Input to FE003 QT board
Entries 4.647731e+07
ADC
channel


Input to FE004 QT board
Entries 4.647731e+07
ADC
channel


Input to FPE L1 DSM
Entries 5809664
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.323866e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 93397
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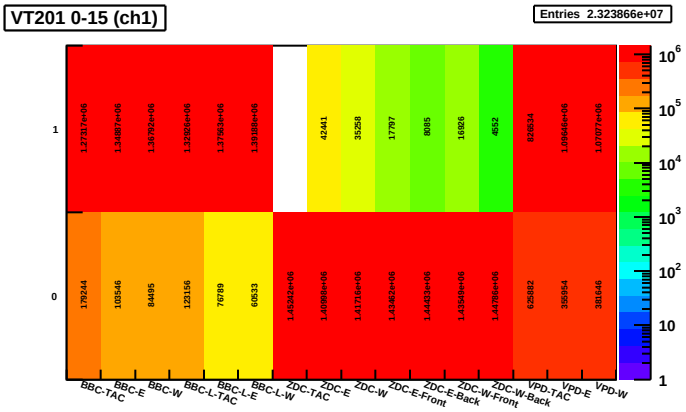
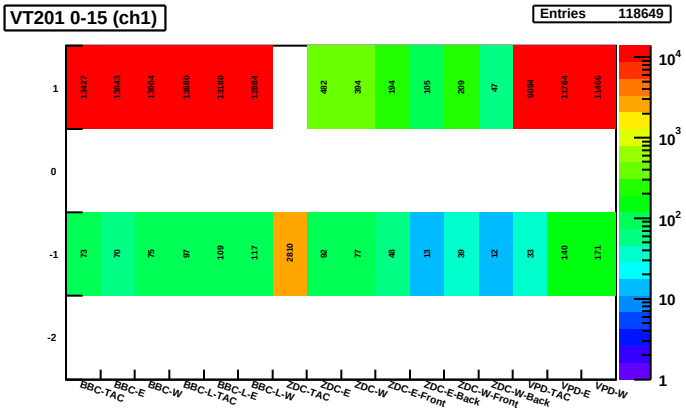

VT201 0-15 (ch1)
Entries 2.323866e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 118649
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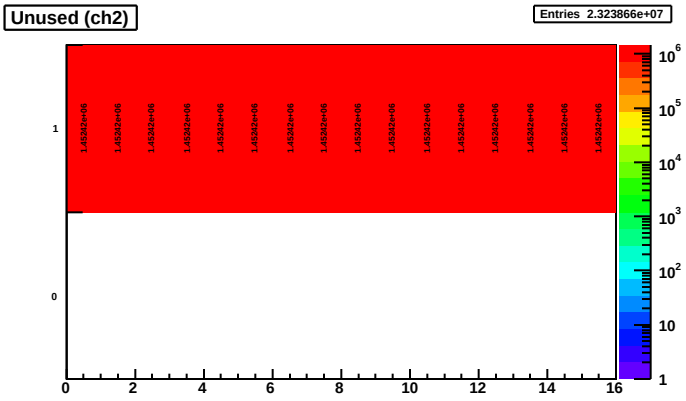
Unused (ch2)
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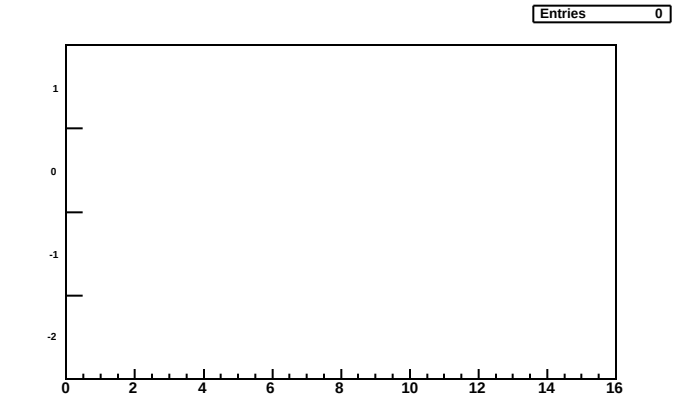
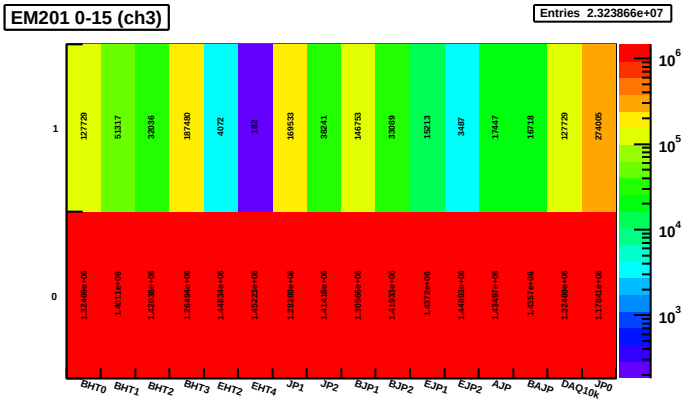
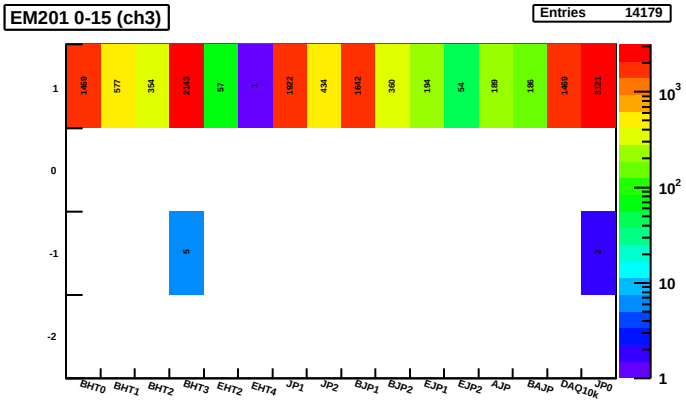

Entries 0


EM201 0-15 (ch3)
Entries 2.323866e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0


EM201 0-15 (ch3)
Entries 14179
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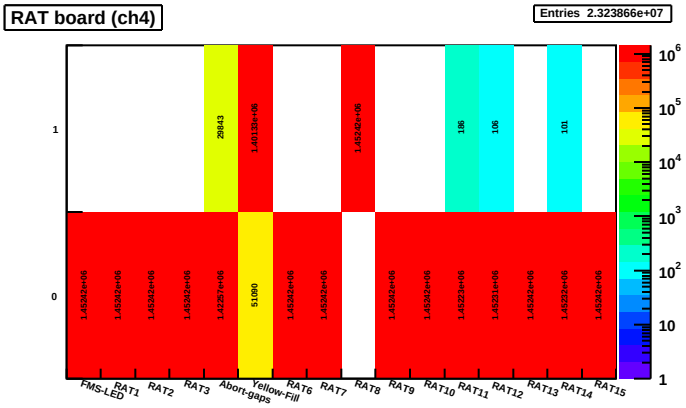
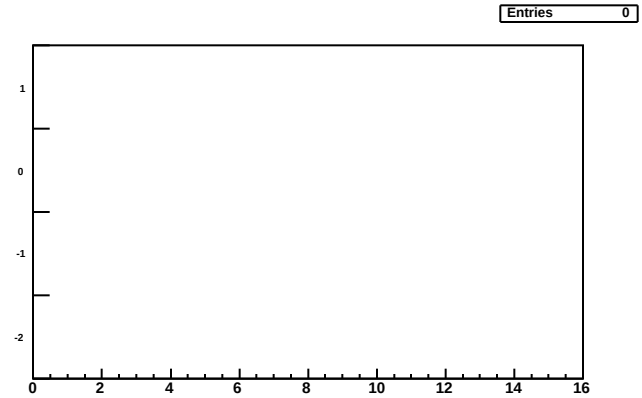
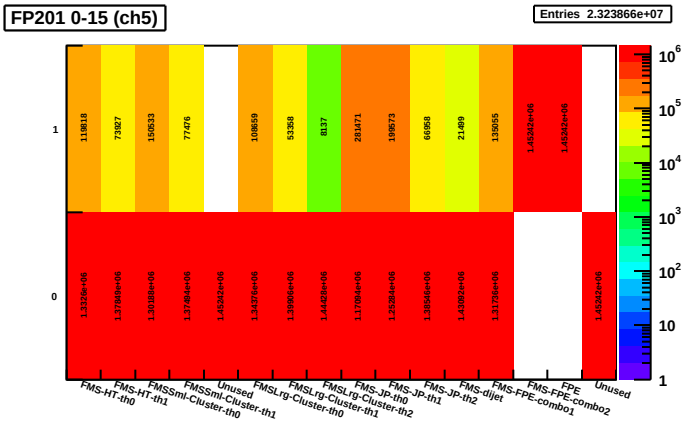
RAT board (ch4)





FP201 0-15 (ch5)



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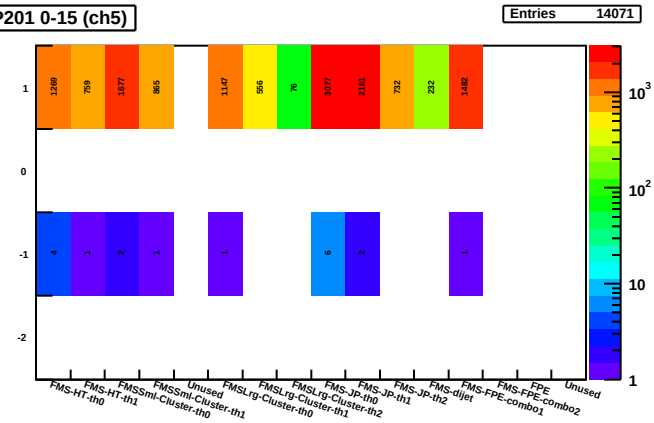
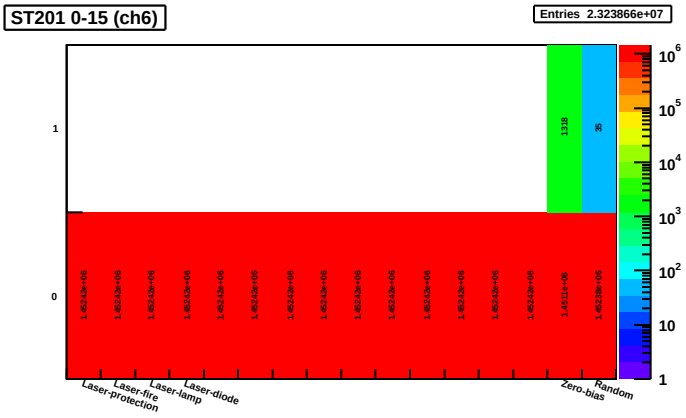
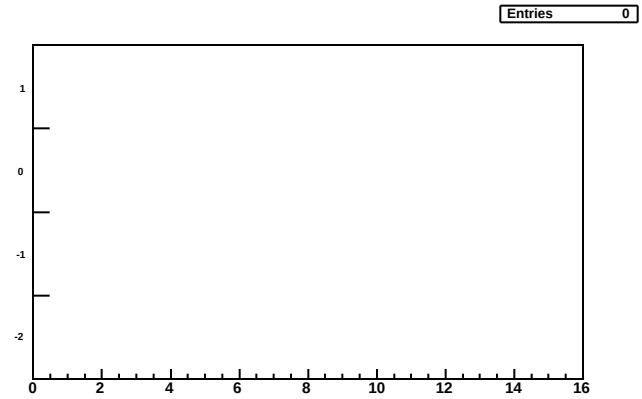
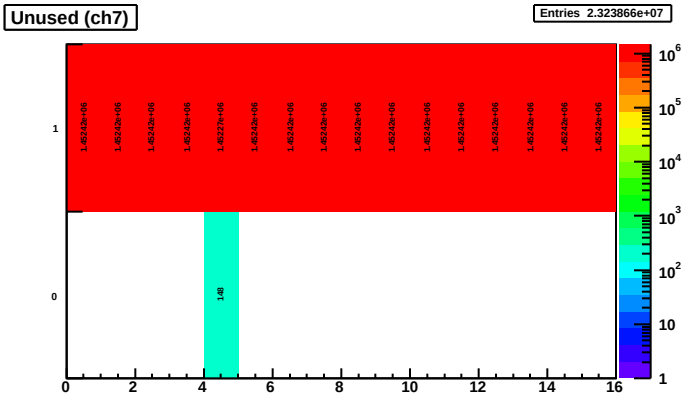
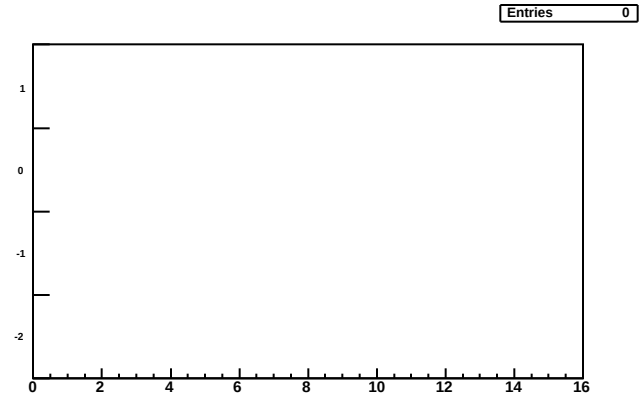


ST201 0-15 (ch6)





Unused (ch7)