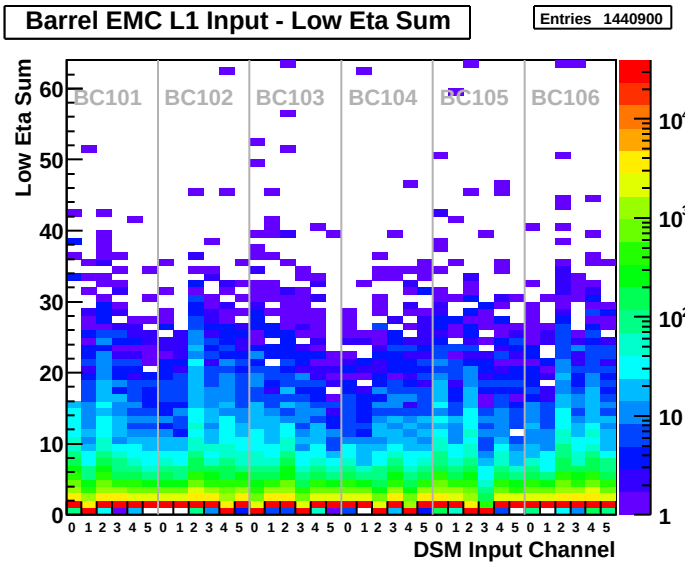
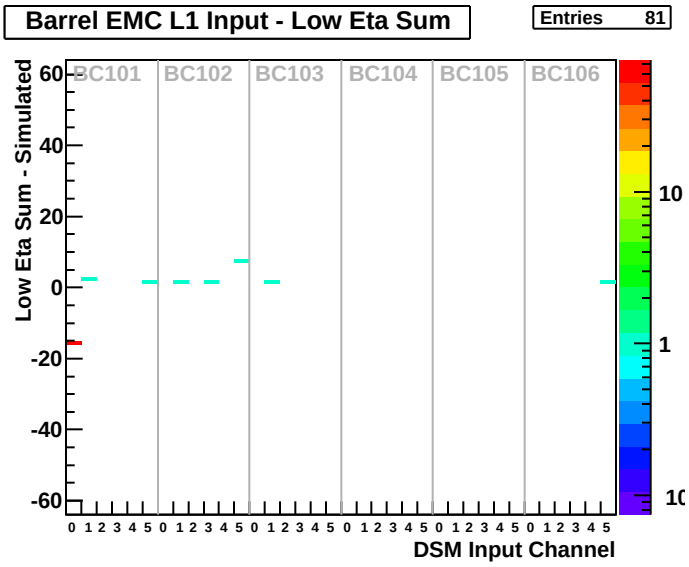
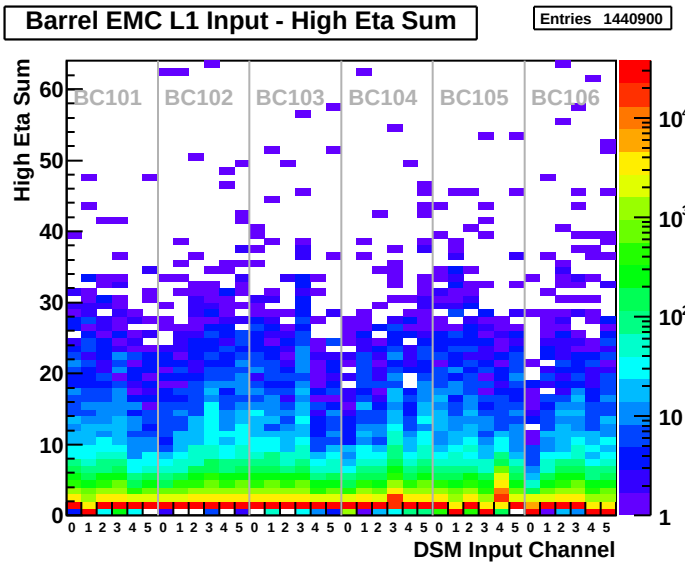
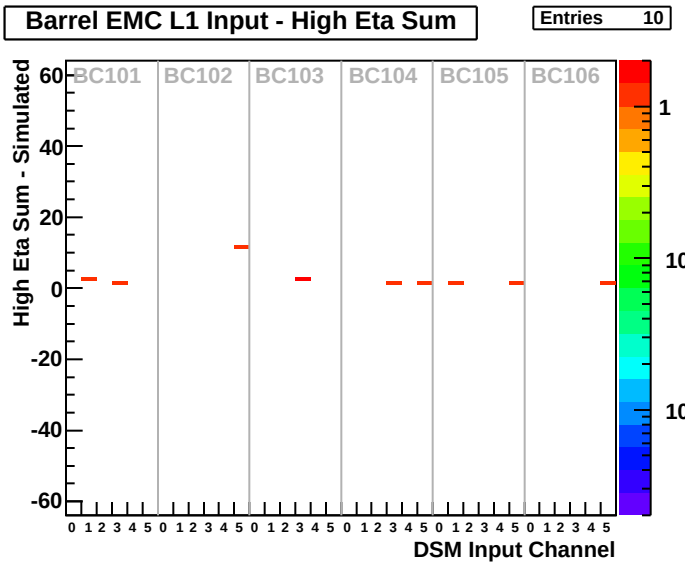
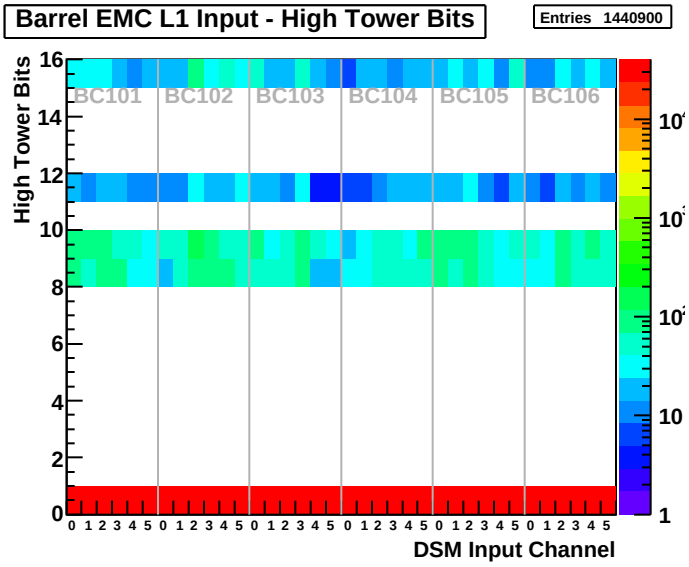
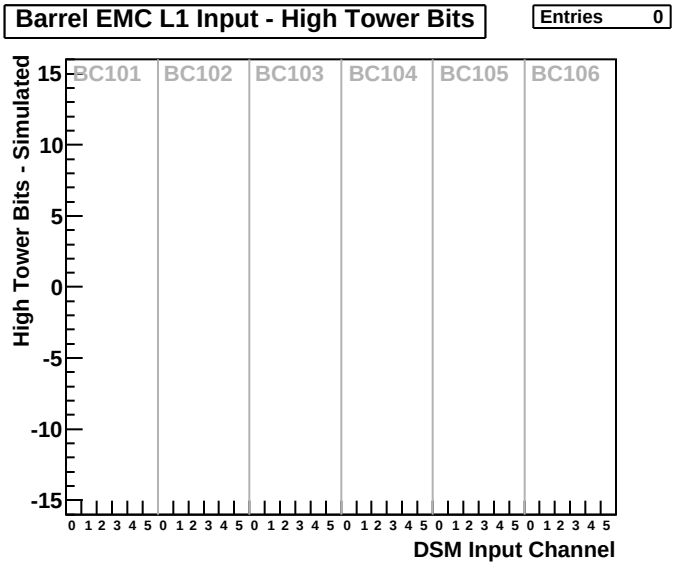

Barrel EMC L1 Input - Low Eta Sum
Entries 1440900
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

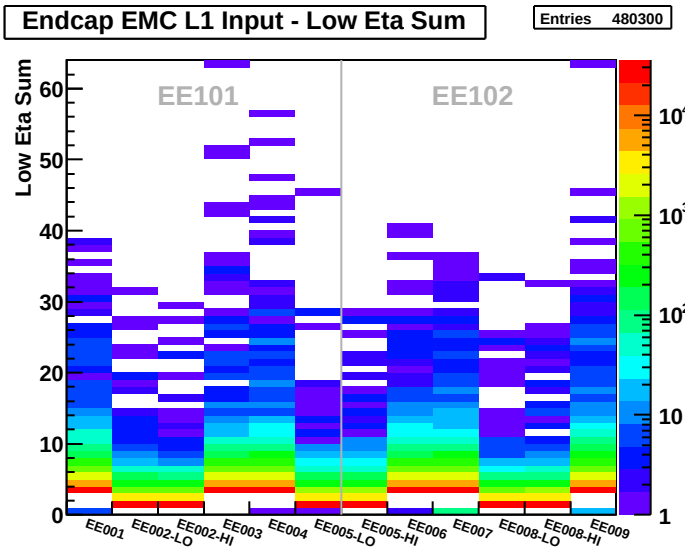

Barrel EMC L1 Input - Low Eta Sum
Entries 81
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

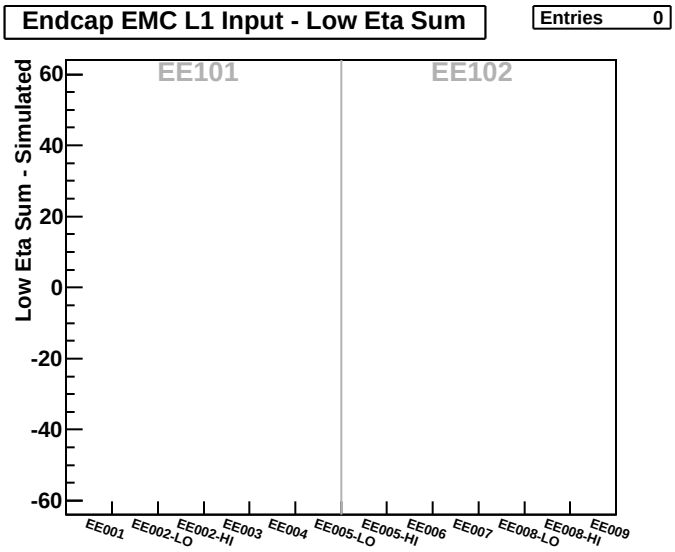

Barrel EMC L1 Input - High Eta Sum
Entries 1440900
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

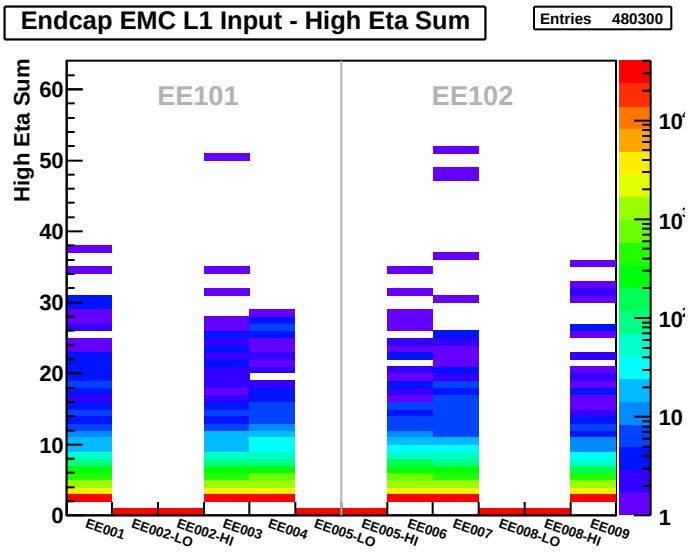

Barrel EMC L1 Input - High Eta Sum
Entries 10
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

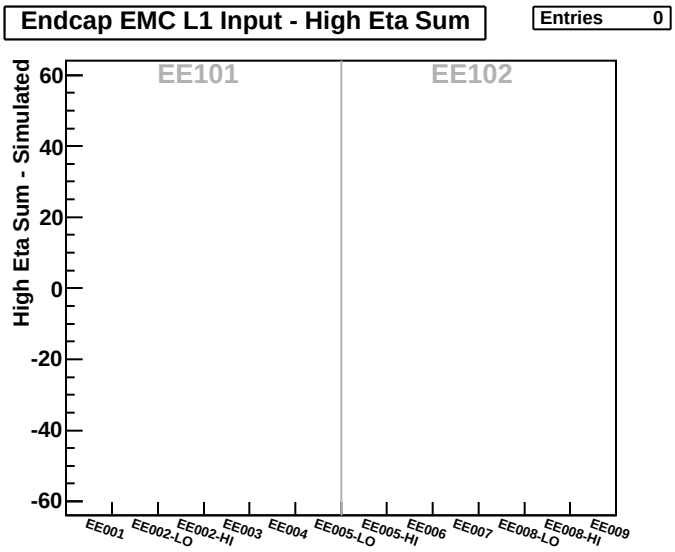

Barrel EMC L1 Input - High Tower Bits
Entries 1440900
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

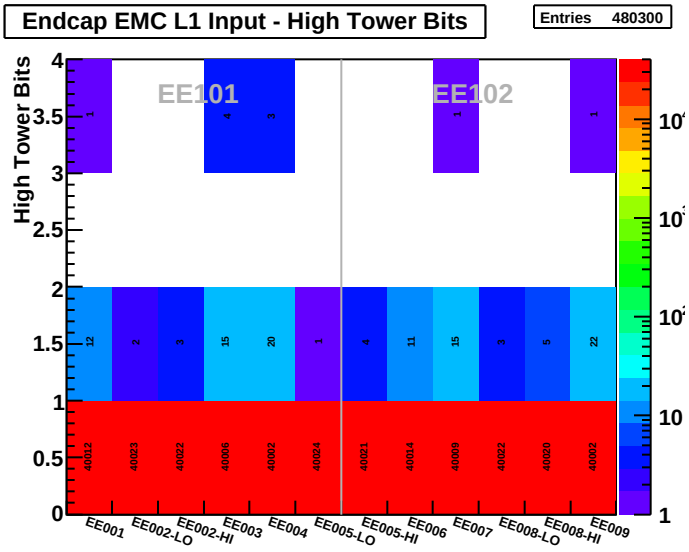

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

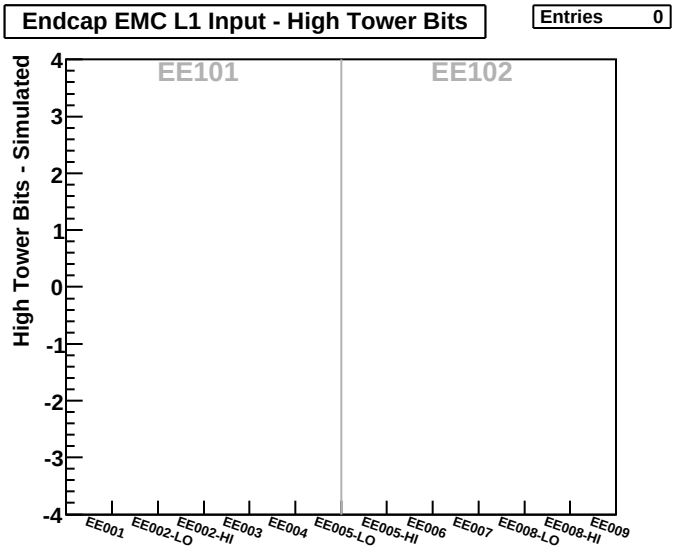

Endcap EMC L1 Input - Low Eta Sum
Entries 480300
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

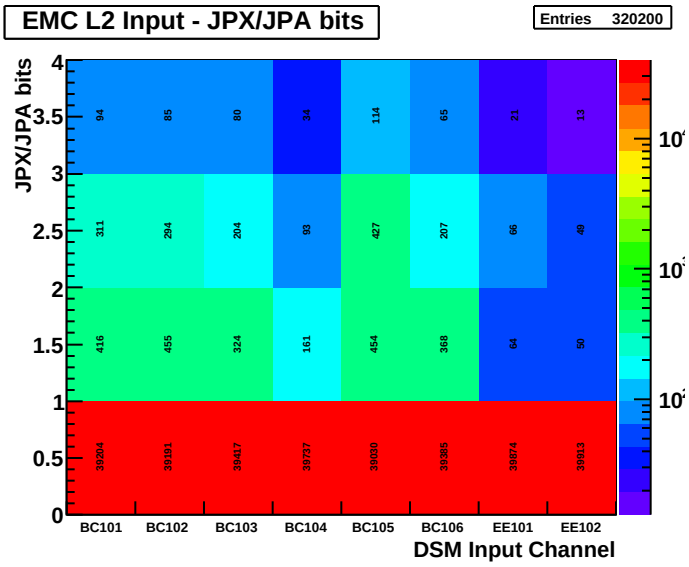

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

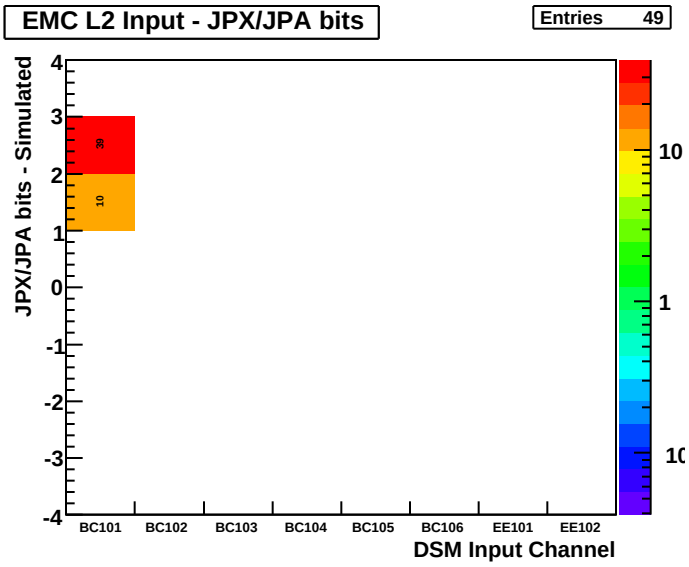

Endcap EMC L1 Input - High Eta Sum
Entries 480300
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

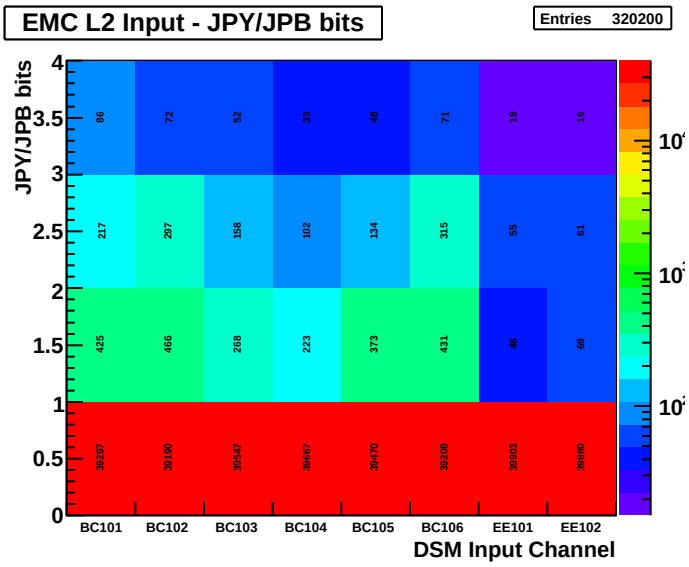

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

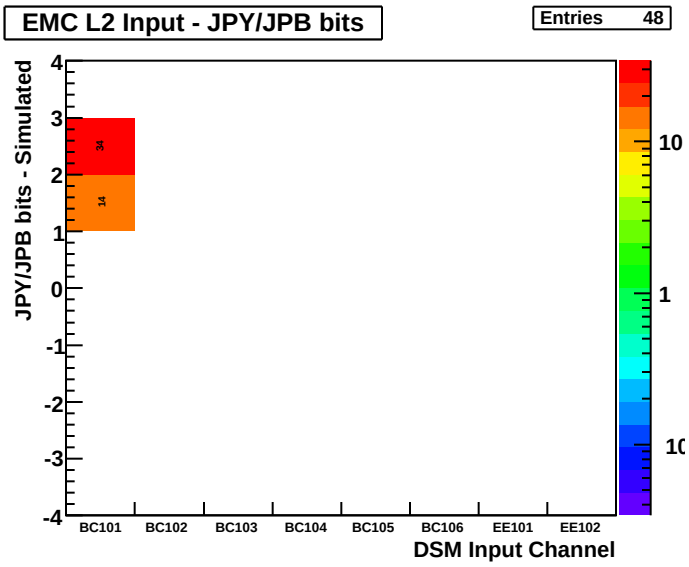

Endcap EMC L1 Input - High Tower Bits
Entries 480300
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

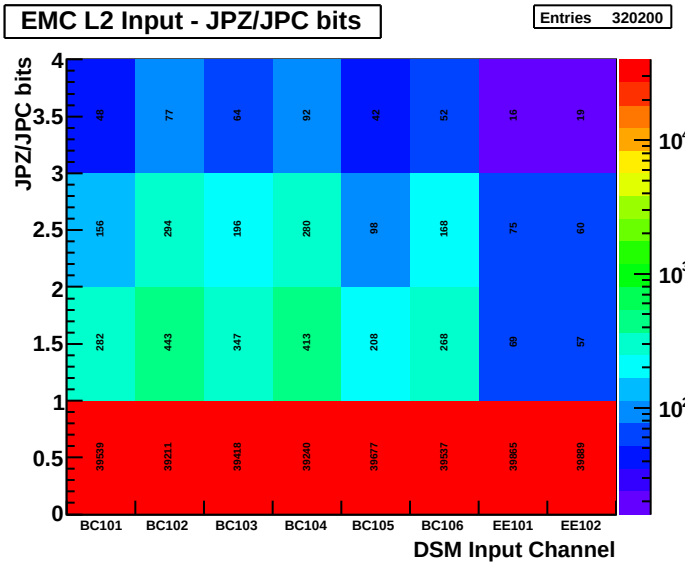

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

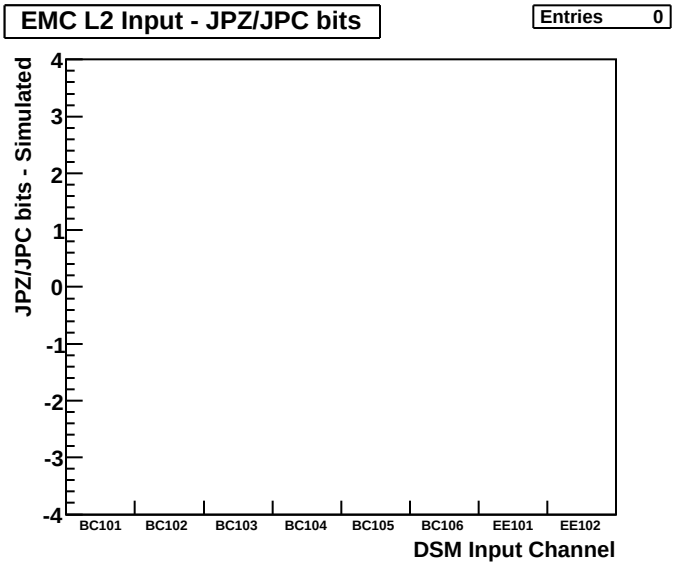

EMC L2 Input - JPX/JPA bits
Entries 320200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94 85 80 34 114 65 21 13
311 294 204 93 427 207 66 49
416 455 324 161 454 368 64 50
39204 39191 39417 39737 39030 39385 39874 39913

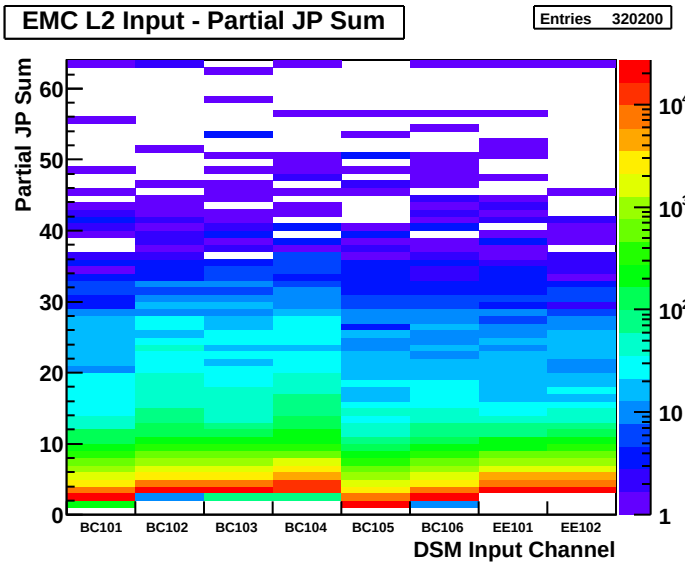

EMC L2 Input - JPX/JPA bits
Entries 49
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
39
10

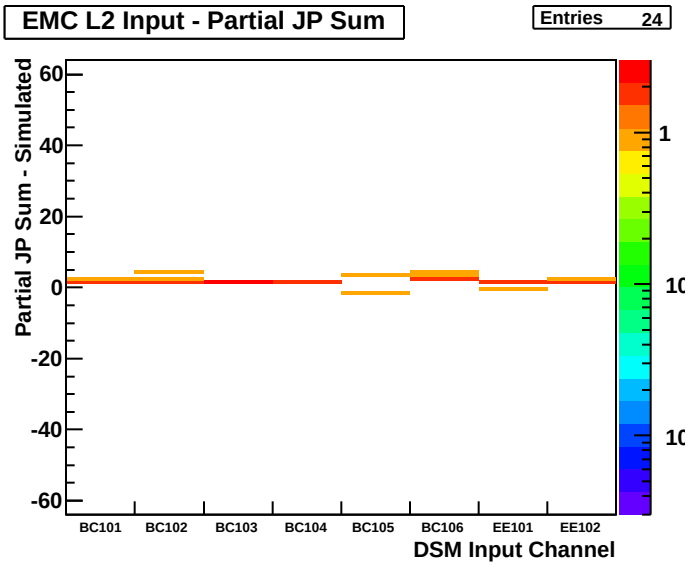

EMC L2 Input - JPY/JPB bits
Entries 320200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
86 72 52 33 48 71 19 15
217 297 158 102 134 315 55 61
425 466 268 223 373 431 48 69
39297 39190 39547 39667 39470 39208 39903 39880

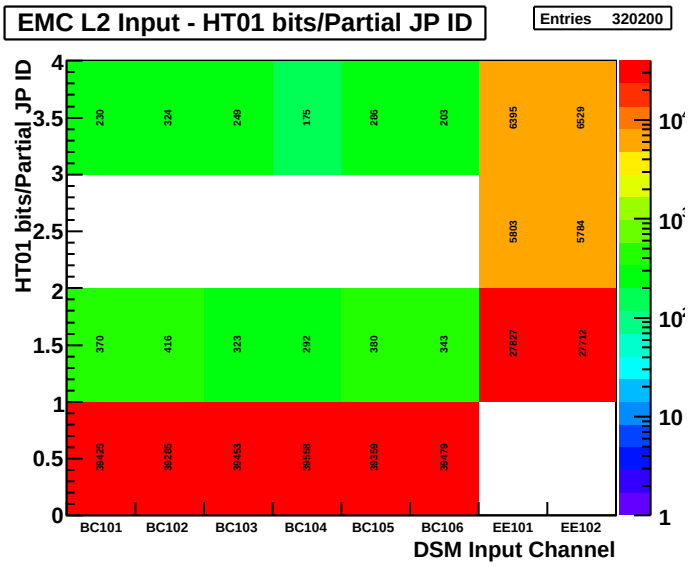

EMC L2 Input - JPY/JPB bits
Entries 48
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
34
14

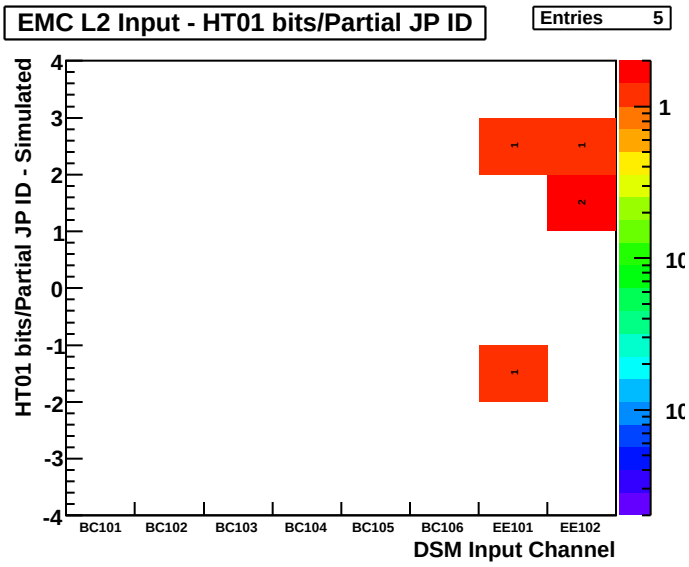

EMC L2 Input - JPZ/JPC bits
Entries 320200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
48 77 64 92 42 52 16 19
156 294 196 280 98 168 75 60
282 443 347 413 208 268 69 57
39539 39211 39418 39240 39677 39537 39865 39889

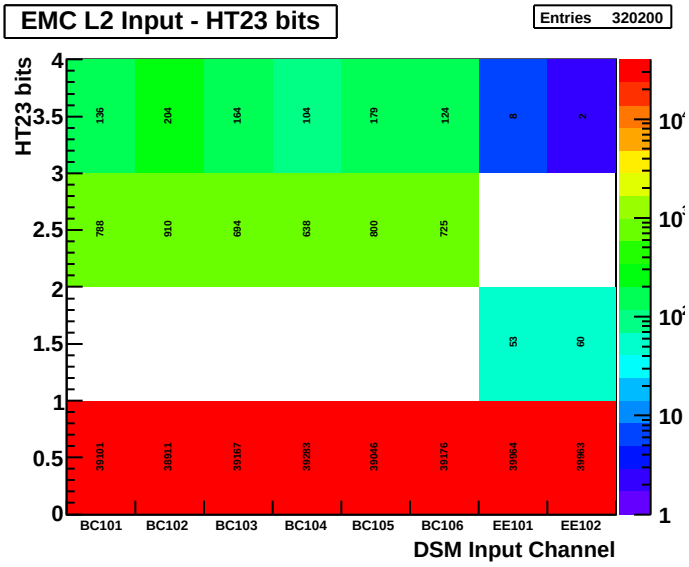

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

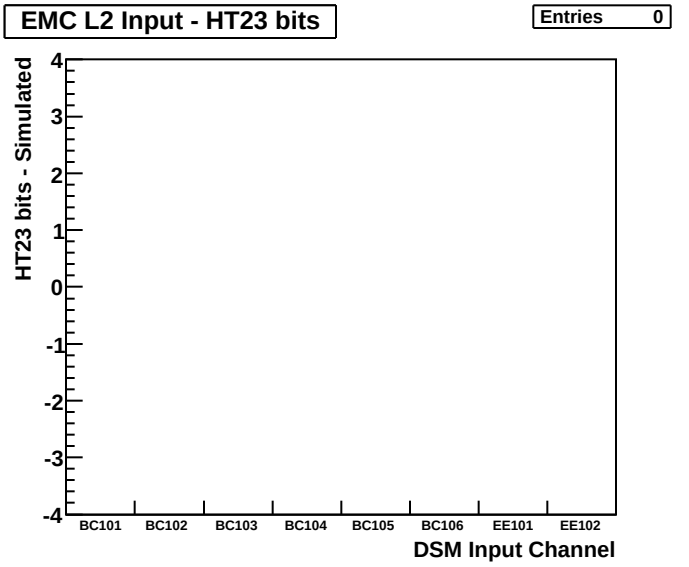

EMC L2 Input - Partial JP Sum
Entries 320200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

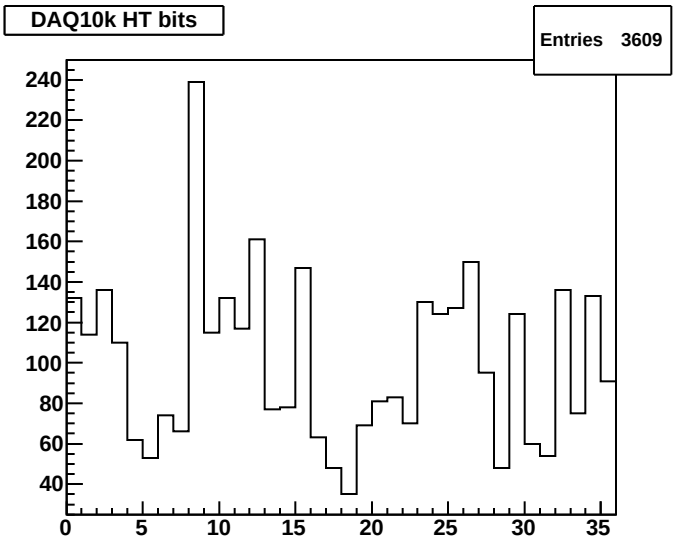

EMC L2 Input - Partial JP Sum
Entries 24
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

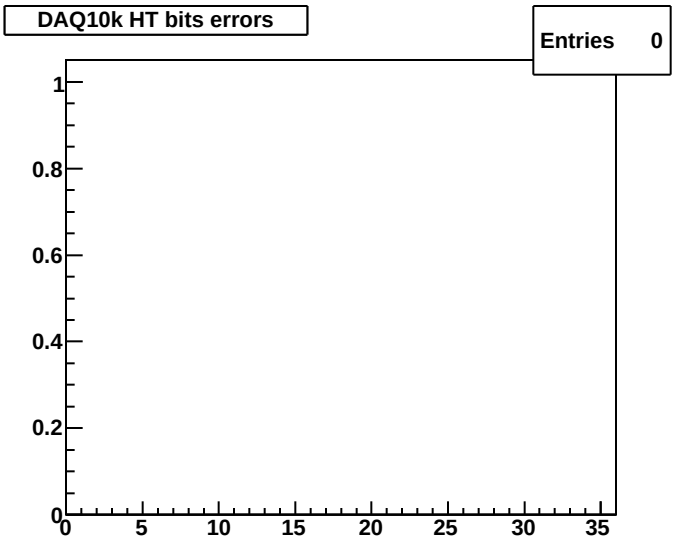

EMC L2 Input - HT01 bits/Partial JP ID
Entries 320200
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

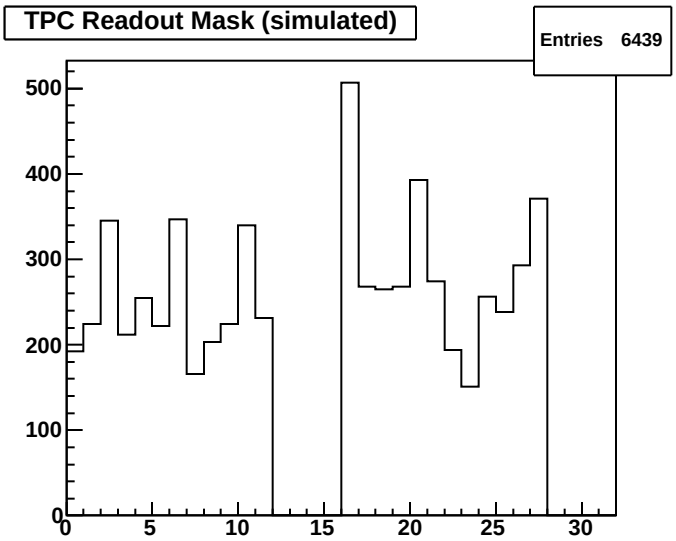

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

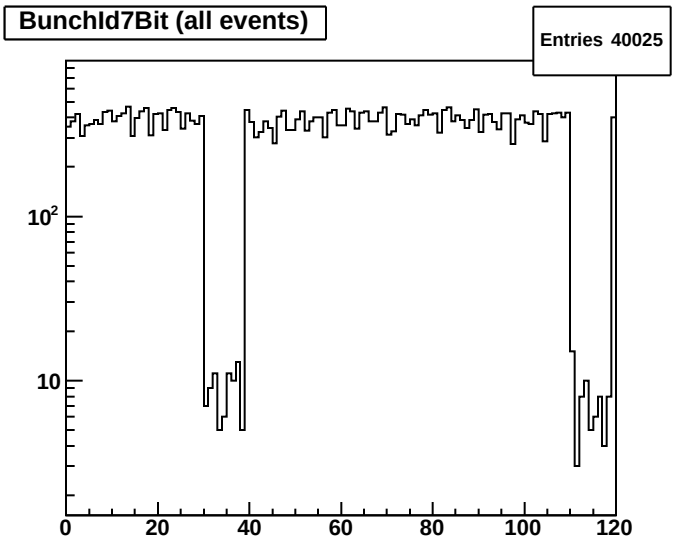

EMC L2 Input - HT23 bits
Entries 320200
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

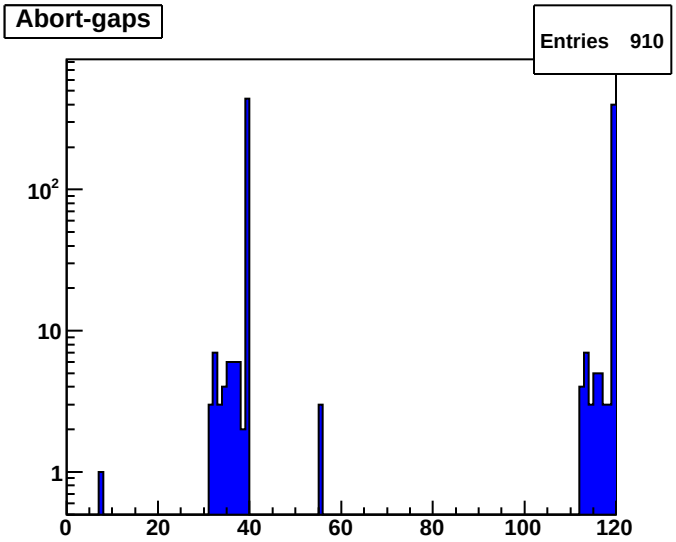

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

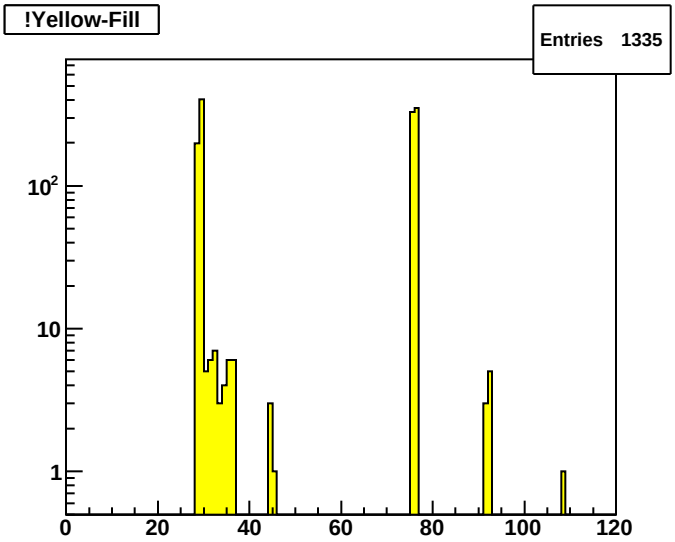

DAQ10k HT bits
Entries 3609

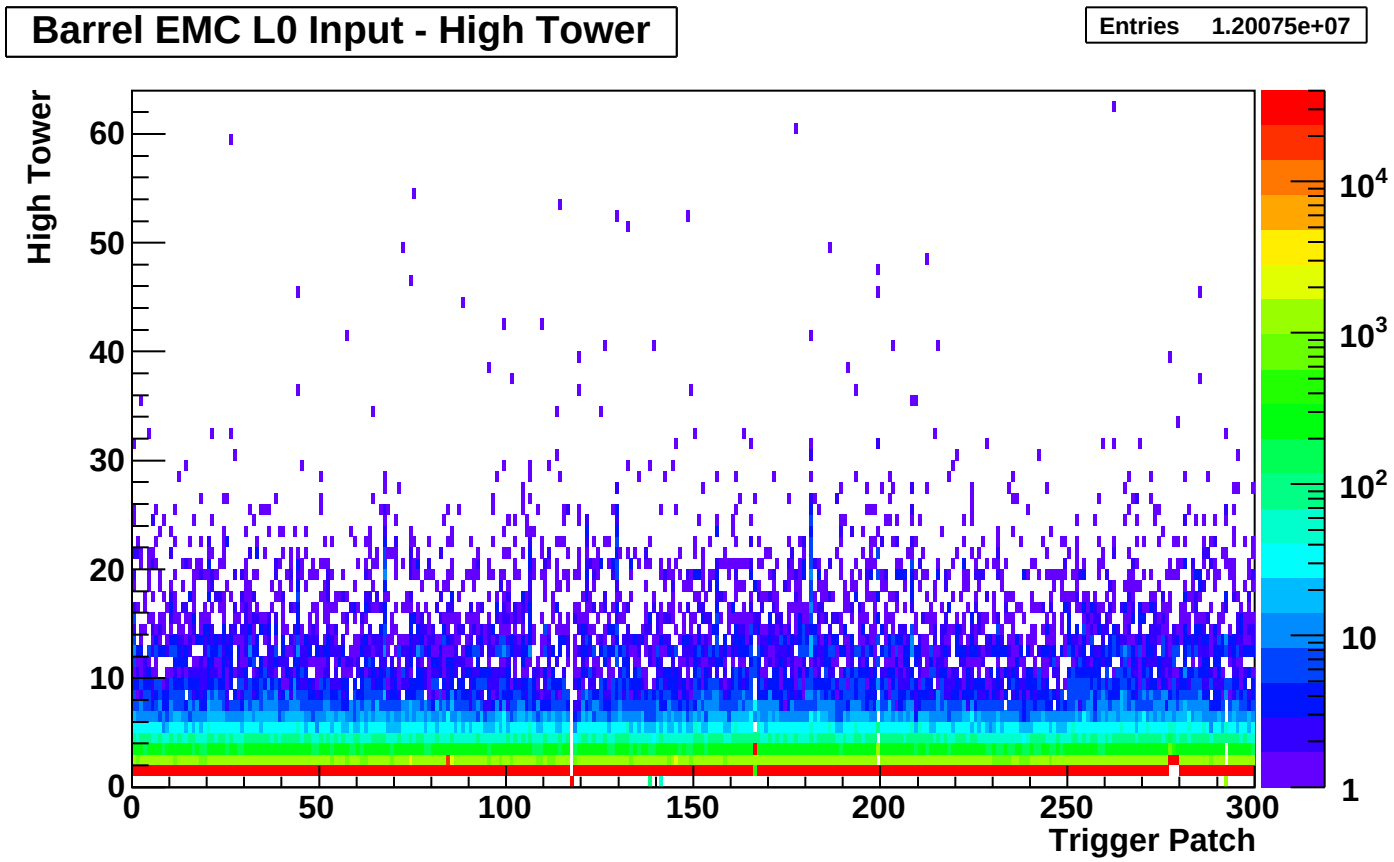

DAQ10k HT bits errors
Entries 0

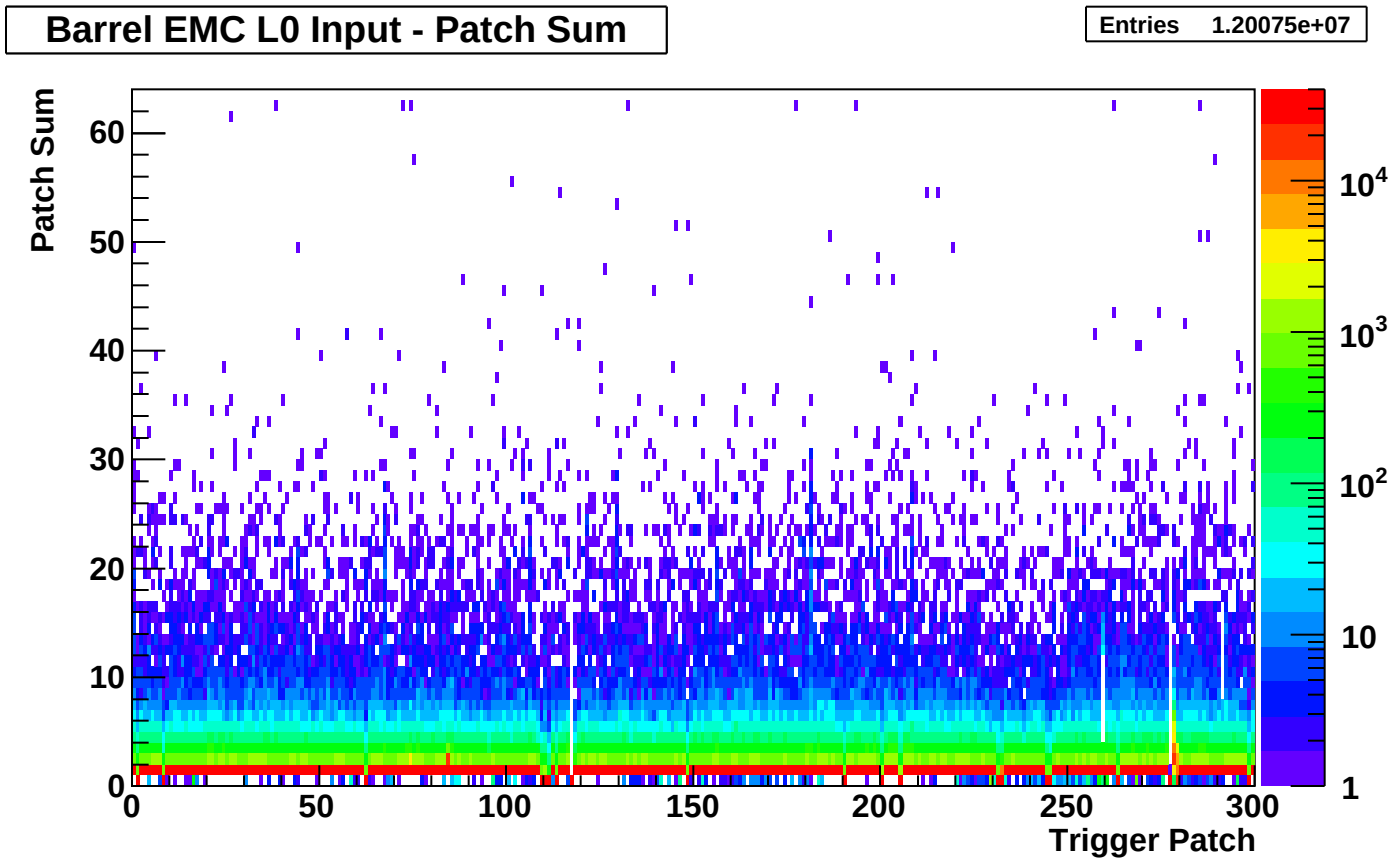

TPC Readout Mask (simulated)
Entries 6439

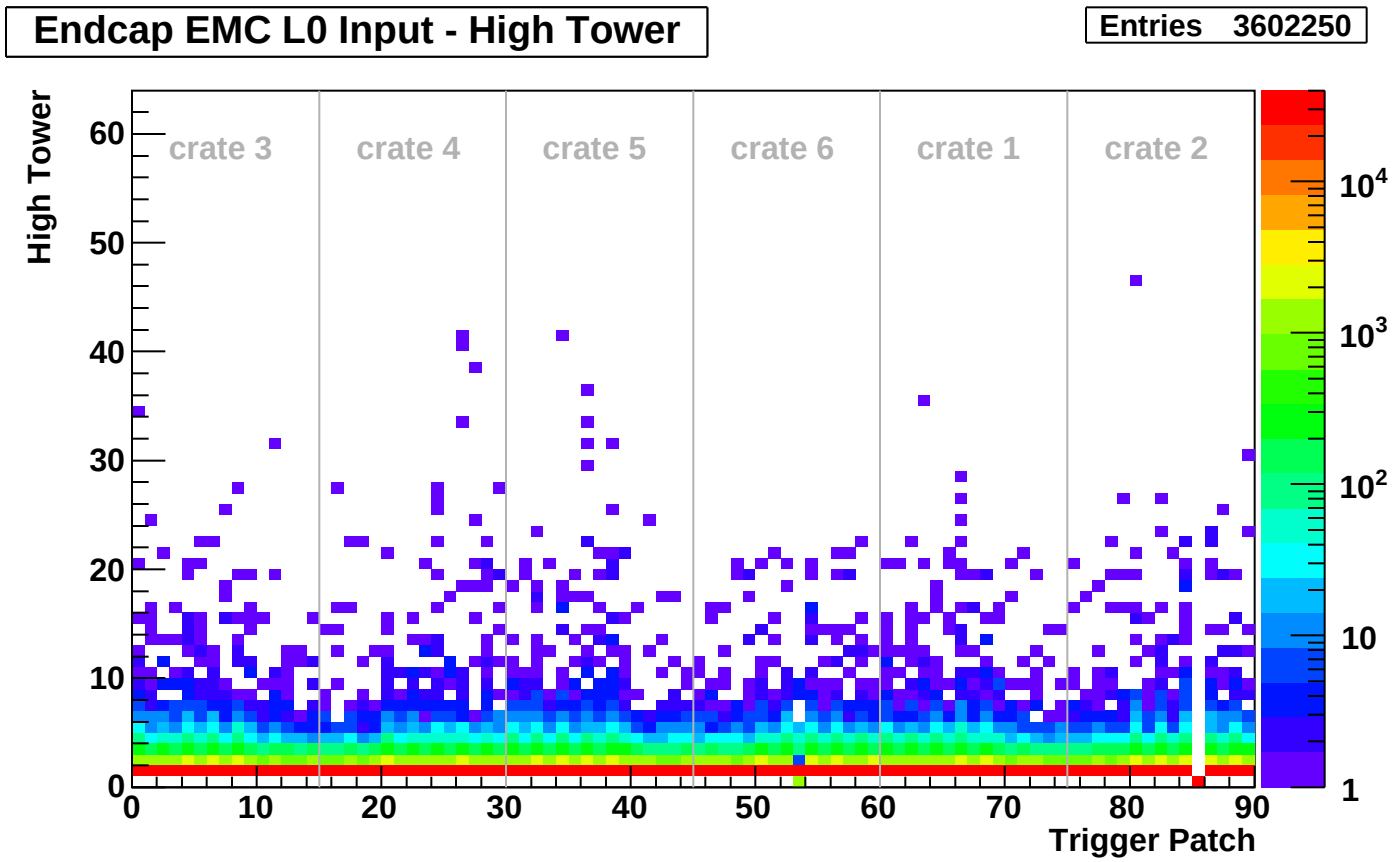

BunchId7Bit (all events)
Entries 40025

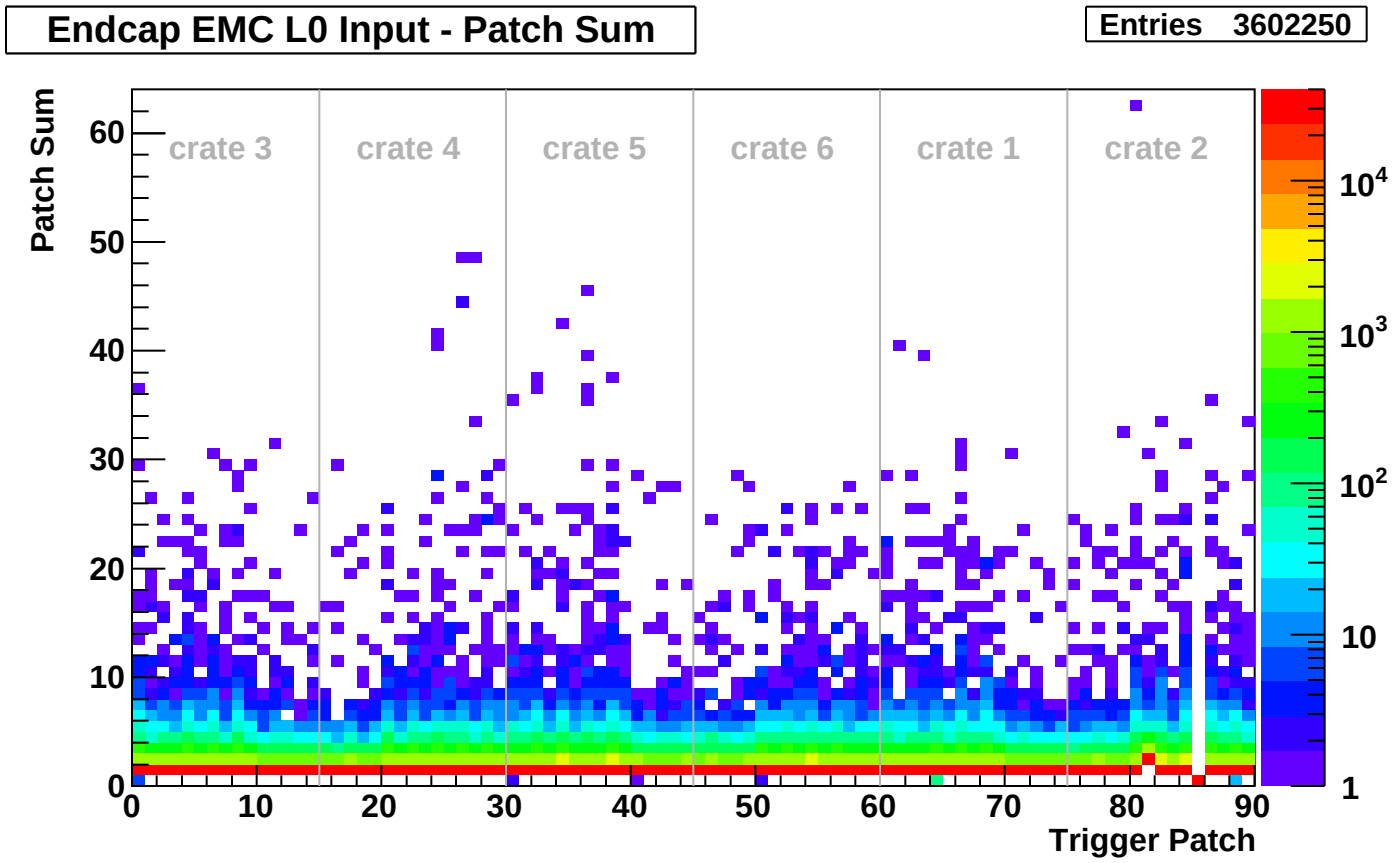

Abort-gaps
Entries 910

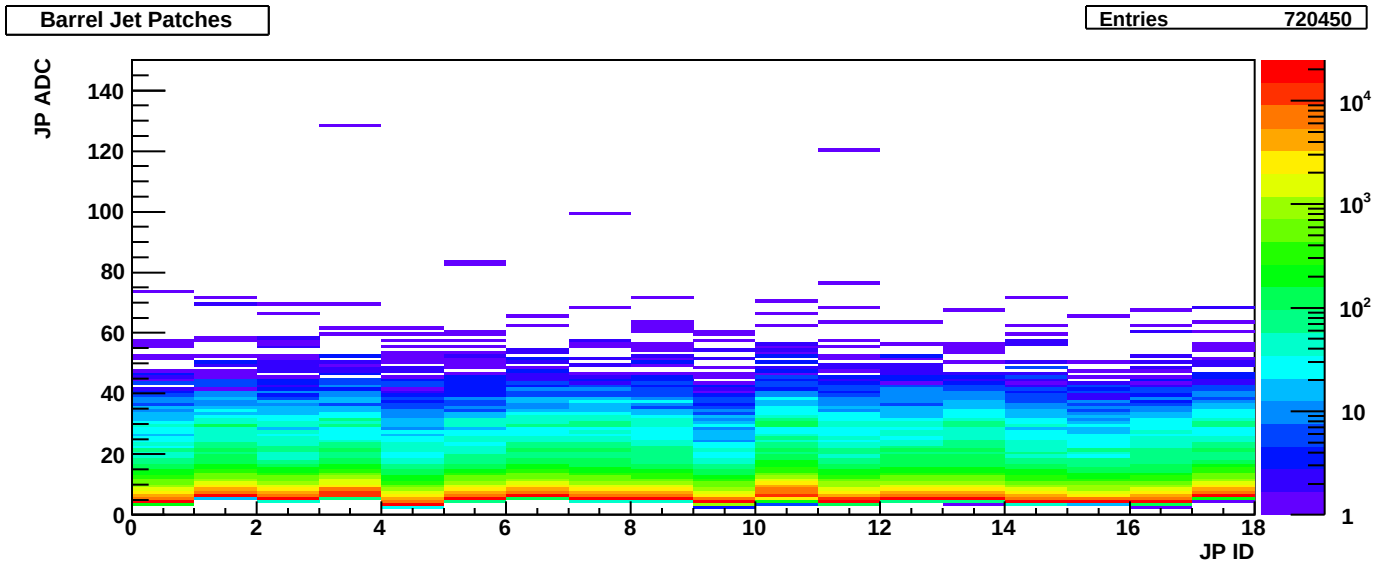

!Yellow-Fill
Entries 1335

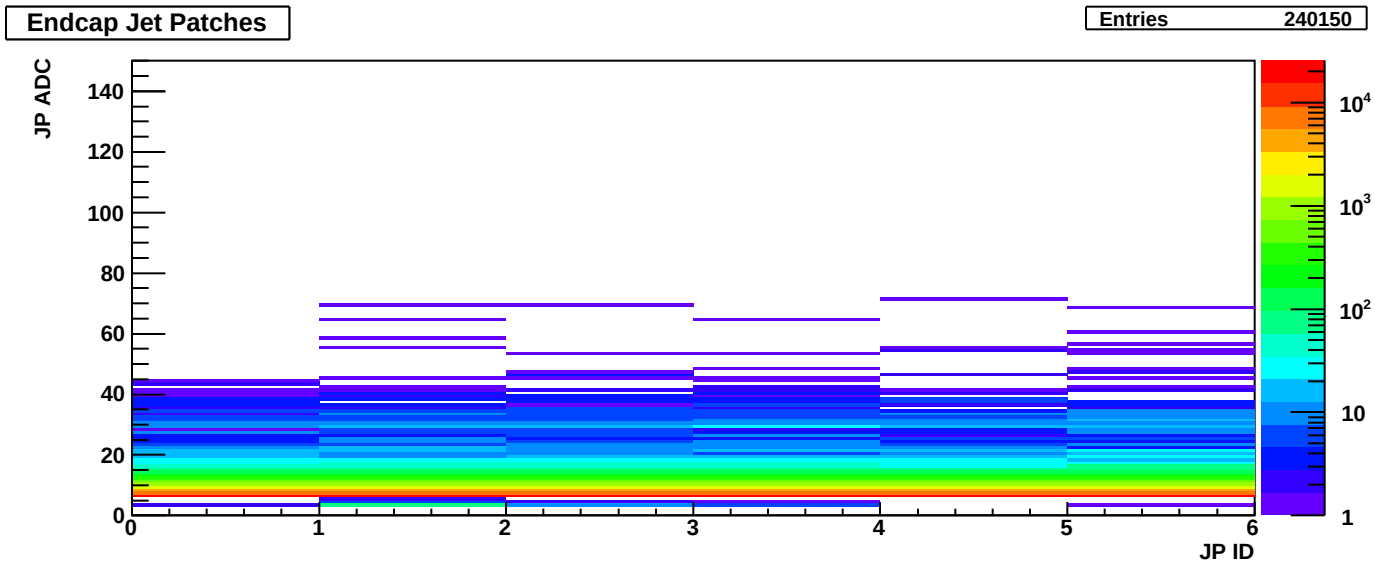

Barrel EMC L0 Input - High Tower
Entries 1.20075e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

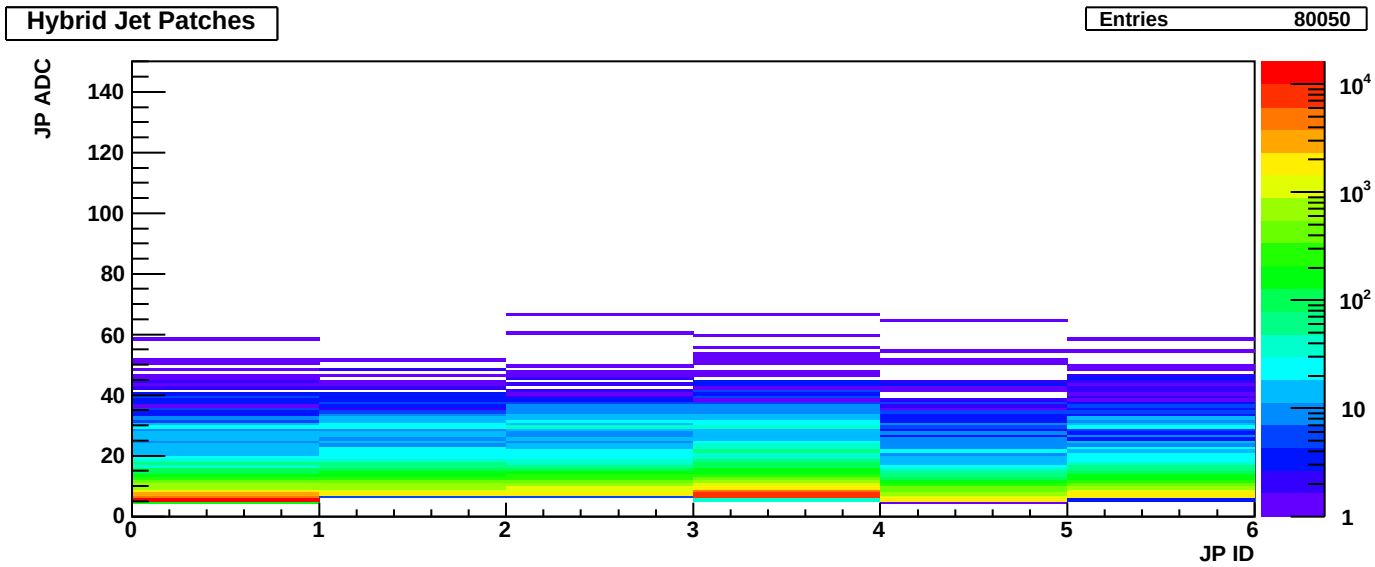

Barrel EMC L0 Input - Patch Sum
Entries 1.20075e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

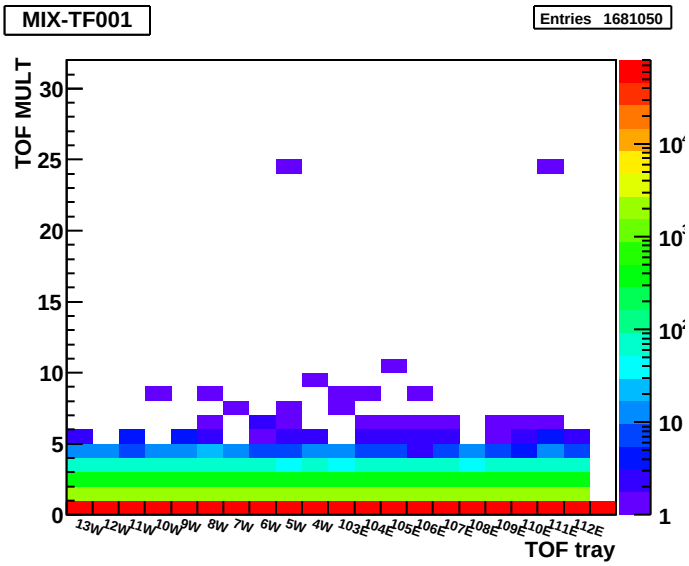

Endcap EMC L0 Input - High Tower
Entries 3602250
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

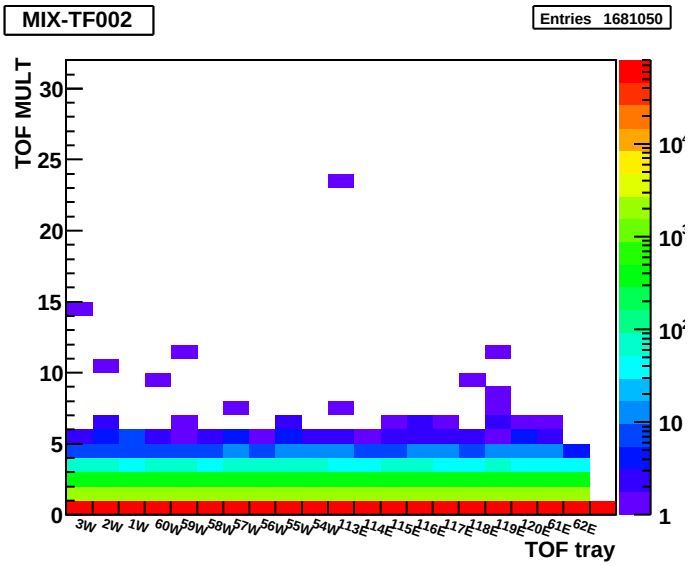

Endcap EMC L0 Input - Patch Sum
Entries 3602250
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

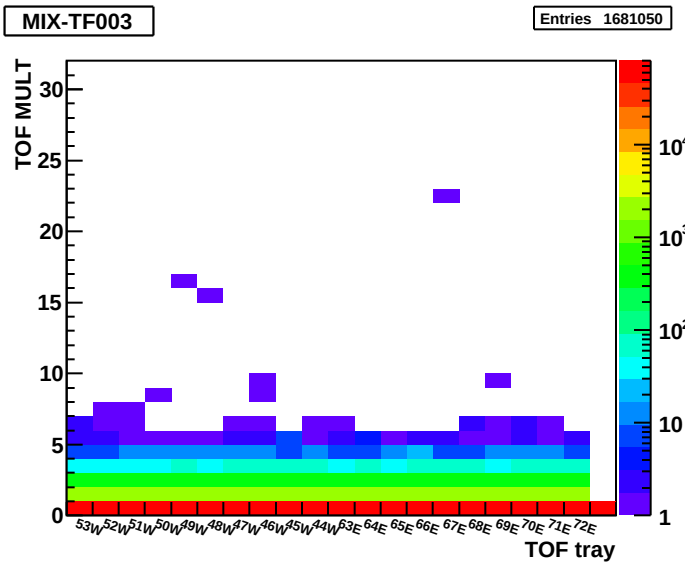

Barrel Jet Patches
Entries 720450
JP ADC
JP ID

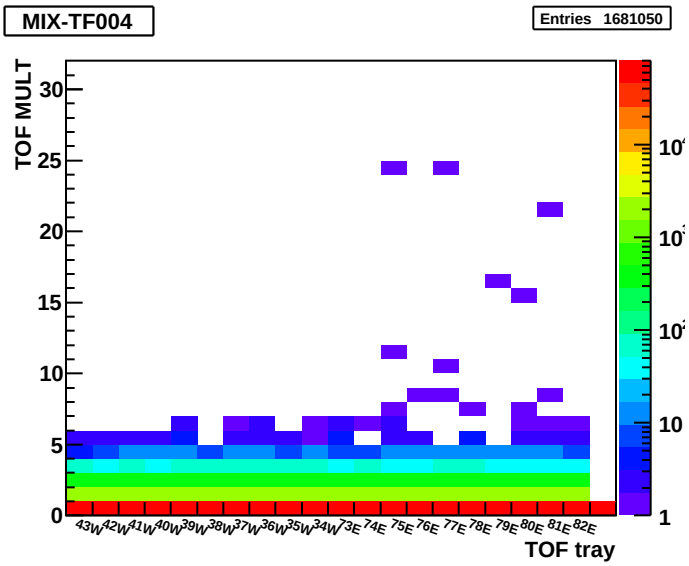

Endcap Jet Patches
Entries 240150
JP ADC
JP ID

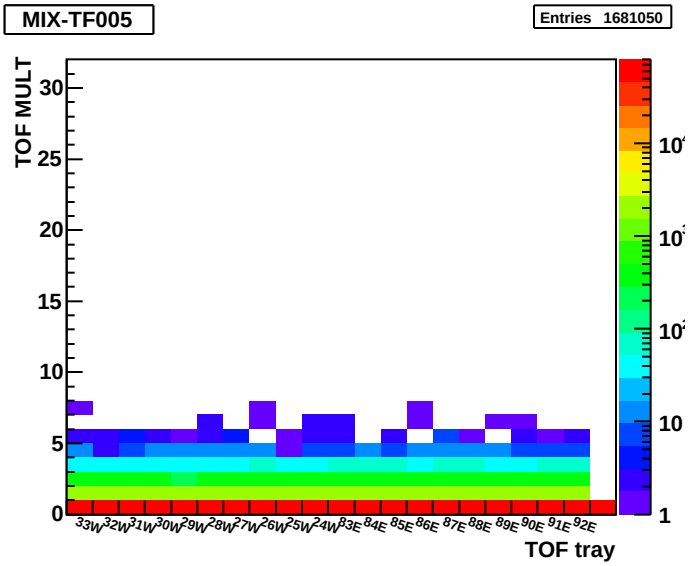

Hybrid Jet Patches
Entries 80050
JP ADC
JP ID

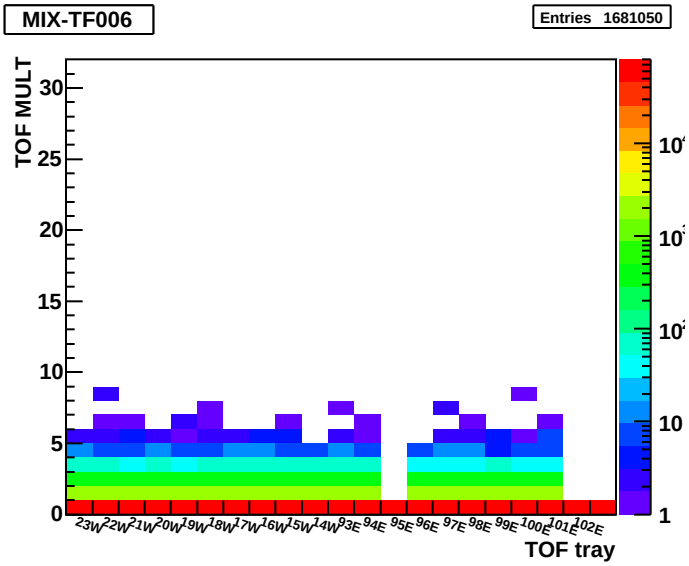

MIX-TF001
Entries 1681050
TOF MULT
TOF tray

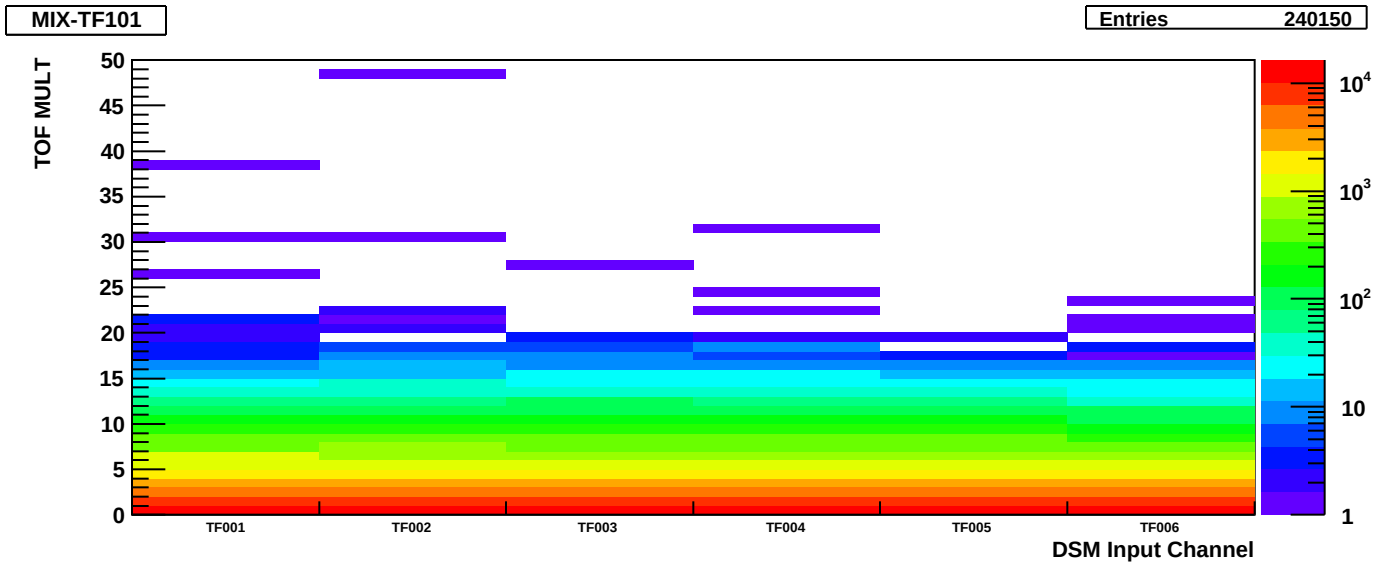

MIX-TF002
Entries 1681050
TOF MULT
TOF tray

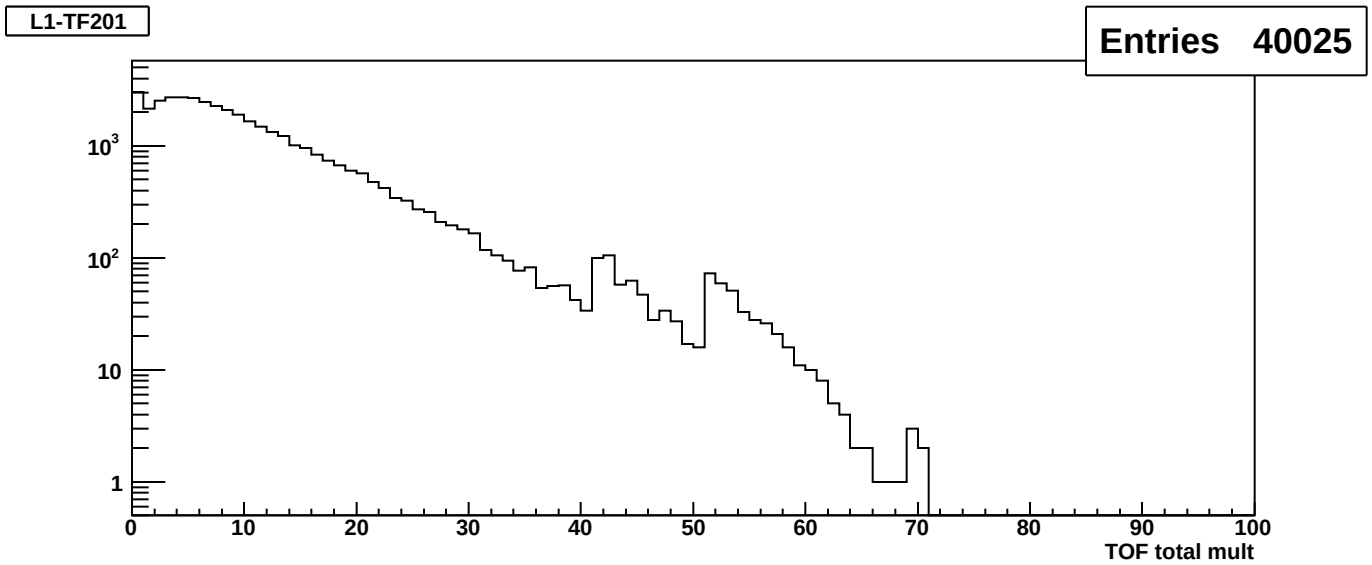

MIX-TF003
Entries 1681050
TOF MULT
TOF tray

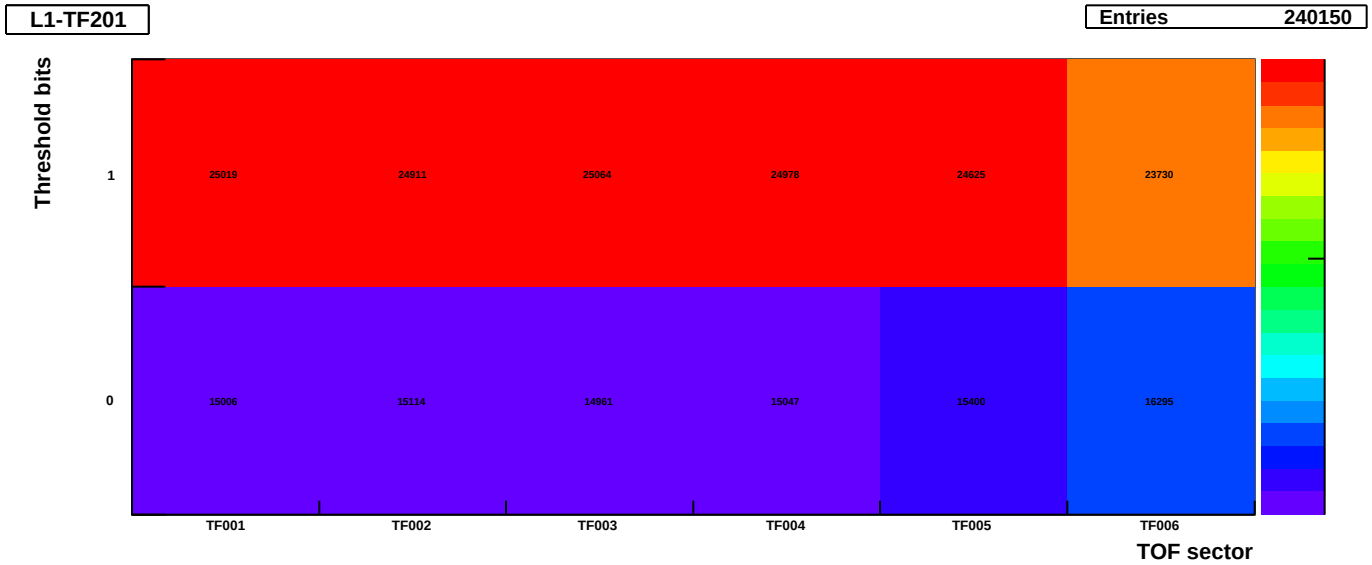

MIX-TF004
Entries 1681050
TOF MULT
TOF tray

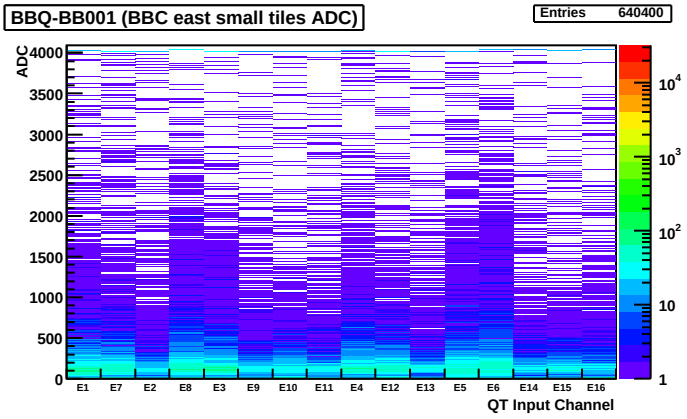

MIX-TF005
Entries 1681050
TOF MULT
TOF tray

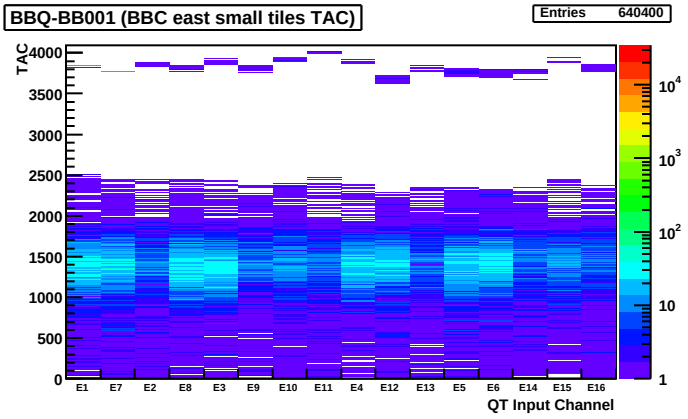

MIX-TF006
Entries 1681050
TOF MULT
TOF tray

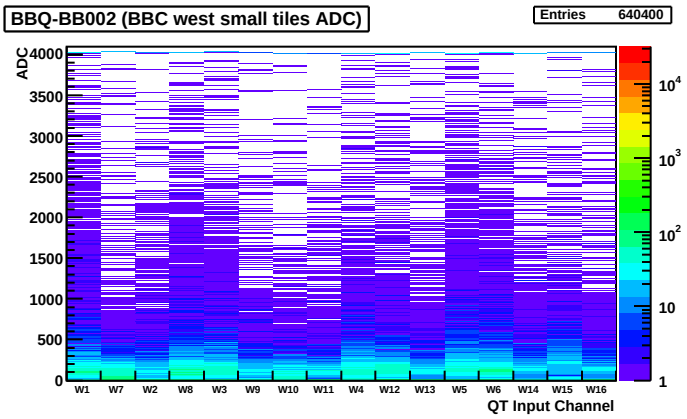

MIX-TF101
Entries 240150
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

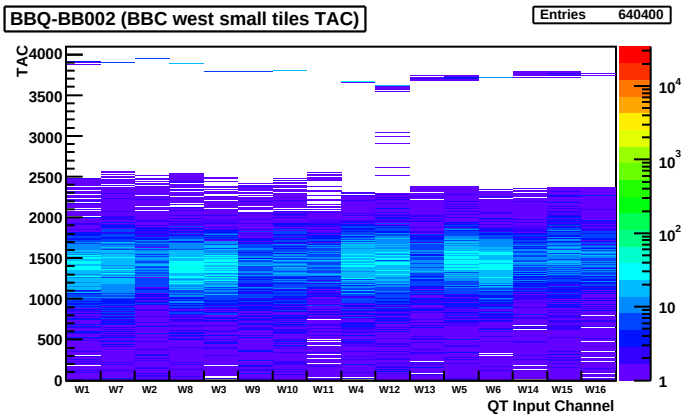

L1-TF201
Entries 40025
TOF total mult

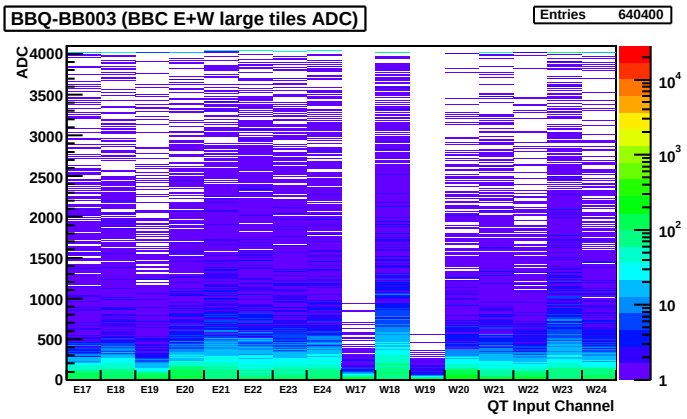

L1-TF201
Entries 240150
Threshold bits
25019
24911
25064
24978
24625
23730
15006
15114
14961
15047
15400
16295
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

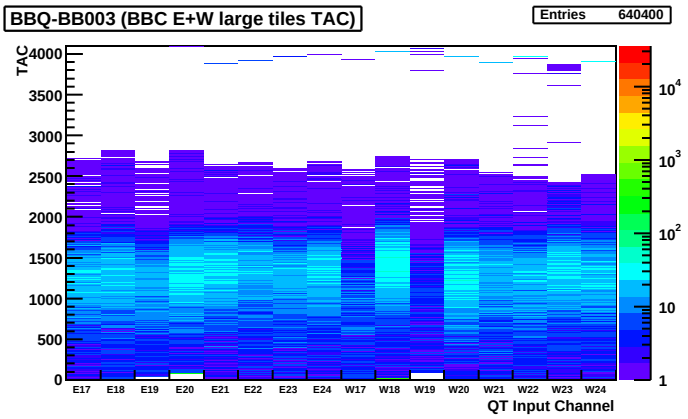

BBQ-BB001 (BBC east small tiles ADC)
Entries 640400
ADC
QT Input Channel

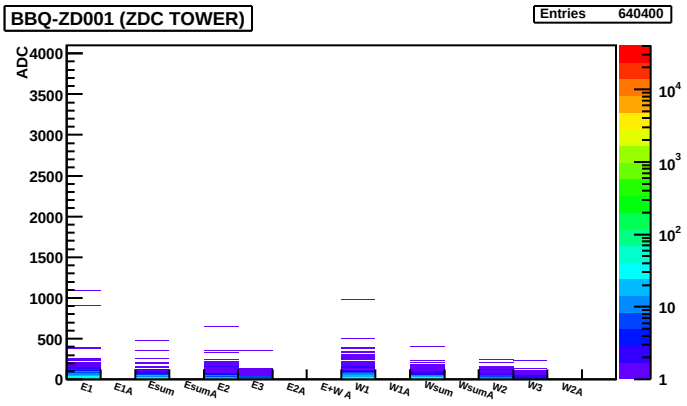

BBQ-BB001 (BBC east small tiles TAC)
Entries 640400
TAC
QT Input Channel

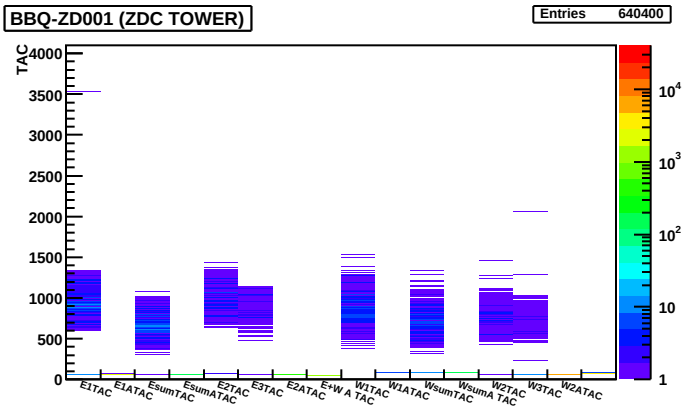

BBQ-BB002 (BBC west small tiles ADC)
Entries 640400
ADC
QT Input Channel

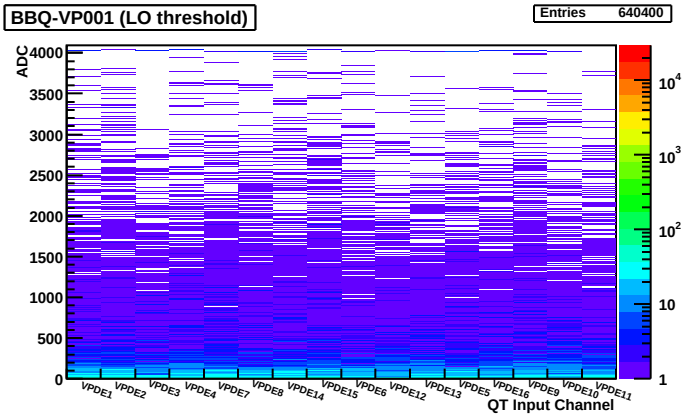

BBQ-BB002 (BBC west small tiles TAC)
Entries 640400
TAC
QT Input Channel

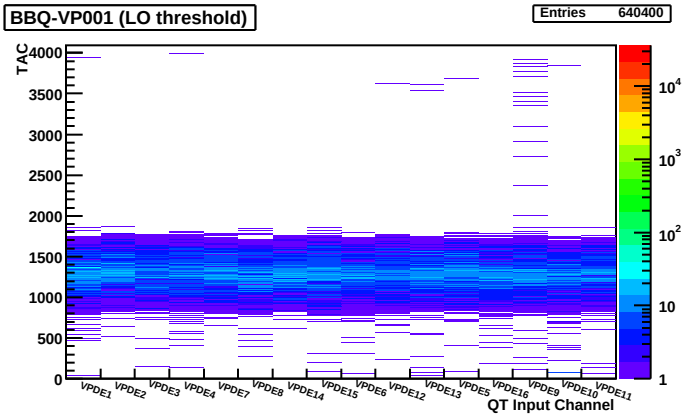

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 640400
ADC
QT Input Channel

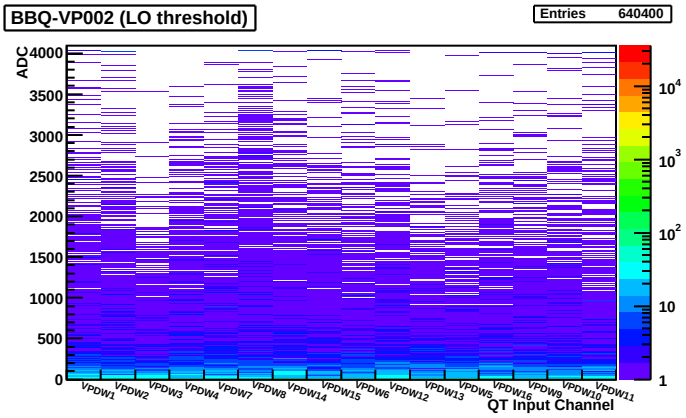

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 640400
TAC
QT Input Channel

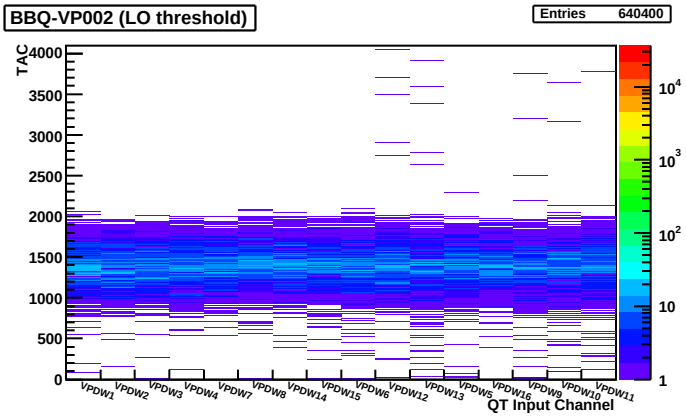

BBQ-ZD001 (ZDC TOWER)
Entries 640400
ADC

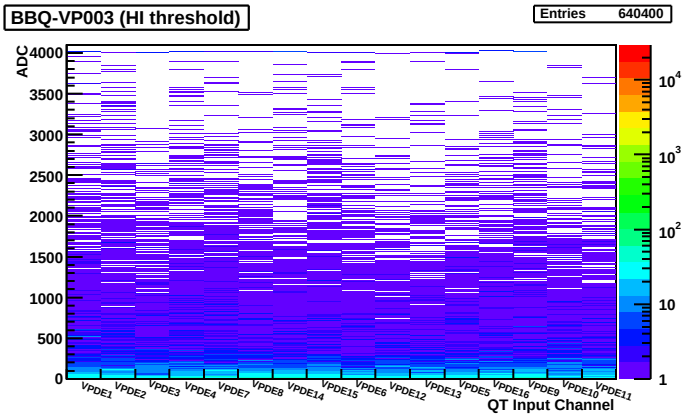

BBQ-ZD001 (ZDC TOWER)
Entries 640400
TAC

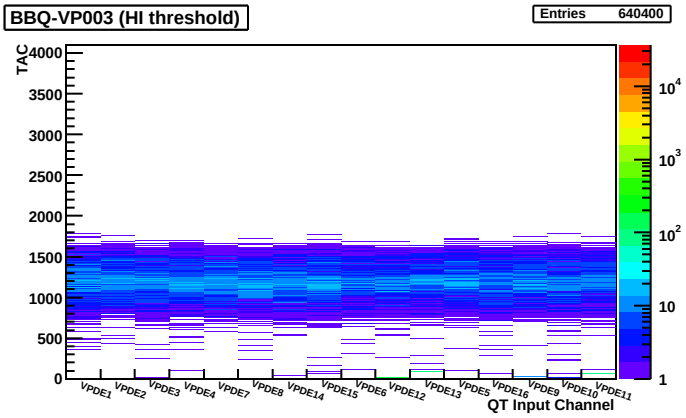

BBQ-VP001 (LO threshold)
Entries 640400
ADC
QT Input Channel

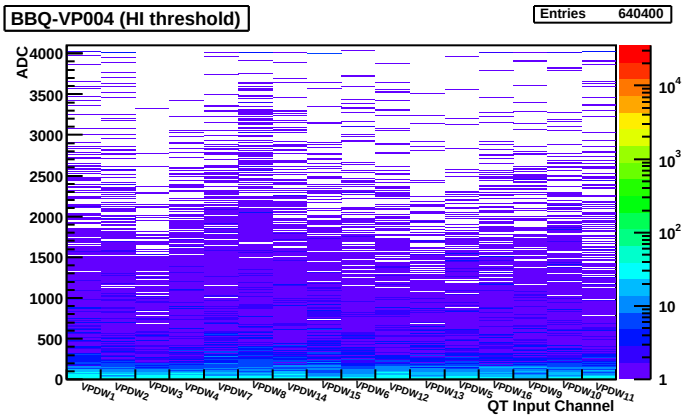

BBQ-VP001 (LO threshold)
Entries 640400
TAC
QT Input Channel

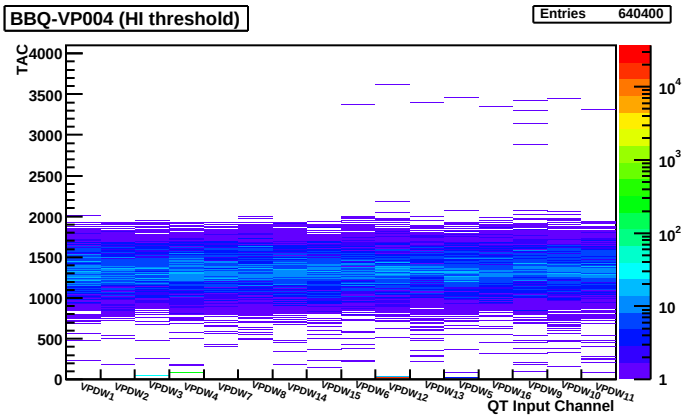

BBQ-VP002 (LO threshold)
Entries 640400
ADC
QT Input Channel

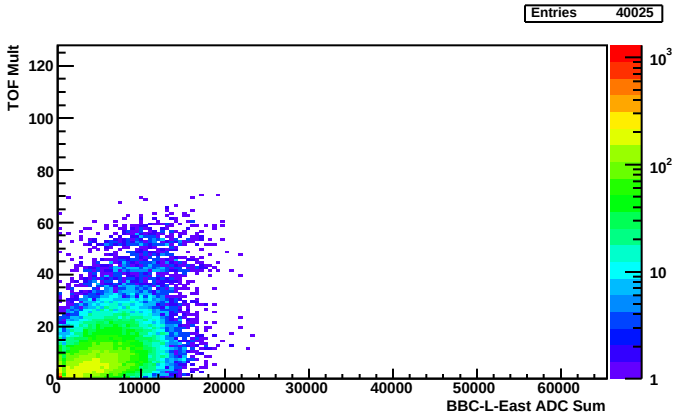

BBQ-VP002 (LO threshold)
Entries 640400
TAC
QT Input Channel

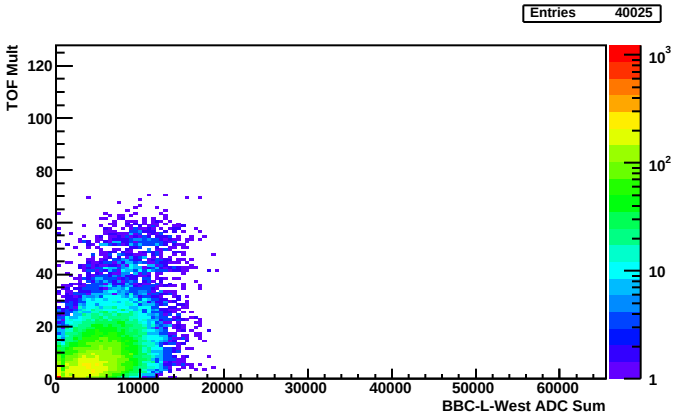

BBQ-VP003 (HI threshold)
Entries 640400
ADC
QT Input Channel

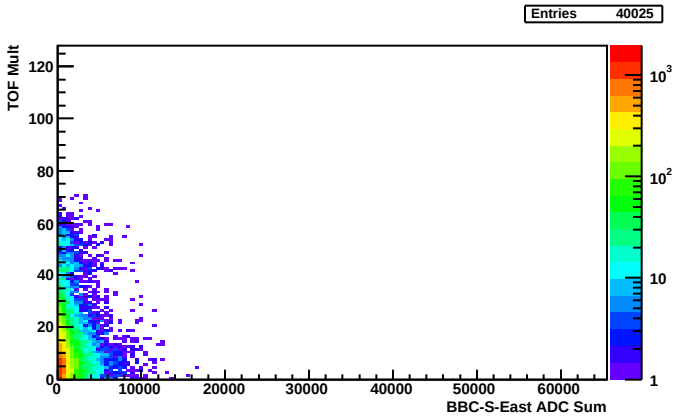

BBQ-VP003 (HI threshold)
Entries 640400
TAC
QT Input Channel

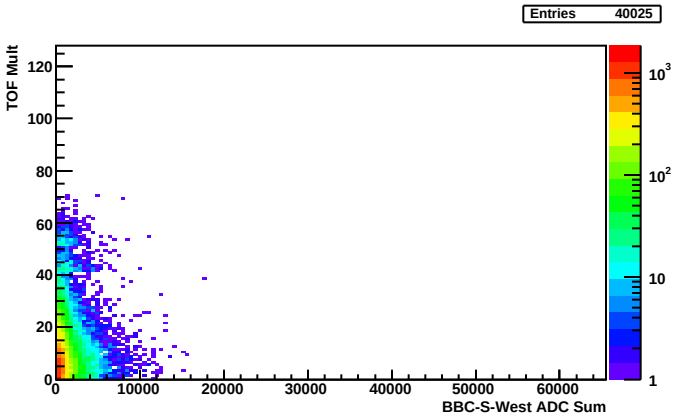

BBQ-VP004 (HI threshold)
Entries 640400
ADC
QT Input Channel

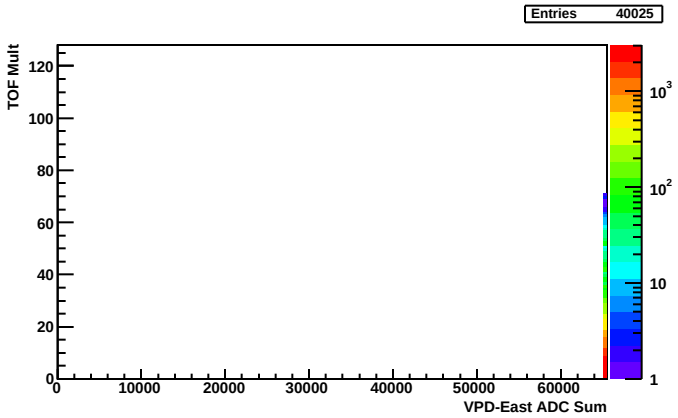

BBQ-VP004 (HI threshold)
Entries 640400
TAC
QT Input Channel

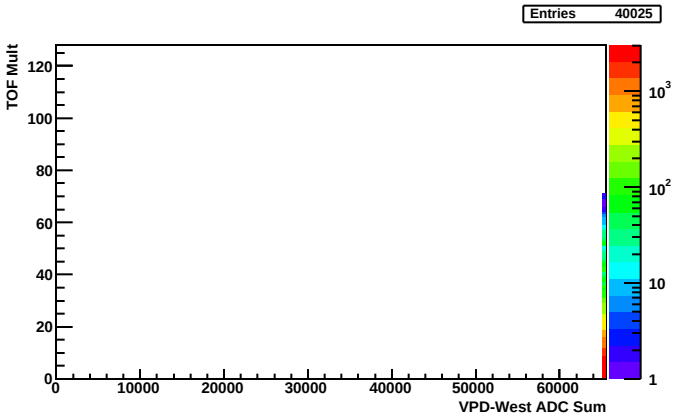

Entries 40025
TOF Mult
BBC-L-East ADC Sum

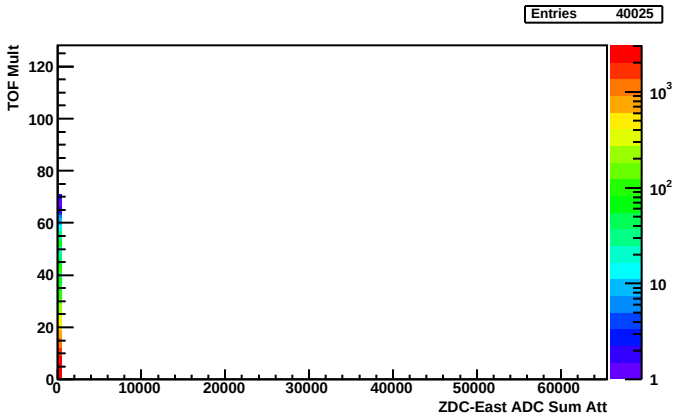

Entries 40025
TOF Mult
BBC-L-West ADC Sum

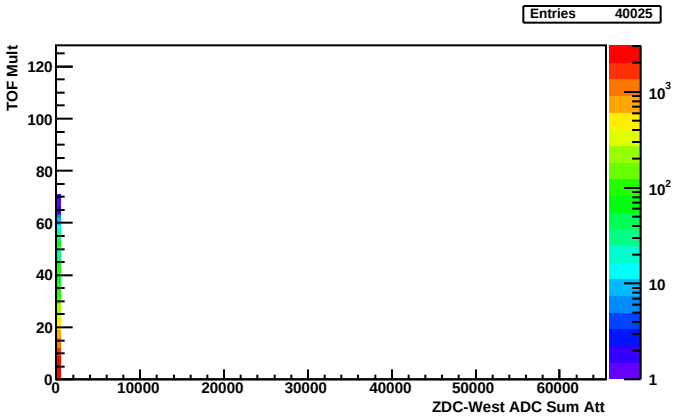

Entries 40025
TOF Mult
BBC-S-East ADC Sum

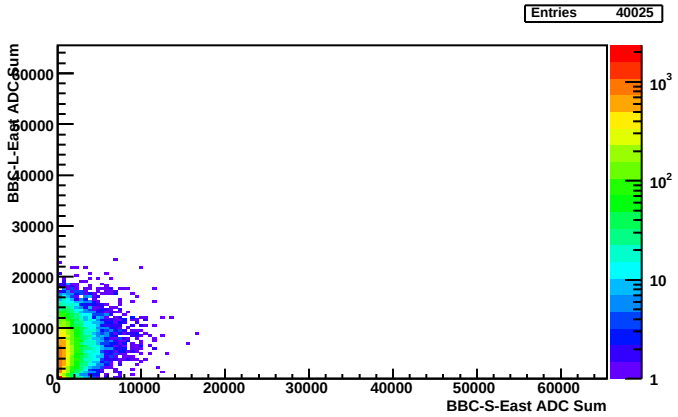

Entries 40025
TOF Mult
BBC-S-West ADC Sum

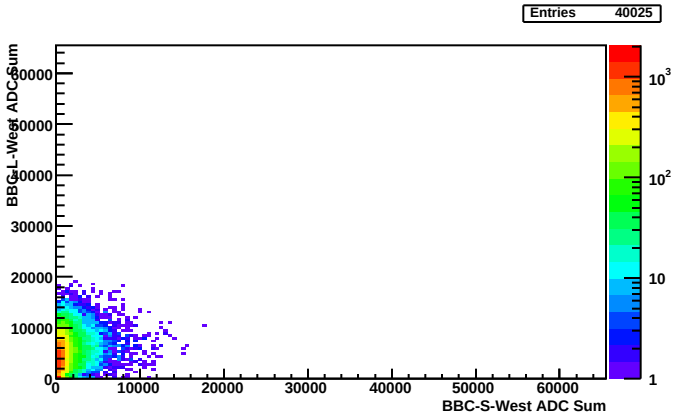

Entries 40025
TOF Mult
VPD-East ADC Sum

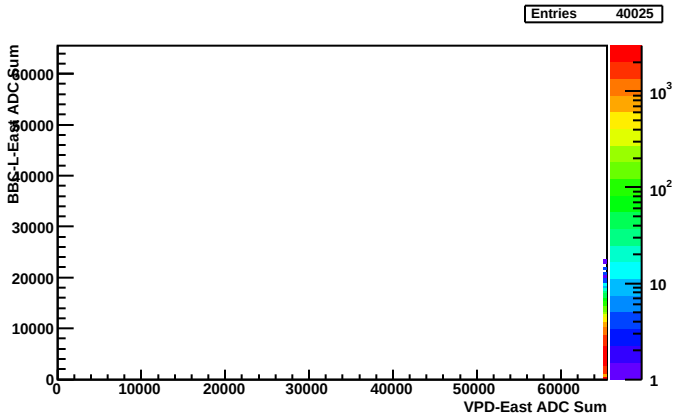

Entries 40025
TOF Mult
VPD-West ADC Sum

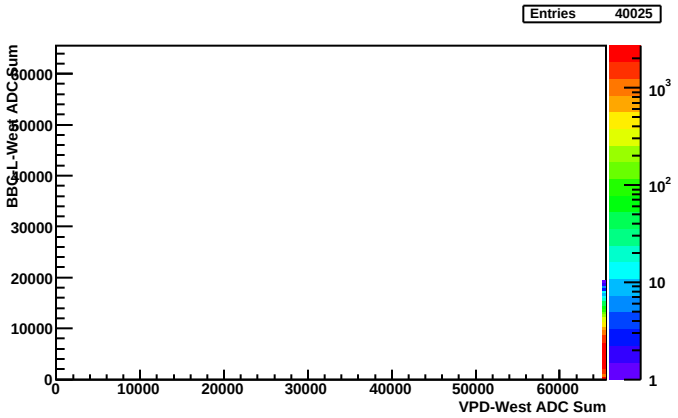

Entries 40025
TOF Mult
ZDC-East ADC Sum Att

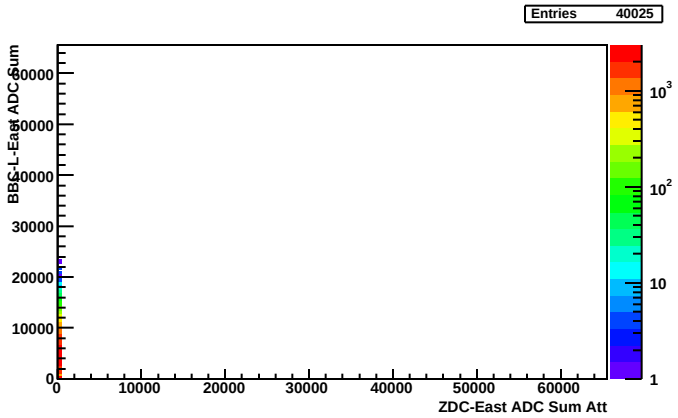

Entries 40025
TOF Mult
ZDC-West ADC Sum Att

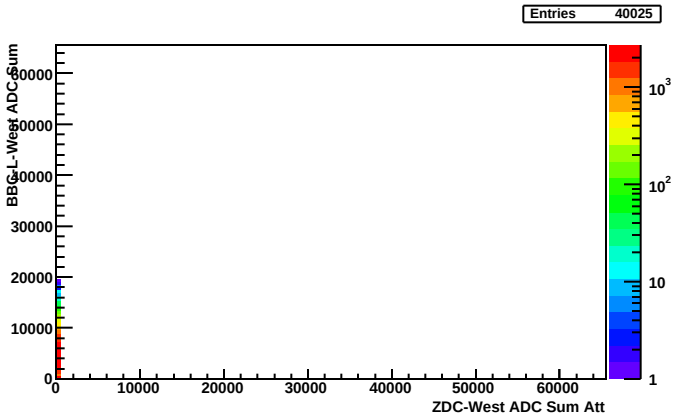

Entries 40025
BBC-L-East ADC Sum
BBC-S-East ADC Sum

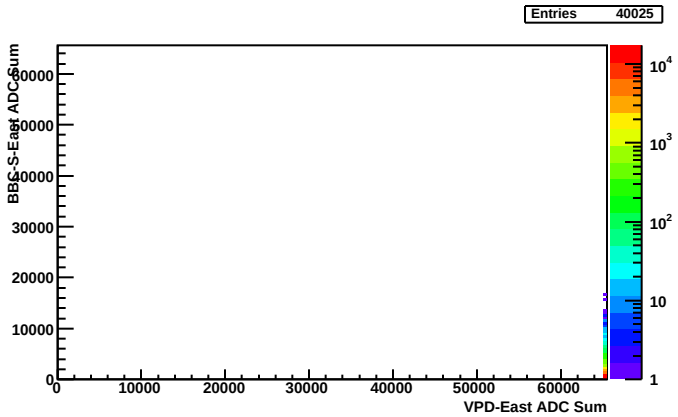

Entries 40025
BBC-L-West ADC Sum
BBC-S-West ADC Sum

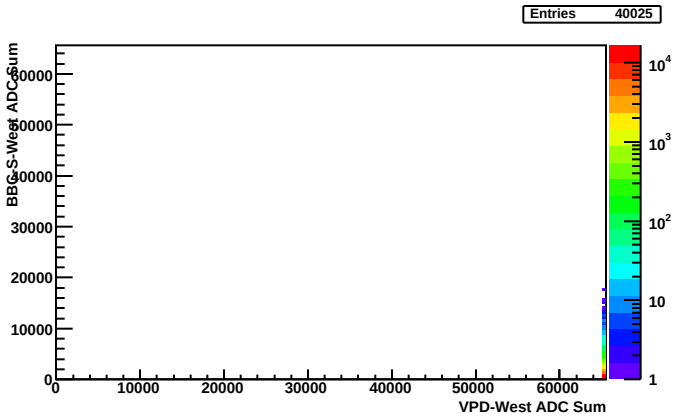

Entries 40025
BBC-L-East ADC Sum
VPD-East ADC Sum

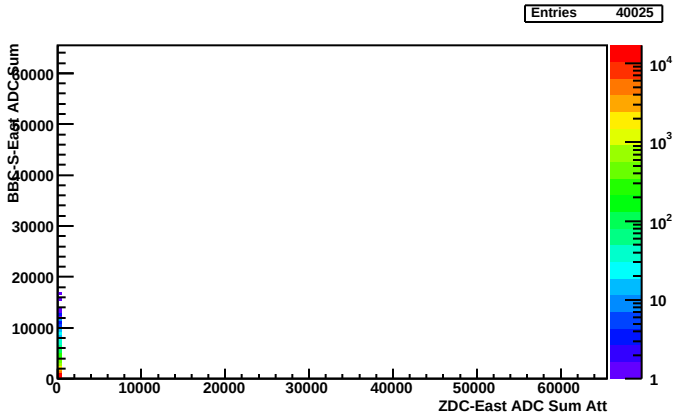

Entries 40025
BBC-L-West ADC Sum
VPD-West ADC Sum

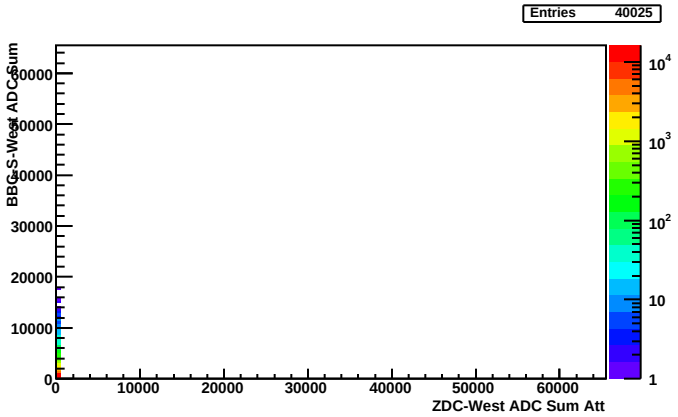

Entries 40025
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 40025
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 40025
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 40025
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 40025
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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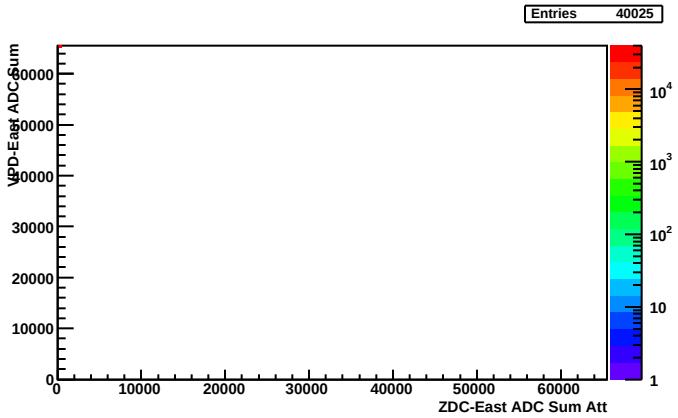
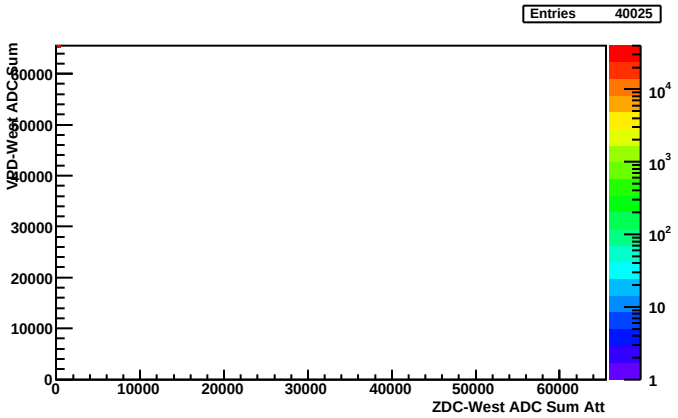
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 40025
VPD-East ADC Sum
ZDC-East ADC Sum Att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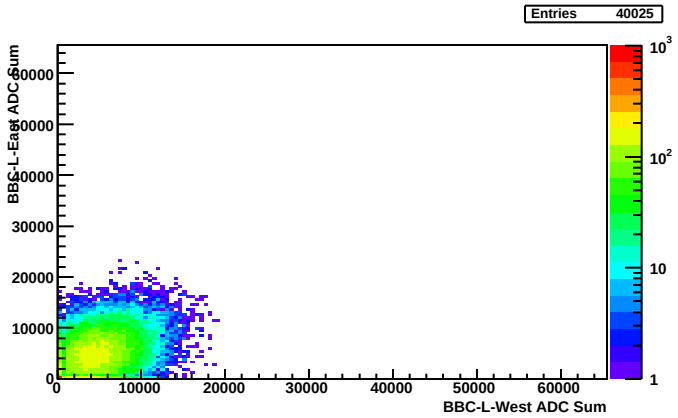
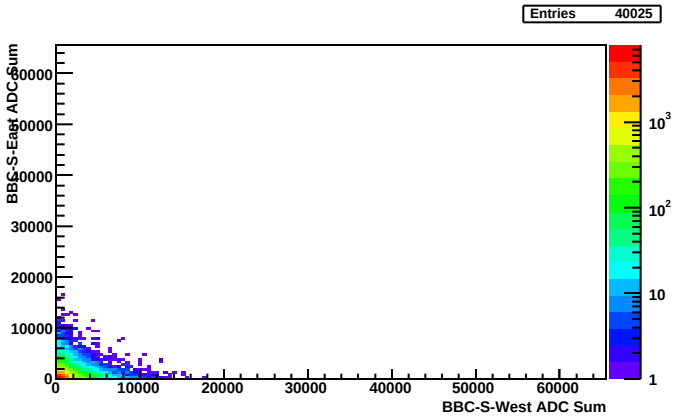
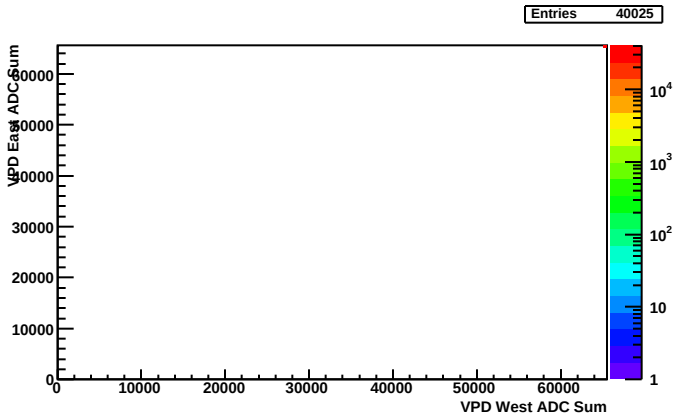
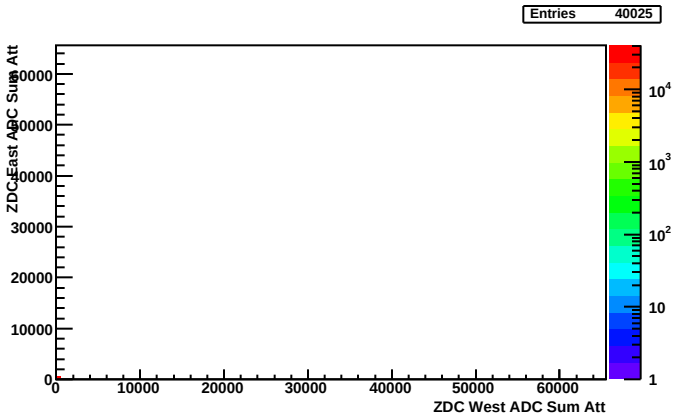
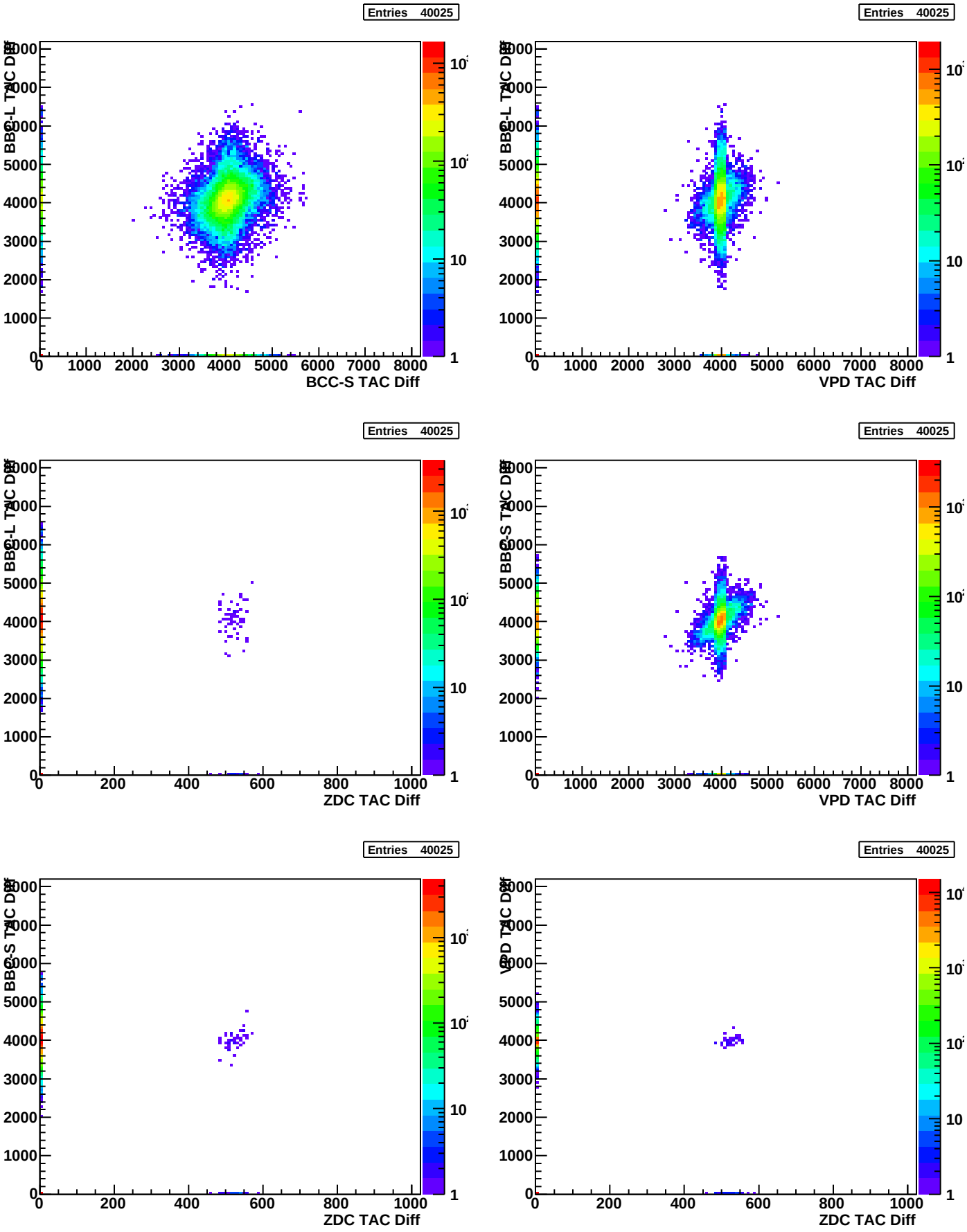
VPD-West ADC Sum
ZDC-West ADC Sum Att

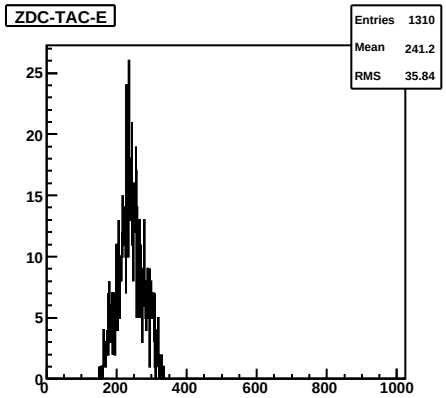

Entries 40025
BBC-L-East ADC Sum
BBC-L-West ADC Sum

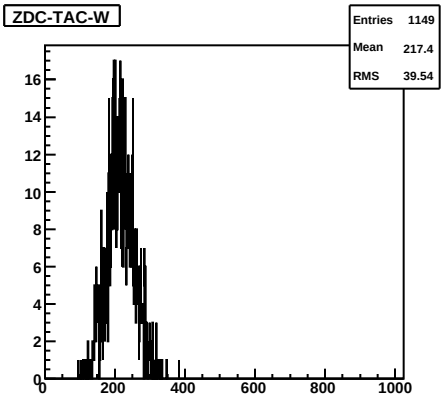

Entries 40025
BBC-S-East ADC Sum
BBC-S-West ADC Sum

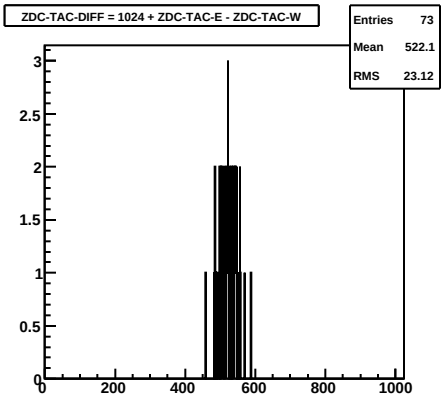

Entries 40025
VPD East ADC Sum
VPD West ADC Sum

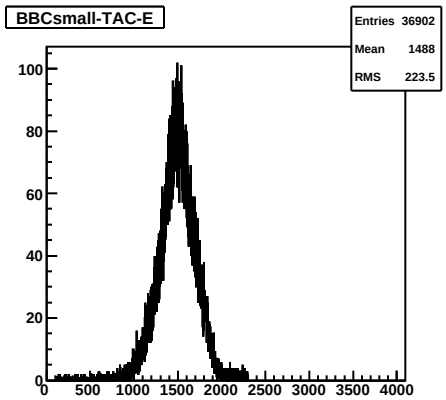

Entries 40025
ZDC East ADC Sum Att
ZDC West ADC Sum Att

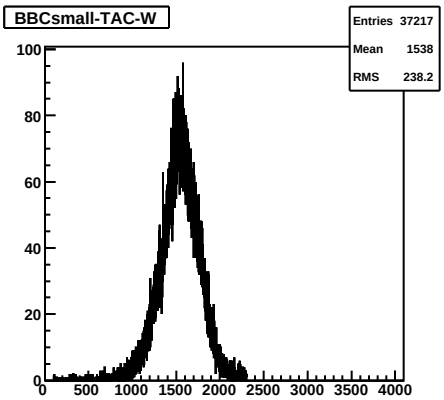

Entries 40025
BBC-L TAC Diff
BCC-S TAC Diff
Entries 40025
BBC-L TAC Diff
VPD TAC Diff
Entries 40025
BBC-L TAC Diff
ZDC TAC Diff
Entries 40025
BBC-S TAC Diff
VPD TAC Diff
Entries 40025
BBC-S TAC Diff
ZDC TAC Diff
Entries 40025
VPD TAC Diff
ZDC TAC Diff

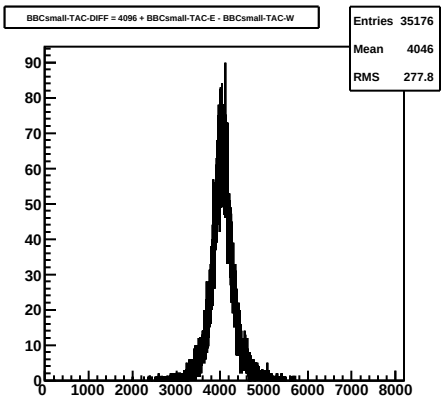

ZDC-TAC-E
Entries 1310
Mean 241.2
RMS 35.84

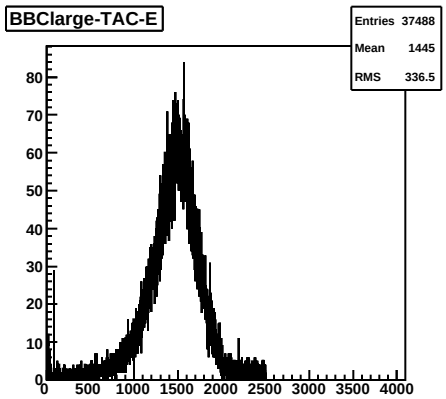

ZDC-TAC-W
Entries 1149
Mean 217.4
RMS 39.54

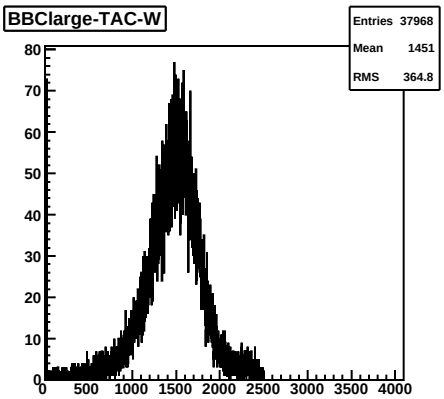

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 73
Mean 522.1
RMS 23.12

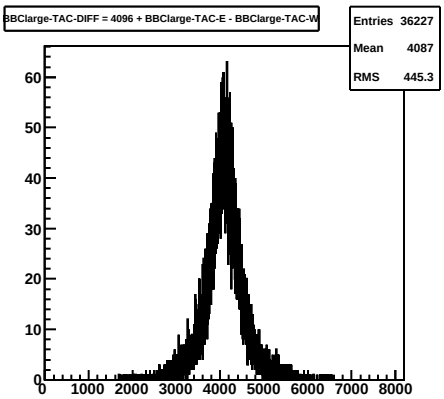

BBCsmall-TAC-E
Entries 36902
Mean 1488
RMS 223.5

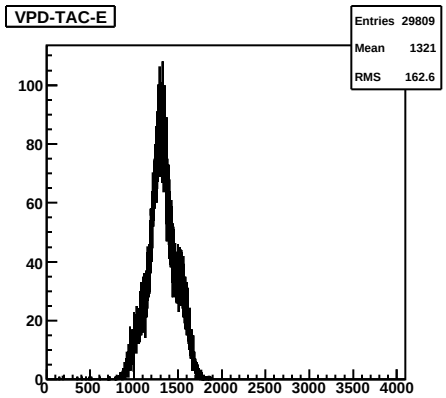

BBCsmall-TAC-W
Entries 37217
Mean 1538
RMS 238.2

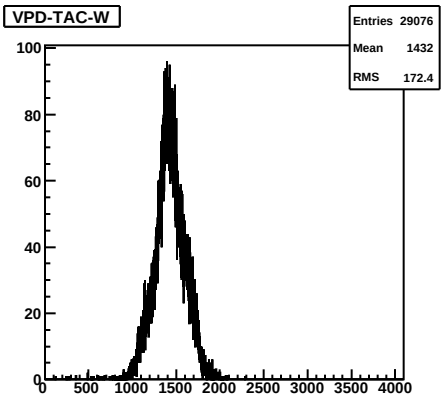

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 35176
Mean 4046
RMS 277.8

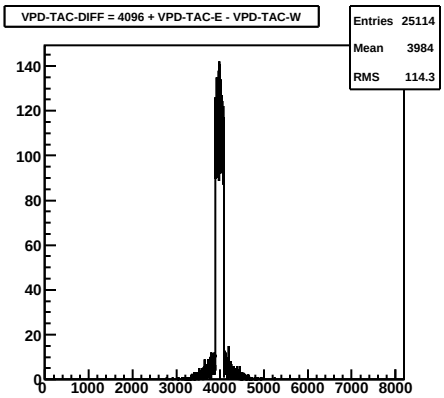

BBClarge-TAC-E
Entries 37488
Mean 1445
RMS 336.5

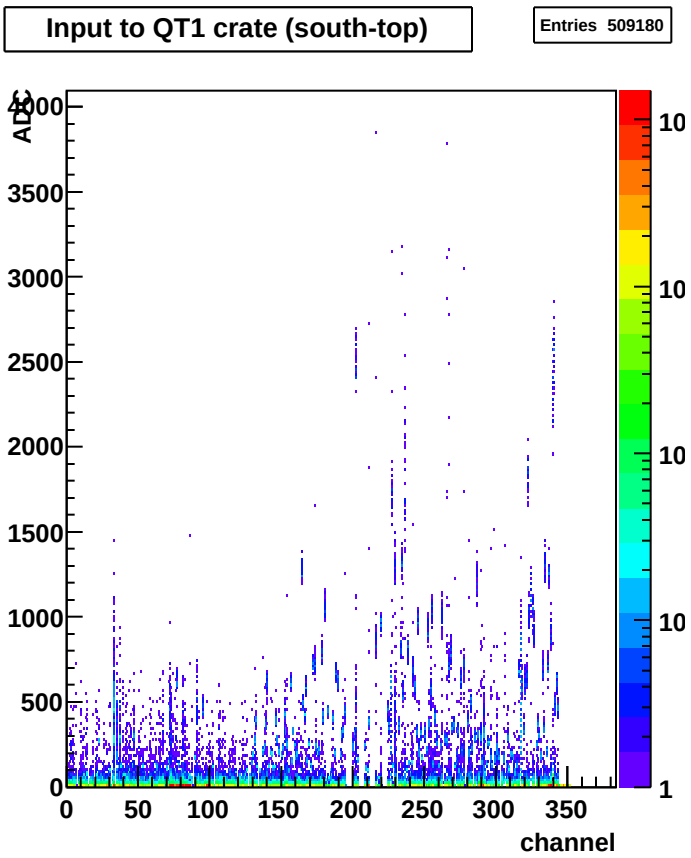

BBClarge-TAC-W
Entries 37968
Mean 1451
RMS 364.8

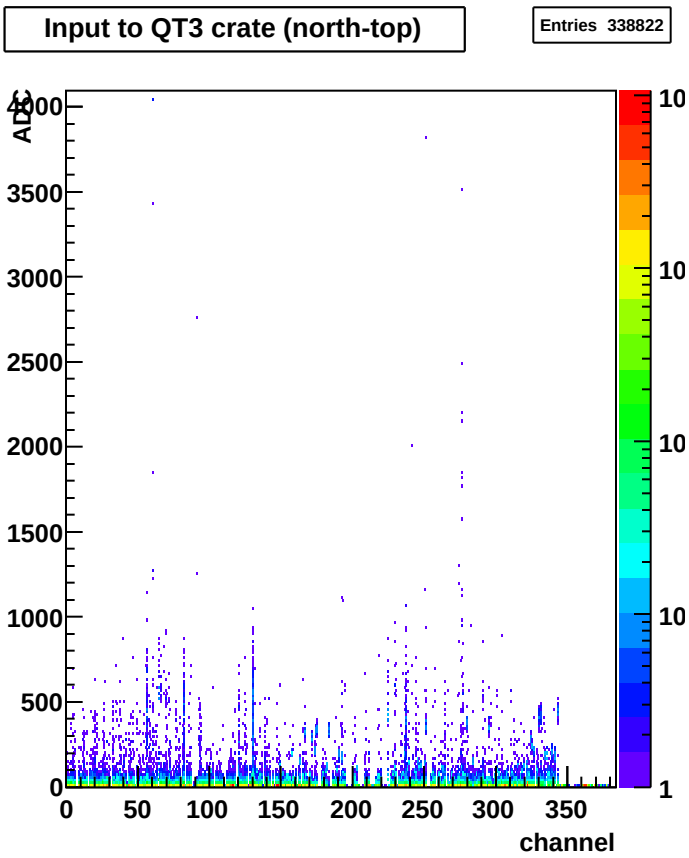

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 36227
Mean 4087
RMS 445.3

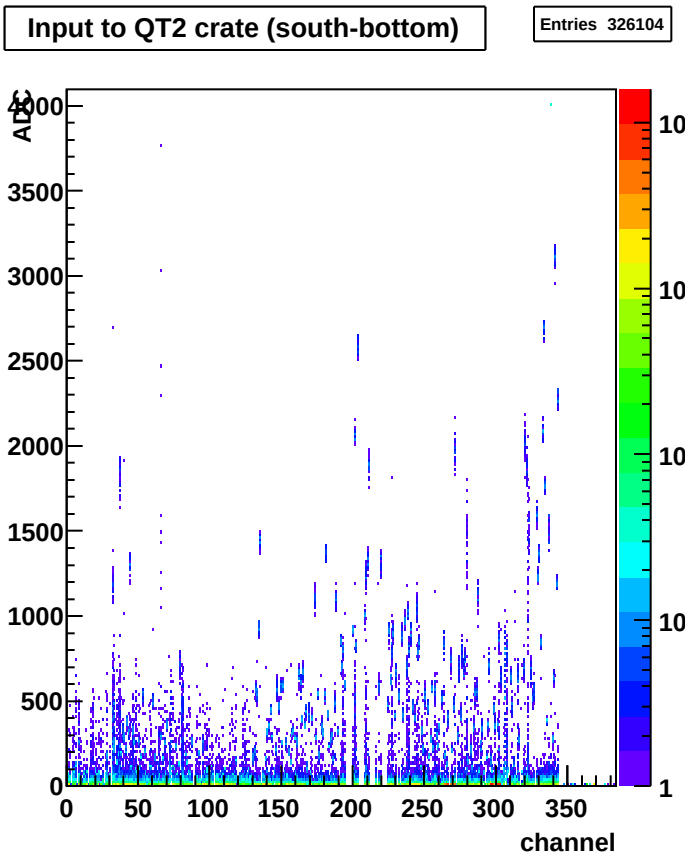

VPD-TAC-E
Entries 29809
Mean 1321
RMS 162.6

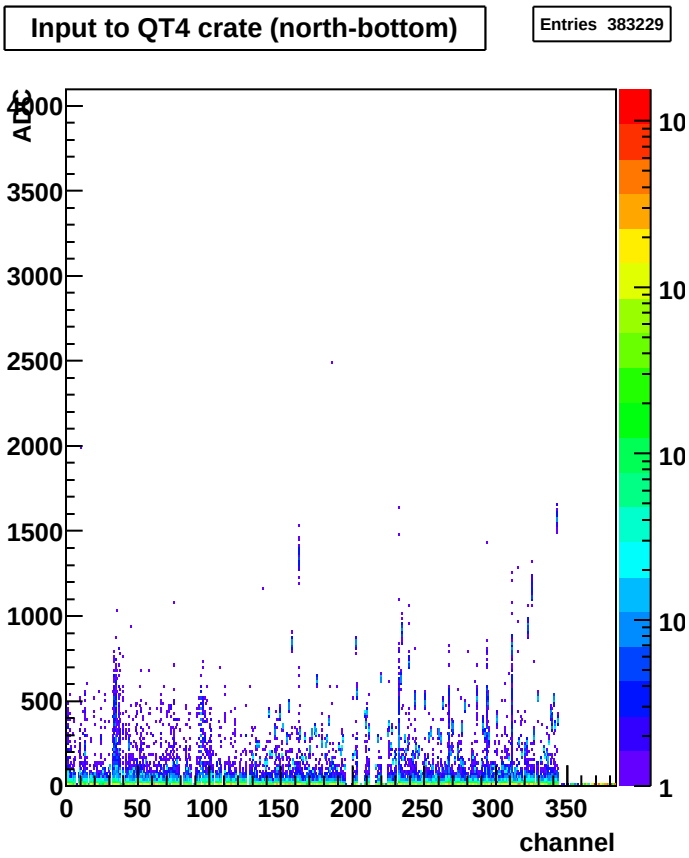

VPD-TAC-W
Entries 29076
Mean 1432
RMS 172.4

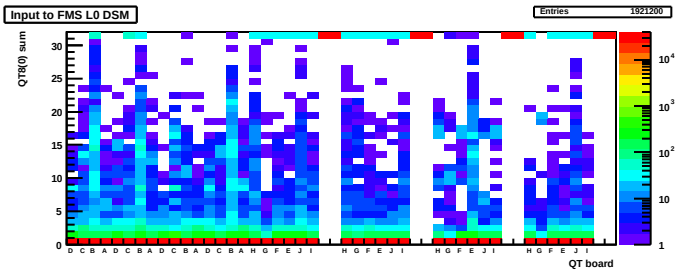

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 25114
Mean 3984
RMS 114.3

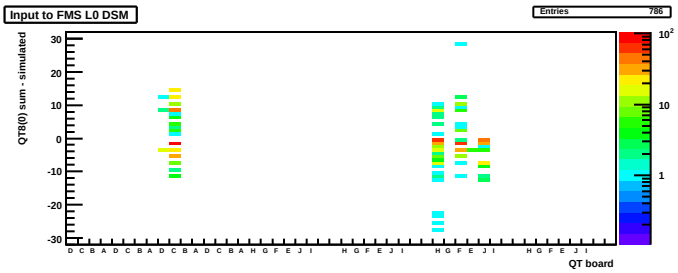

Input to QT1 crate (south-top)
Entries 509180
ADC
channel

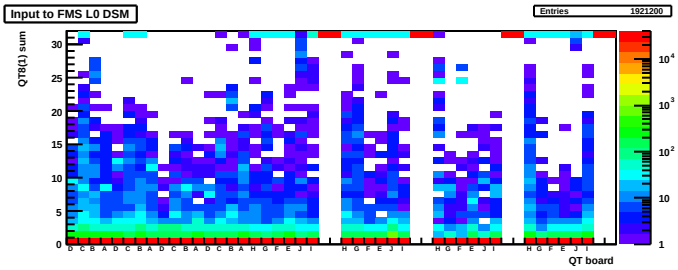

Input to QT3 crate (north-top)
Entries 338822
ADC
channel

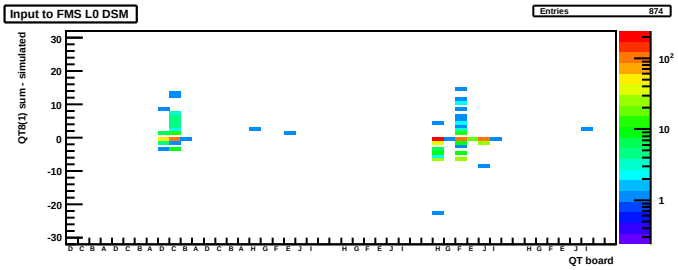

Input to QT2 crate (south-bottom)
Entries 326104
ADC
channel

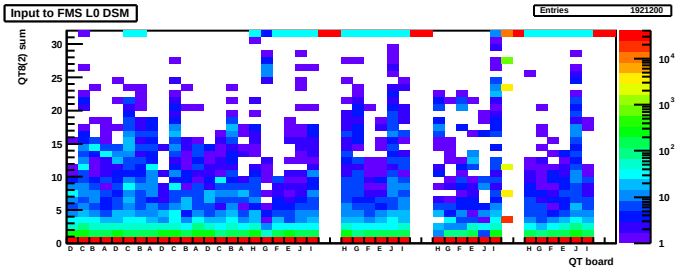

Input to QT4 crate (north-bottom)
Entries 383229
ADC
channel

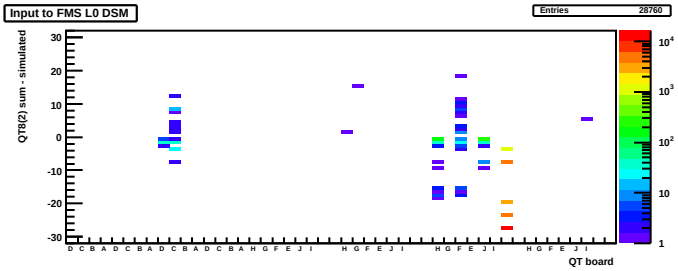

Input to FMS L0 DSM
Entries 1921200
QT8(0) sum
QT board

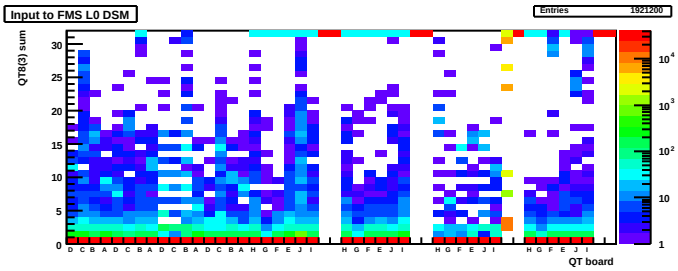

Input to FMS L0 DSM
Entries 786
QT8(0) sum - simulated
QT board

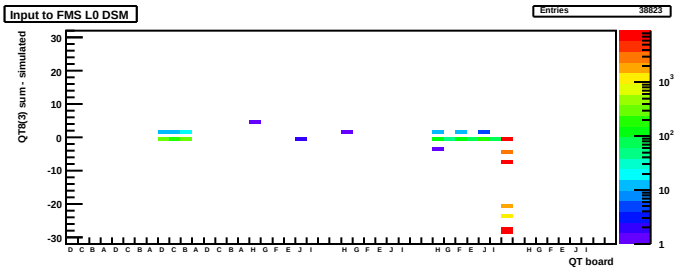

Input to FMS L0 DSM
Entries 1921200
QT8(1) sum
QT board

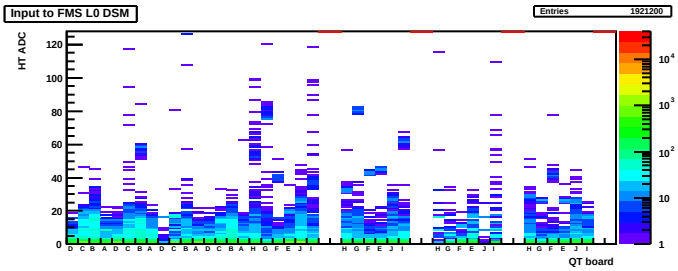

Input to FMS L0 DSM
Entries 874
QT8(1) sum - simulated
QT board

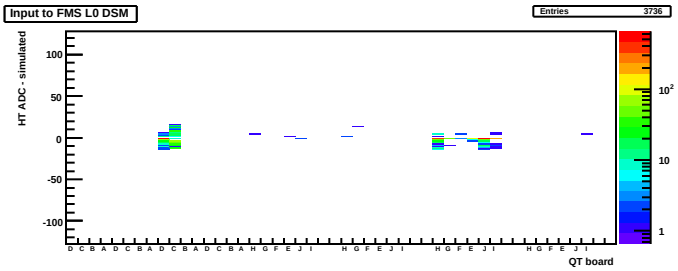

Input to FMS L0 DSM
Entries 1921200
QT8(2) sum
QT board

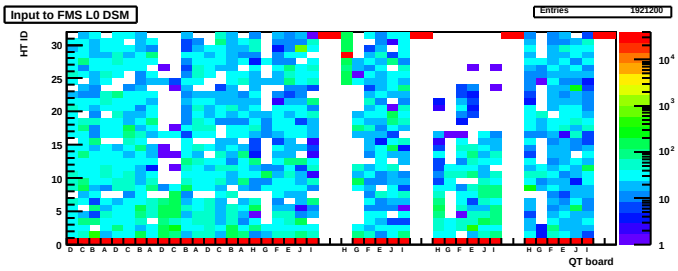

Input to FMS L0 DSM
Entries 28760
QT8(2) sum - simulated
QT board

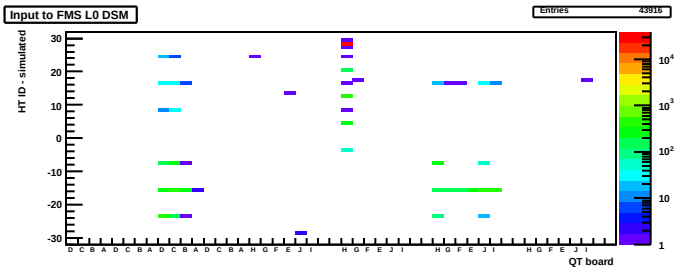

Input to FMS L0 DSM
Entries 1921200
QT8(3) sum
QT board

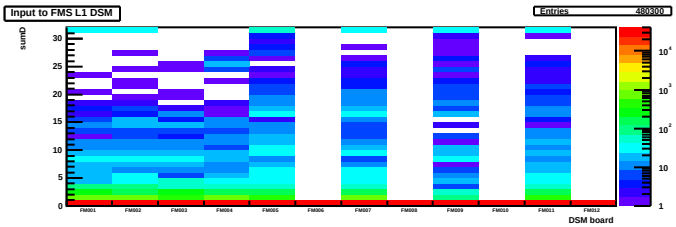

Input to FMS L0 DSM
Entries 38823
QT8(3) sum - simulated
QT board

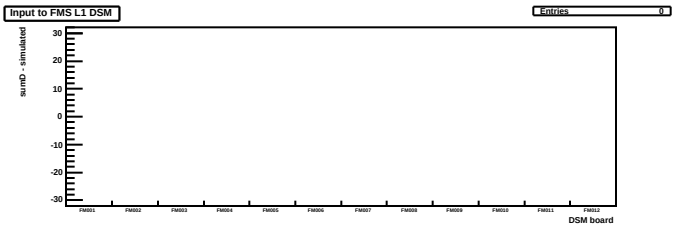

Input to FMS L0 DSM
Entries 1921200
HT ADC
QT board

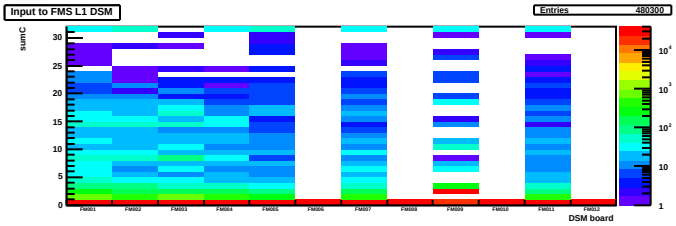

Input to FMS L0 DSM
Entries 3736
HT ADC - simulated
QT board

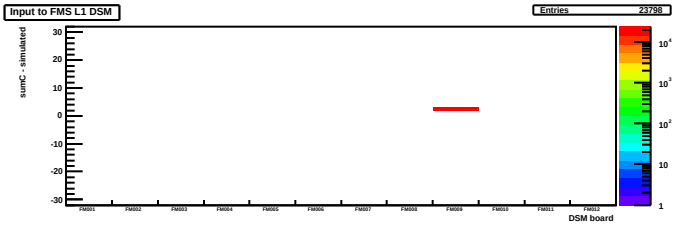

Input to FMS L0 DSM
Entries 1921200
HT ID
QT board

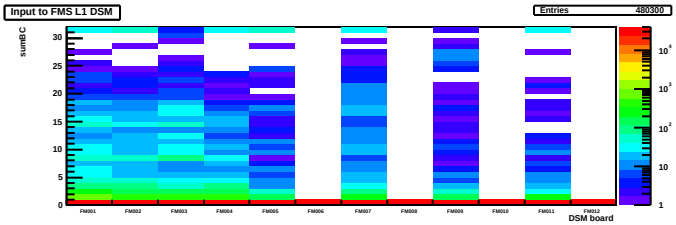

Input to FMS L0 DSM
Entries 43916
HT ID - simulated
QT board

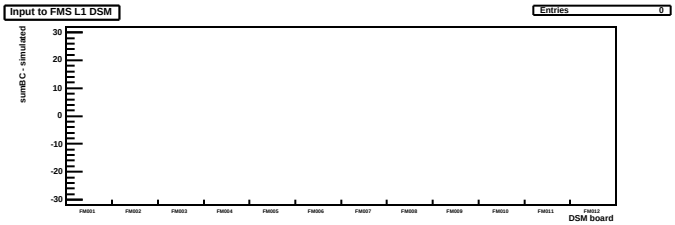

Input to FMS L1 DSM
Entries 480300
sumD
DSM board

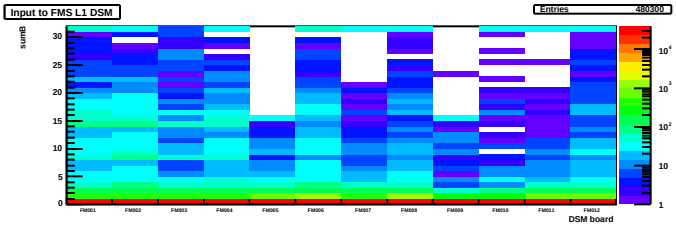

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

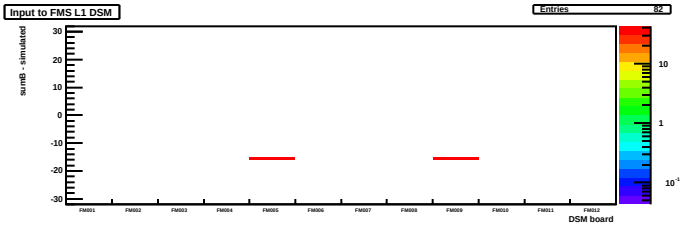

Input to FMS L1 DSM
Entries 480300
sumC
DSM board

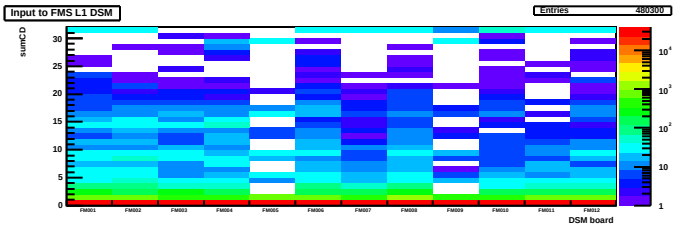

Input to FMS L1 DSM
Entries 23798
sumC - simulated
DSM board

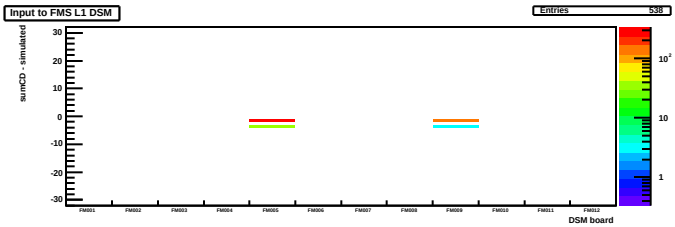

Input to FMS L1 DSM
Entries 480300
sumBC
DSM board

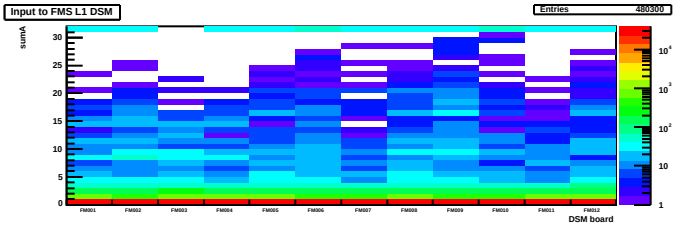

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

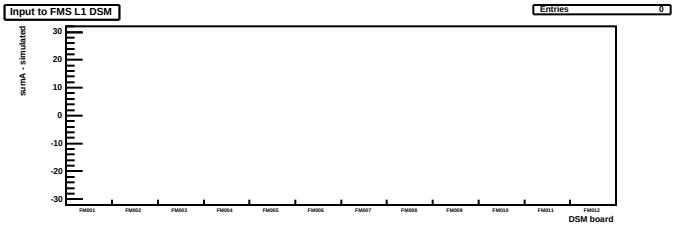

Input to FMS L1 DSM
Entries 480300
sumB
DSM board

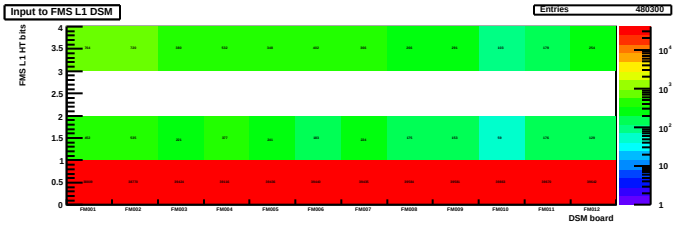

Input to FMS L1 DSM
Entries 82
sumB - simulated
DSM board

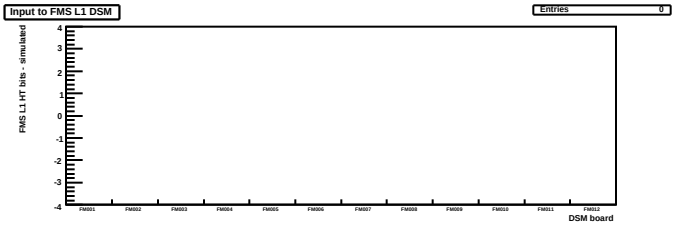

Input to FMS L1 DSM
Entries 480300
sumCD
DSM board

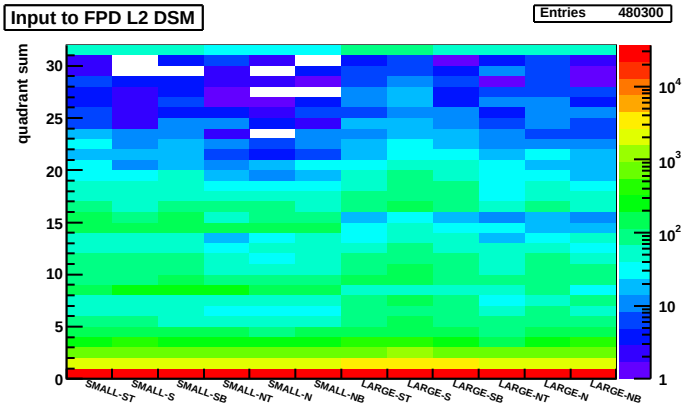

Input to FMS L1 DSM
Entries 538
sumCD - simulated
DSM board

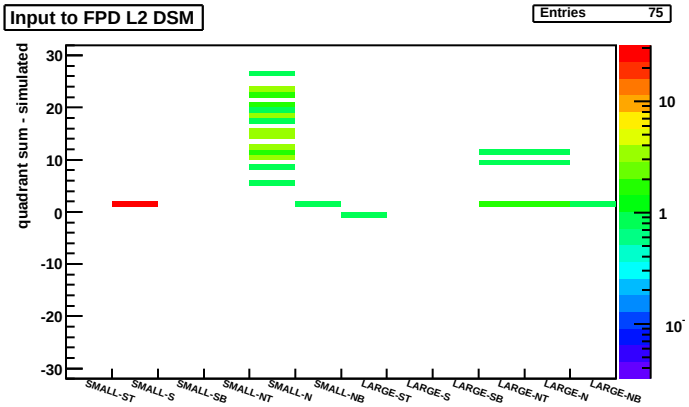

Input to FMS L1 DSM
Entries 480300
sumA
DSM board

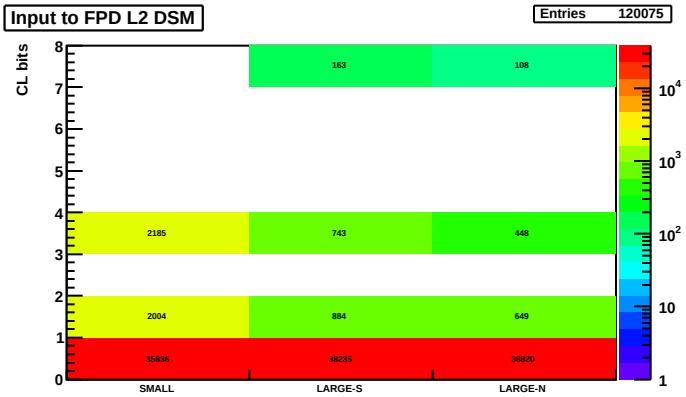

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

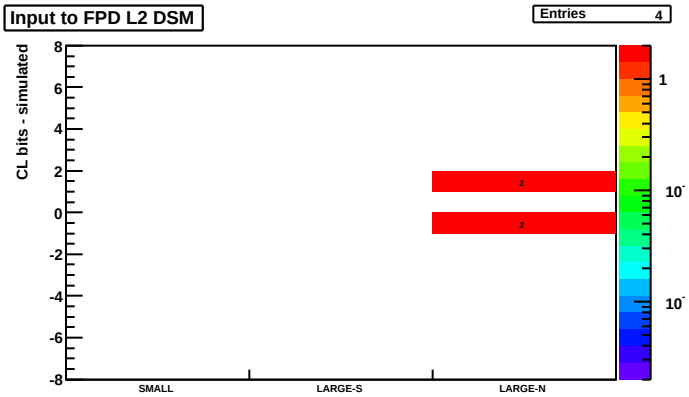

Input to FMS L1 DSM
Entries 480300
FMS L1 HT bits
DSM board

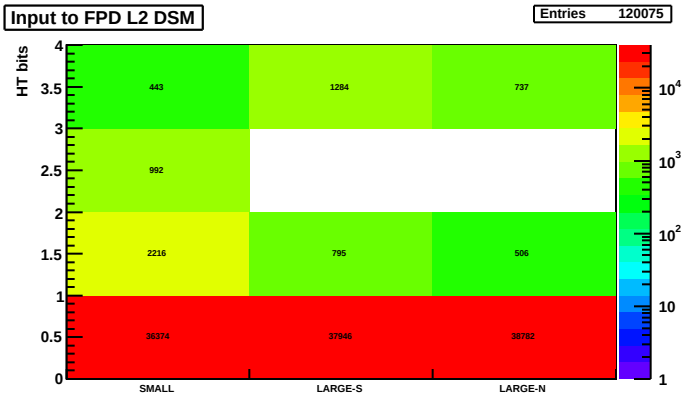

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

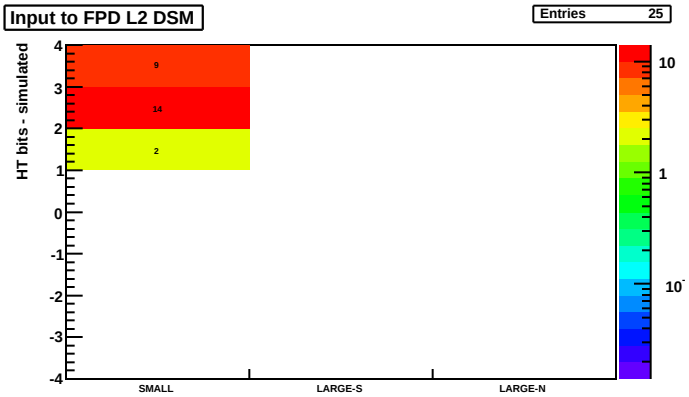

Input to FPD L2 DSM
Entries 480300
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

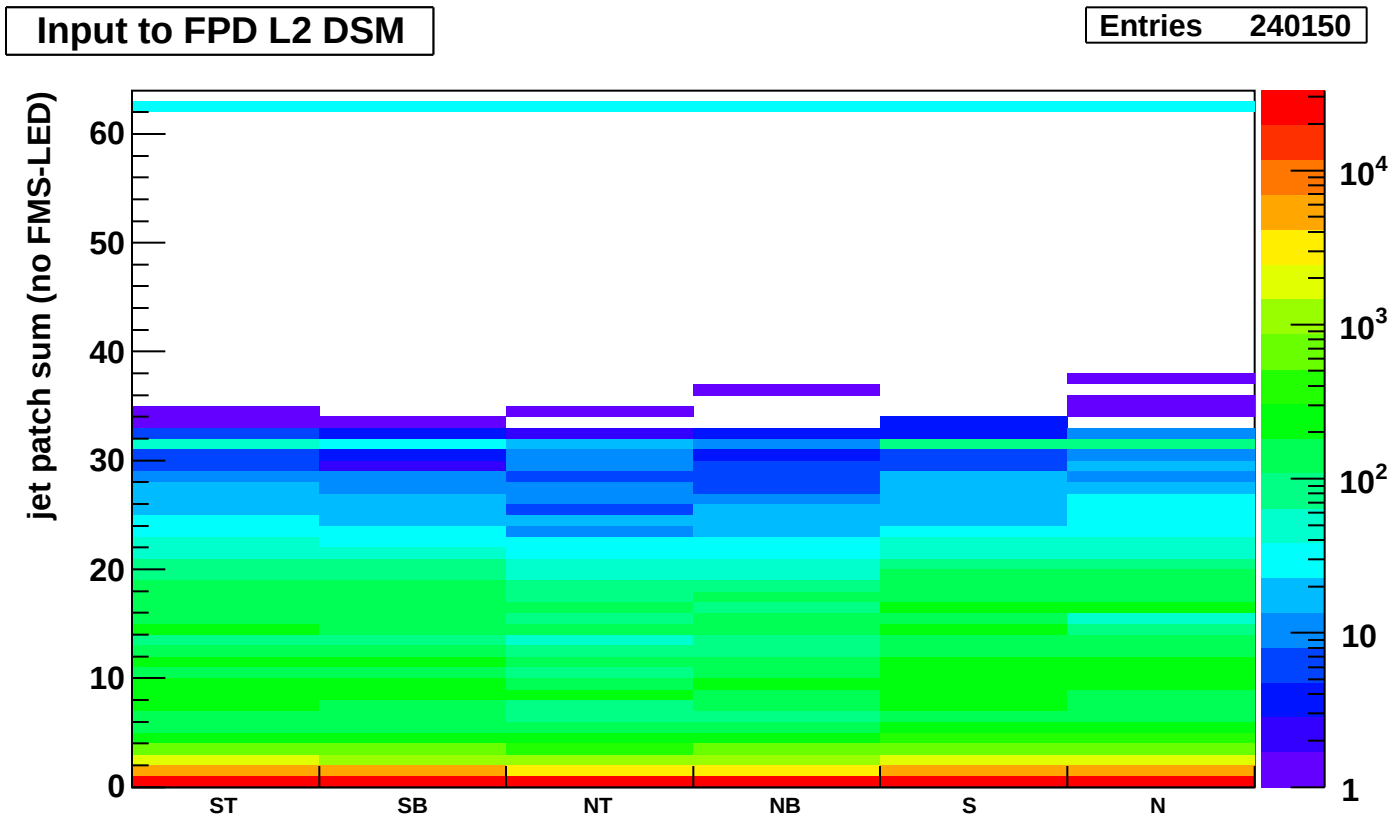

Input to FPD L2 DSM
Entries 75
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

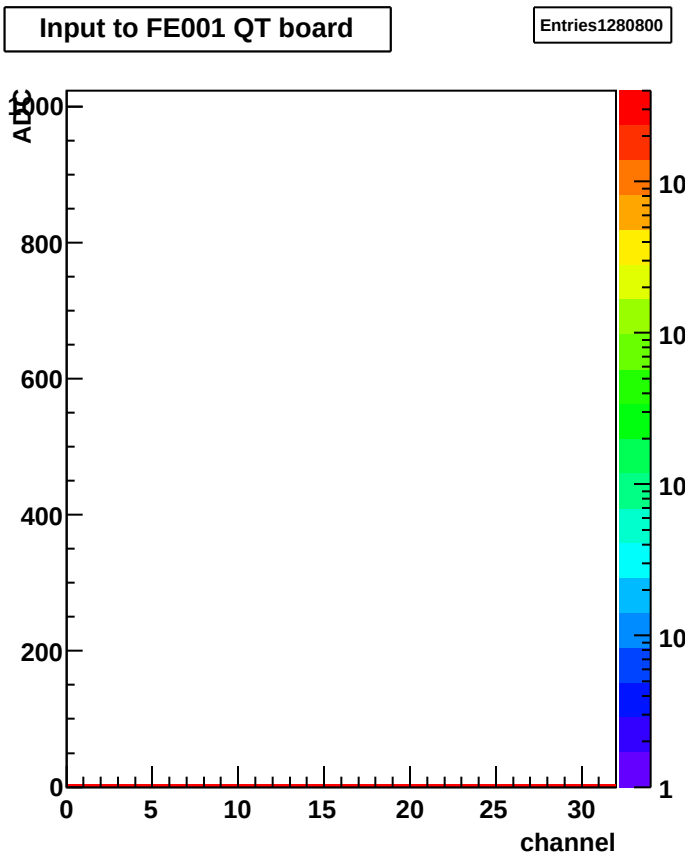

Input to FPD L2 DSM
Entries 120075
CL bits
163
108
2185
743
448
2004
884
649
35836
38235
38820
SMALL
LARGE-S
LARGE-N

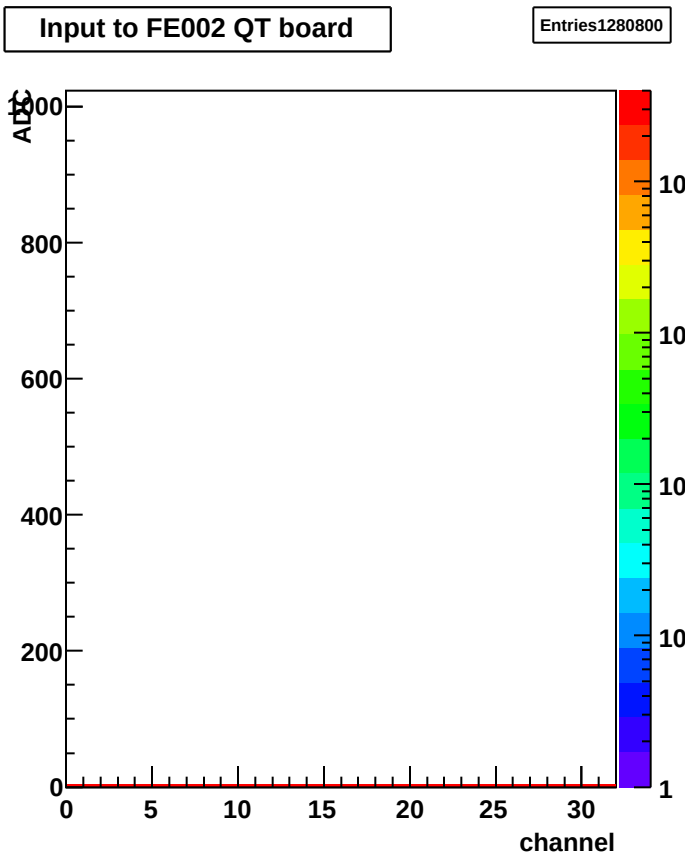

Input to FPD L2 DSM
Entries 4
CL bits - simulated
2
2
SMALL
LARGE-S
LARGE-N

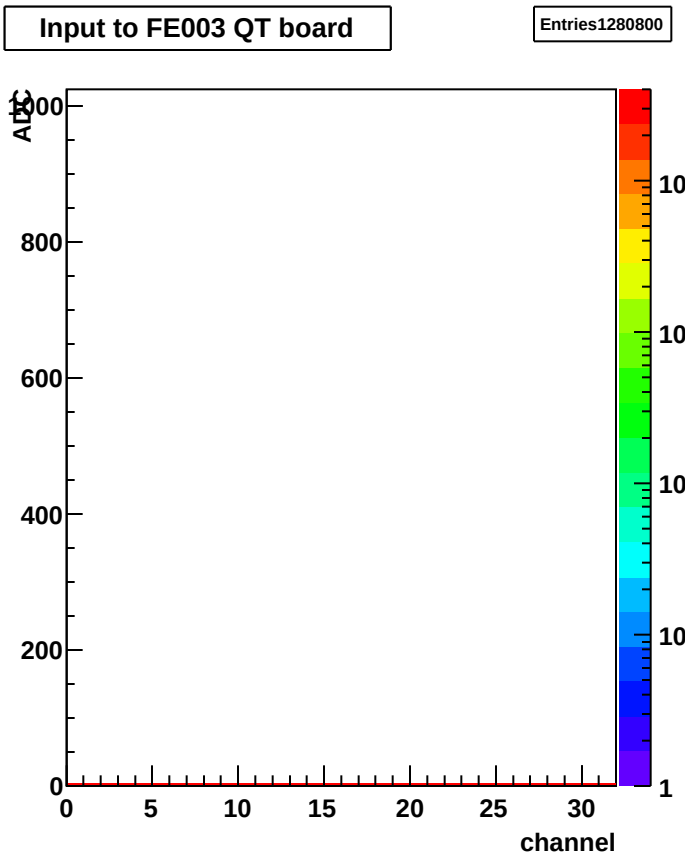

Input to FPD L2 DSM
Entries 120075
HT bits
443
1284
737
992
2216
795
506
36374
37946
38782
SMALL
LARGE-S
LARGE-N

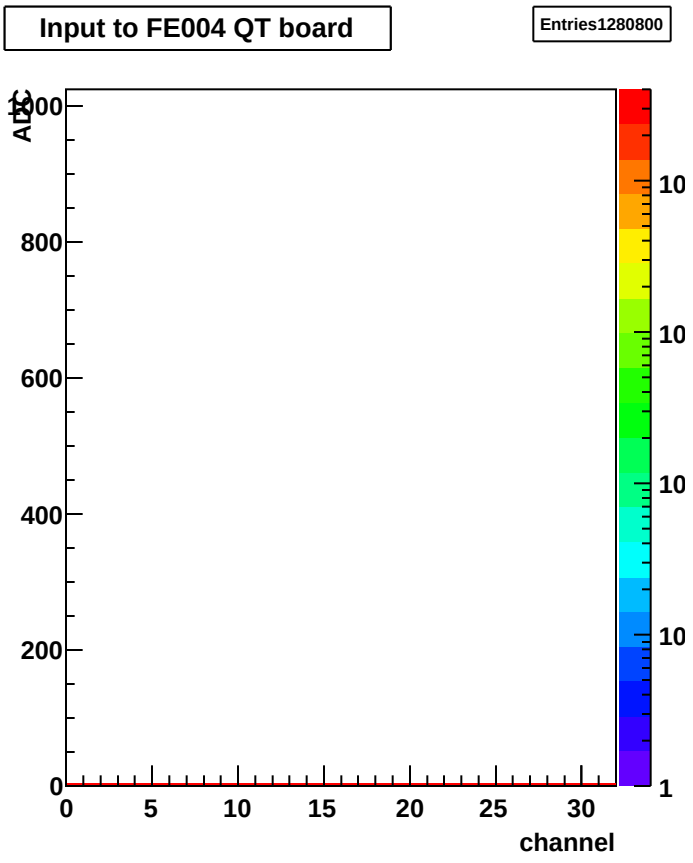

Input to FPD L2 DSM
Entries 25
HT bits - simulated
9
14
2
SMALL
LARGE-S
LARGE-N

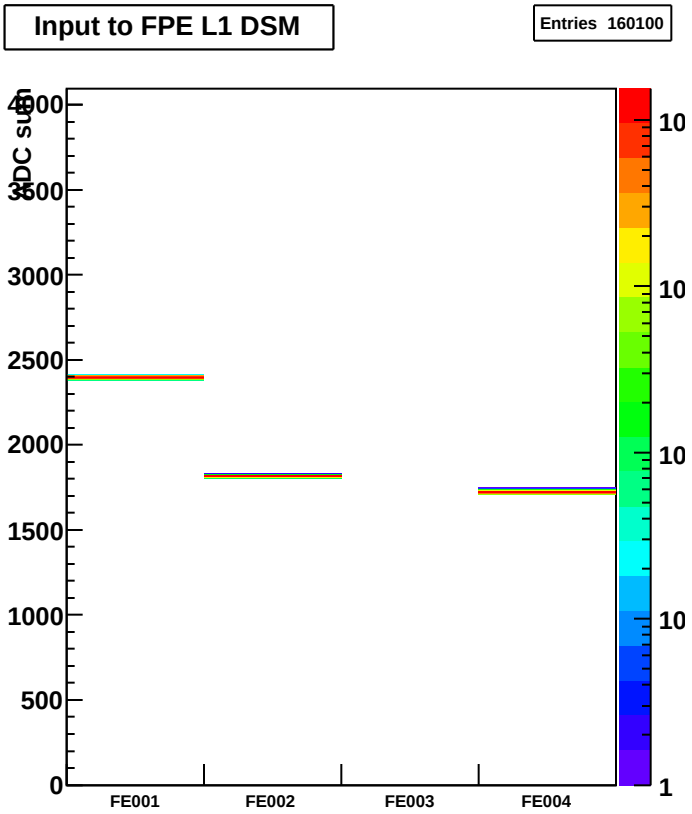

Input to FPD L2 DSM
Entries 240150
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4


Input to FE001 QT board
Entries1280800
ADC
channel

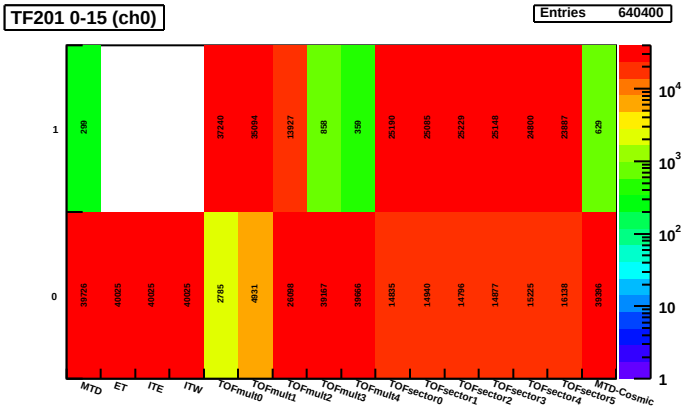

Input to FE002 QT board
Entries1280800
ADC
channel


Input to FE003 QT board
Entries1280800
ADC
channel

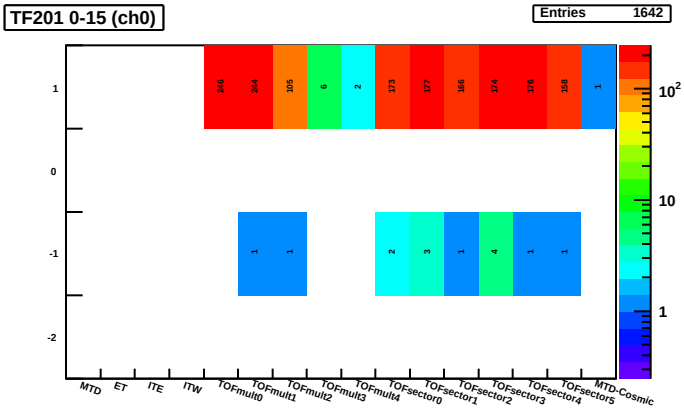

Input to FE004 QT board
Entries1280800
ADC
channel


Input to FPE L1 DSM
Entries 160100
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

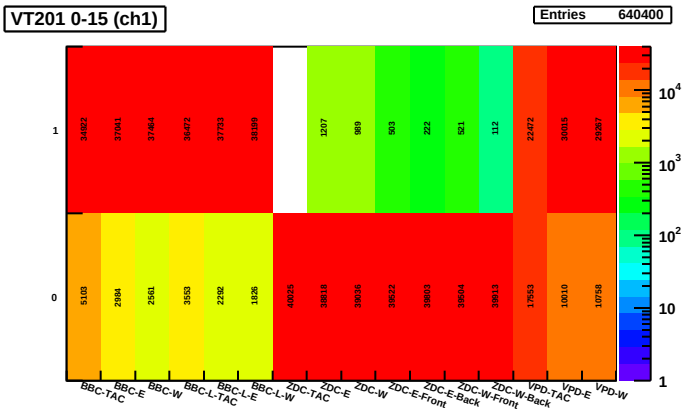
TF201 0-15 (ch0)



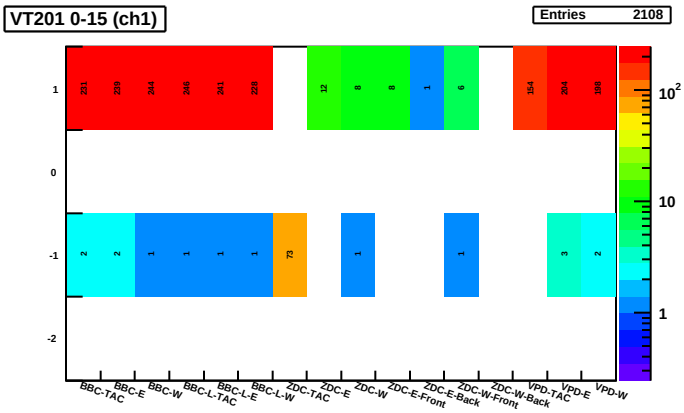
TF201 0-15 (ch0)



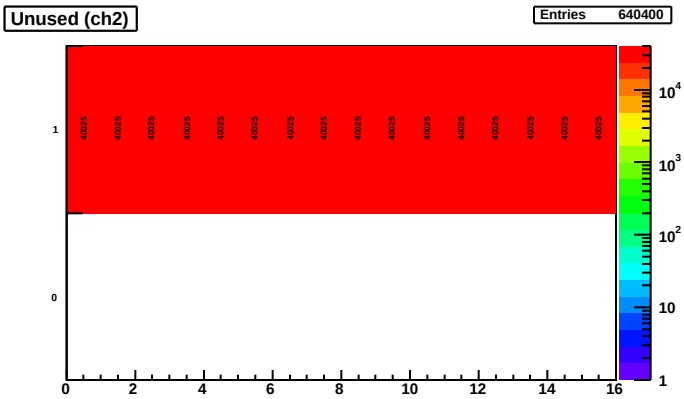
VT201 0-15 (ch1)

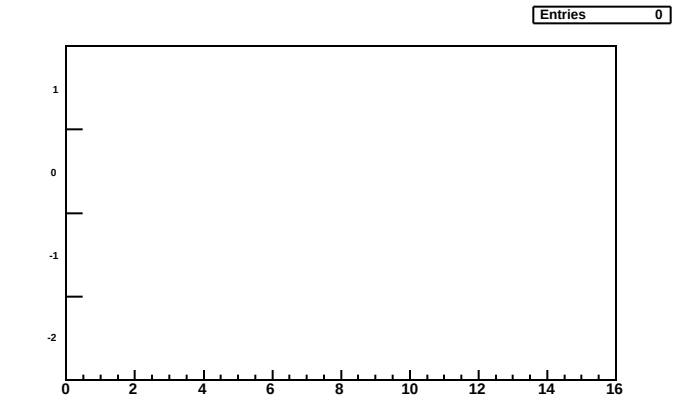


VT201 0-15 (ch1)

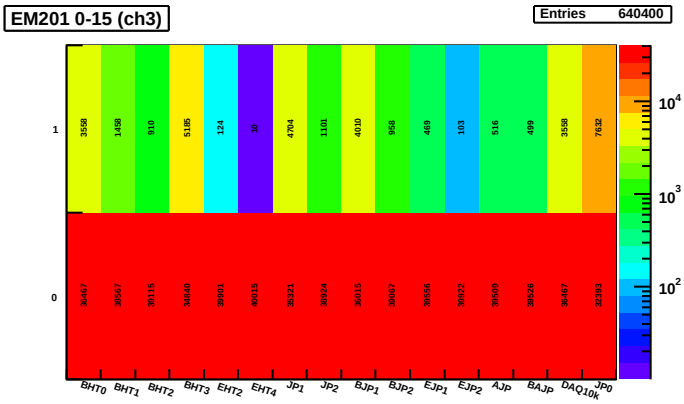


Unused (ch2)

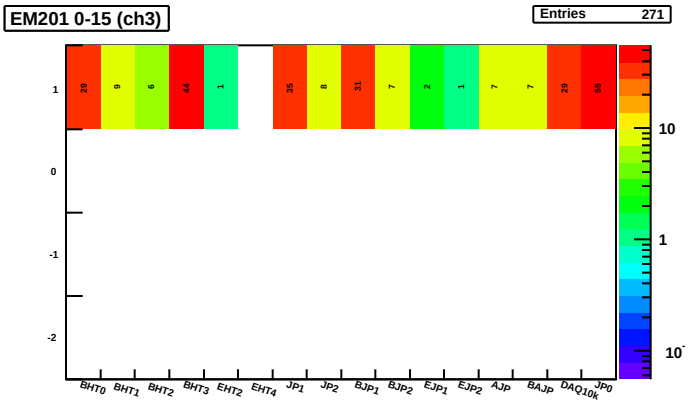




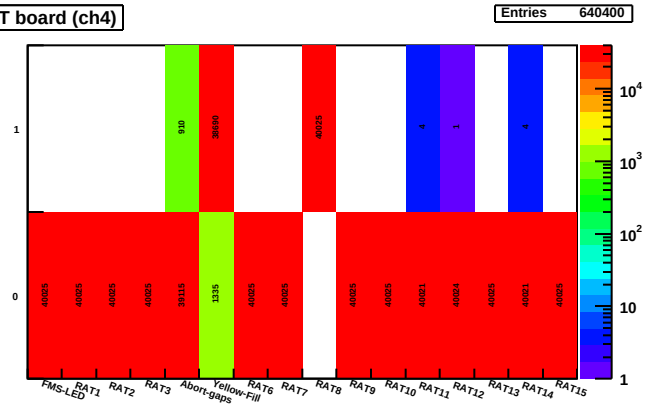
EM201 0-15 (ch3)

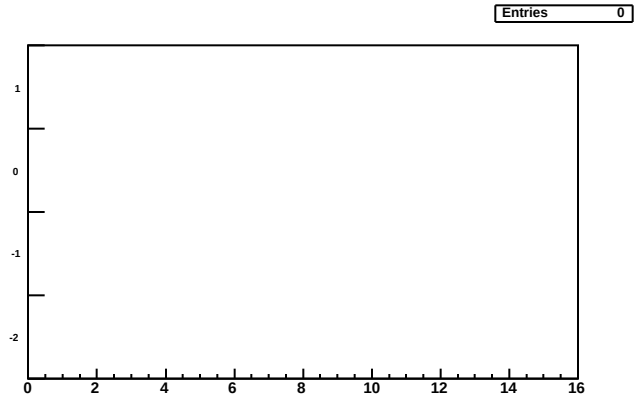


EM201 0-15 (ch3)

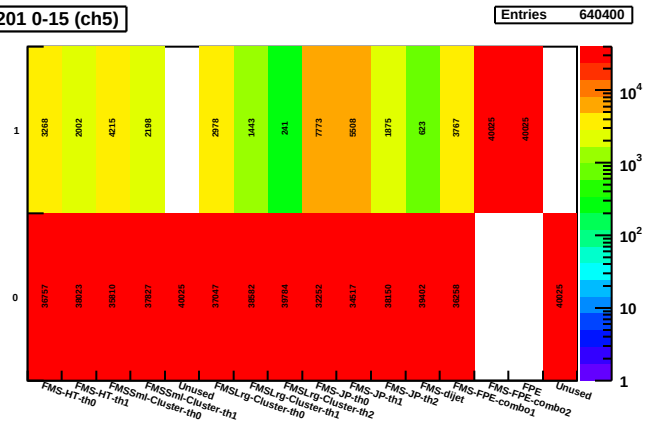


RAT board (ch4)





FP201 0-15 (ch5)



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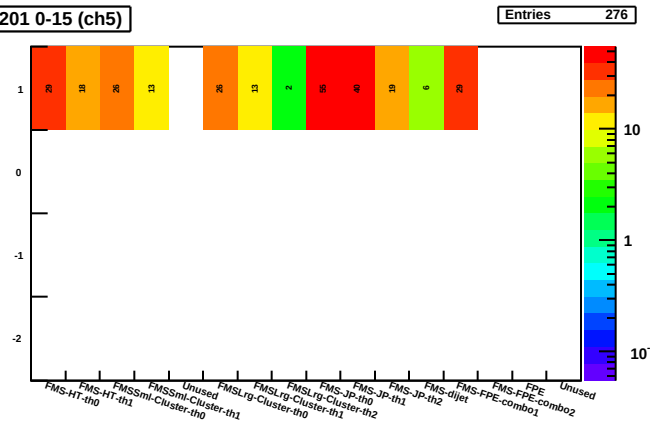

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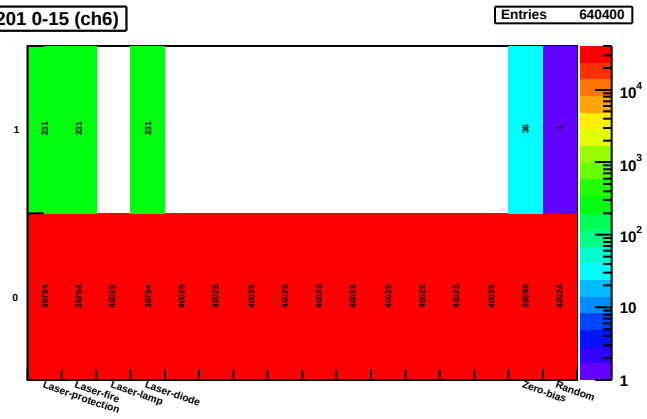

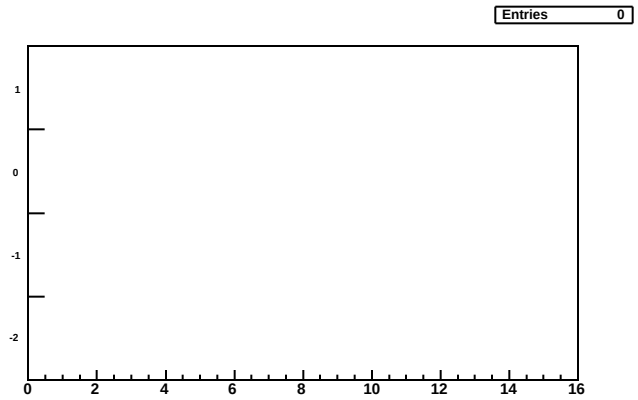
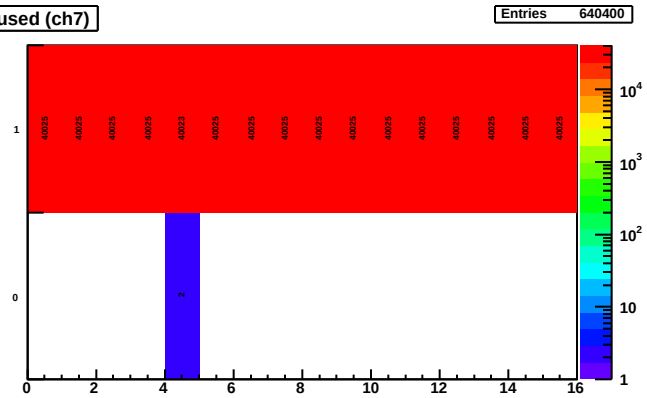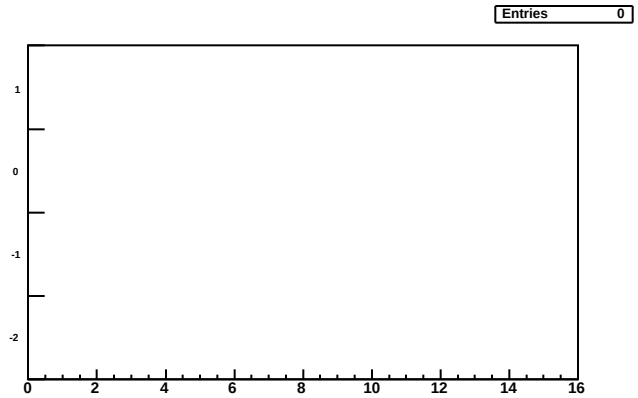
Unused (ch7)