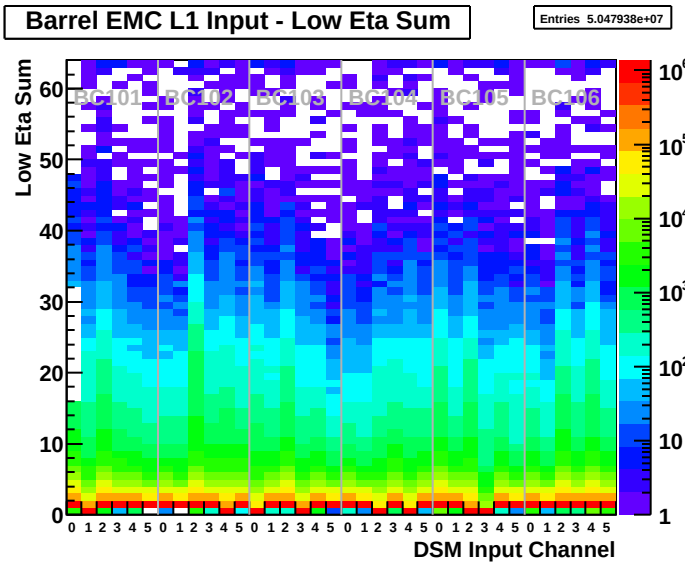
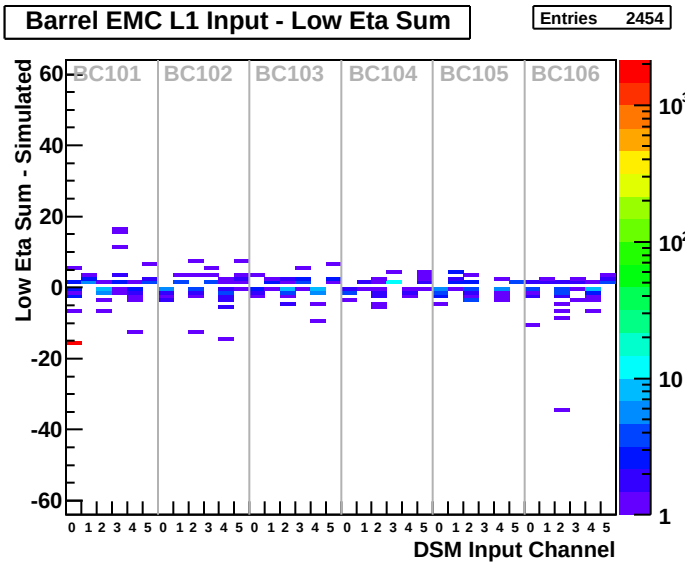
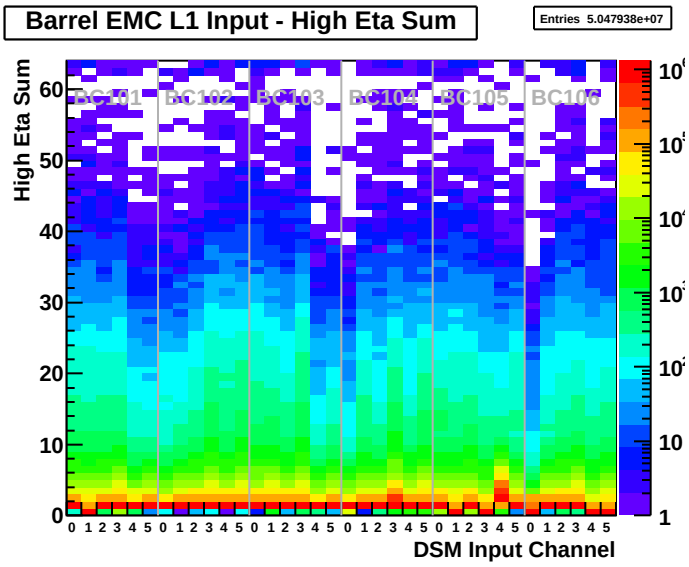
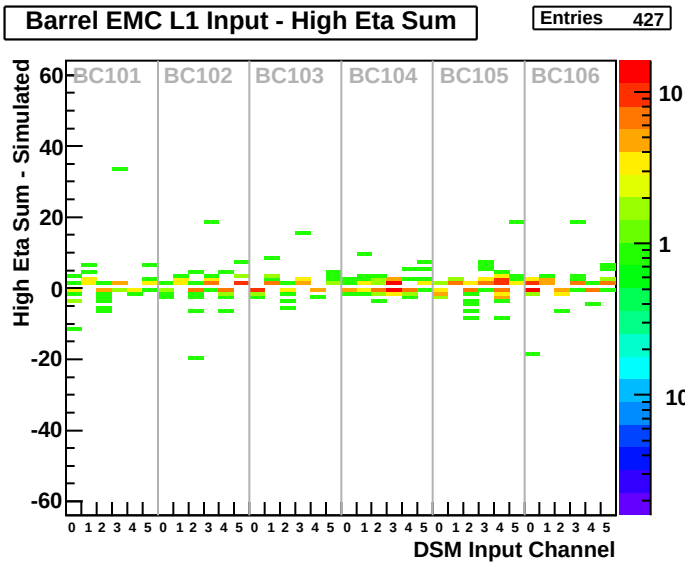
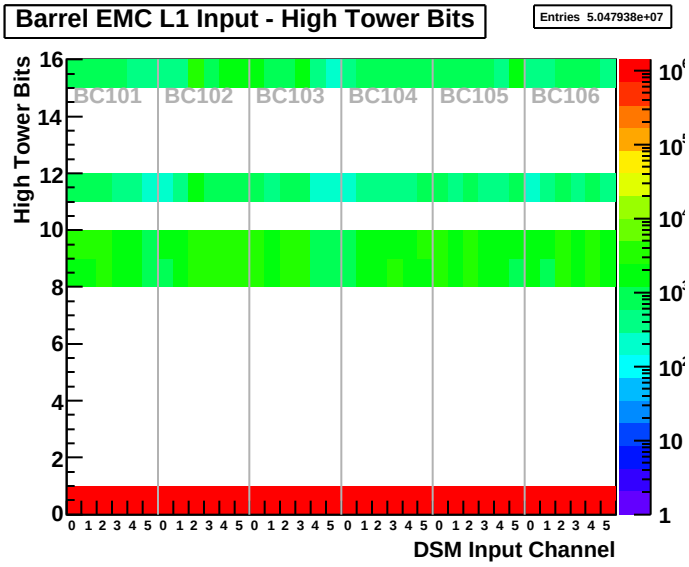
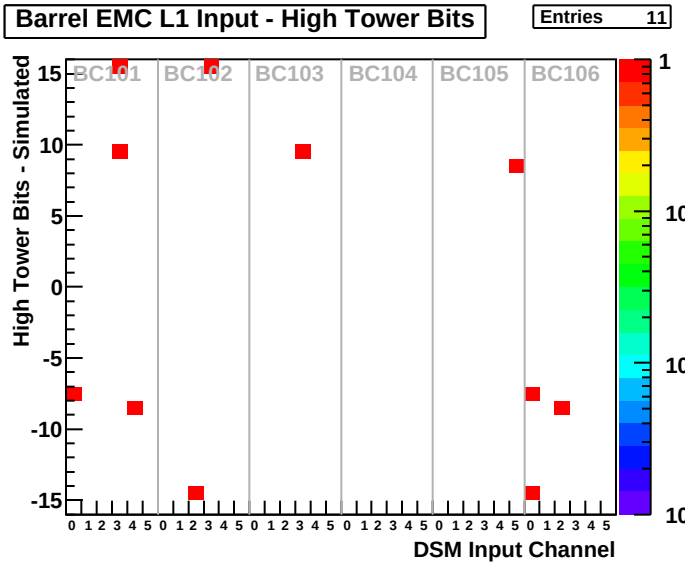

Barrel EMC L1 Input - Low Eta Sum
Entries 5.047938e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

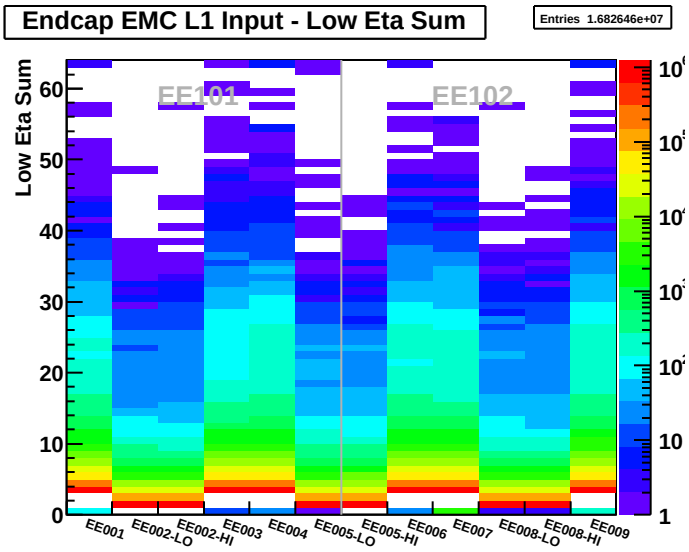

Barrel EMC L1 Input - Low Eta Sum
Entries 2454
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

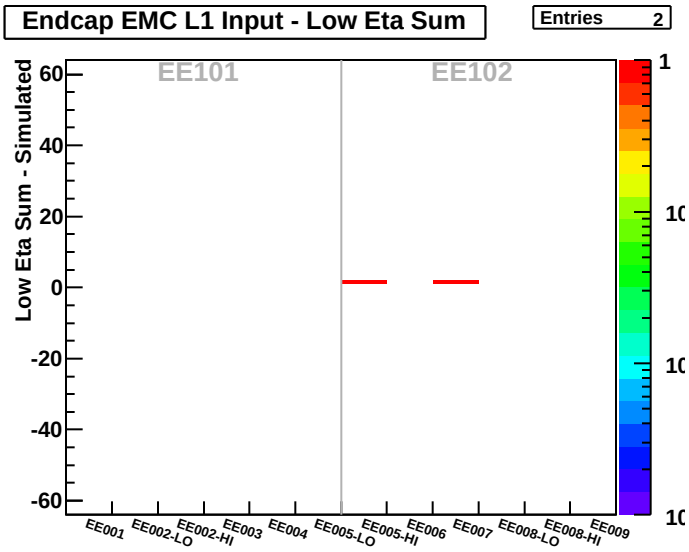

Barrel EMC L1 Input - High Eta Sum
Entries 5.047938e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

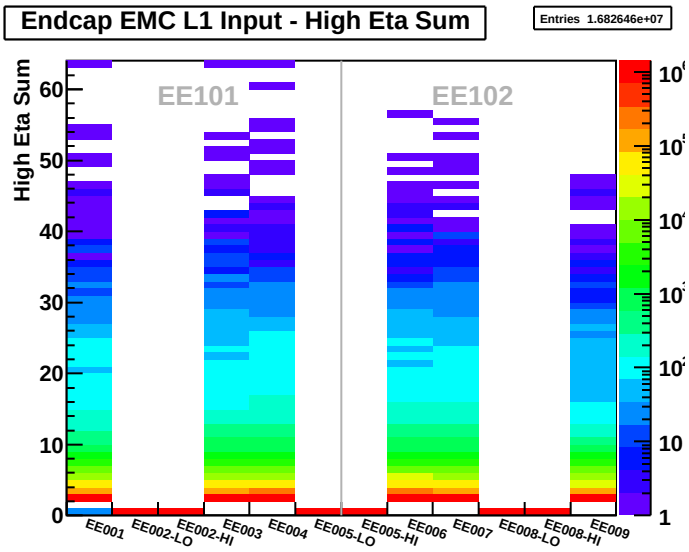

Barrel EMC L1 Input - High Eta Sum
Entries 427
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

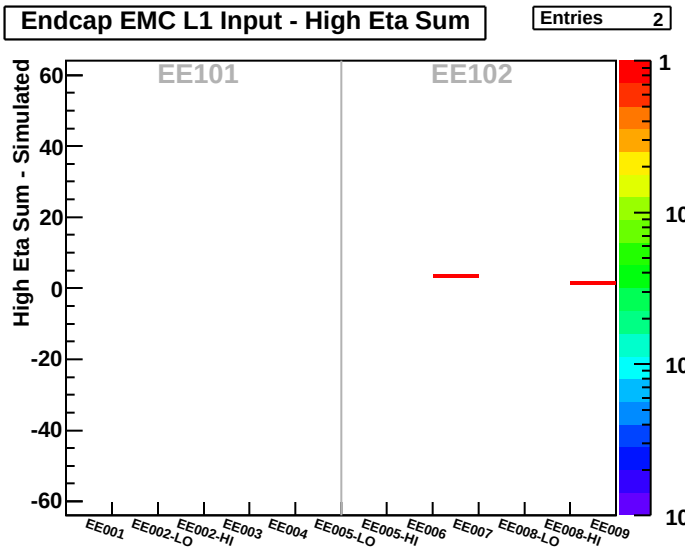

Barrel EMC L1 Input - High Tower Bits
Entries 5.047938e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

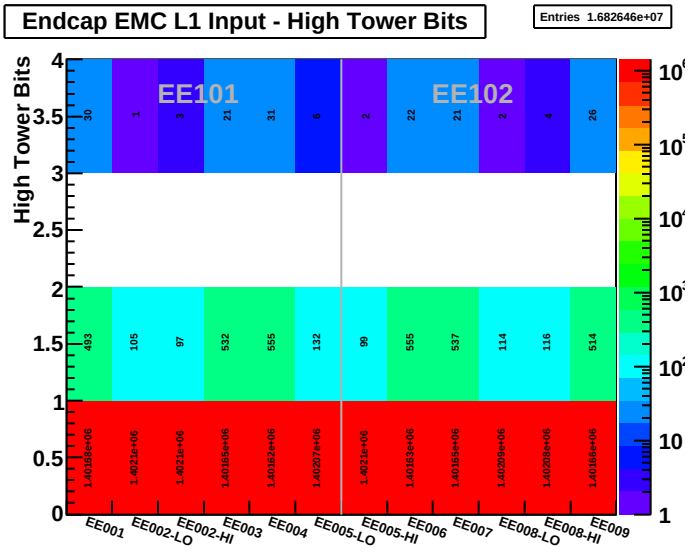

Barrel EMC L1 Input - High Tower Bits
Entries 11
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

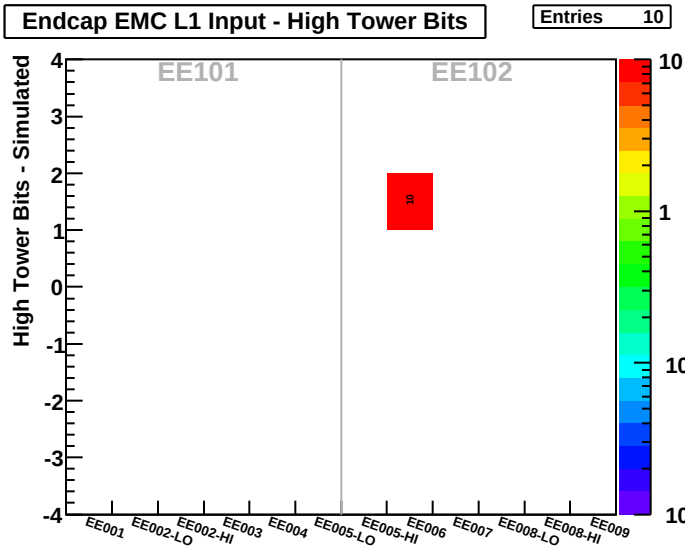

Endcap EMC L1 Input - Low Eta Sum
Entries 1.682646e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

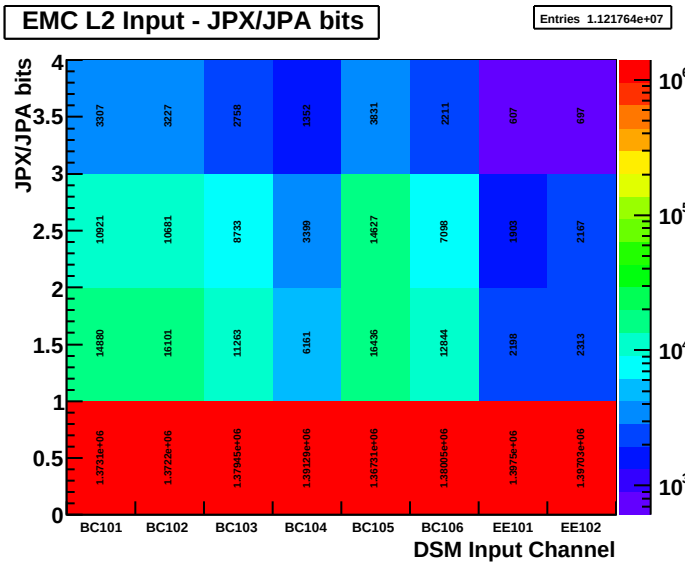

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

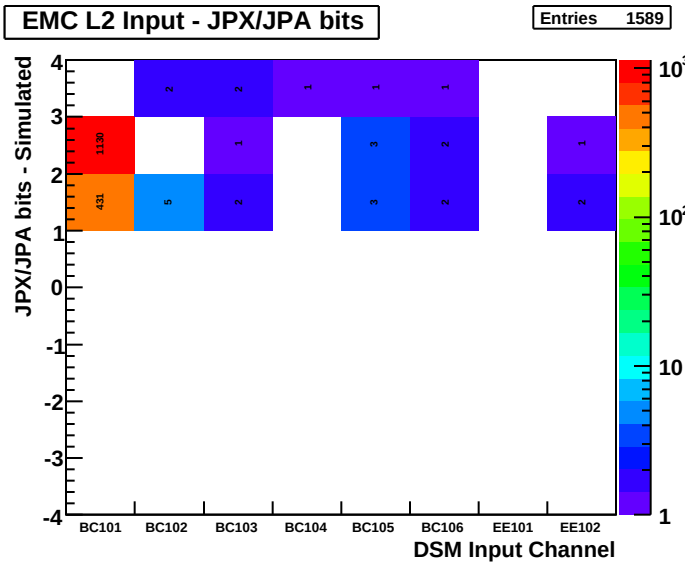

Endcap EMC L1 Input - High Eta Sum
Entries 1.682646e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

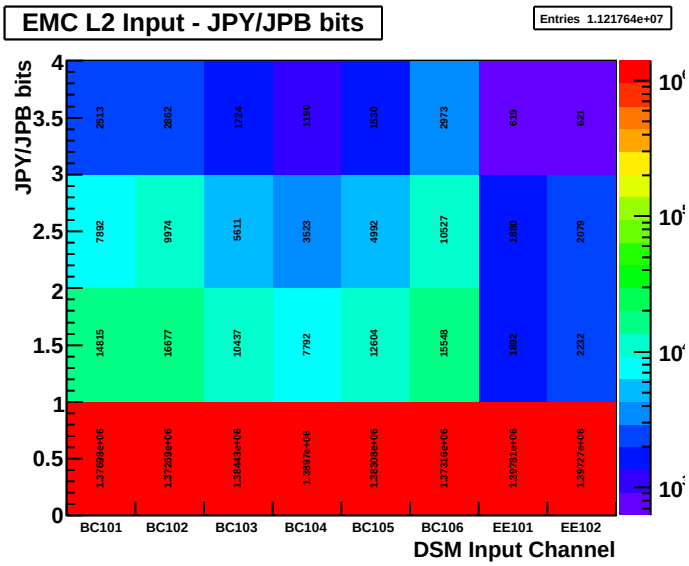

Endcap EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

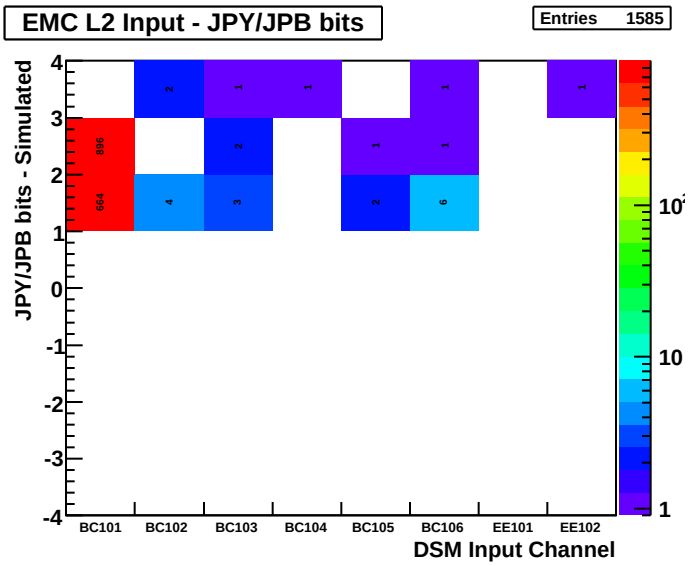

Endcap EMC L1 Input - High Tower Bits
Entries 1.682646e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

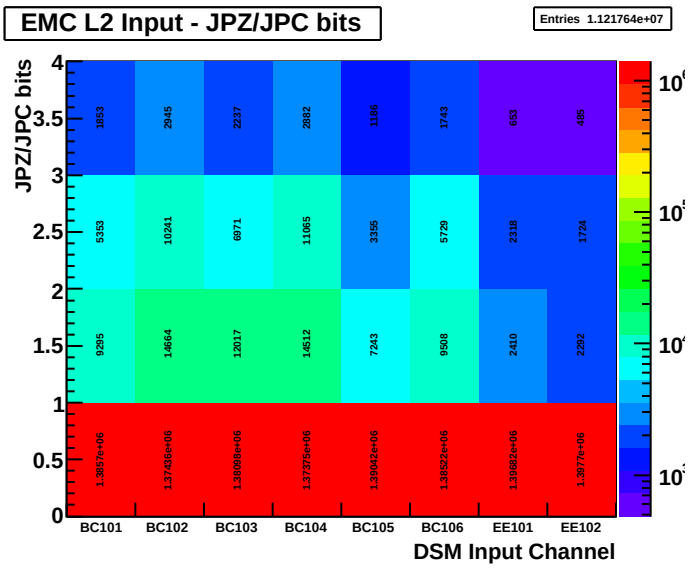

Endcap EMC L1 Input - High Tower Bits
Entries 10
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

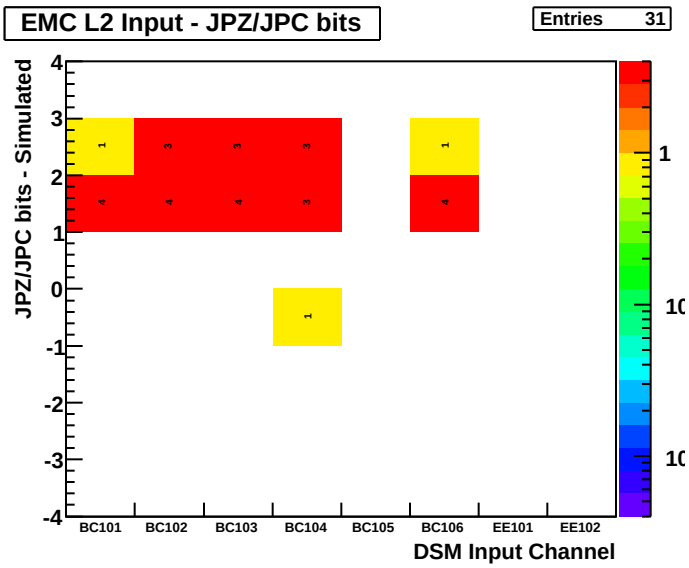

EMC L2 Input - JPX/JPA bits
Entries 1.121764e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3307 3227 2758 1352 3831 2211 607 697
10921 10681 8733 3399 14627 7098 1903 2167
14880 16101 11263 6161 16436 12844 2198 2313
1.3731e+06 1.3722e+06 1.37945e+06 1.39129e+06 1.36731e+06 1.38005e+06 1.3975e+06 1.39703e+06

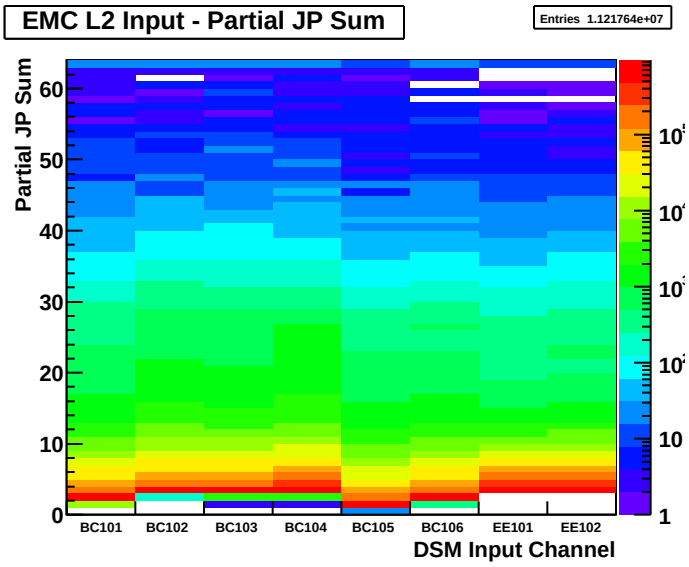

EMC L2 Input - JPX/JPA bits
Entries 1589
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1130 2 2 1 1 1
431 5 2 1 3 2 1
3 2 2

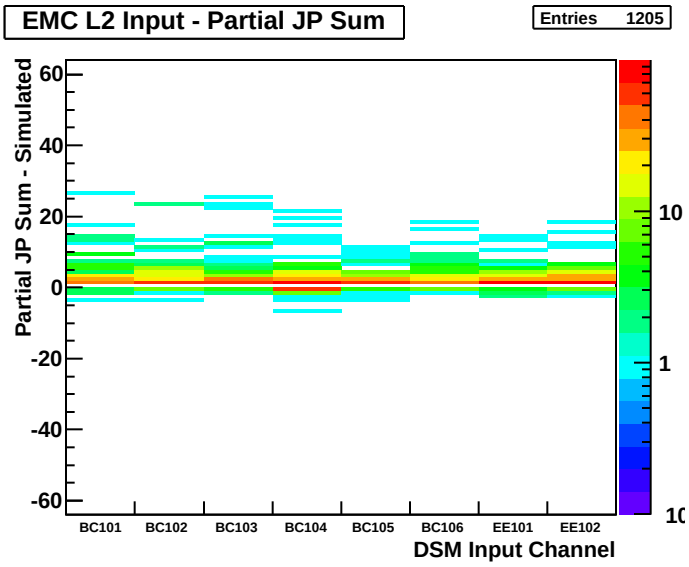

EMC L2 Input - JPY/JPB bits
Entries 1.121764e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2513 2862 1724 1190 1530 2973 619 621
7892 9974 5611 3523 4992 10527 1880 2079
14815 16677 10437 7792 12604 15548 1892 2232
1.37698e+06 1.37289e+06 1.38443e+06 1.3897e+06 1.38308e+06 1.37316e+06 1.39781e+06 1.39727e+06

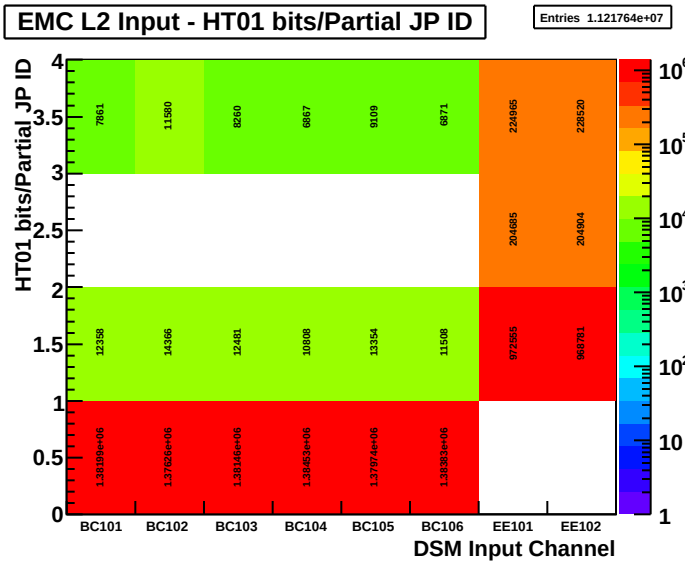

EMC L2 Input - JPY/JPB bits
Entries 1585
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 1 1 1 1
896 2 1 1
664 4 3 2 6

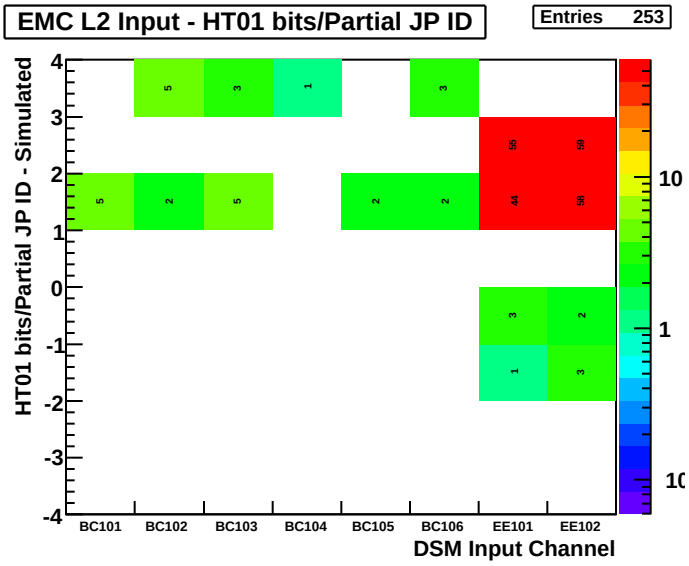

EMC L2 Input - JPZ/JPC bits
Entries 1.121764e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1853 2945 2237 2882 1186 1743 653 485
5353 10241 6971 11065 3355 5729 2318 1724
9295 14664 12017 14612 7243 9508 2410 2292
1.3857e+06 1.37436e+06 1.38098e+06 1.37375e+06 1.39042e+06 1.38522e+06 1.39682e+06 1.3977e+06

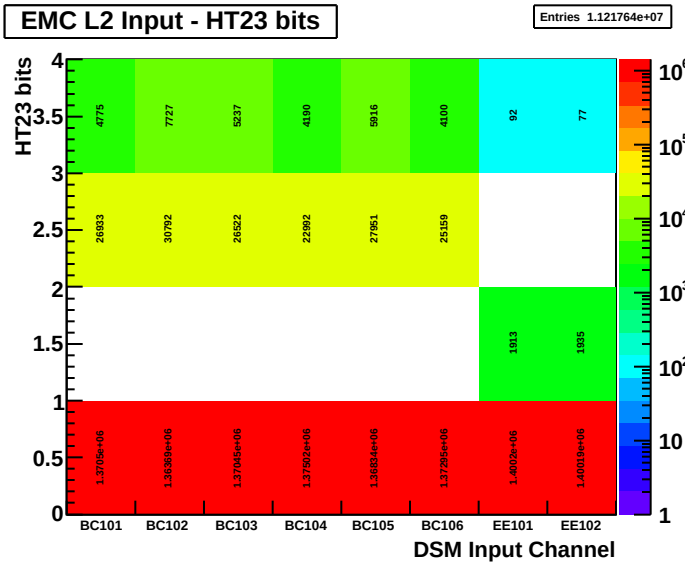

EMC L2 Input - JPZ/JPC bits
Entries 31
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 3 3 3 1
4 4 4 3 4
1

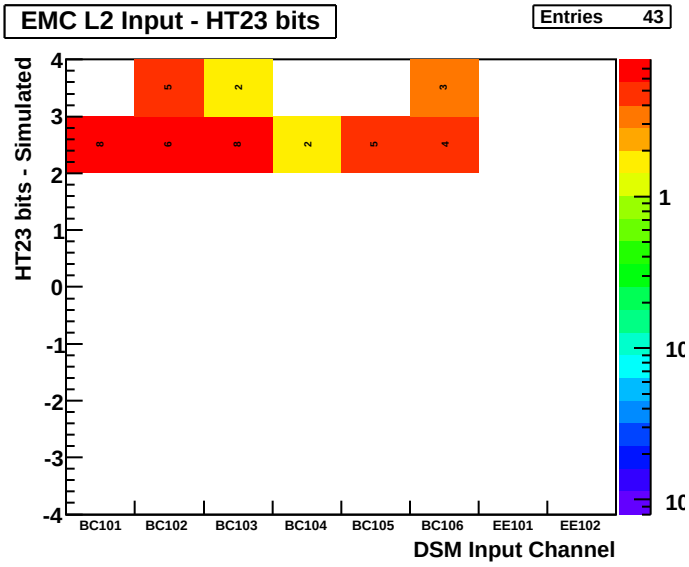

EMC L2 Input - Partial JP Sum
Entries 1.121764e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

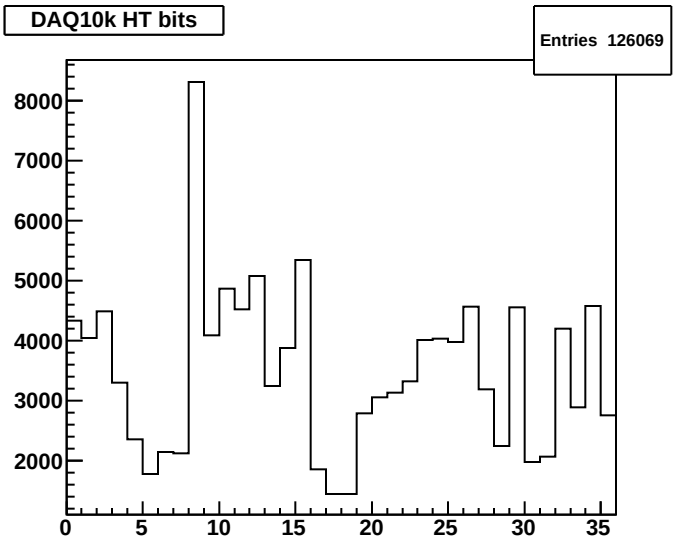

EMC L2 Input - Partial JP Sum
Entries 1205
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

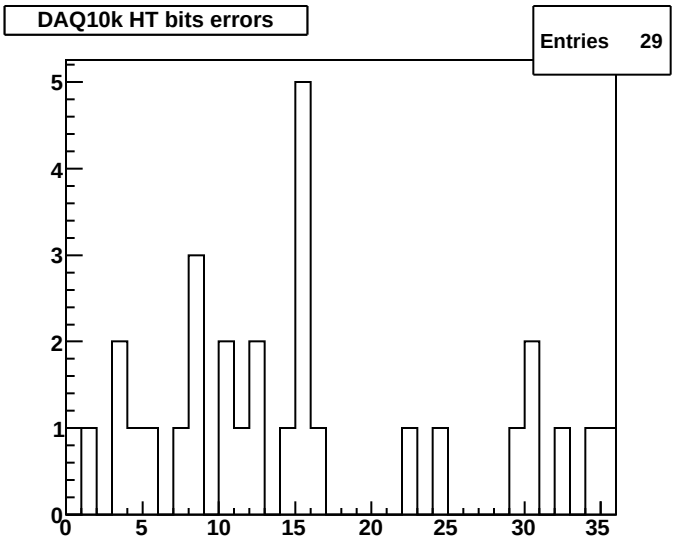

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.121764e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

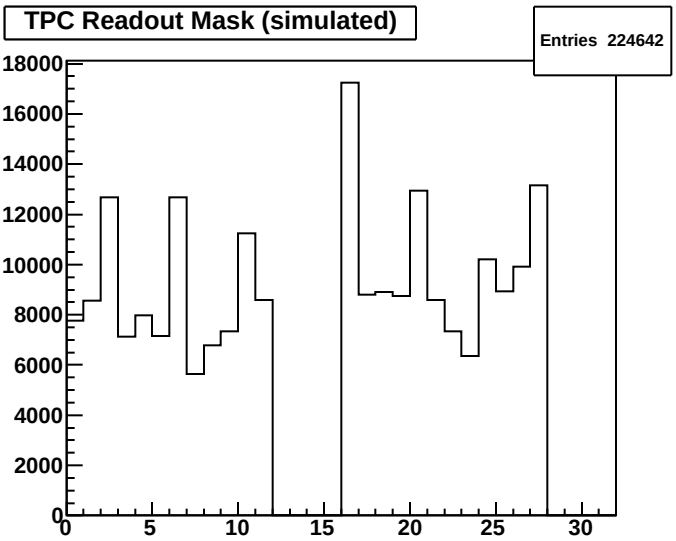

EMC L2 Input - HT01 bits/Partial JP ID
Entries 253
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

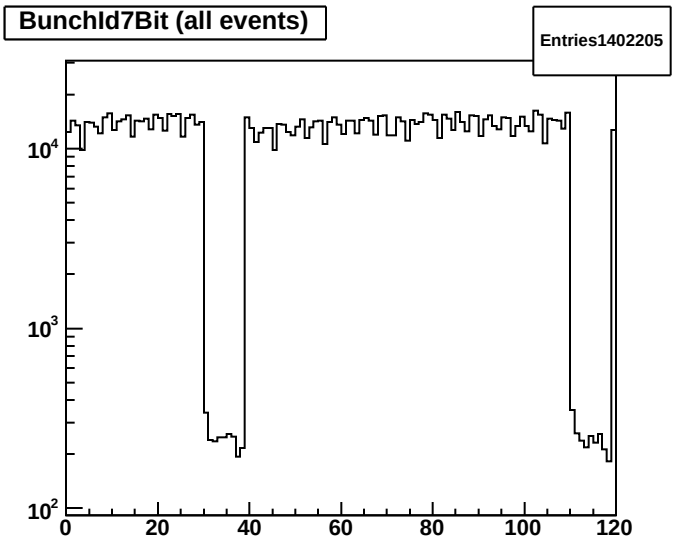

EMC L2 Input - HT23 bits
Entries 1.121764e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

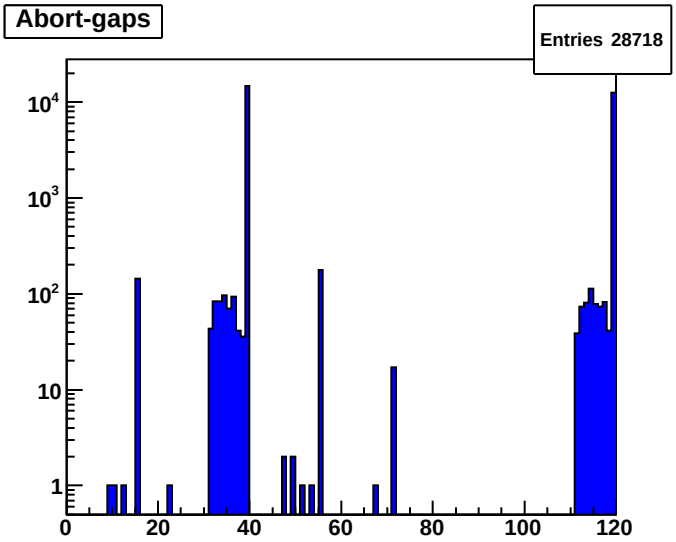

EMC L2 Input - HT23 bits
Entries 43
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

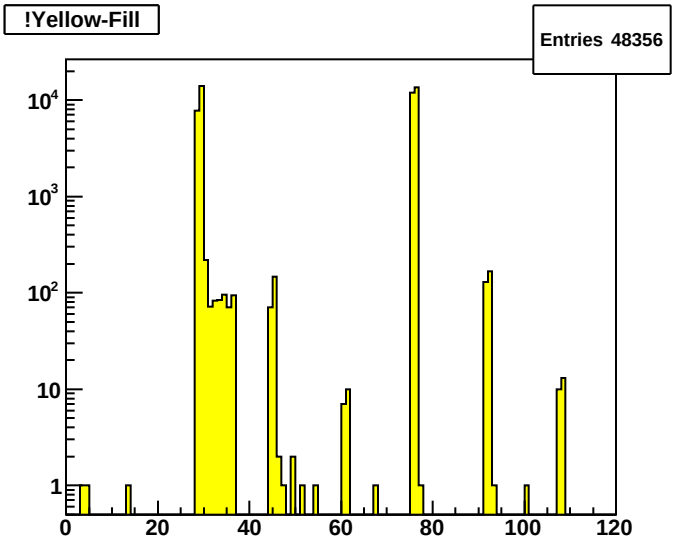

DAQ10k HT bits
Entries 126069

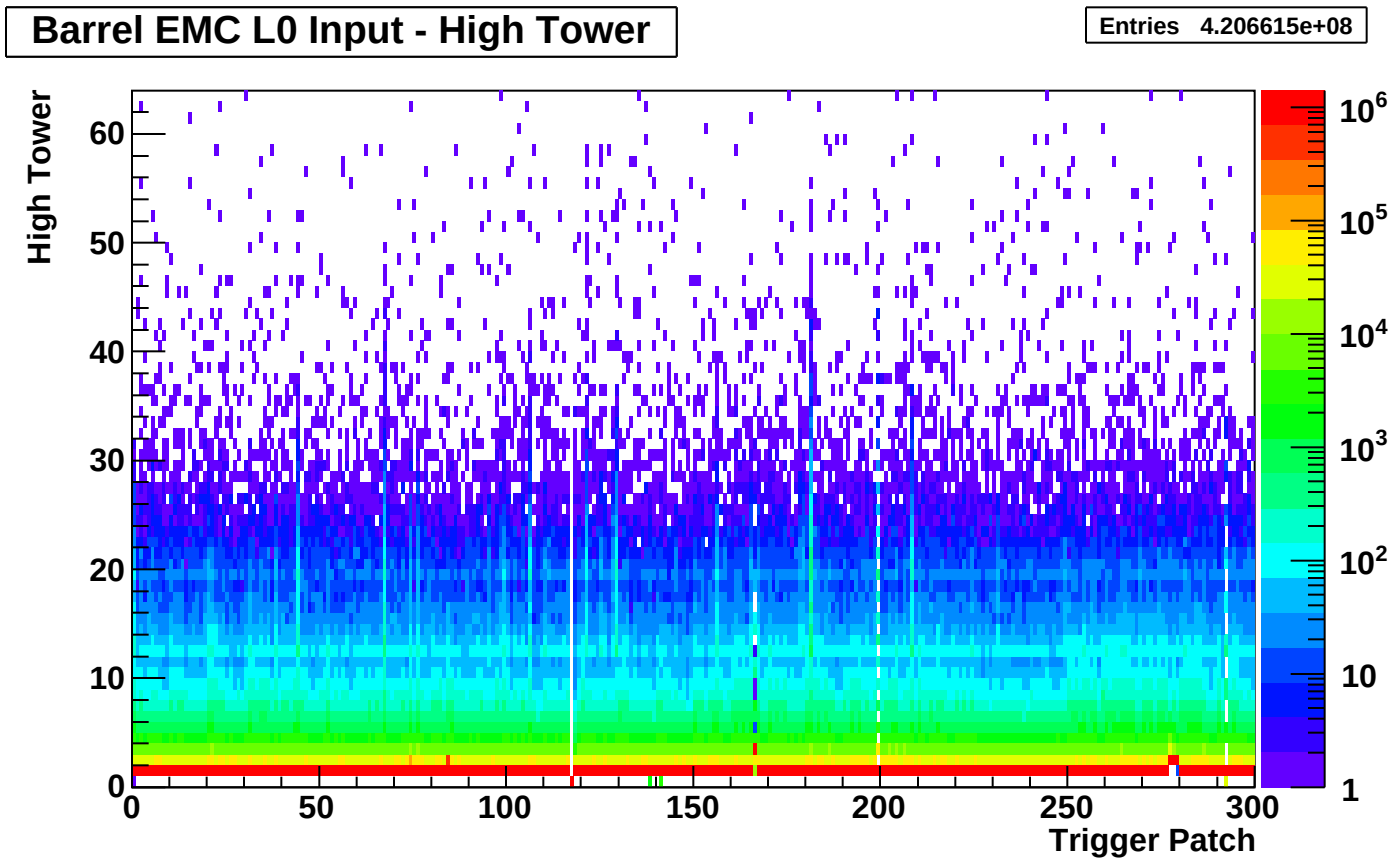

DAQ10k HT bits errors
Entries 29

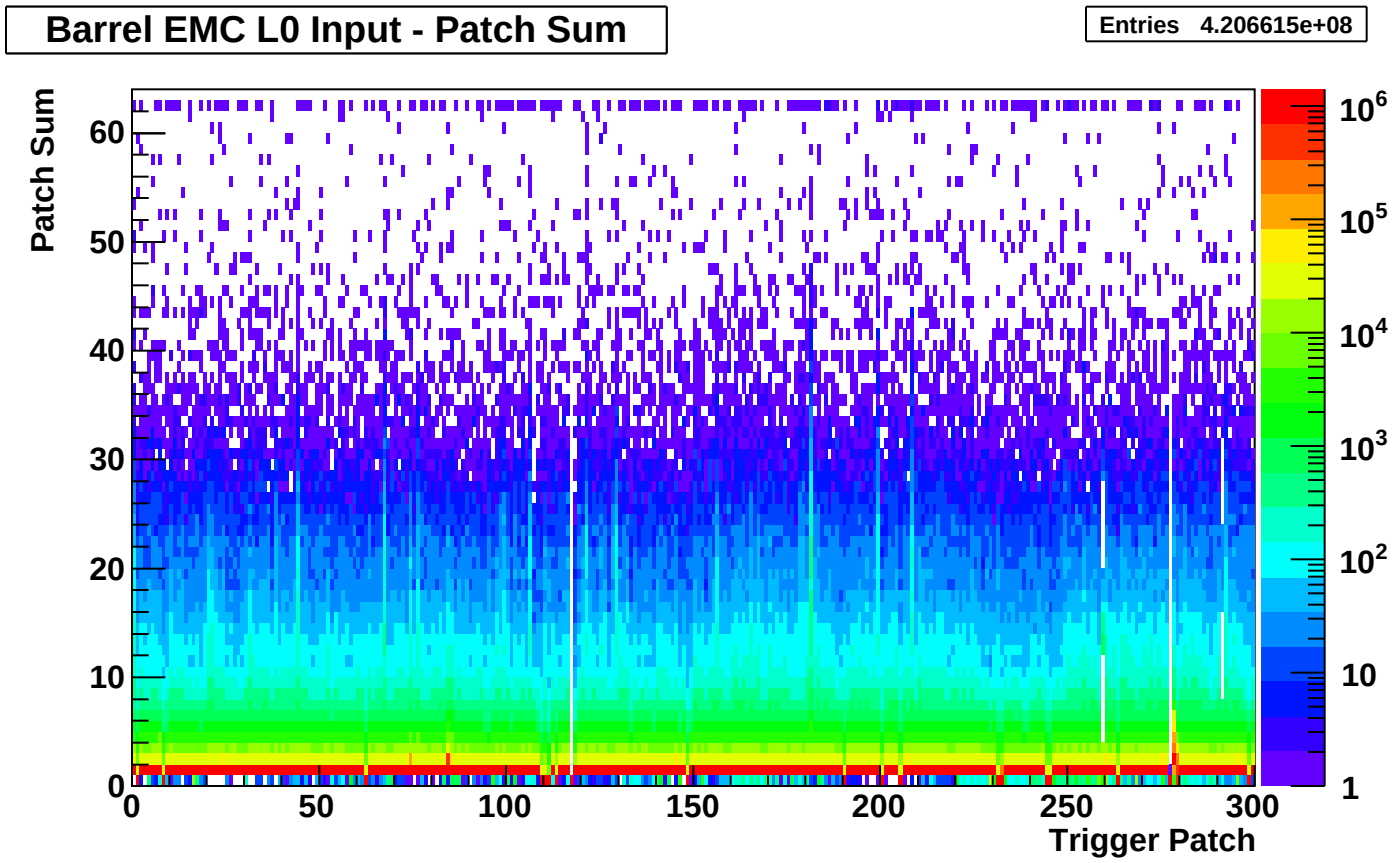

TPC Readout Mask (simulated)
Entries 224642

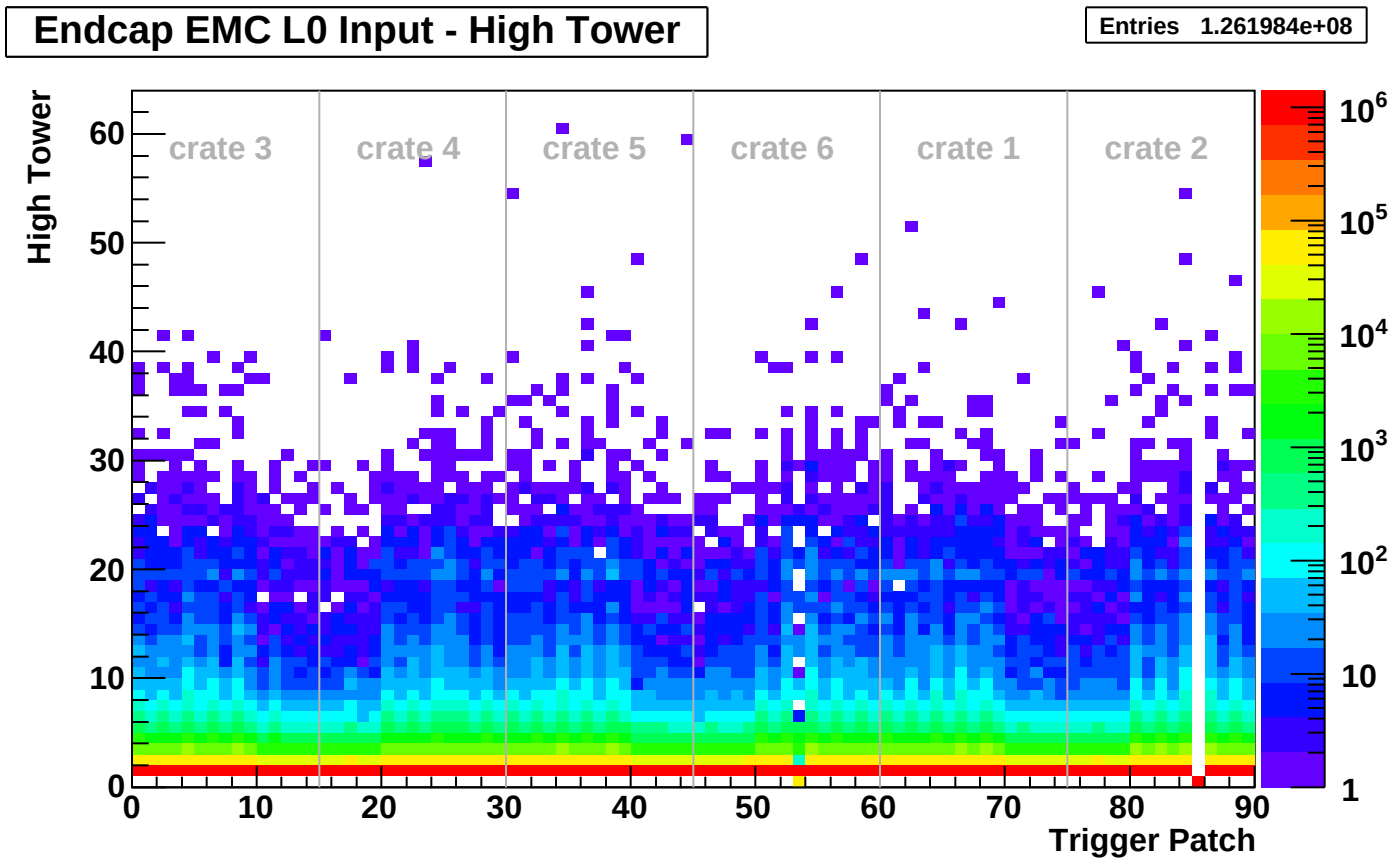

BunchId7Bit (all events)
Entries1402205

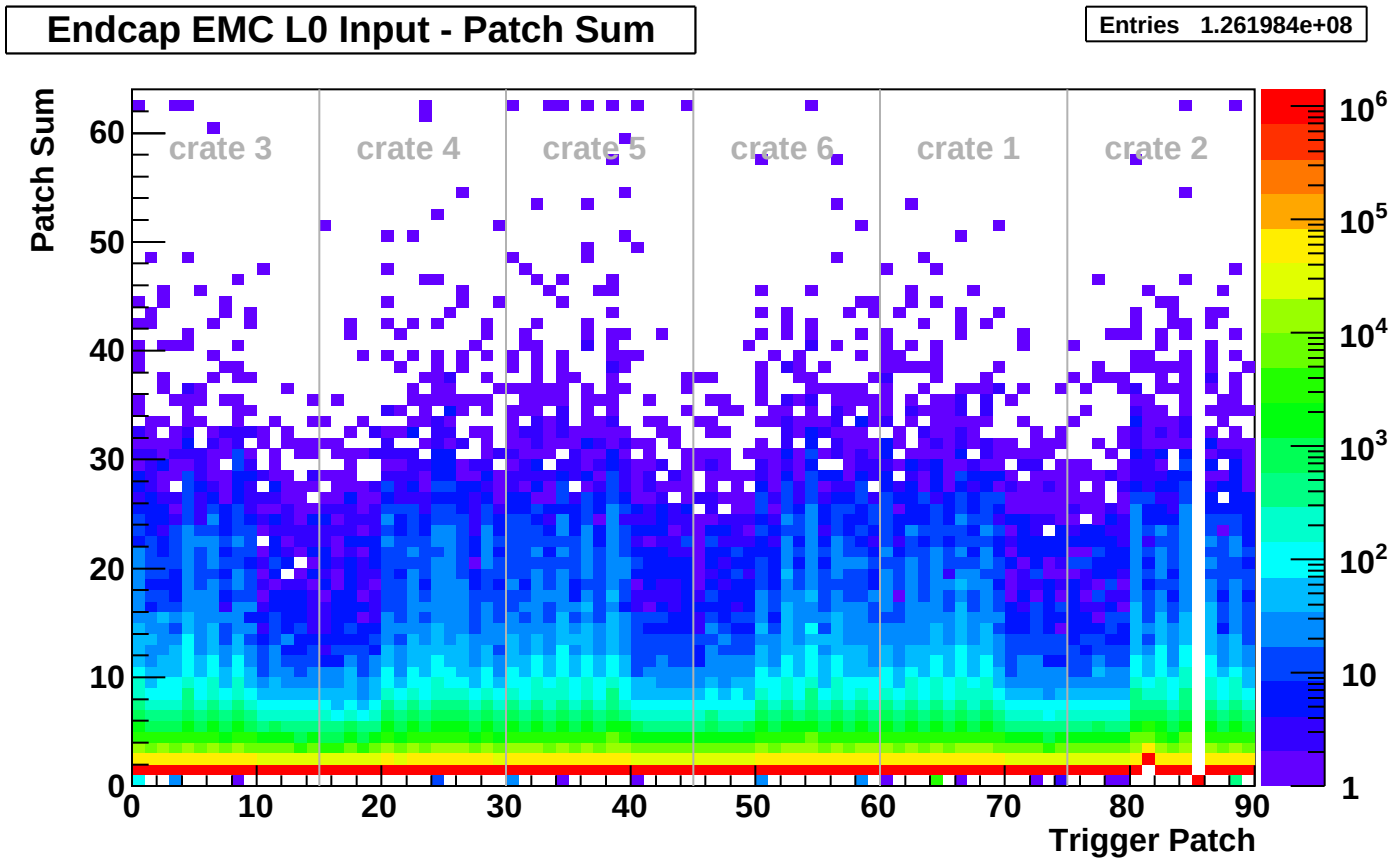

Abort-gaps
Entries 28718

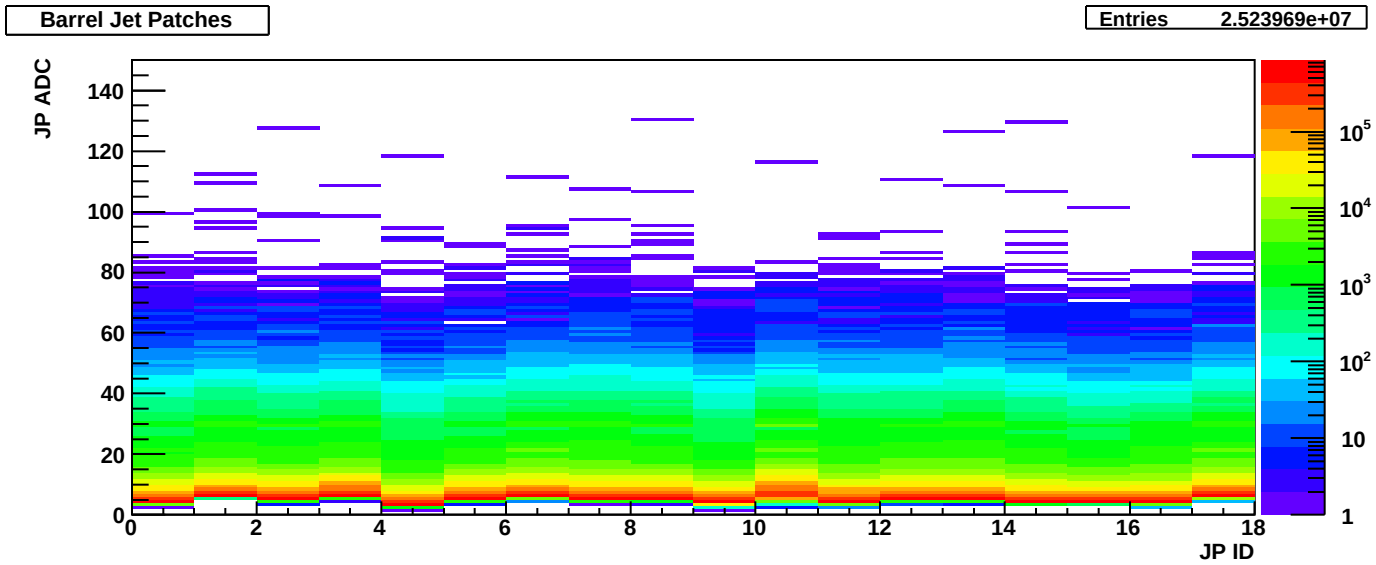

!Yellow-Fill
Entries 48356

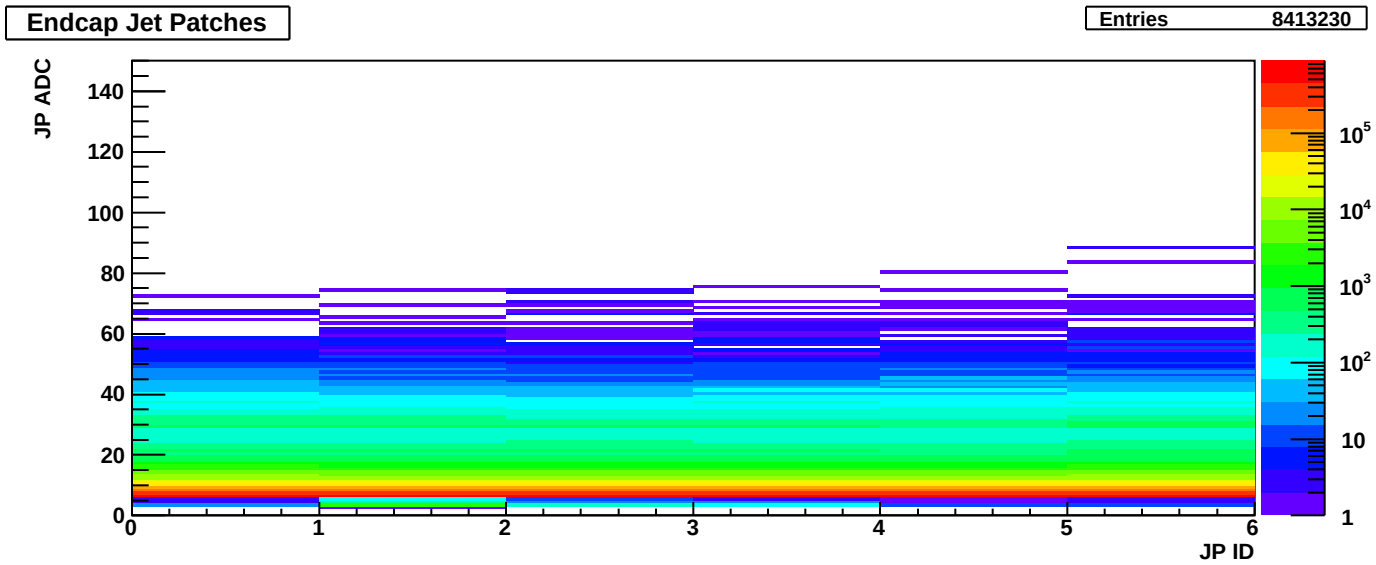

Barrel EMC L0 Input - High Tower
Entries 4.206615e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

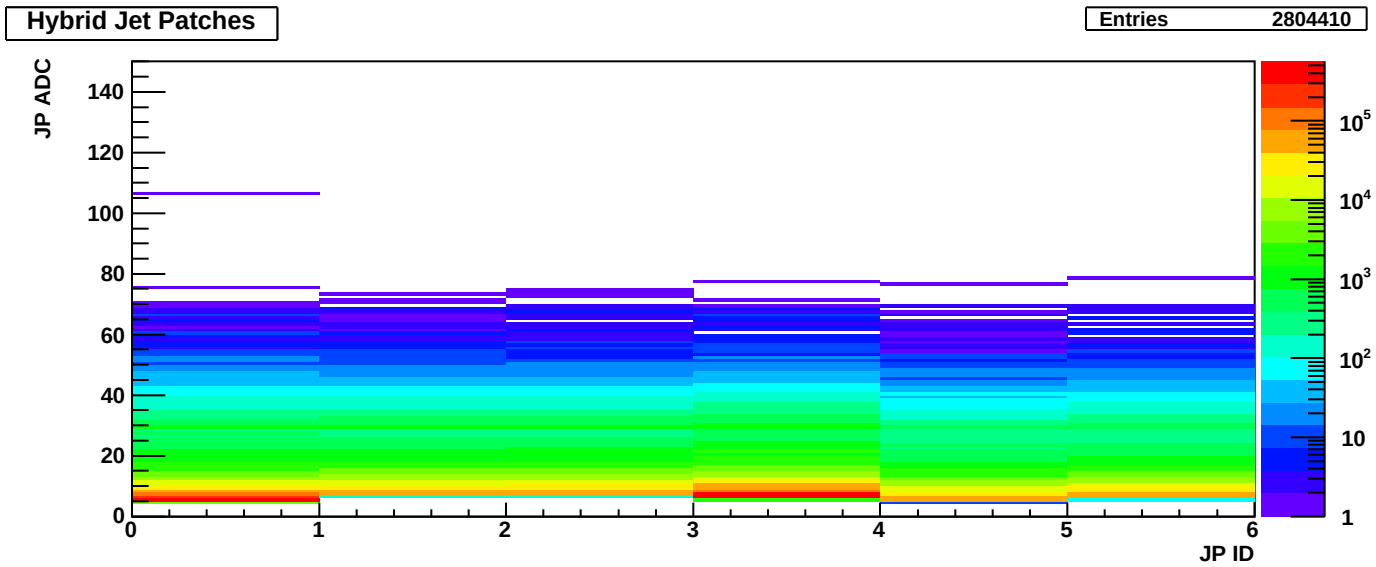

Barrel EMC L0 Input - Patch Sum
Entries 4.206615e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

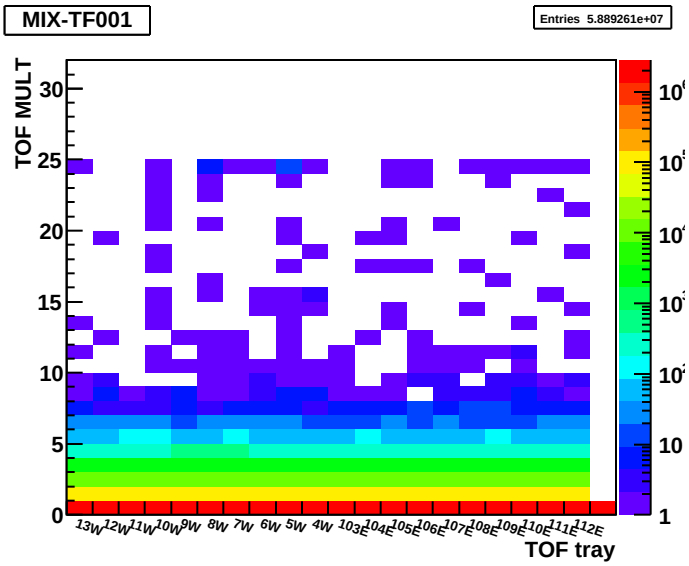

Endcap EMC L0 Input - High Tower
Entries 1.261984e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

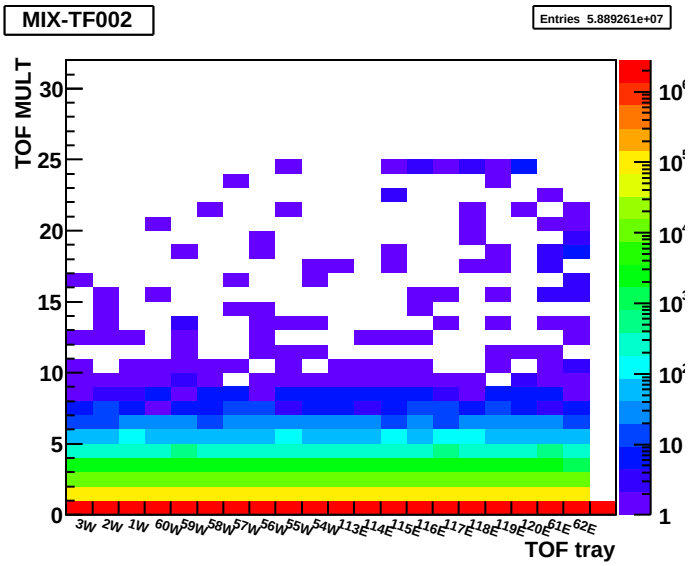

Endcap EMC L0 Input - Patch Sum
Entries 1.261984e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

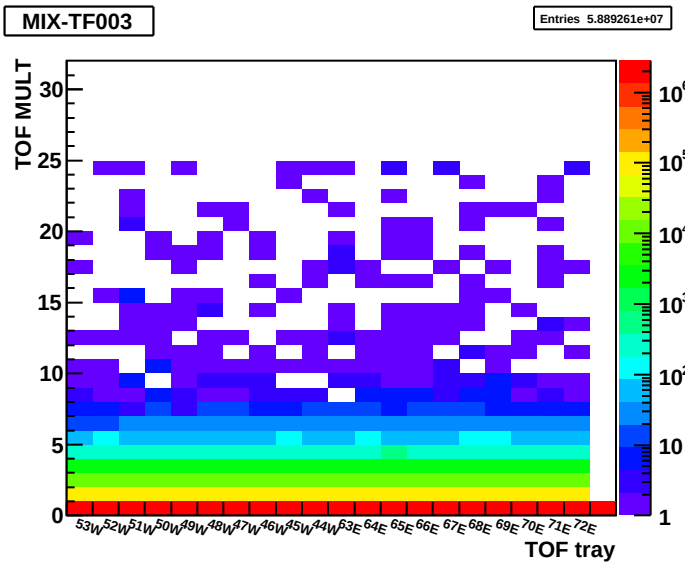

Barrel Jet Patches
Entries 2.523969e+07
JP ADC
JP ID

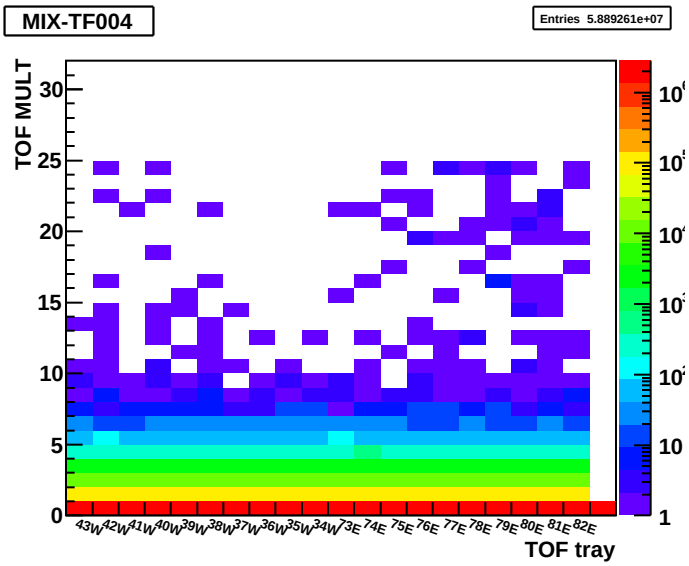

Endcap Jet Patches
Entries 8413230
JP ADC
JP ID

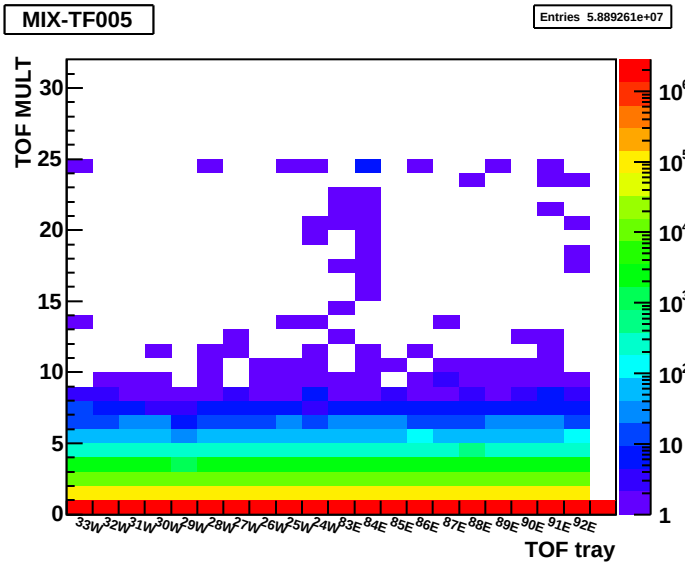

Hybrid Jet Patches
Entries 2804410
JP ADC
JP ID

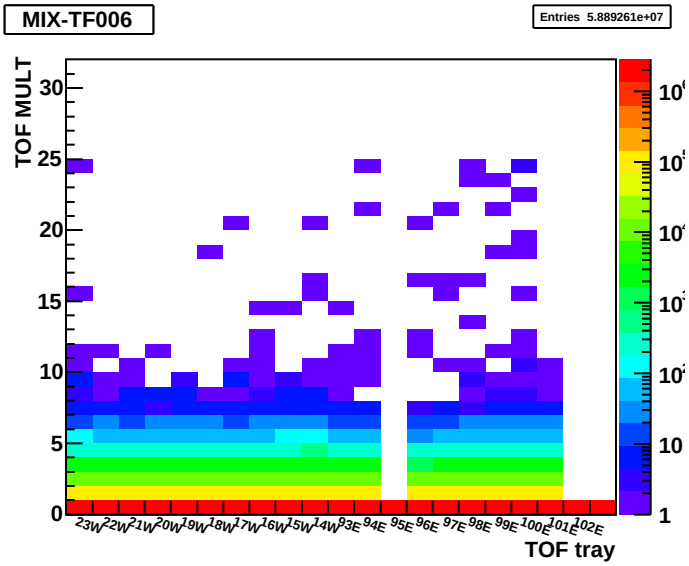

MIX-TF001
Entries 5.889261e+07
TOF MULT
TOF tray

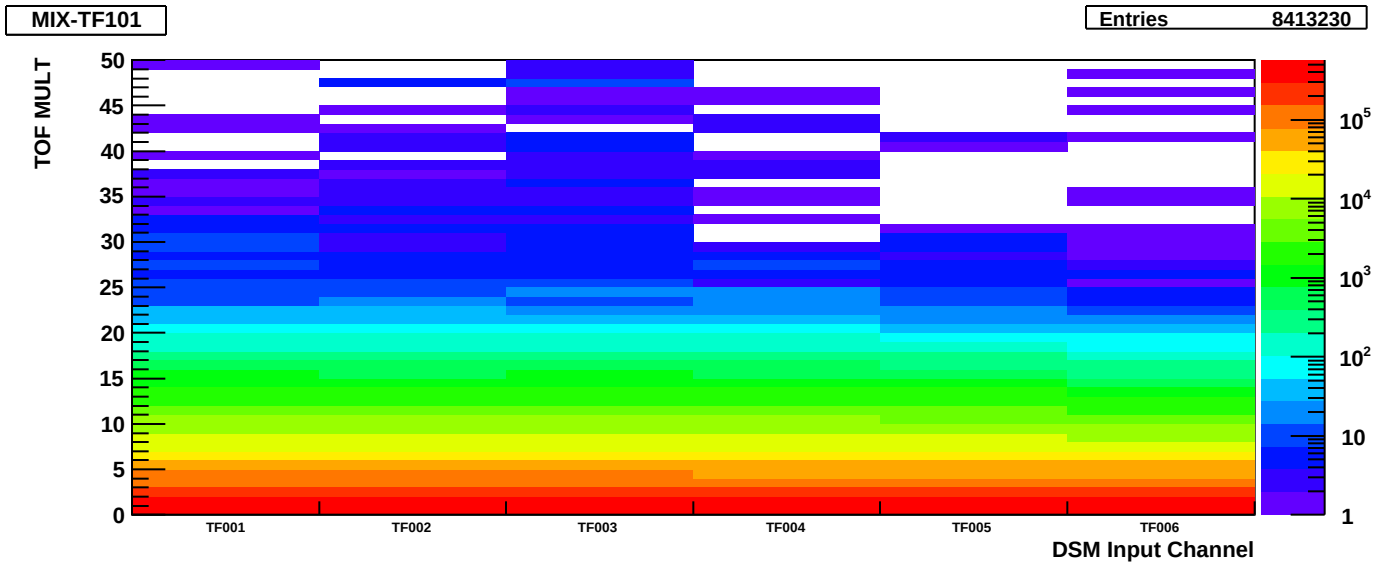

MIX-TF002
Entries 5.889261e+07
TOF MULT
TOF tray

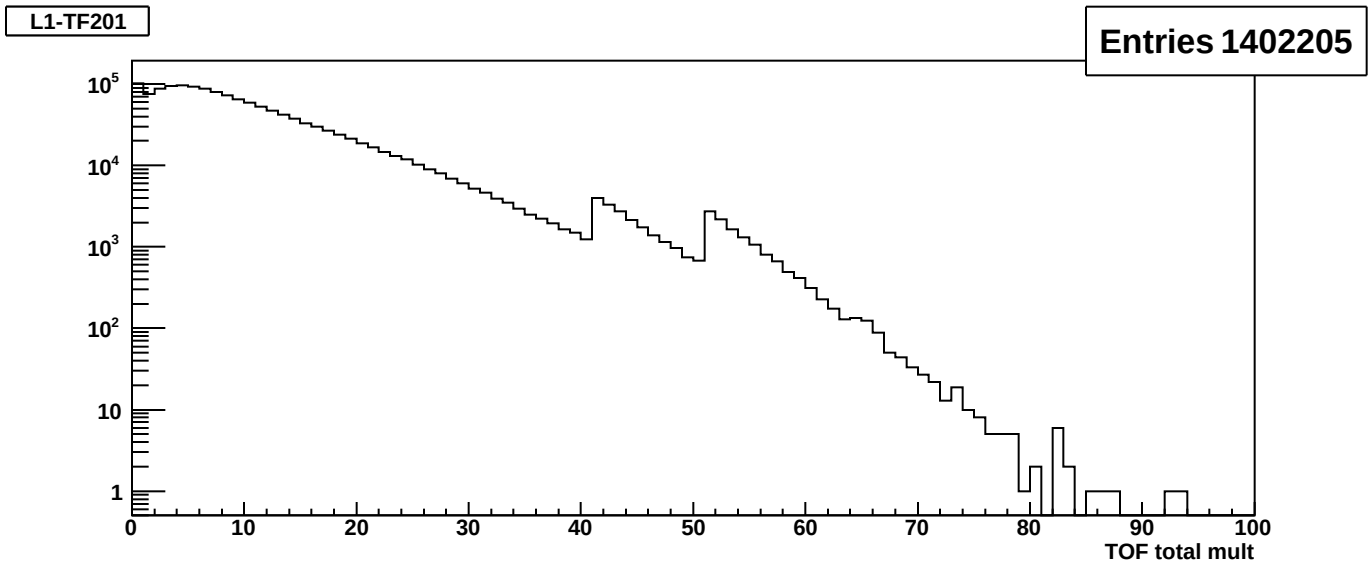

MIX-TF003
Entries 5.889261e+07
TOF MULT
TOF tray

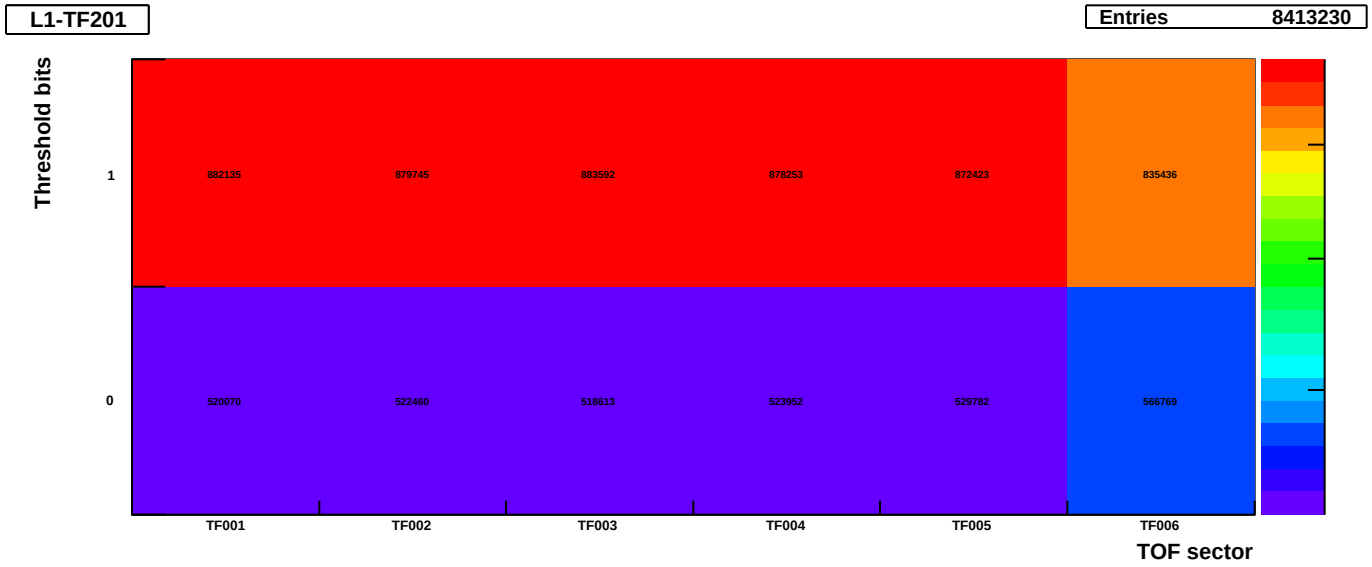

MIX-TF004
Entries 5.889261e+07
TOF MULT
TOF tray

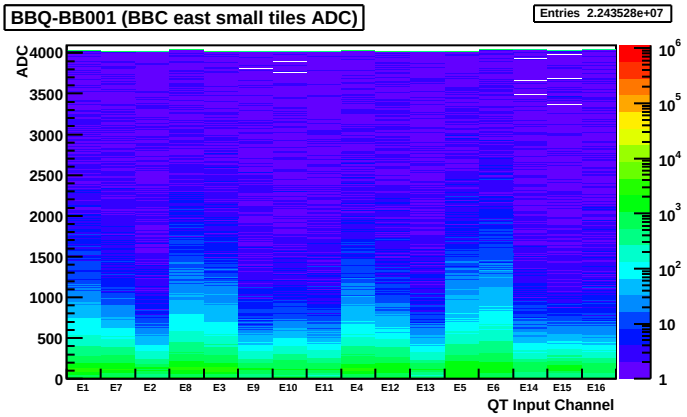

MIX-TF005
Entries 5.889261e+07
TOF MULT
TOF tray

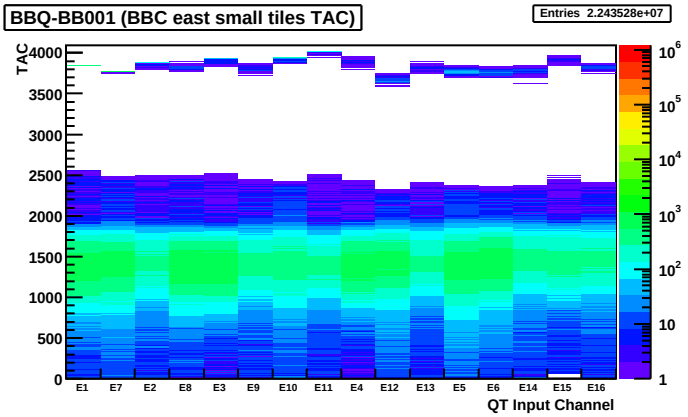

MIX-TF006
Entries 5.889261e+07
TOF MULT
TOF tray

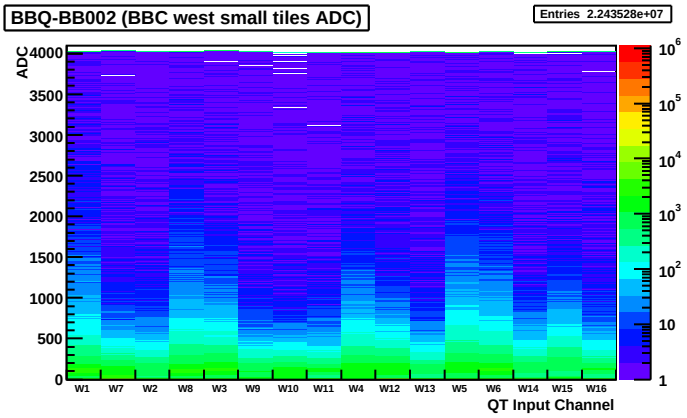

MIX-TF101
Entries 8413230
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

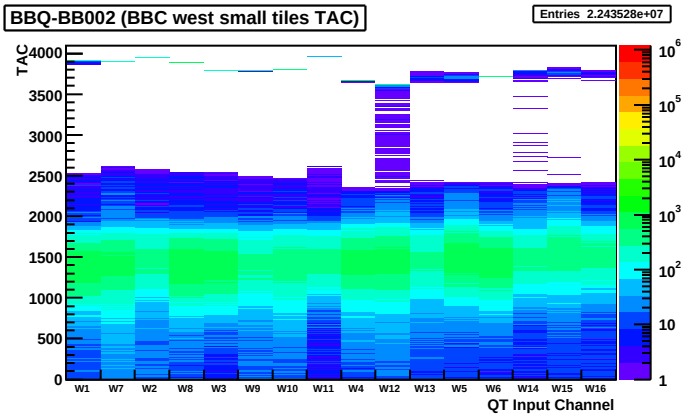

L1-TF201
Entries 1402205
TOF total mult

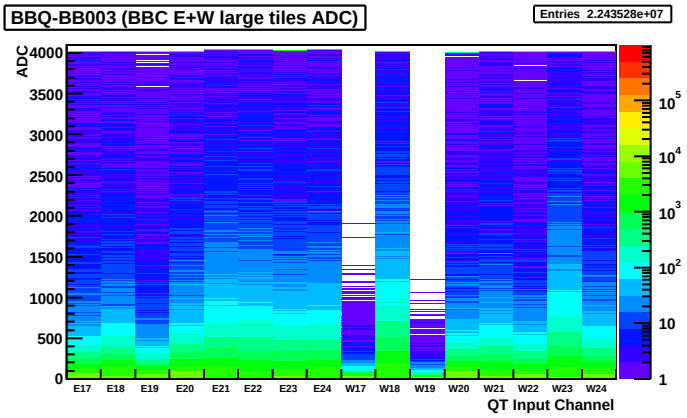

L1-TF201
Entries 8413230
Threshold bits
882135
879745
883592
878253
872423
835436
520070
522460
518613
523952
529782
566769
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

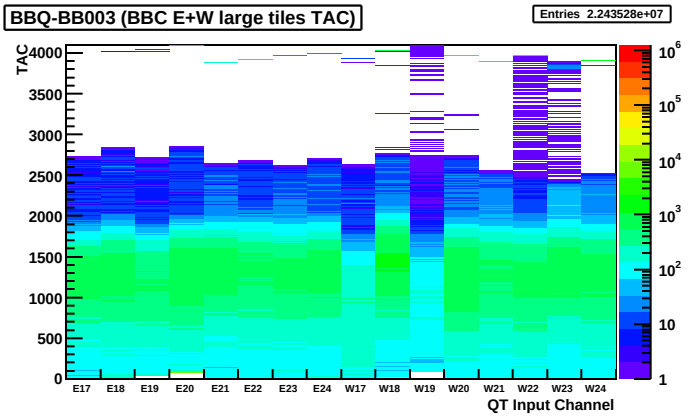

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.243528e+07
ADC
QT Input Channel

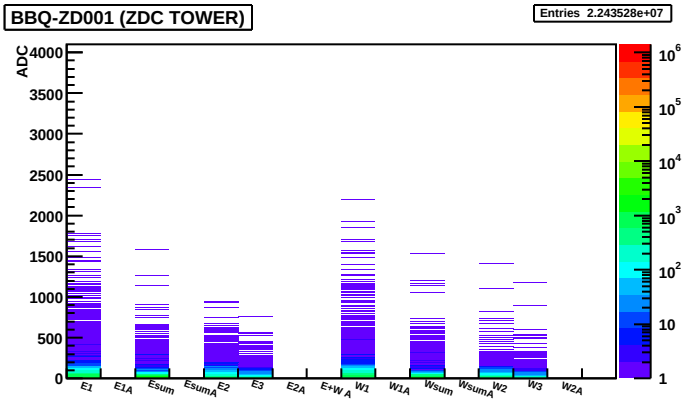

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.243528e+07
TAC
QT Input Channel

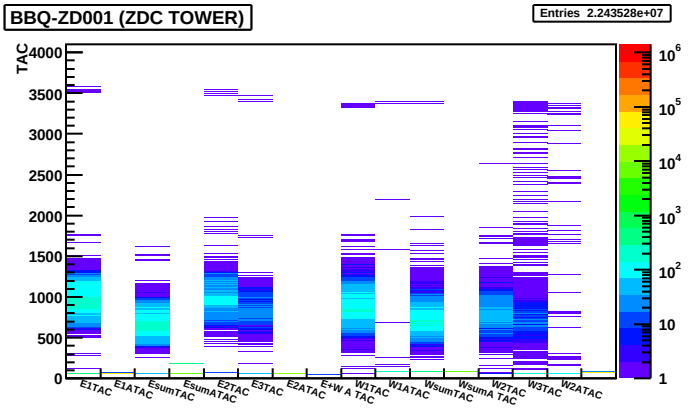

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.243528e+07
ADC
QT Input Channel

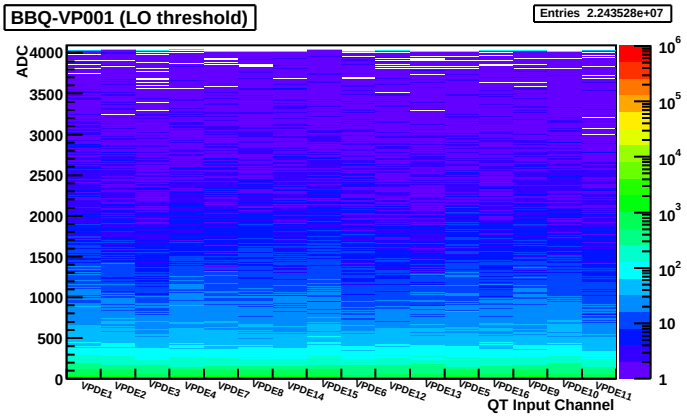

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.243528e+07
TAC
QT Input Channel

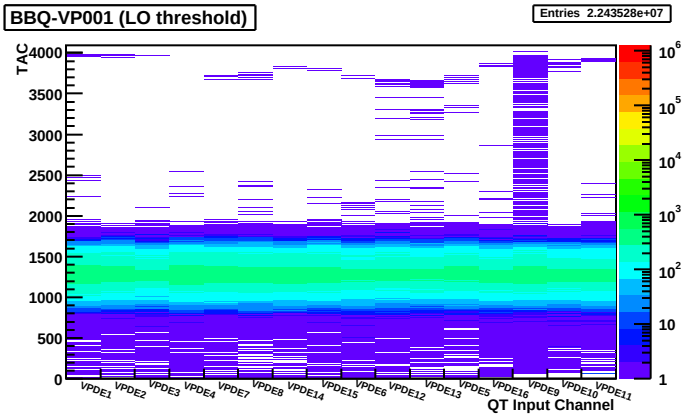

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.243528e+07
ADC
QT Input Channel

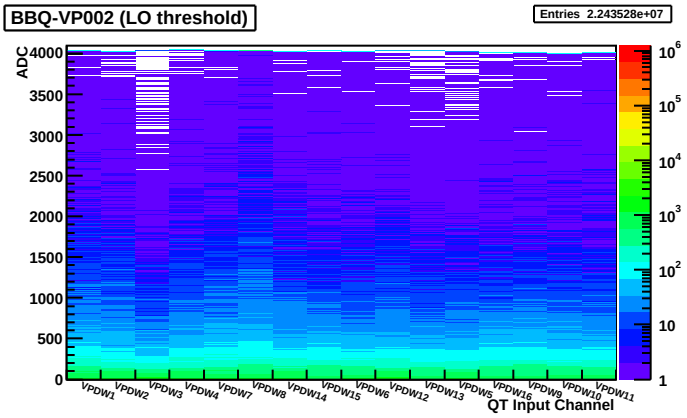

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.243528e+07
TAC
QT Input Channel

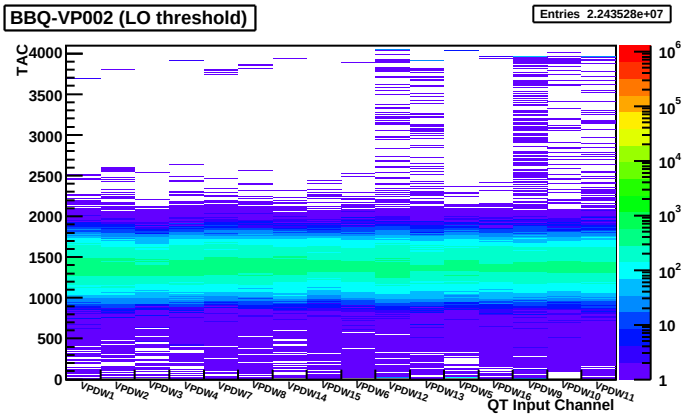

BBQ-ZD001 (ZDC TOWER)
Entries 2.243528e+07
ADC

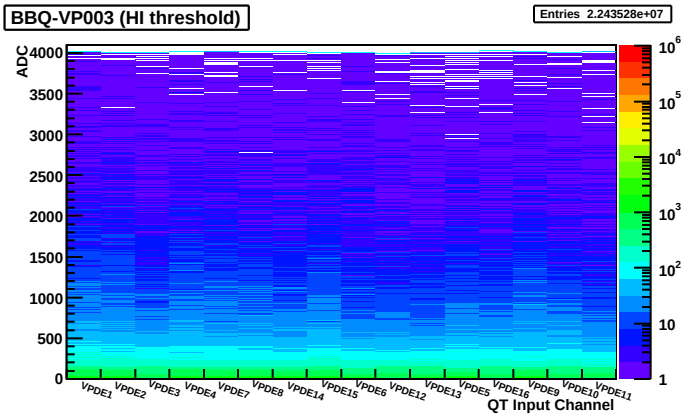

BBQ-ZD001 (ZDC TOWER)
Entries 2.243528e+07
TAC

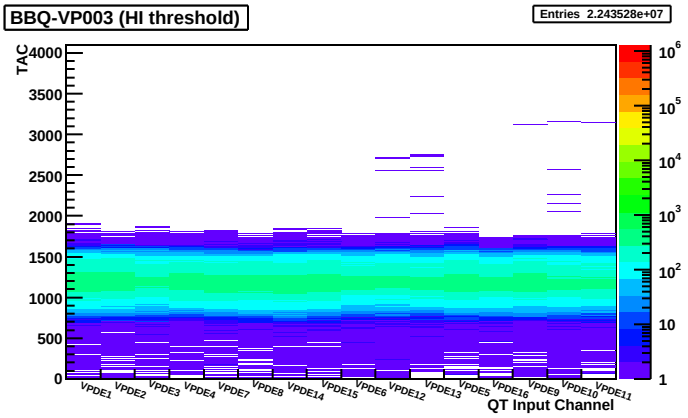

BBQ-VP001 (LO threshold)
Entries 2.243528e+07
ADC
QT Input Channel

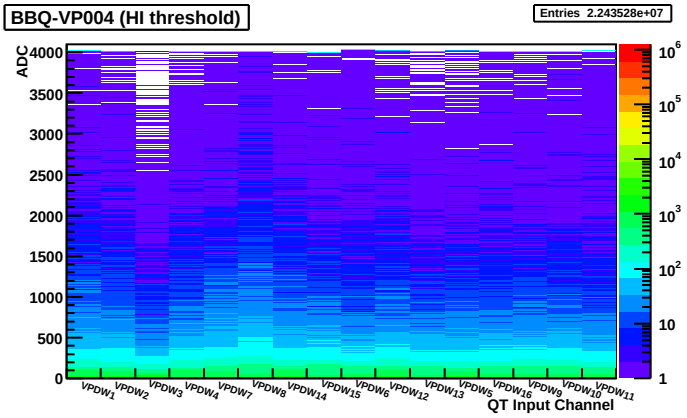

BBQ-VP001 (LO threshold)
Entries 2.243528e+07
TAC
QT Input Channel

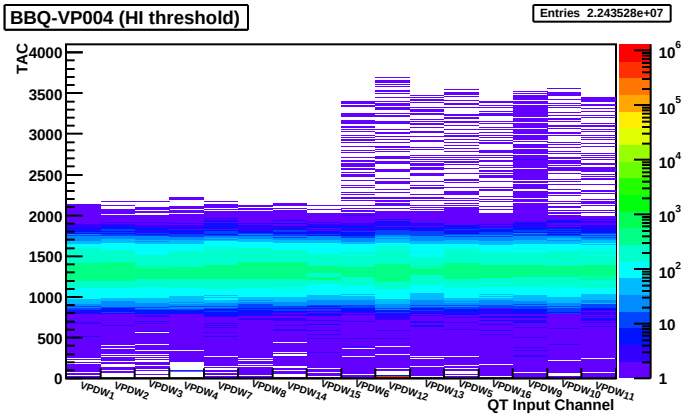

BBQ-VP002 (LO threshold)
Entries 2.243528e+07
ADC
QT Input Channel

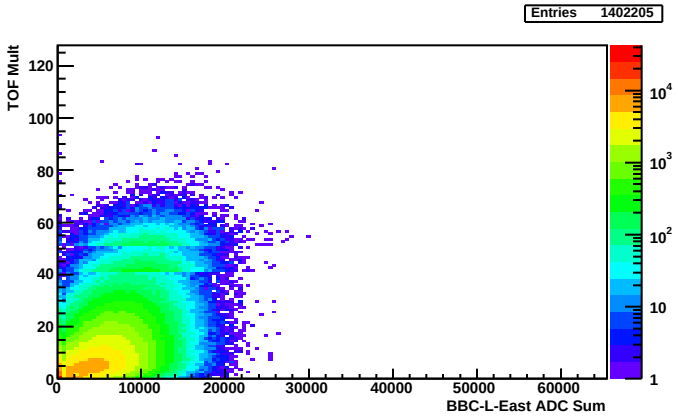

BBQ-VP002 (LO threshold)
Entries 2.243528e+07
TAC
QT Input Channel

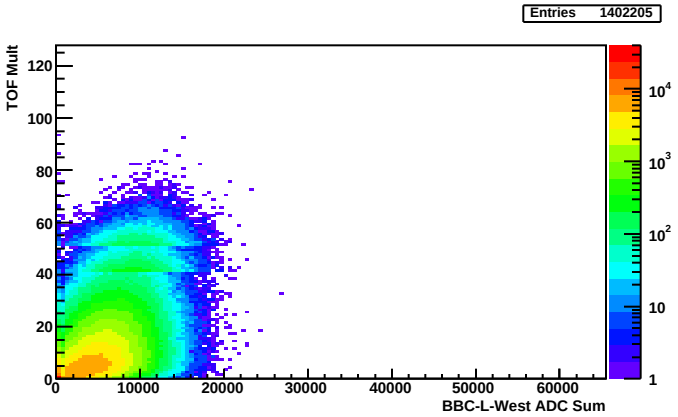

BBQ-VP003 (HI threshold)
Entries 2.243528e+07
ADC
QT Input Channel

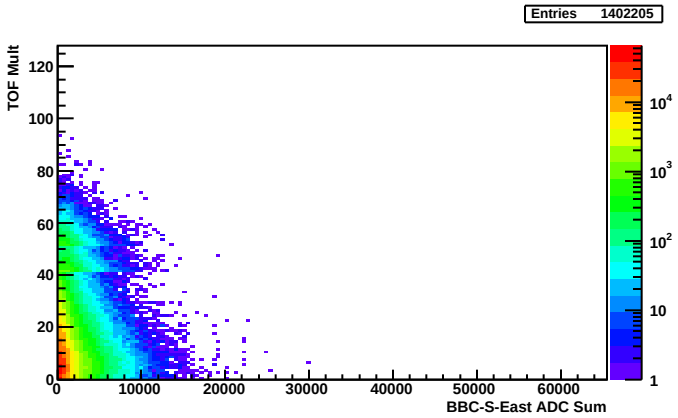

BBQ-VP003 (HI threshold)
Entries 2.243528e+07
TAC
QT Input Channel

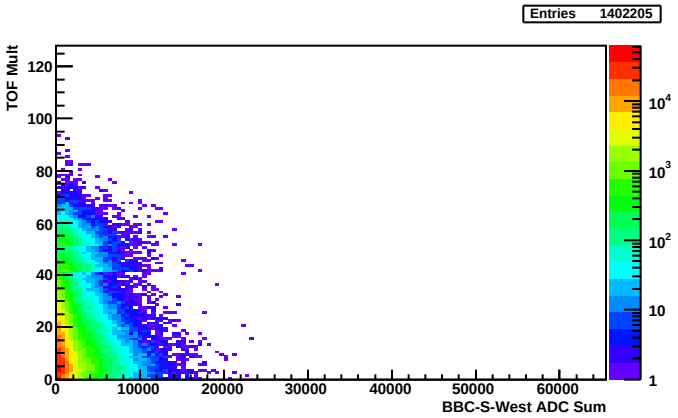

BBQ-VP004 (HI threshold)
Entries 2.243528e+07
ADC
QT Input Channel

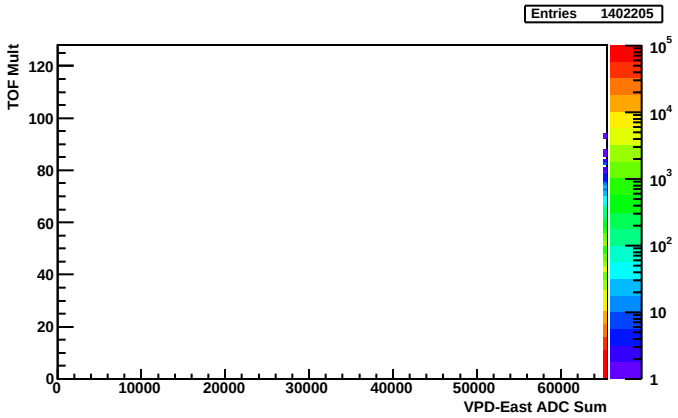

BBQ-VP004 (HI threshold)
Entries 2.243528e+07
TAC
QT Input Channel

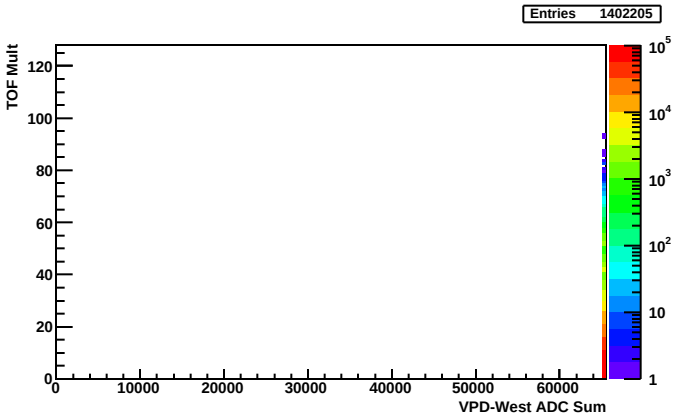

Entries 1402205
TOF Mult
BBC-L-East ADC Sum

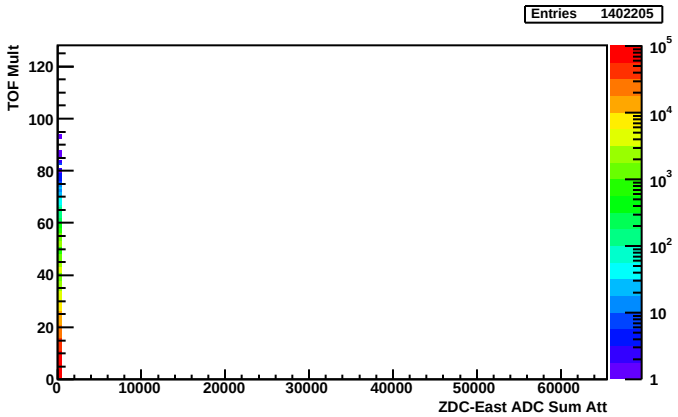

Entries 1402205
TOF Mult
BBC-L-West ADC Sum

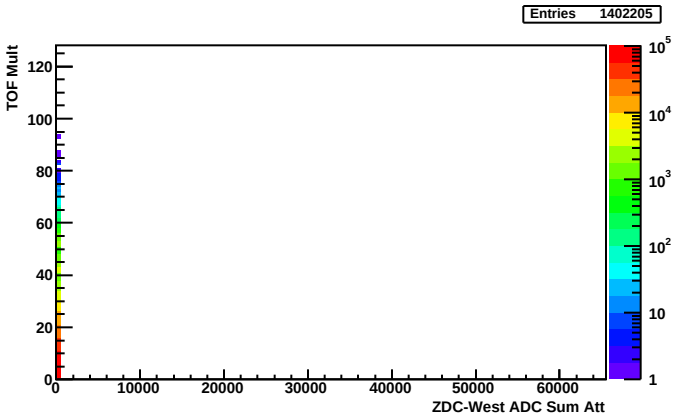

Entries 1402205
TOF Mult
BBC-S-East ADC Sum

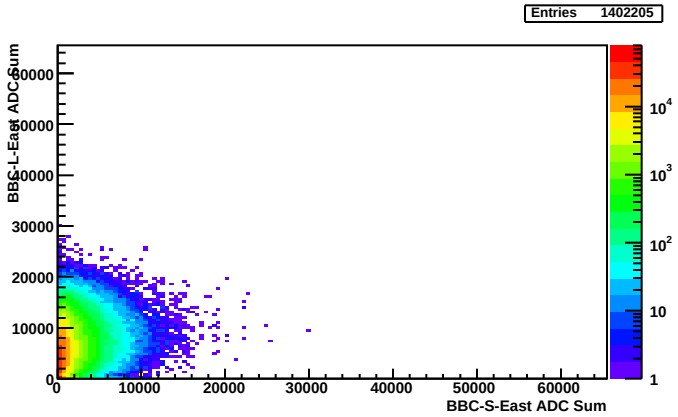

Entries 1402205
TOF Mult
BBC-S-West ADC Sum


Entries 1402205
TOF Mult
VPD-East ADC Sum


Entries 1402205
TOF Mult
VPD-West ADC Sum


Entries 1402205
TOF Mult
ZDC-East ADC Sum Att


Entries 1402205
TOF Mult
ZDC-West ADC Sum Att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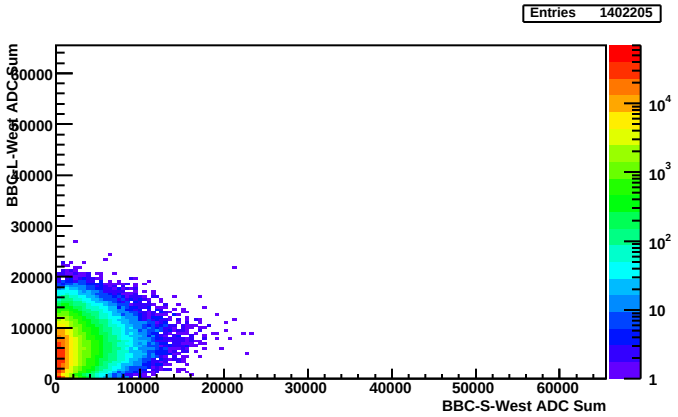
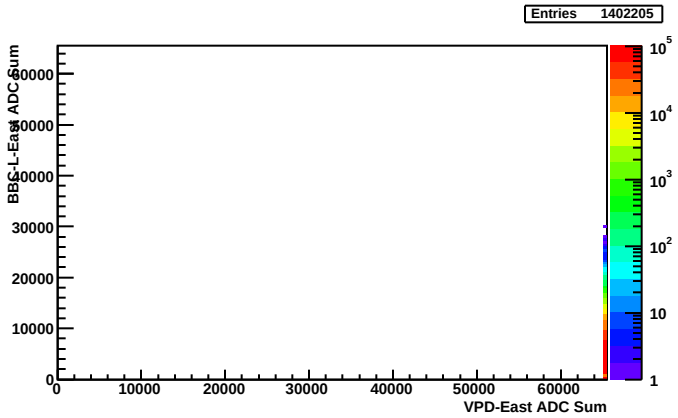
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1402205
BBC-L-West ADC Sum
BBC-S-West ADC Sum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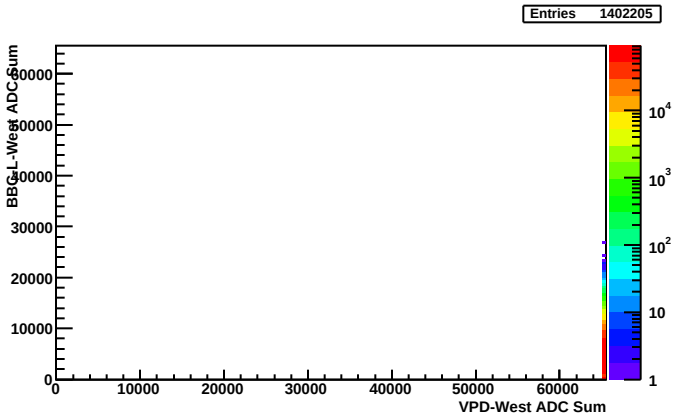
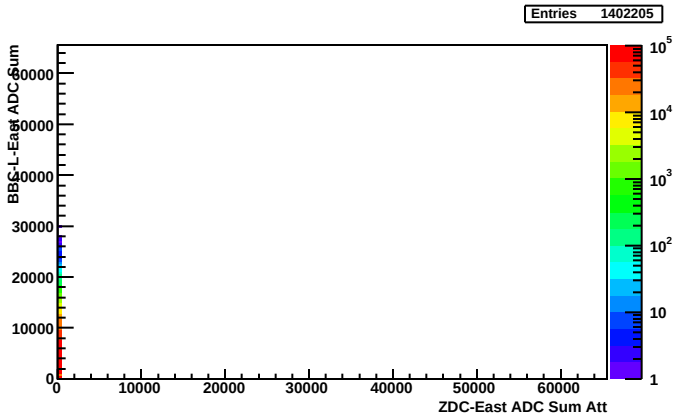
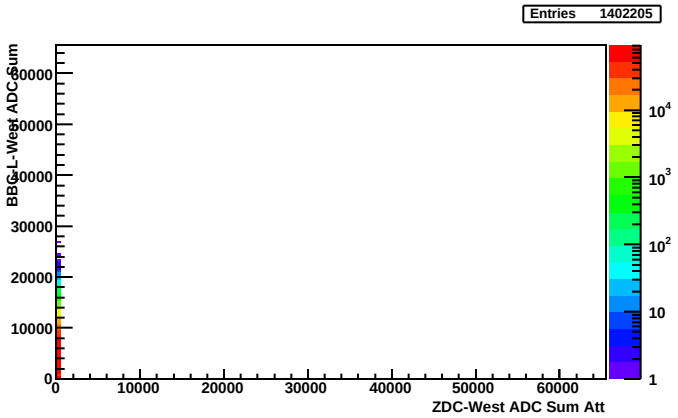
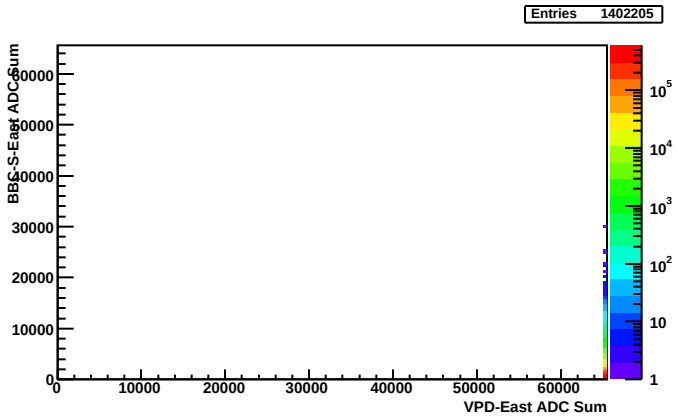
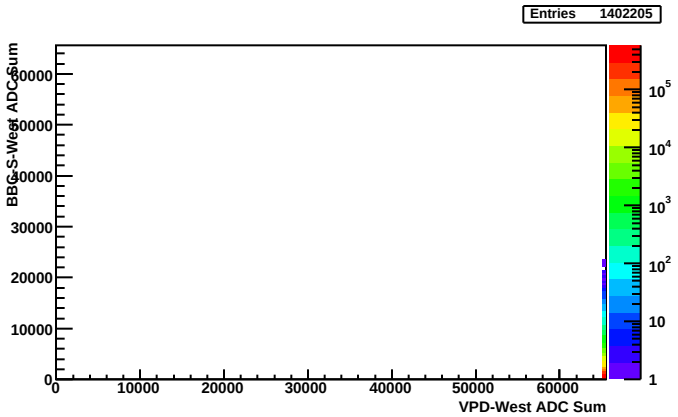
BBC-L-East ADC Sum
VPD-East ADC Sum

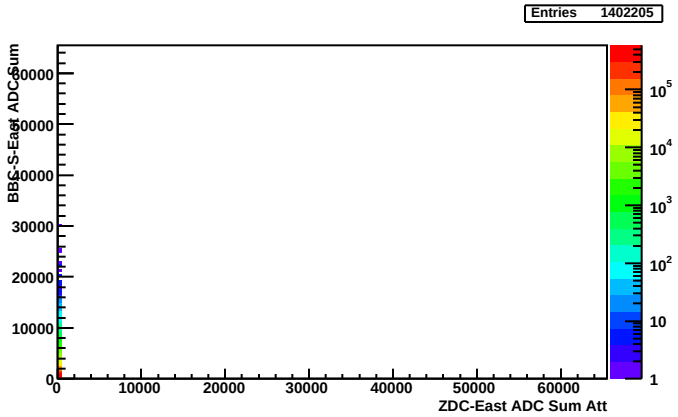

Entries 1402205
BBC-L-West ADC Sum
VPD-West ADC Sum

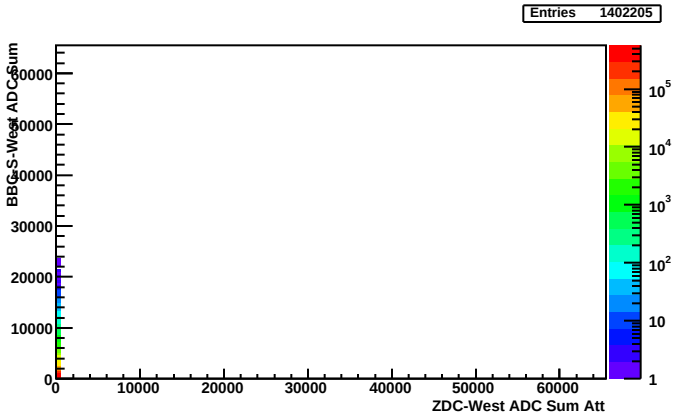

Entries 1402205
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 1402205
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 1402205
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1402205
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1402205
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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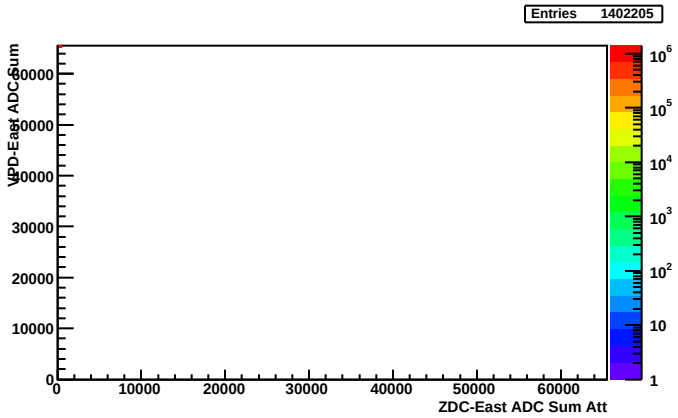
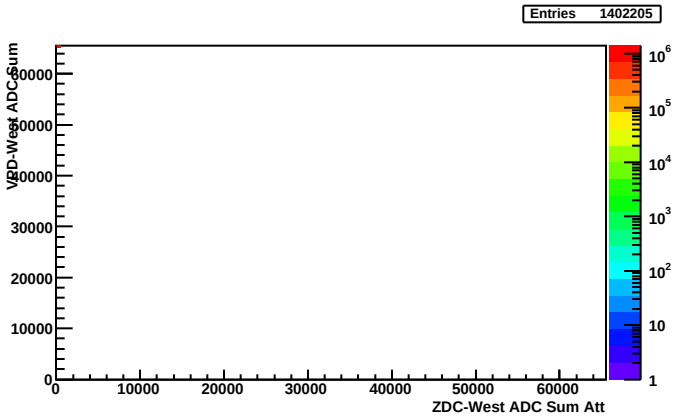
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1402205
VPD-East ADC Sum
ZDC-East ADC Sum Att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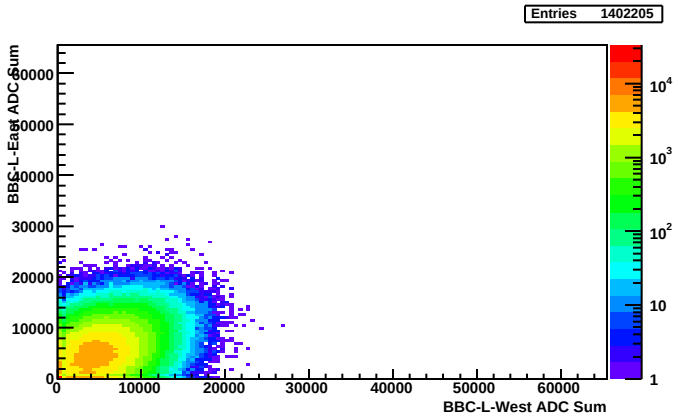
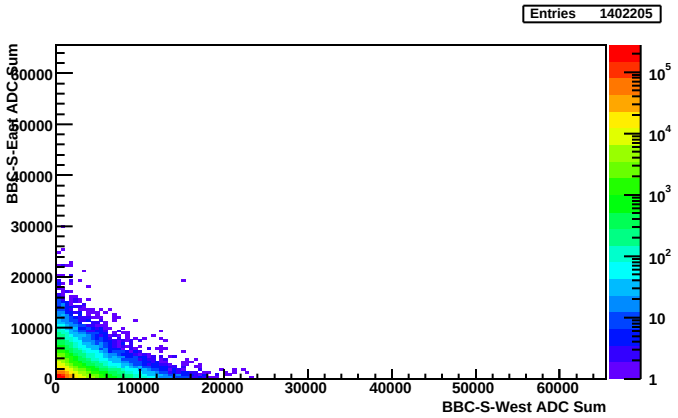
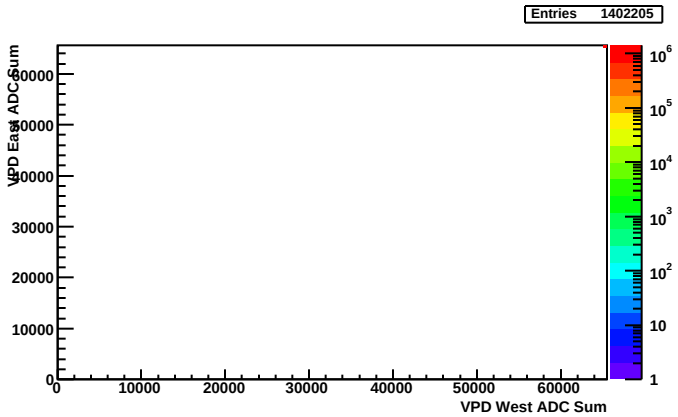
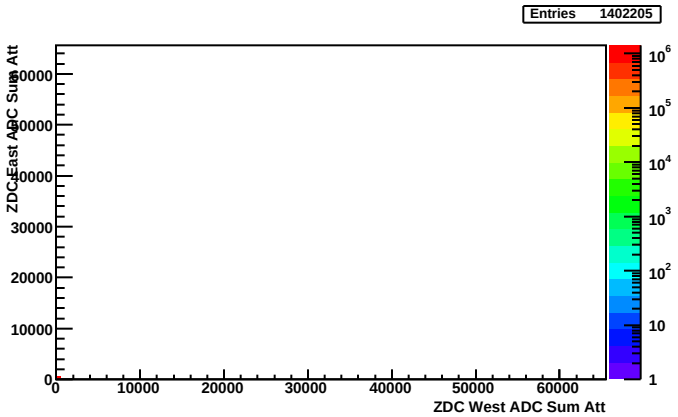
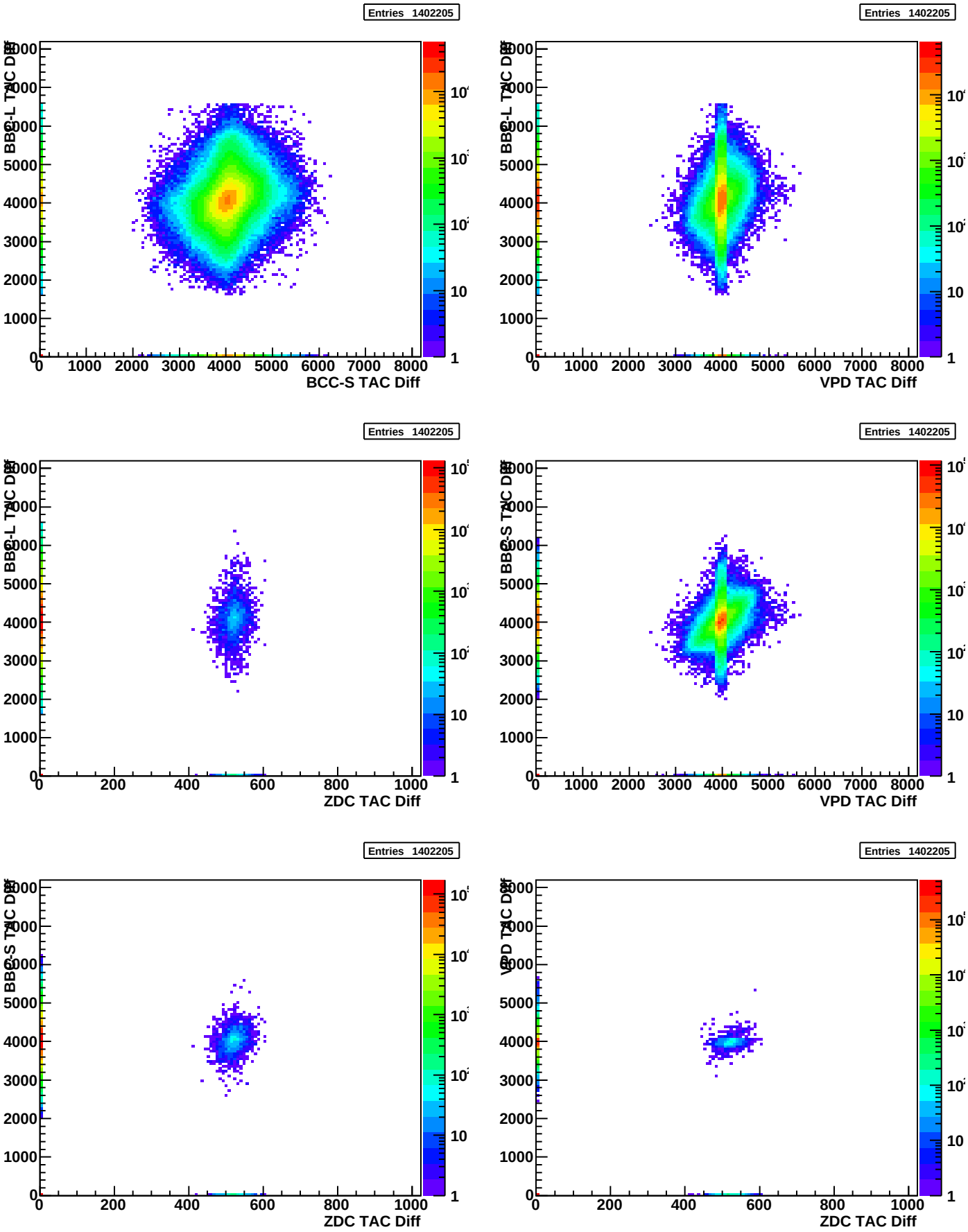
VPD-West ADC Sum
ZDC-West ADC Sum Att

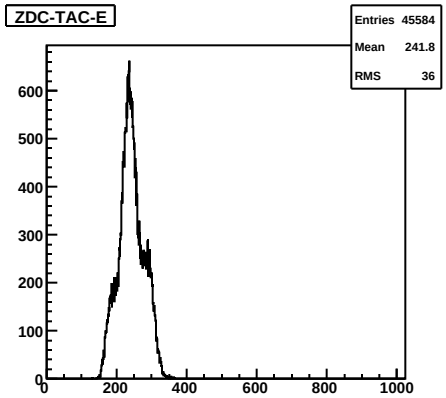

Entries 1402205
BBC-L-East ADC Sum
BBC-L-West ADC Sum

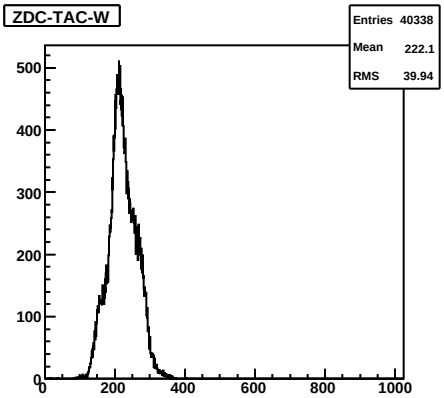

Entries 1402205
BBC-S-East ADC Sum
BBC-S-West ADC Sum

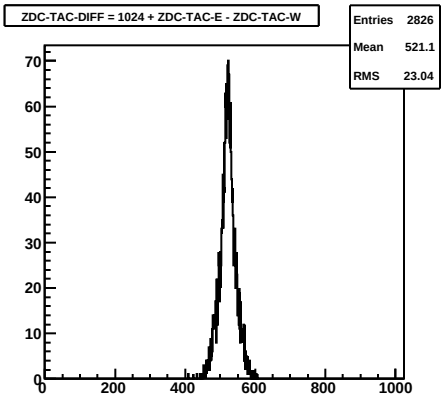

Entries 1402205
VPD East ADC Sum
VPD West ADC Sum

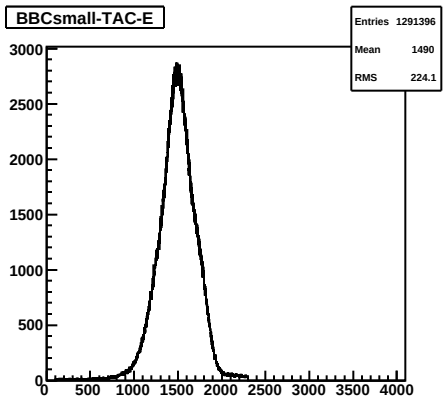

Entries 1402205
ZDC East ADC Sum Att
ZDC West ADC Sum Att

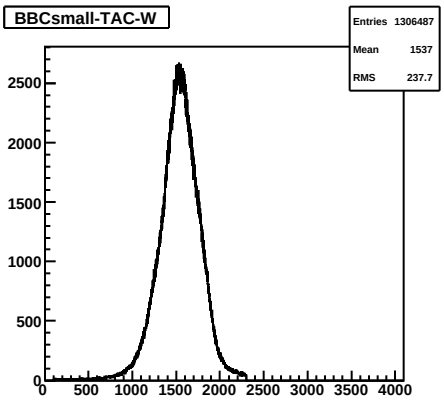

Entries 1402205
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1402205
BBC-L TAC Diff
VPD TAC Diff
Entries 1402205
BBC-L TAC Diff
ZDC TAC Diff
Entries 1402205
BBC-S TAC Diff
VPD TAC Diff
Entries 1402205
BBC-S TAC Diff
ZDC TAC Diff
Entries 1402205
VPD TAC Diff
ZDC TAC Diff

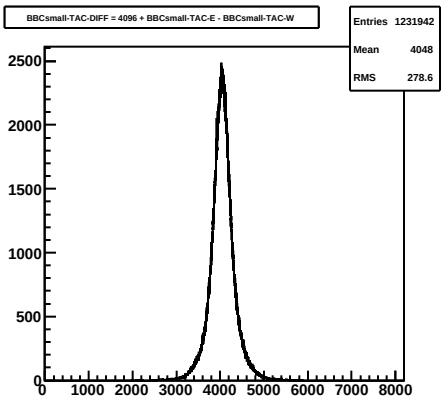

ZDC-TAC-E
Entries 45584
Mean 241.8
RMS 36

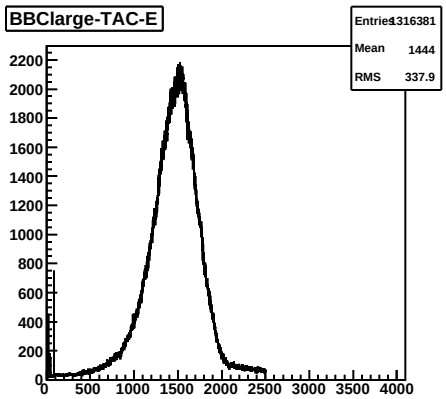

ZDC-TAC-W
Entries 40338
Mean 222.1
RMS 39.94

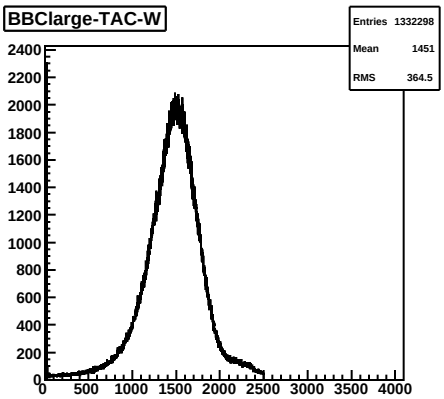

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2826
Mean 521.1
RMS 23.04

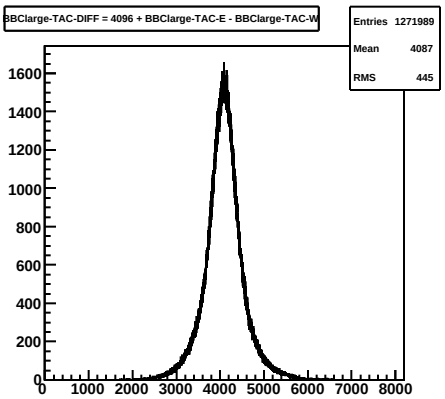

BBCsmall-TAC-E
Entries 1291396
Mean 1490
RMS 224.1

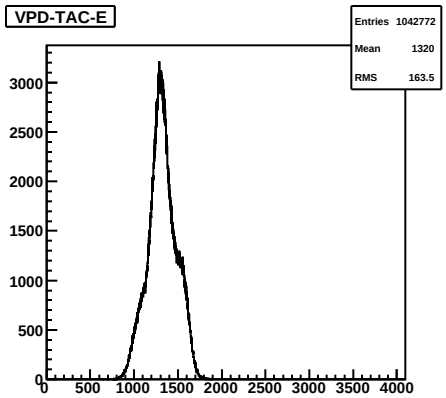

BBCsmall-TAC-W
Entries 1306487
Mean 1537
RMS 237.7

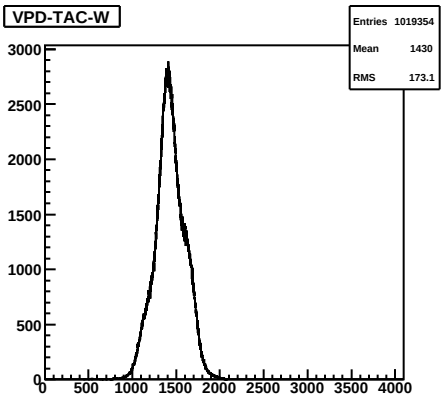

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1231942
Mean 4048
RMS 278.6

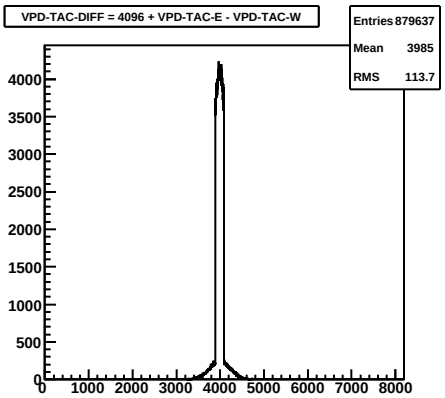

BBClarge-TAC-E
Entries 1316381
Mean 1444
RMS 337.9

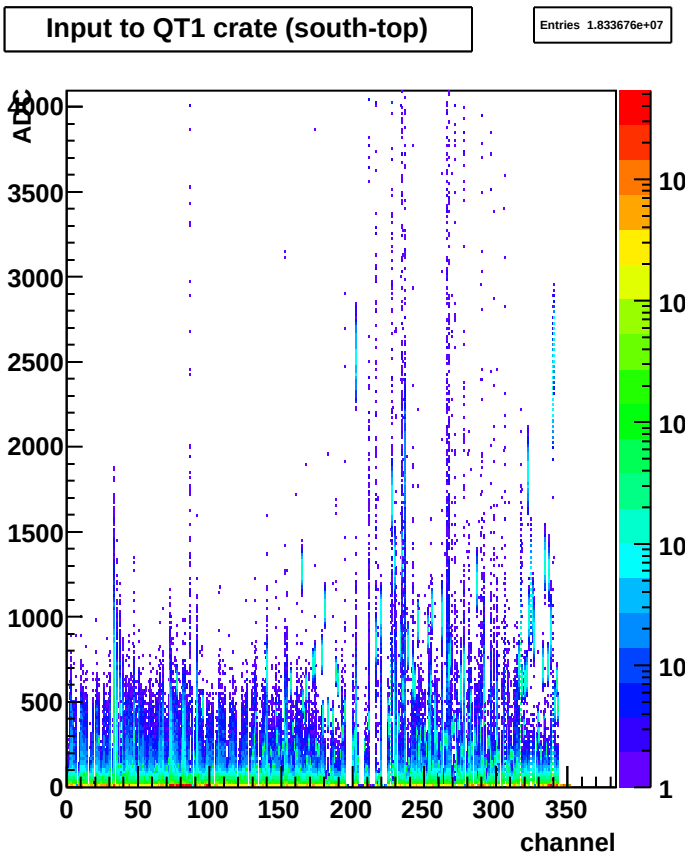

BBClarge-TAC-W
Entries 1332298
Mean 1451
RMS 364.5

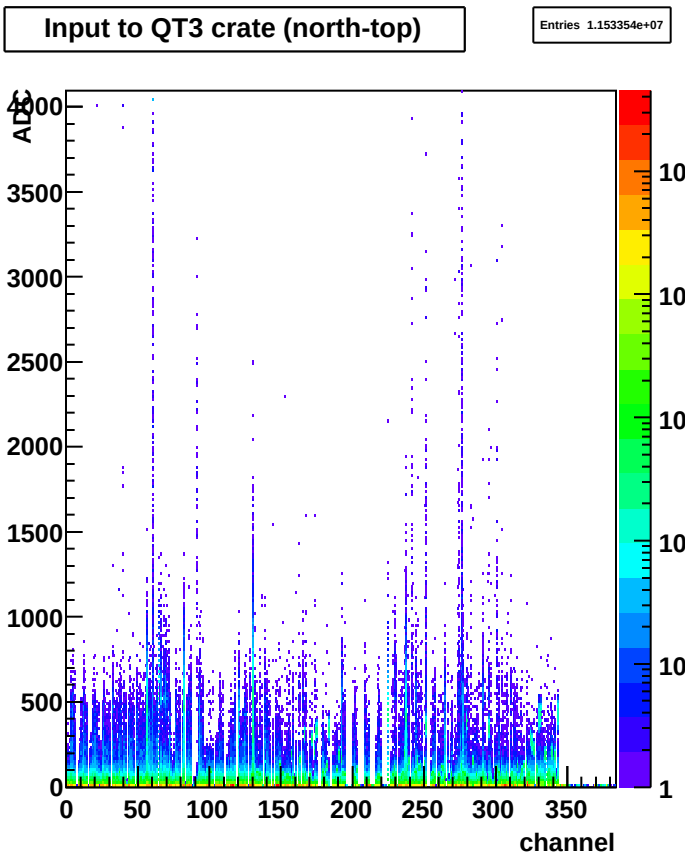

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1271989
Mean 4087
RMS 445

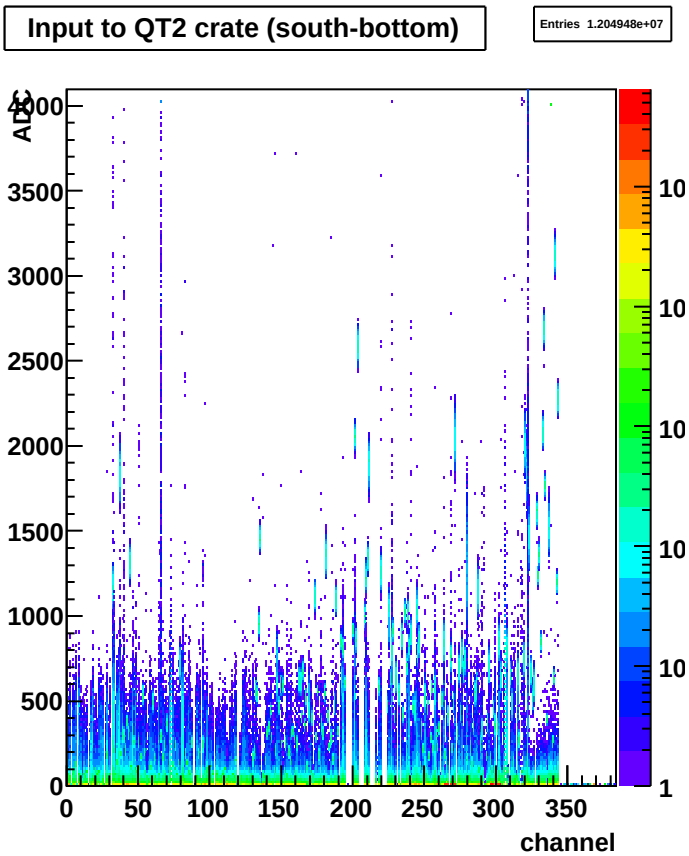

VPD-TAC-E
Entries 1042772
Mean 1320
RMS 163.5

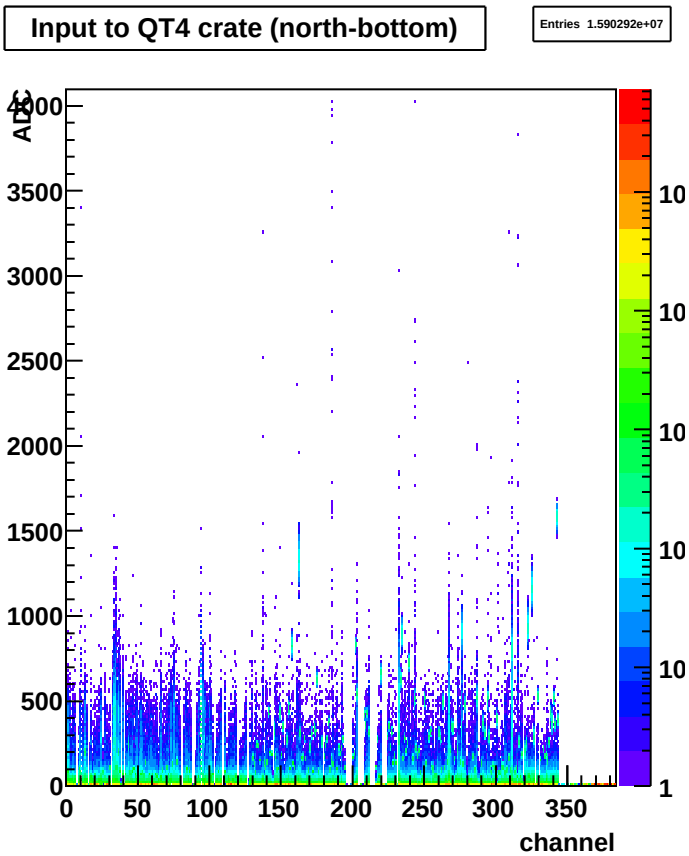

VPD-TAC-W
Entries 1019354
Mean 1430
RMS 173.1

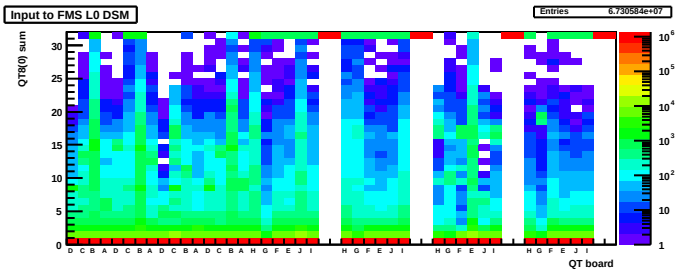

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 879637
Mean 3985
RMS 113.7

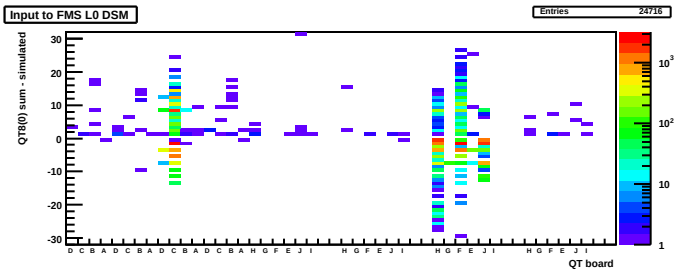

Input to QT1 crate (south-top)
Entries 1.833676e+07
ADC
channel

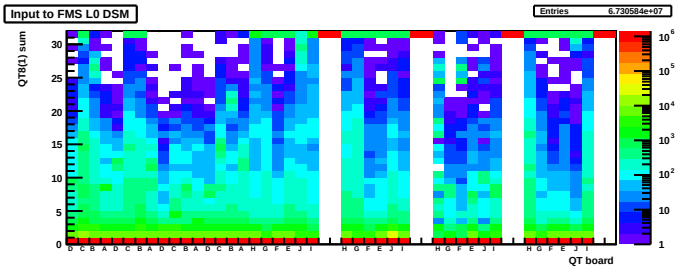

Input to QT3 crate (north-top)
Entries 1.153354e+07
ADC
channel

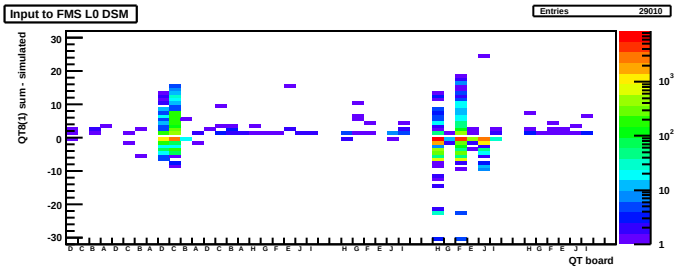

Input to QT2 crate (south-bottom)
Entries 1.204948e+07
ADC
channel

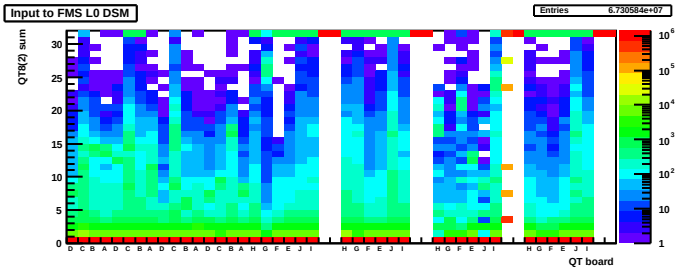

Input to QT4 crate (north-bottom)
Entries 1.590292e+07
ADC
channel

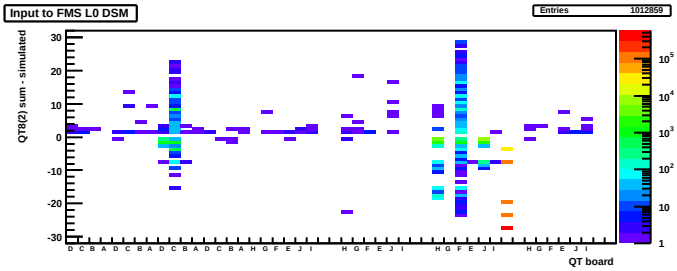

Input to FMS L0 DSM
Entries 6.730584e+07
QT8(0) sum
QT board

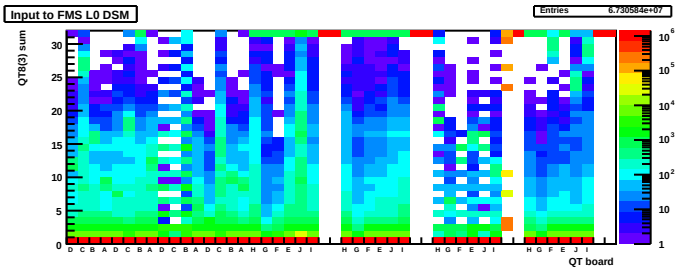

Input to FMS L0 DSM
Entries 24716
QT8(0) sum - simulated
QT board

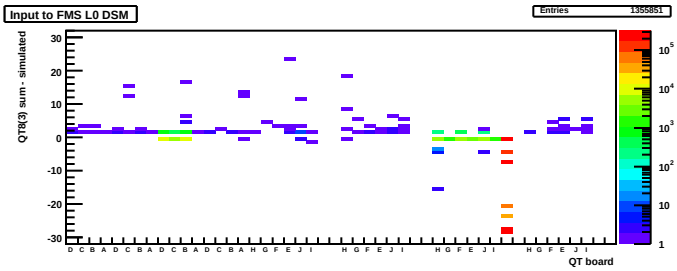

Input to FMS L0 DSM
Entries 6.730584e+07
QT8(1) sum
QT board

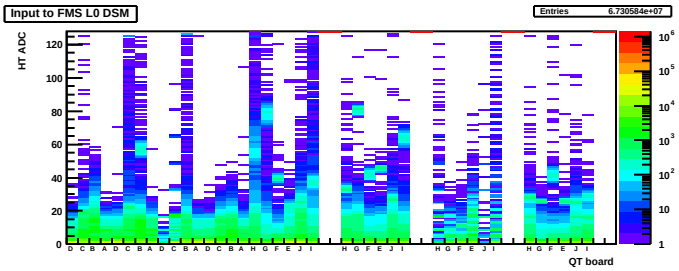

Input to FMS L0 DSM
Entries 29010
QT8(1) sum - simulated
QT board

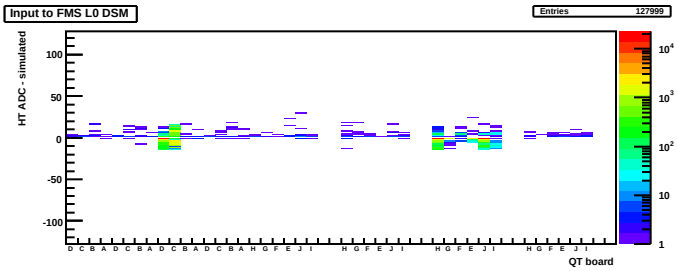

Input to FMS L0 DSM
Entries 6.730584e+07
QT8(2) sum
QT board

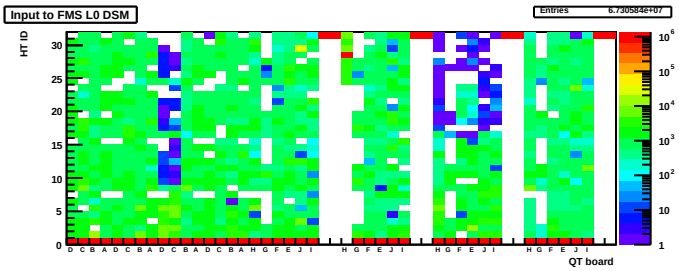

Input to FMS L0 DSM
Entries 1012859
QT8(2) sum - simulated
QT board

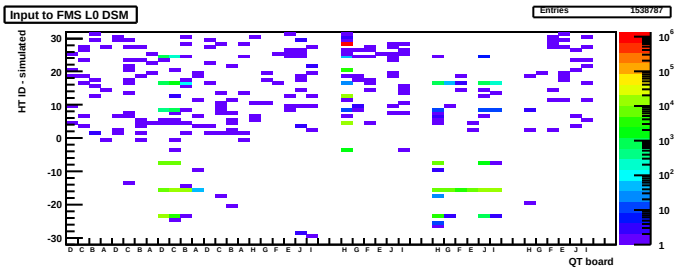

Input to FMS L0 DSM
Entries 6.730584e+07
QT8(3) sum
QT board

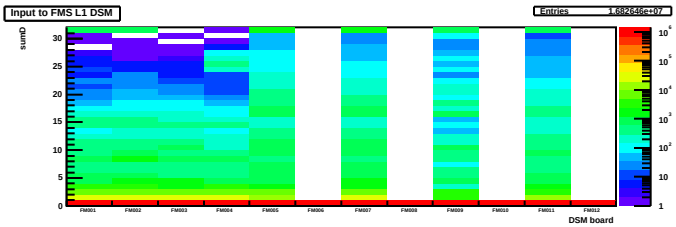

Input to FMS L0 DSM
Entries 1355851
QT8(3) sum - simulated
QT board

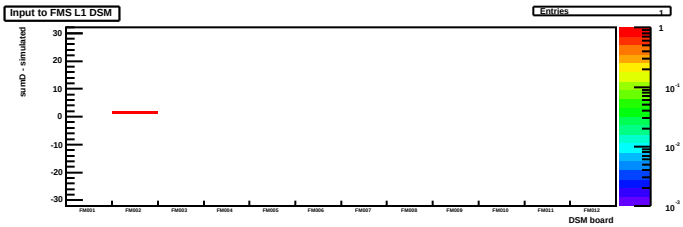

Input to FMS L0 DSM
Entries 6.730584e+07
HT ADC
QT board

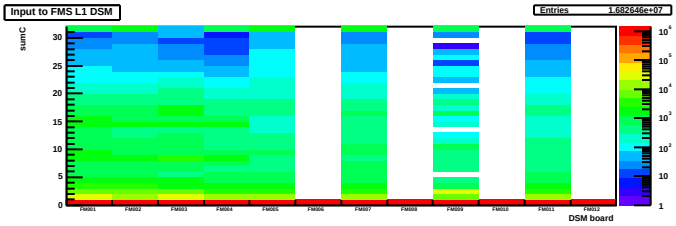

Input to FMS L0 DSM
Entries 127999
HT ADC - simulated
QT board

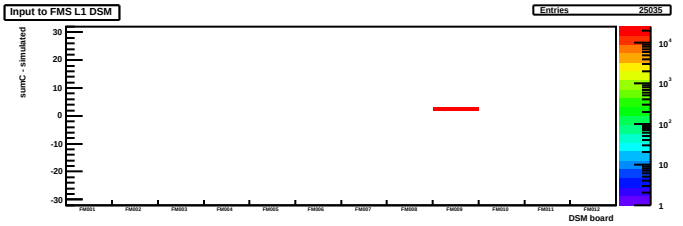

Input to FMS L0 DSM
Entries 6.730584e+07
HT ID
QT board

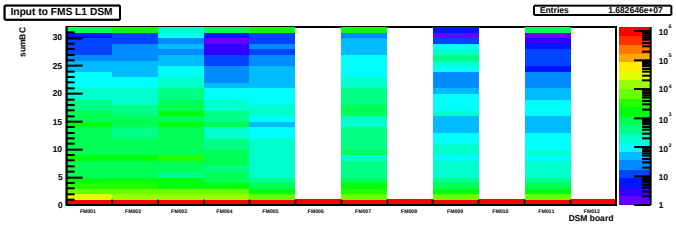

Input to FMS L0 DSM
Entries 1538787
HT ID - simulated
QT board

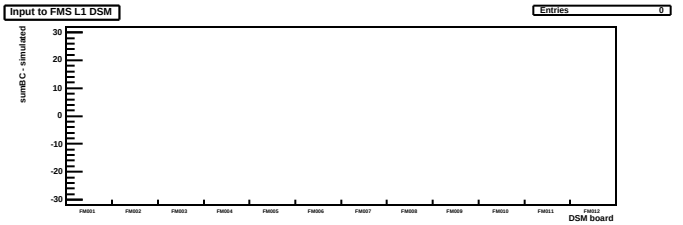

Input to FMS L1 DSM
Entries 1.682646e+07
sumD
DSM board

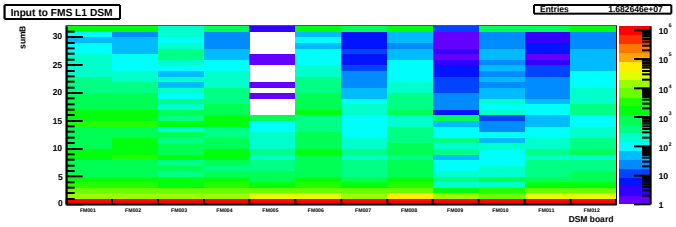

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

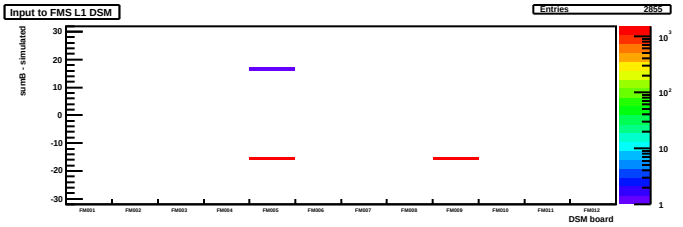

Input to FMS L1 DSM
Entries 1.682646e+07
sumC
DSM board

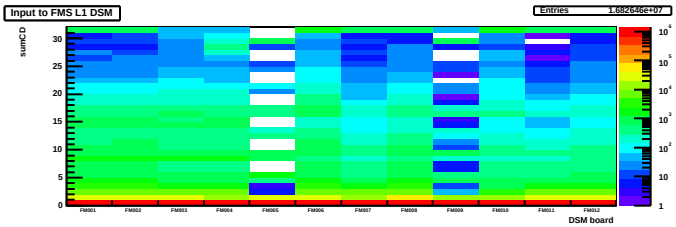

Input to FMS L1 DSM
Entries 25035
sumC - simulated
DSM board

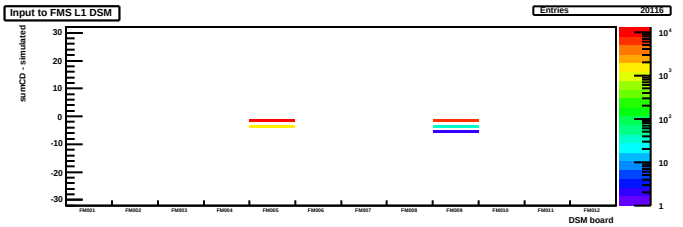

Input to FMS L1 DSM
Entries 1.682646e+07
sumBC
DSM board

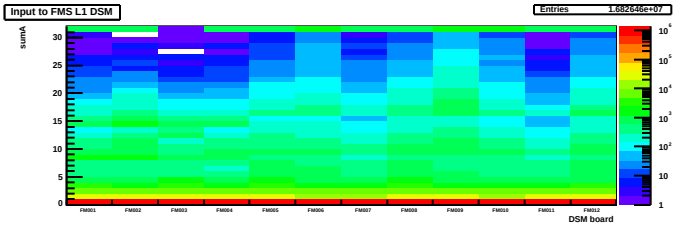

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

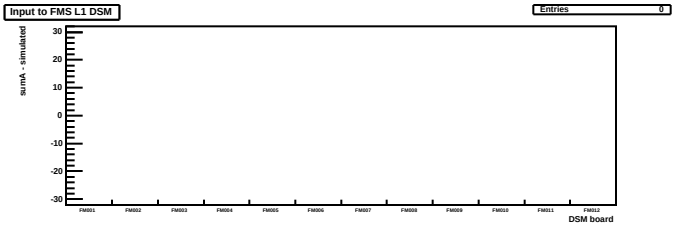

Input to FMS L1 DSM
Entries 1.682646e+07
sumB
DSM board

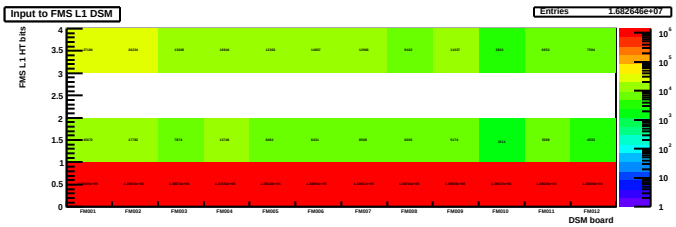

Input to FMS L1 DSM
Entries 2855
sumB - simulated
DSM board

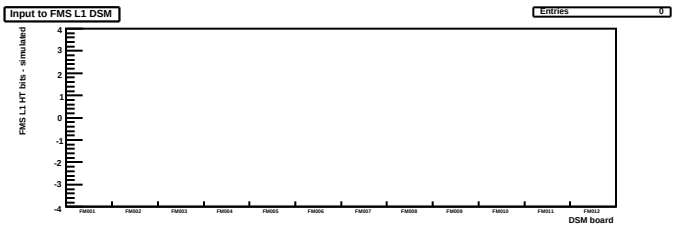

Input to FMS L1 DSM
Entries 1.682646e+07
sumCD
DSM board

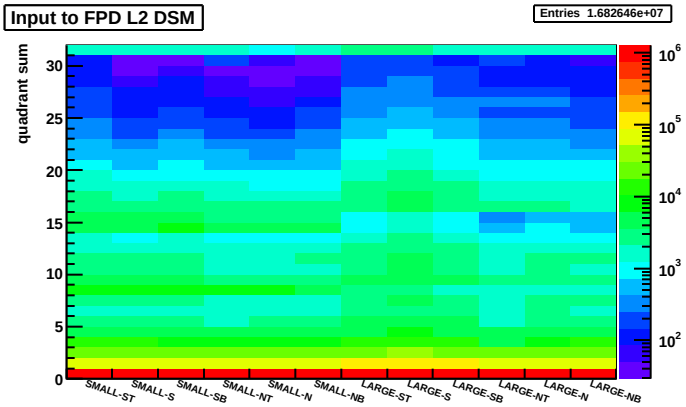

Input to FMS L1 DSM
Entries 20116
sumCD - simulated
DSM board

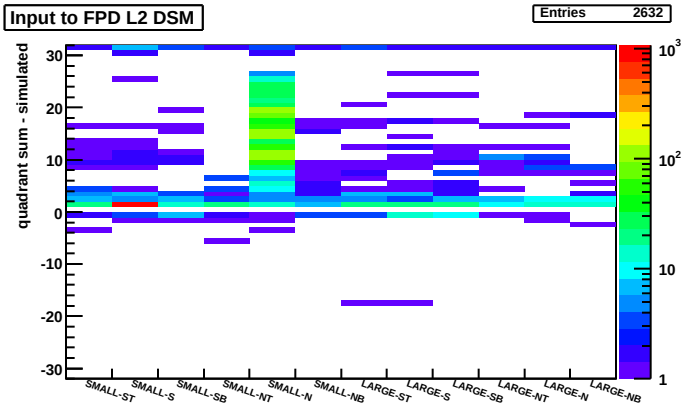

Input to FMS L1 DSM
Entries 1.682646e+07
sumA
DSM board

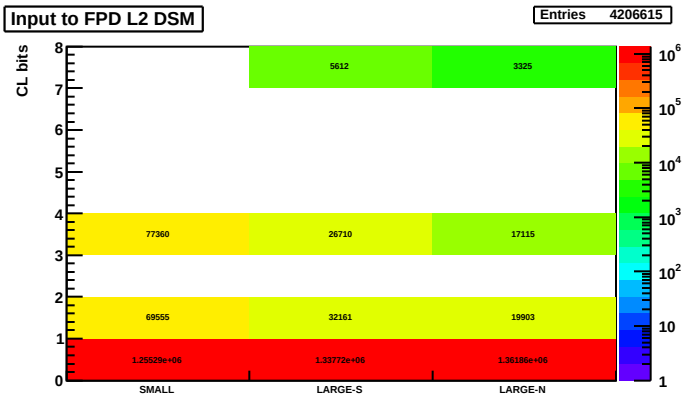

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

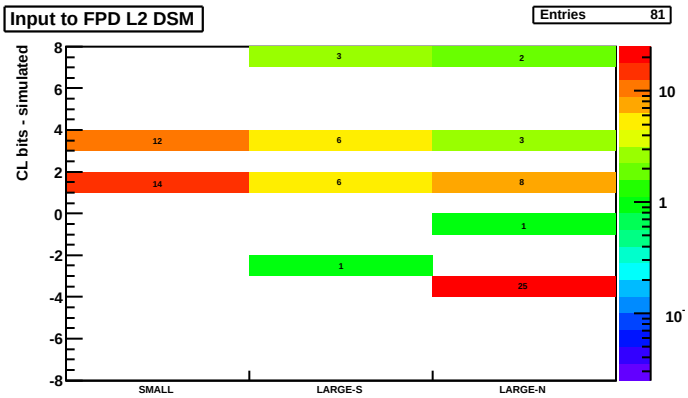

Input to FMS L1 DSM
Entries 1.682646e+07
FMS L1 HT bits
DSM board

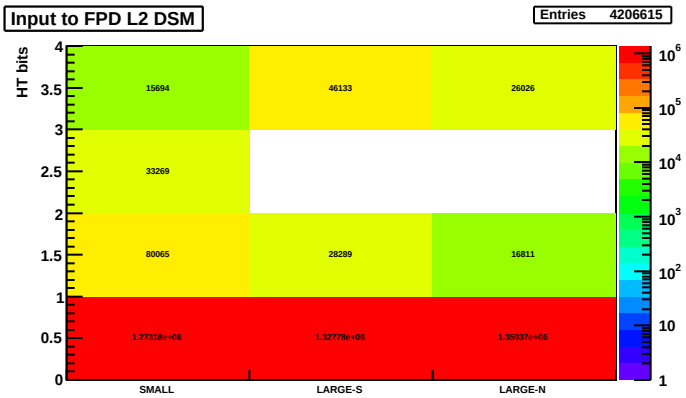

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

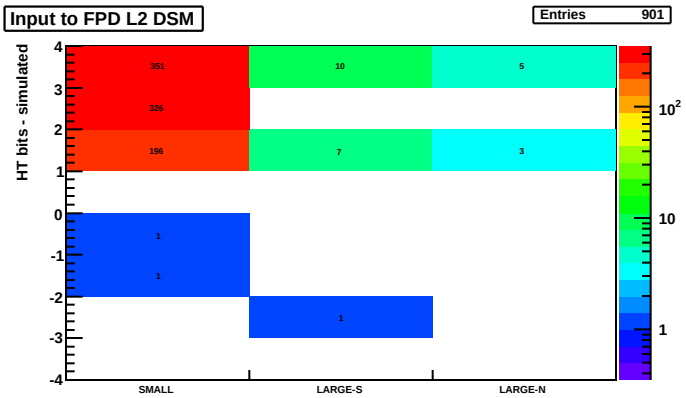

Input to FPD L2 DSM
Entries 1.682646e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

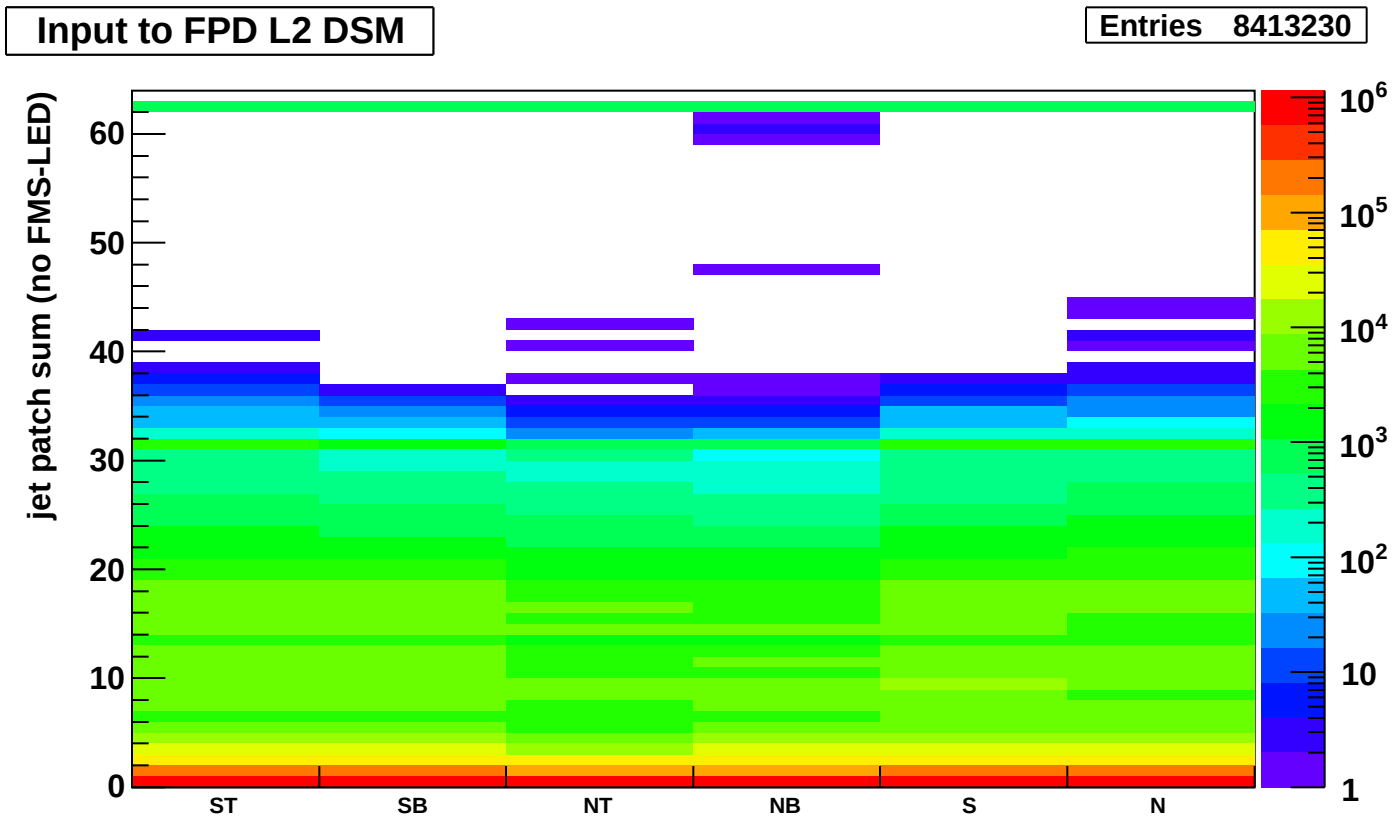

Input to FPD L2 DSM
Entries 2632
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

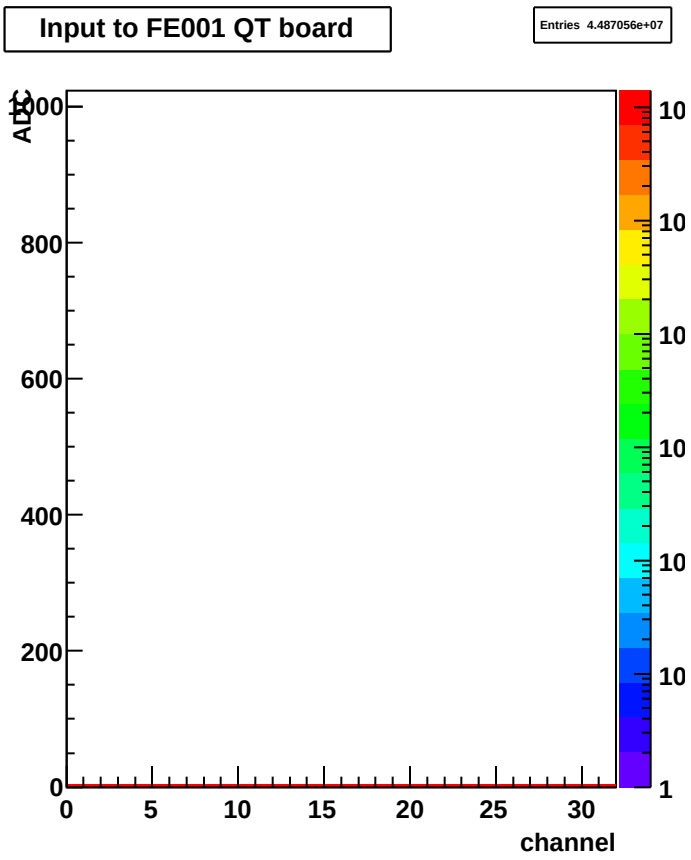

Input to FPD L2 DSM
Entries 4206615
CL bits
5612
3325
77360
26710
17115
69555
32161
19903
1.25529e+06
1.33772e+06
1.36186e+06
SMALL
LARGE-S
LARGE-N

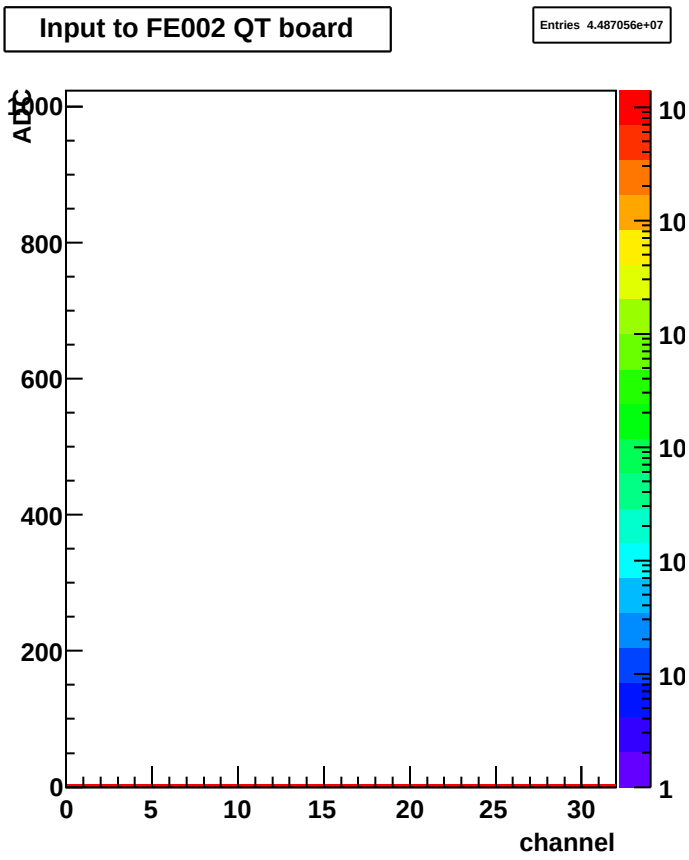

Input to FPD L2 DSM
Entries 81
CL bits - simulated
3
2
12
6
3
14
6
8
1
1
25
SMALL
LARGE-S
LARGE-N

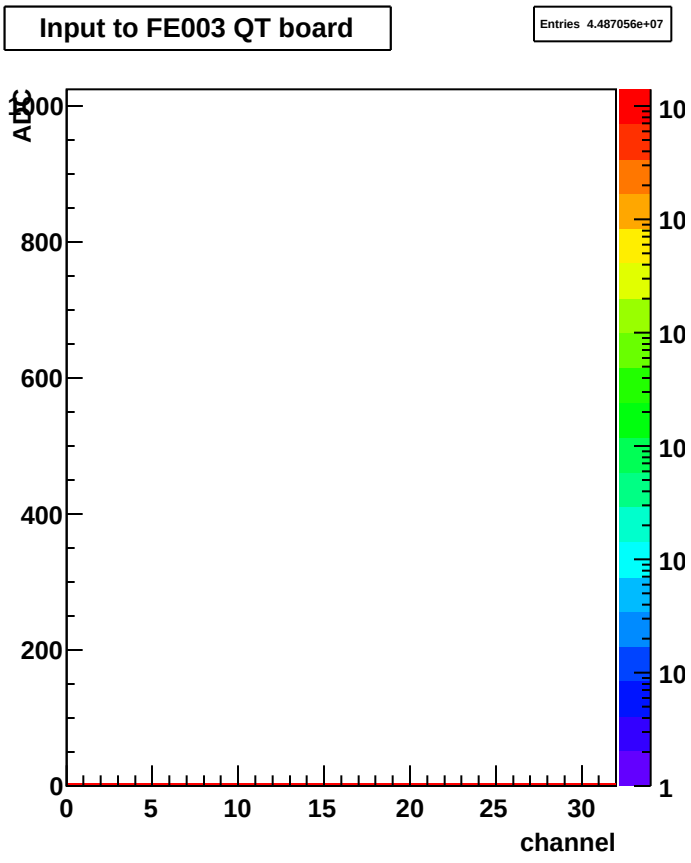

Input to FPD L2 DSM
Entries 4206615
HT bits
15694
46133
26026
33269
80065
28289
16811
1.27318e+06
1.32778e+06
1.35937e+06
SMALL
LARGE-S
LARGE-N

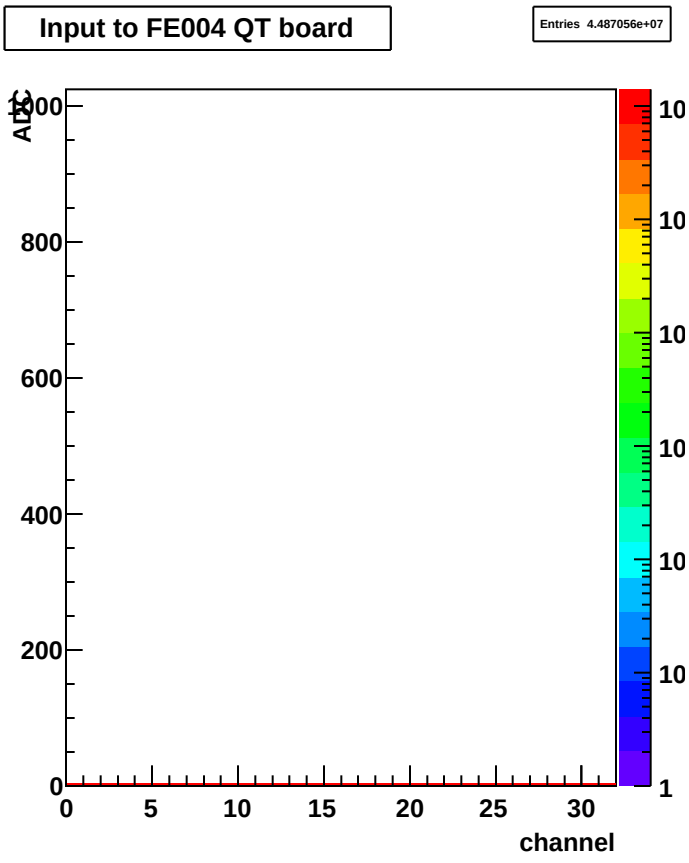

Input to FPD L2 DSM
Entries 901
HT bits - simulated
351
10
5
326
196
7
3
1
1
1
SMALL
LARGE-S
LARGE-N

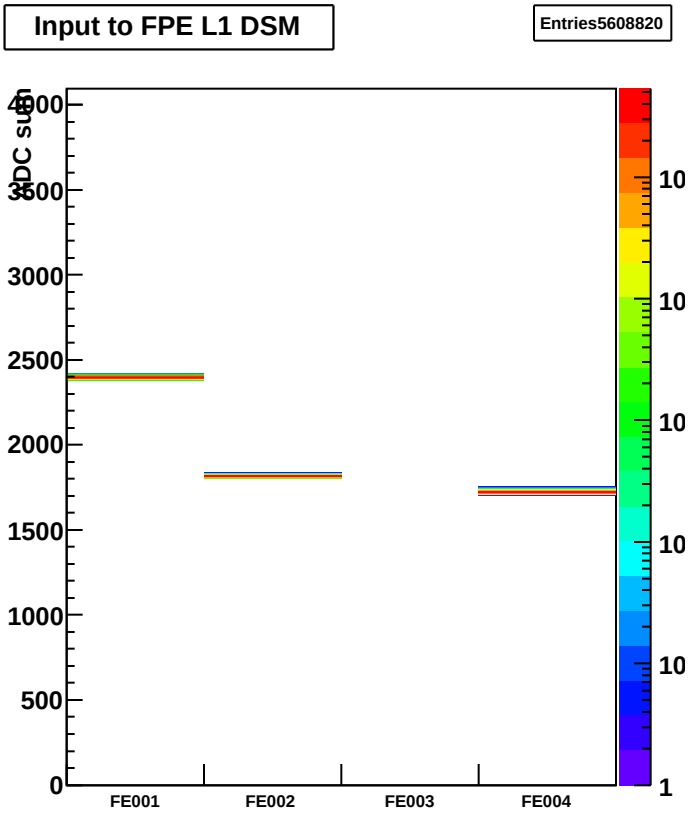

Input to FPD L2 DSM
Entries 8413230
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

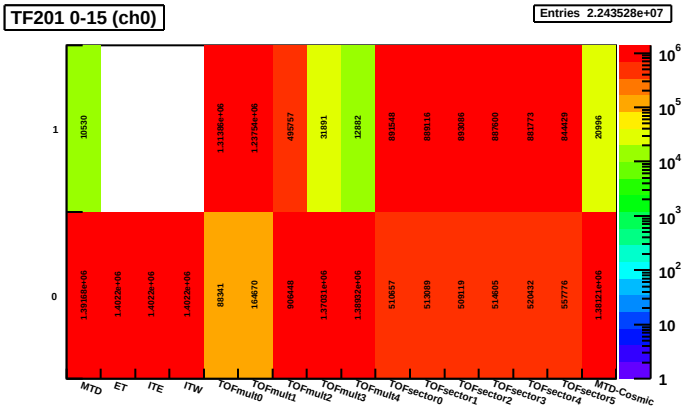

Input to FE001 QT board
Entries 4.487056e+07
ADC
channel

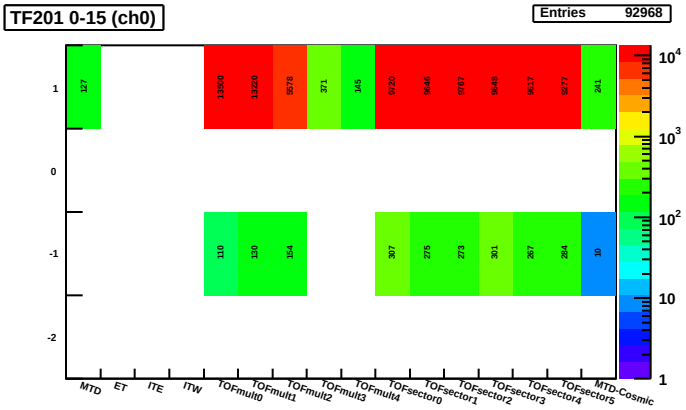

Input to FE002 QT board
Entries 4.487056e+07
ADC
channel

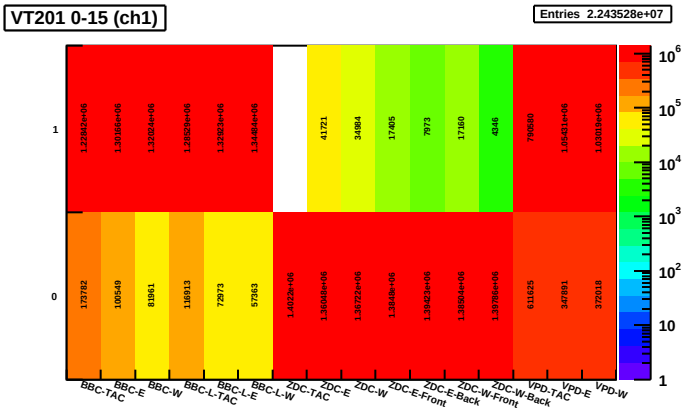

Input to FE003 QT board
Entries 4.487056e+07
ADC
channel

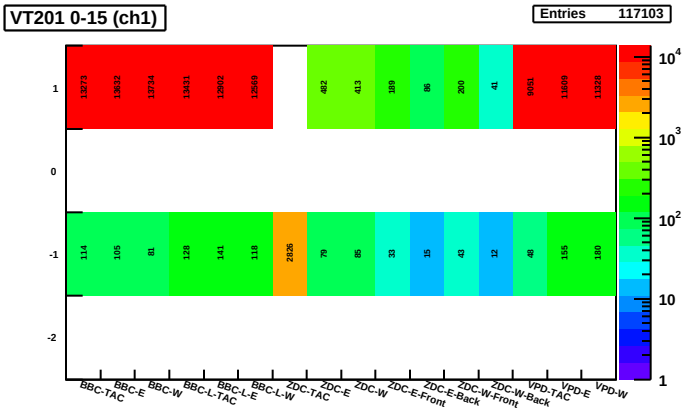

Input to FE004 QT board
Entries 4.487056e+07
ADC
channel

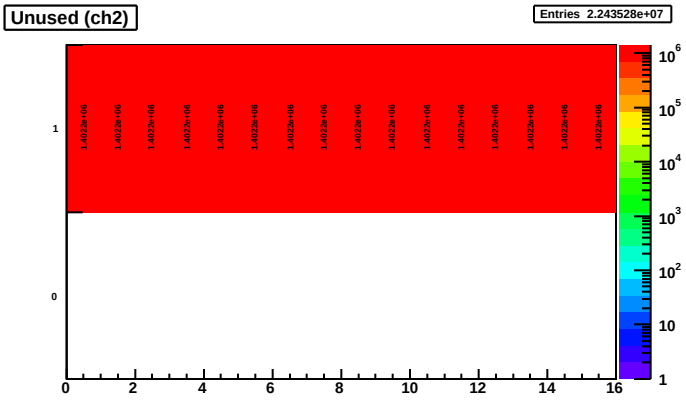

Input to FPE L1 DSM
Entries5608820
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

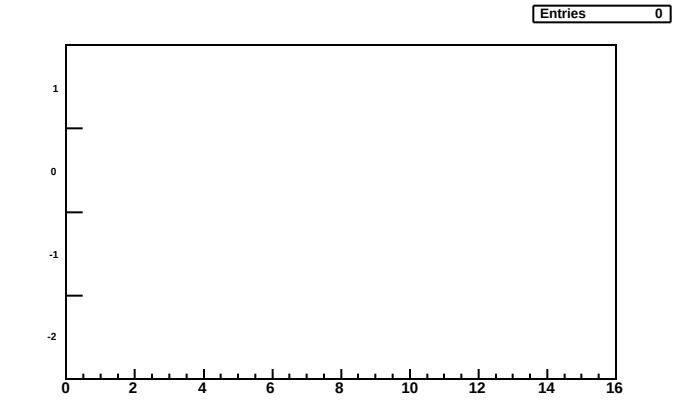

TF201 0-15 (ch0)
Entries 2.243528e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic

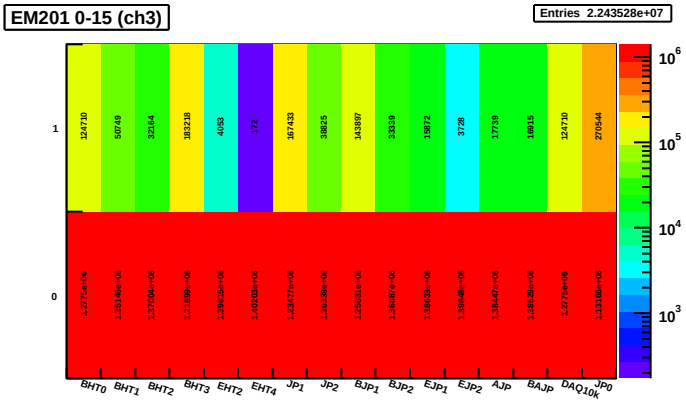

TF201 0-15 (ch0)
Entries 92968
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5
MTD-Cosmic

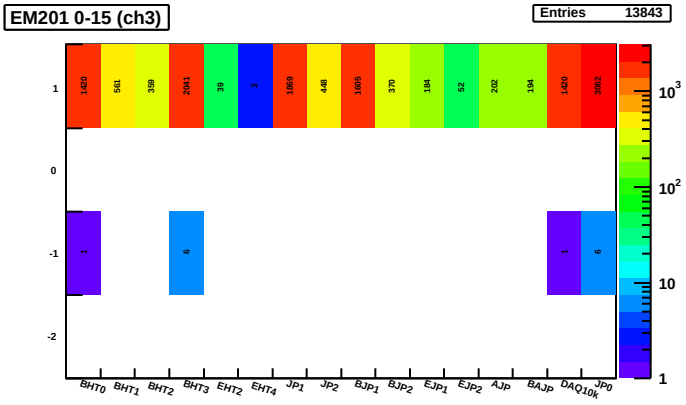

VT201 0-15 (ch1)
Entries 2.243528e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-E-Front
ZDC-E-Back
ZDC-W-Front
ZDC-W-Back
VPD-TAC
VPD-E
VPD-W


VT201 0-15 (ch1)
Entries 117103
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-E-Front
ZDC-E-Back
ZDC-W-Front
ZDC-W-Back
VPD-TAC
VPD-E
VPD-W

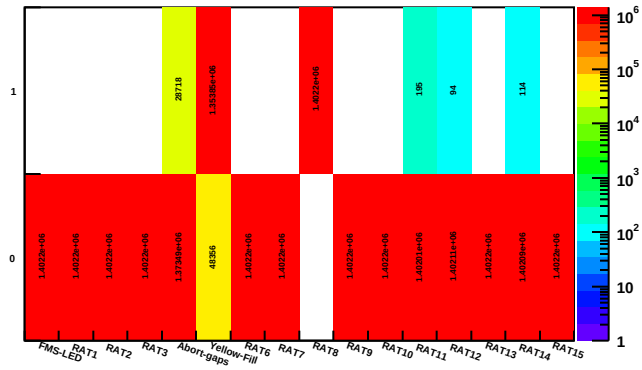

Unused (ch2)
Entries 2.243528e+07

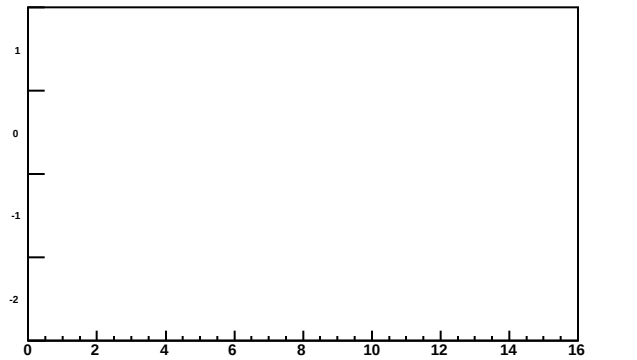

Entries 0


EM201 0-15 (ch3)
Entries 2.243528e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
DAQ10k
JP0

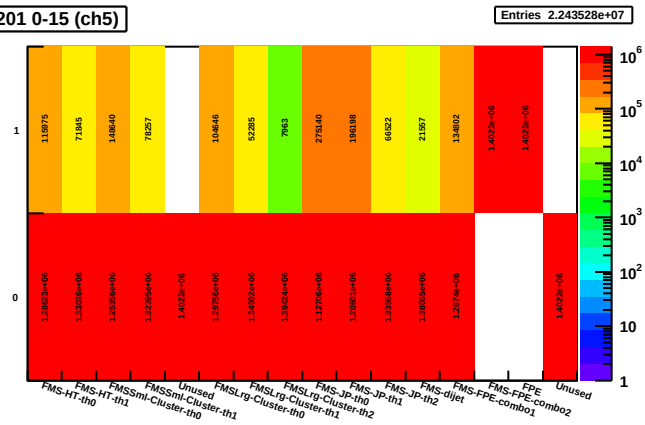

EM201 0-15 (ch3)
Entries 13843
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
DAQ10k
JP0

RAT board (ch4)

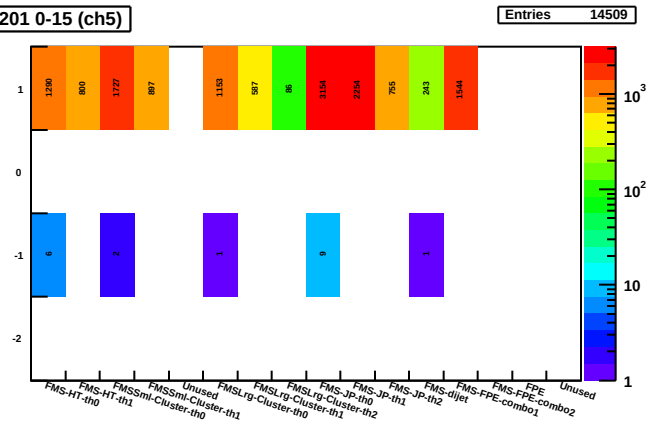




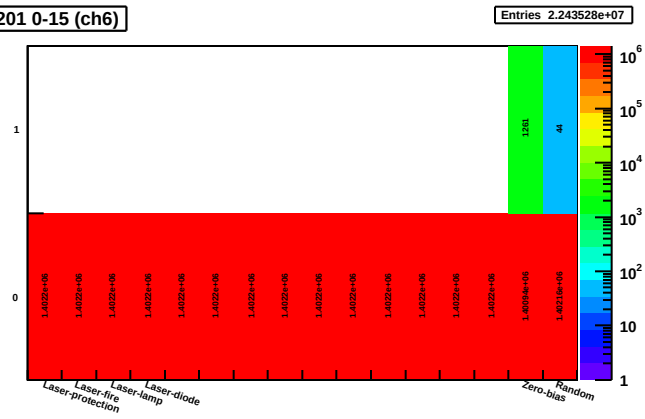
FP201 0-15 (ch5)

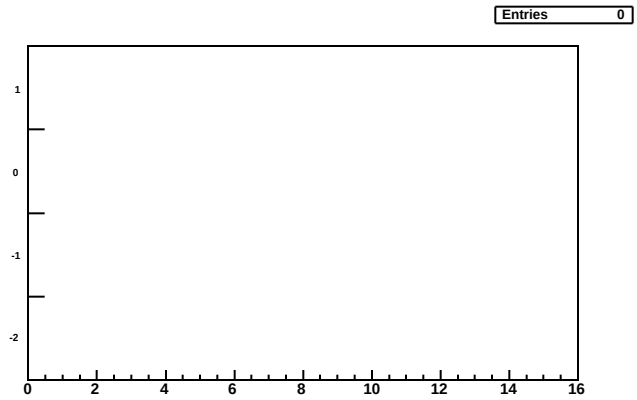
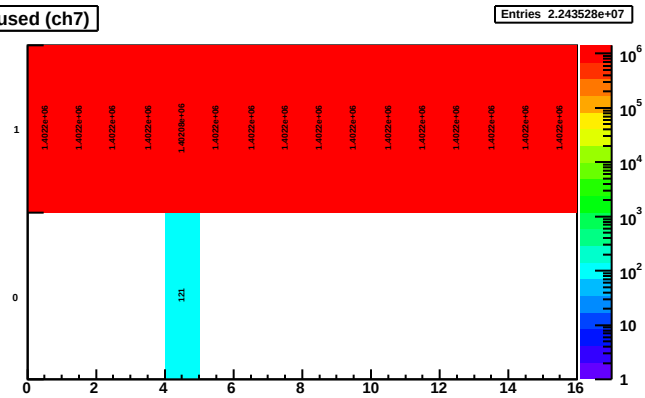
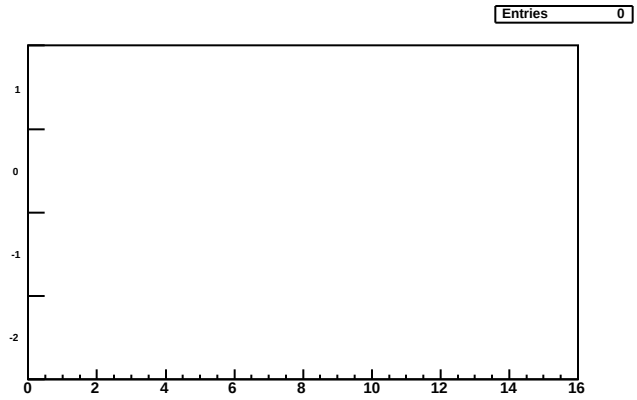


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)