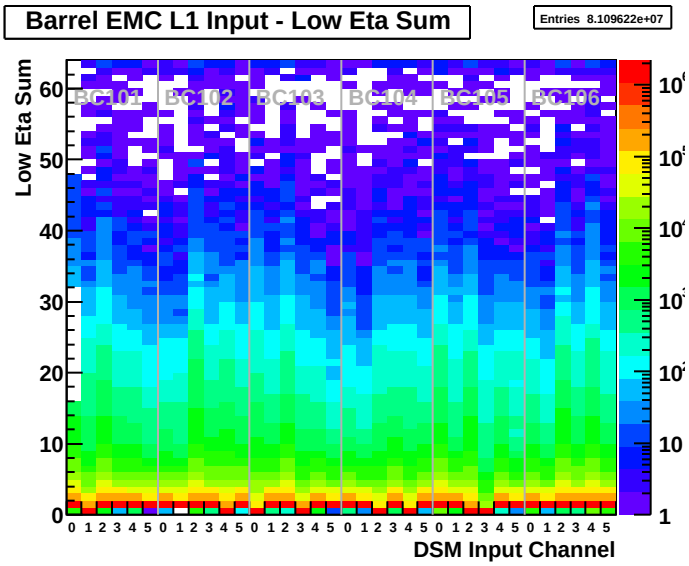
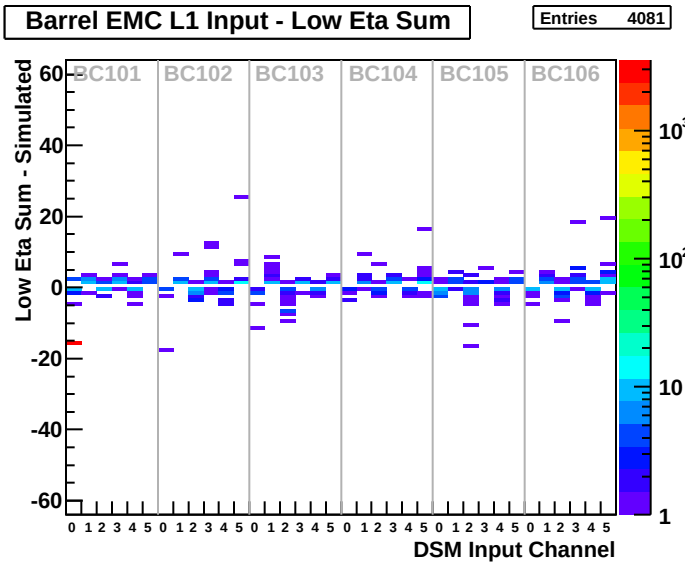
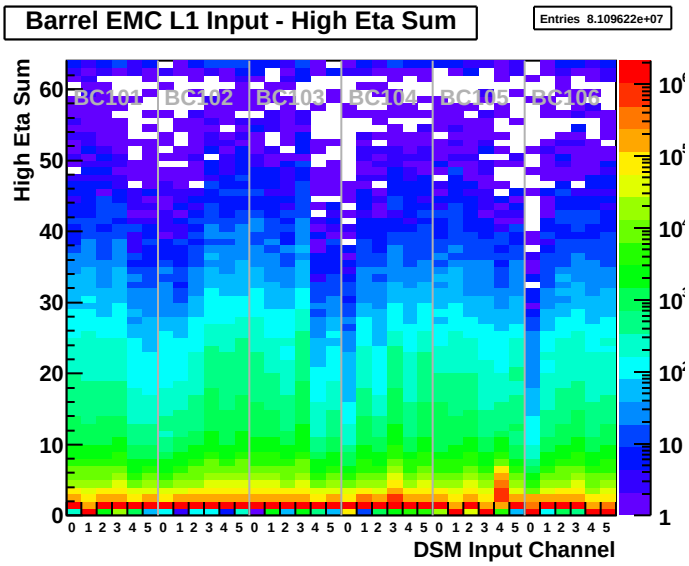
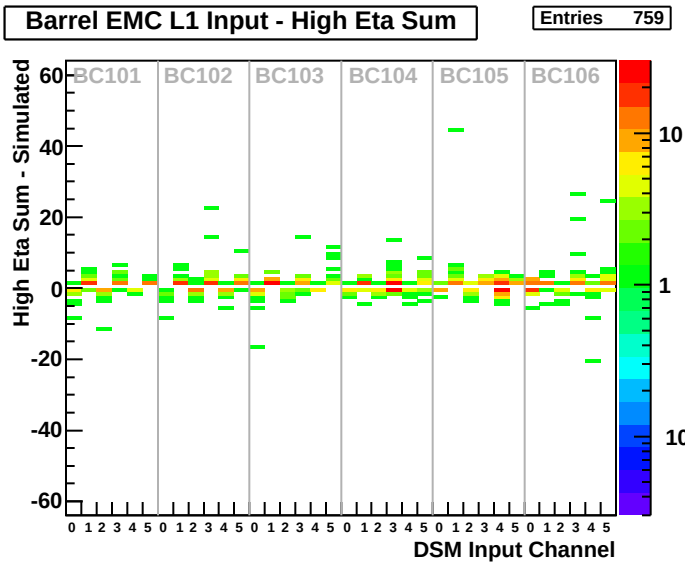
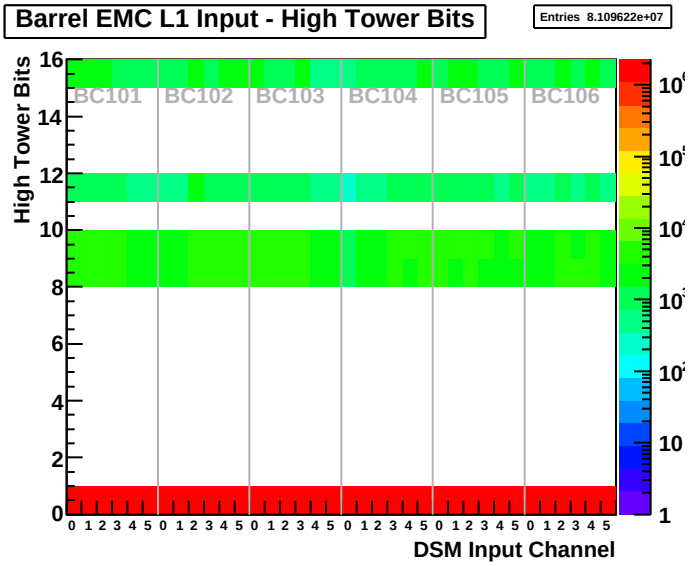
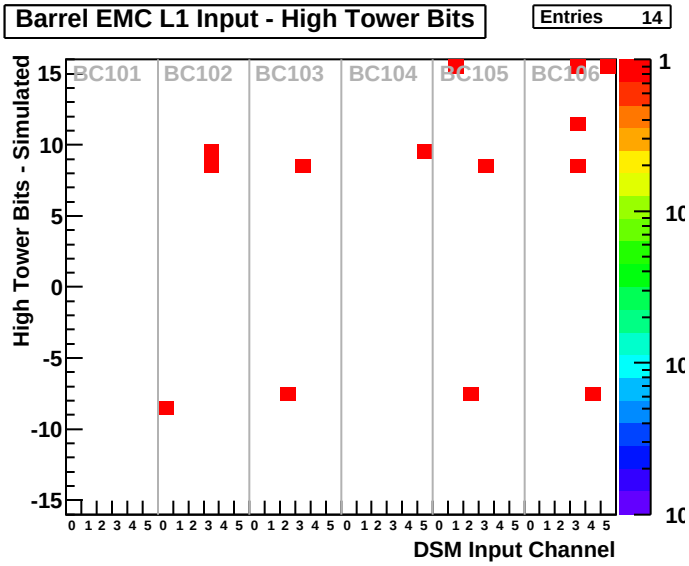

Barrel EMC L1 Input - Low Eta Sum
Entries 8.109622e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

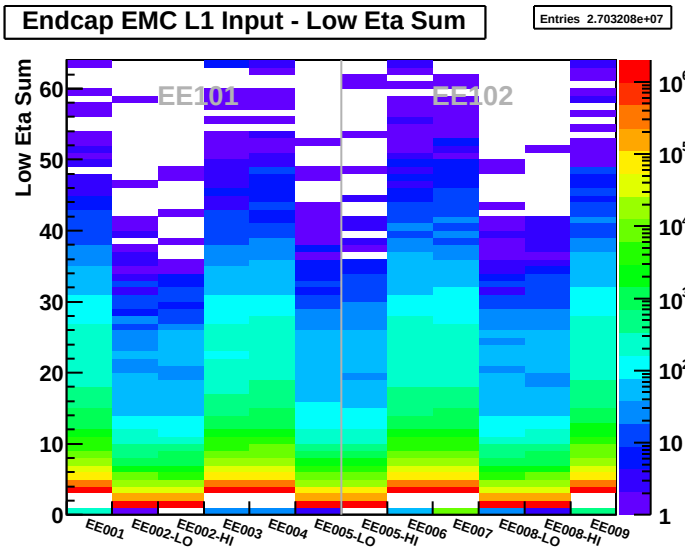

Barrel EMC L1 Input - Low Eta Sum
Entries 4081
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

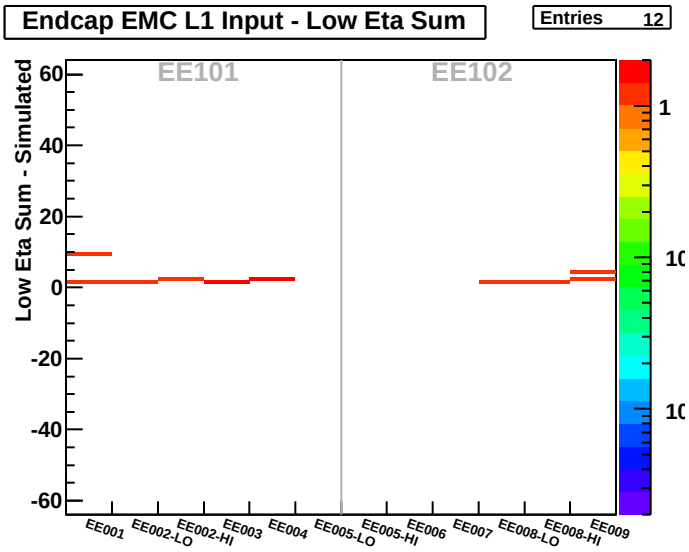

Barrel EMC L1 Input - High Eta Sum
Entries 8.109622e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

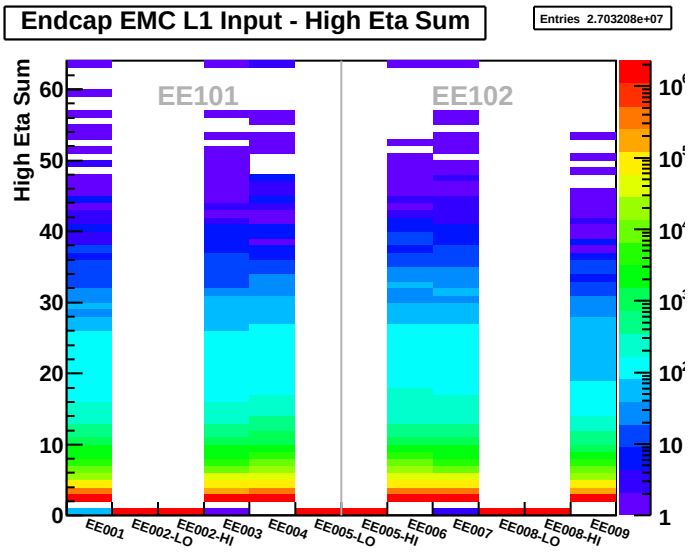

Barrel EMC L1 Input - High Eta Sum
Entries 759
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

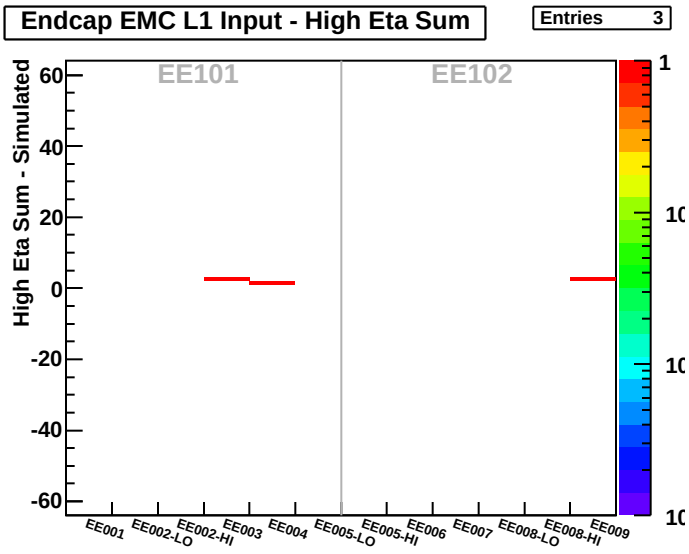

Barrel EMC L1 Input - High Tower Bits
Entries 8.109622e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

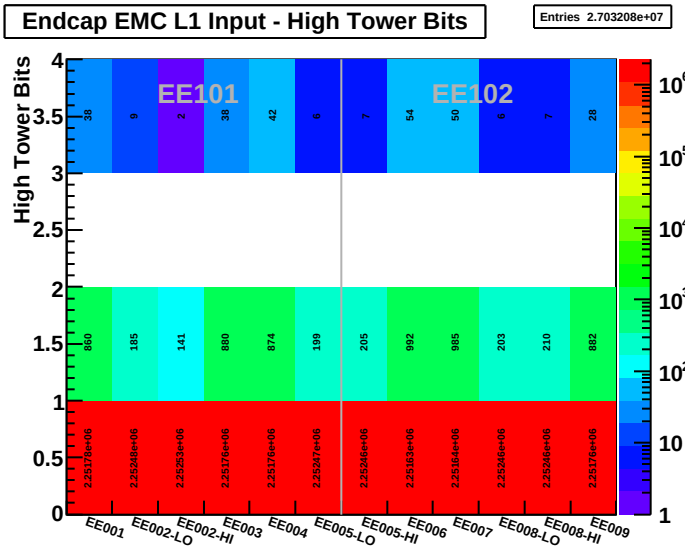

Barrel EMC L1 Input - High Tower Bits
Entries 14
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

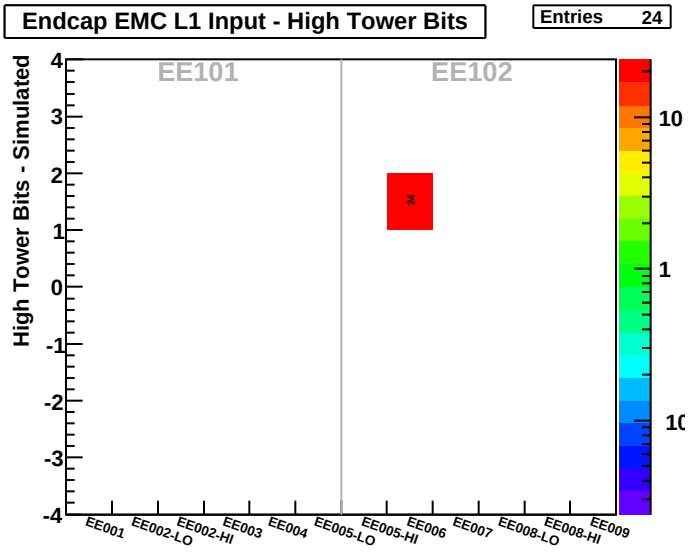

Endcap EMC L1 Input - Low Eta Sum
Entries 2.703208e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

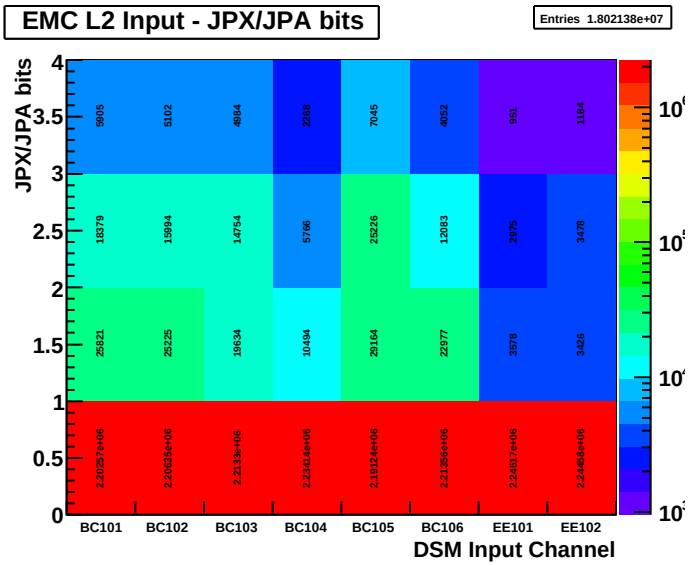

Endcap EMC L1 Input - Low Eta Sum
Entries 12
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

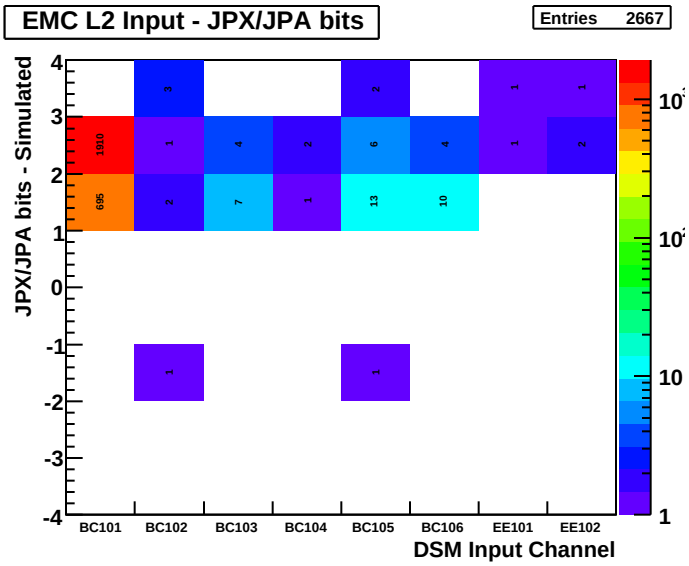

Endcap EMC L1 Input - High Eta Sum
Entries 2.703208e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

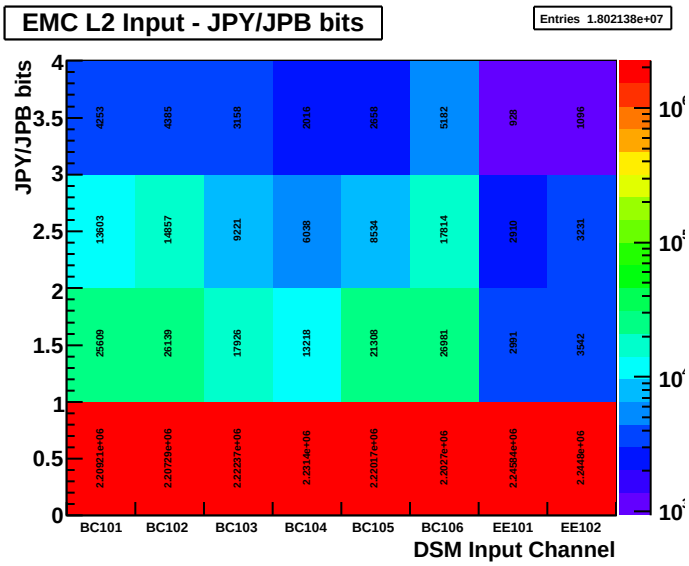

Endcap EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

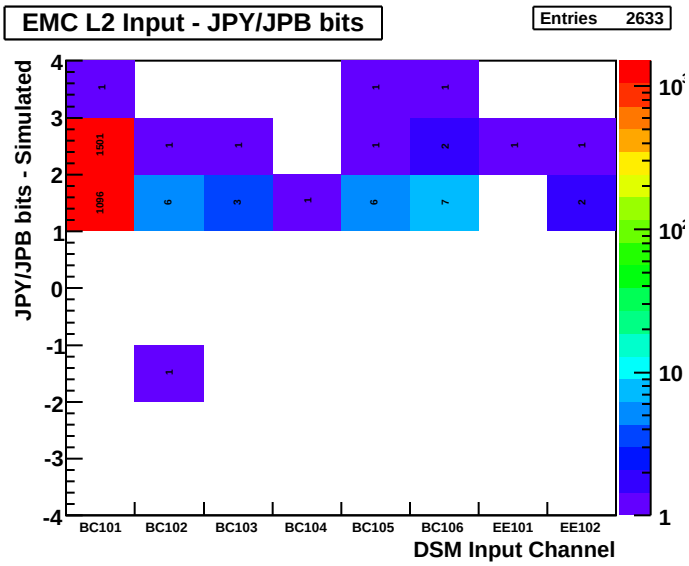

Endcap EMC L1 Input - High Tower Bits
Entries 2.703208e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

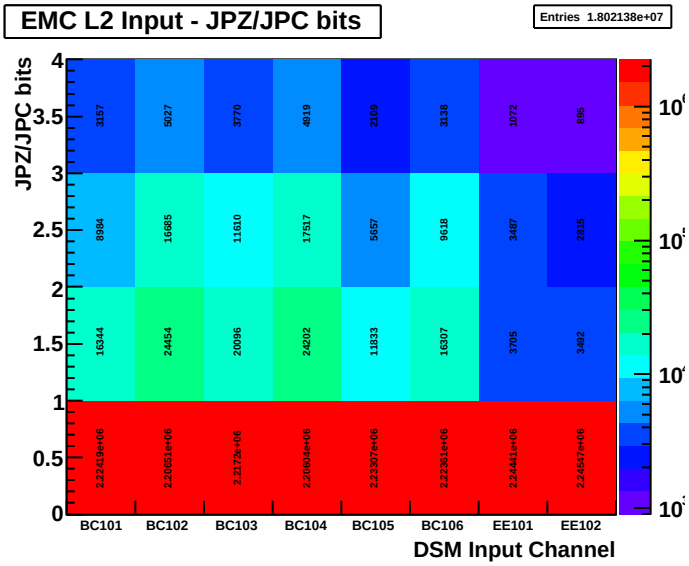

Endcap EMC L1 Input - High Tower Bits
Entries 24
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

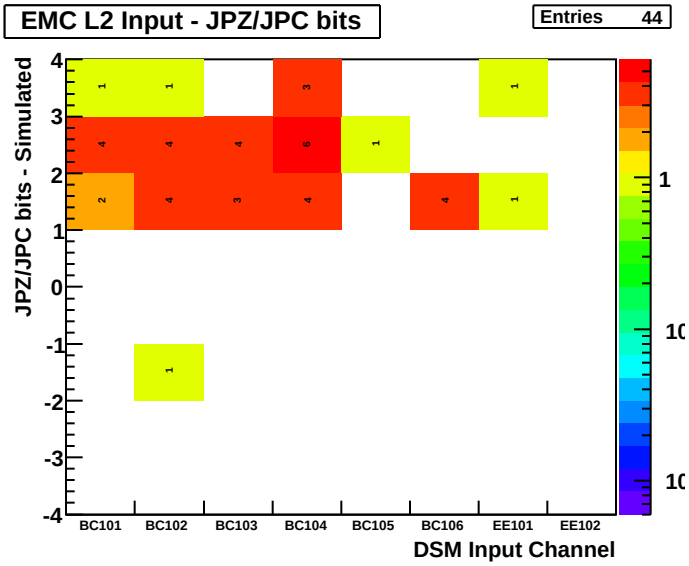

EMC L2 Input - JPX/JPA bits
Entries 1.802138e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5905 5102 4984 2268 7045 4052 951 1184
18379 15994 14754 5766 25226 12083 2975 3478
25821 25225 19634 10494 29164 22977 3578 3426
2.20257e+06 2.20635e+06 2.2133e+06 2.23414e+06 2.19124e+06 2.21356e+06 2.24517e+06 2.24458e+06

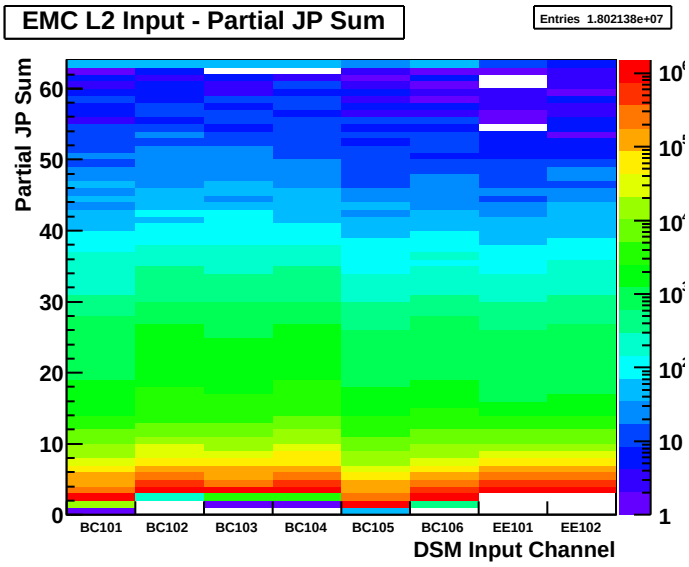

EMC L2 Input - JPX/JPA bits
Entries 2667
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1910 1 4 2 6 4 1 2
695 2 7 1 13 10

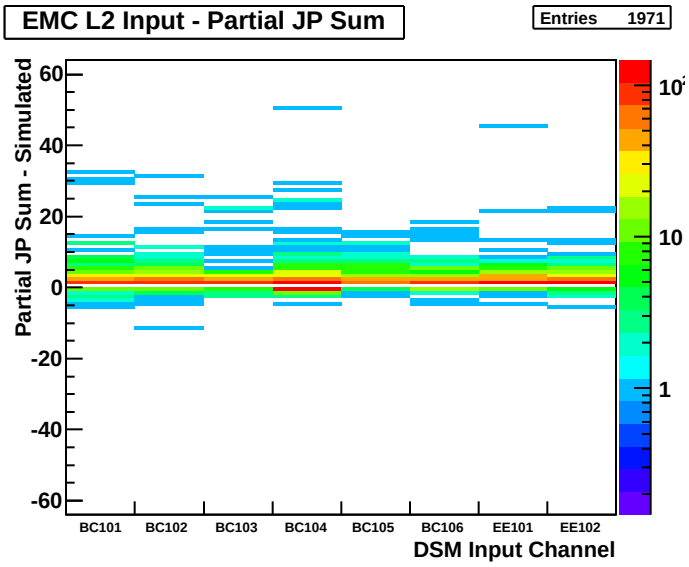

EMC L2 Input - JPY/JPB bits
Entries 1.802138e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4253 4385 3158 2016 2658 5182 928 1096
13603 14857 9221 6038 8534 17814 2910 3231
25609 26139 17926 13218 21308 26981 2991 3542
2.20921e+06 2.20729e+06 2.22237e+06 2.2314e+06 2.22017e+06 2.2027e+06 2.24584e+06 2.2448e+06

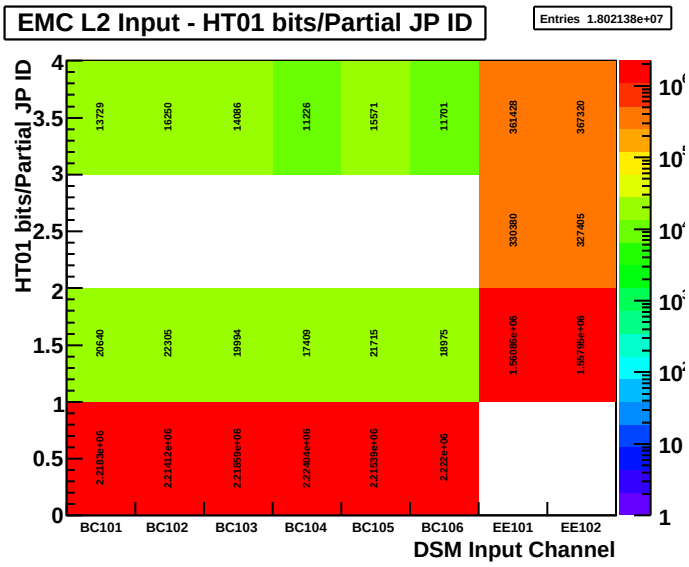

EMC L2 Input - JPY/JPB bits
Entries 2633
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1501 1 1 1 2 1 1
1096 6 3 1 6 7 2

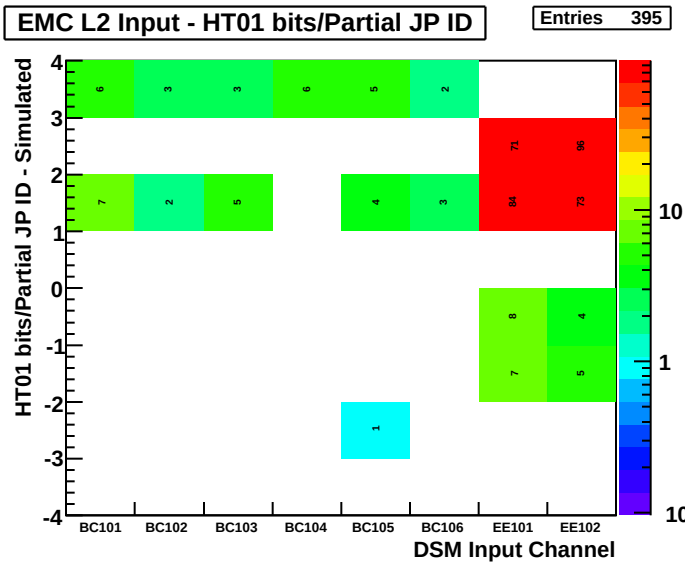

EMC L2 Input - JPZ/JPC bits
Entries 1.802138e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3157 5027 3770 4919 2109 3138 1072 895
8984 16685 11610 17517 5657 9618 3487 2815
16344 24454 20096 24202 11833 16307 3705 3492
2.22419e+06 2.20651e+06 2.2172e+06 2.20604e+06 2.23307e+06 2.22361e+06 2.24441e+06 2.24547e+06

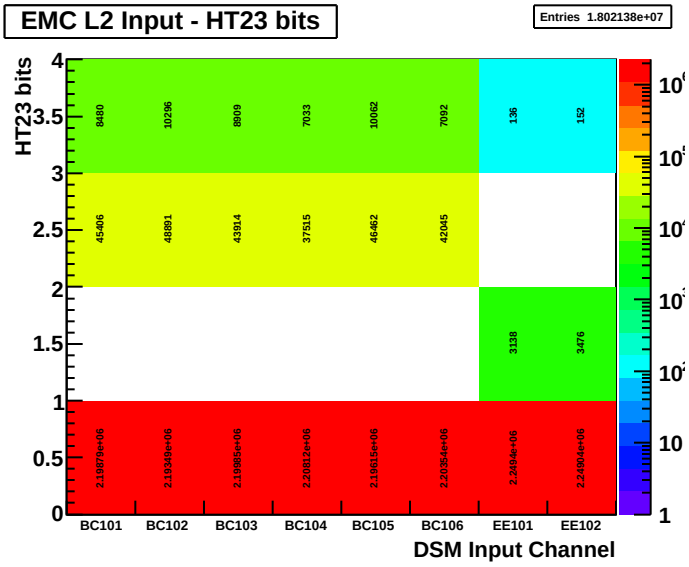

EMC L2 Input - JPZ/JPC bits
Entries 44
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 3 1
4 4 4 6 1
2 4 3 4 4 1
1

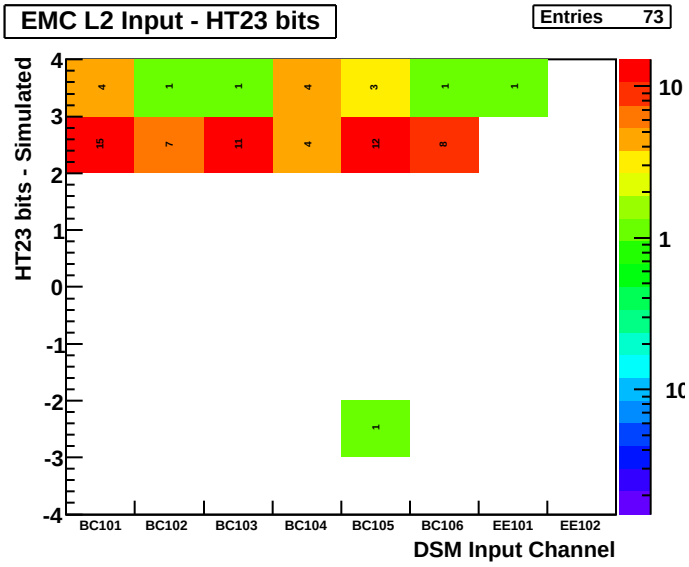

EMC L2 Input - Partial JP Sum
Entries 1.802138e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

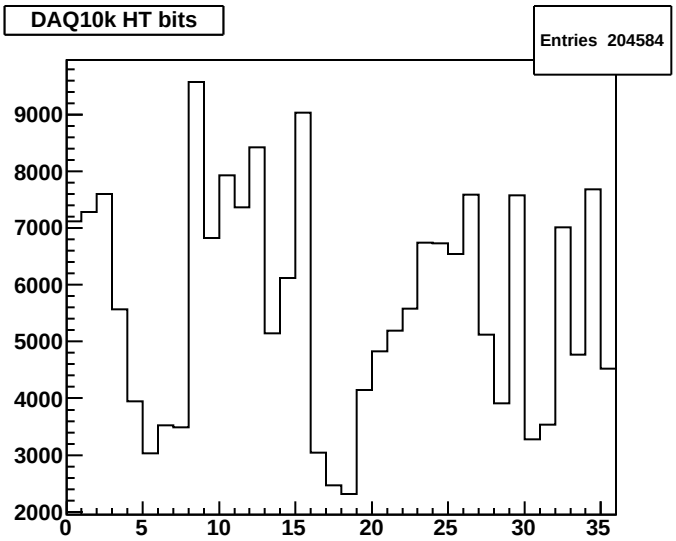

EMC L2 Input - Partial JP Sum
Entries 1971
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

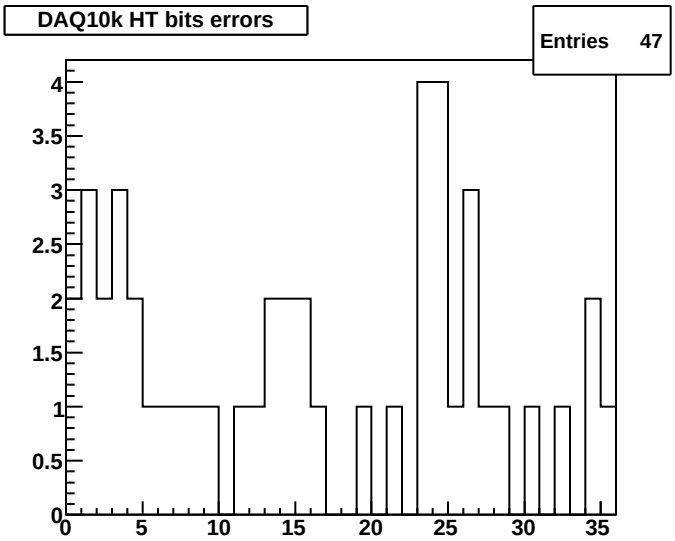

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.802138e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

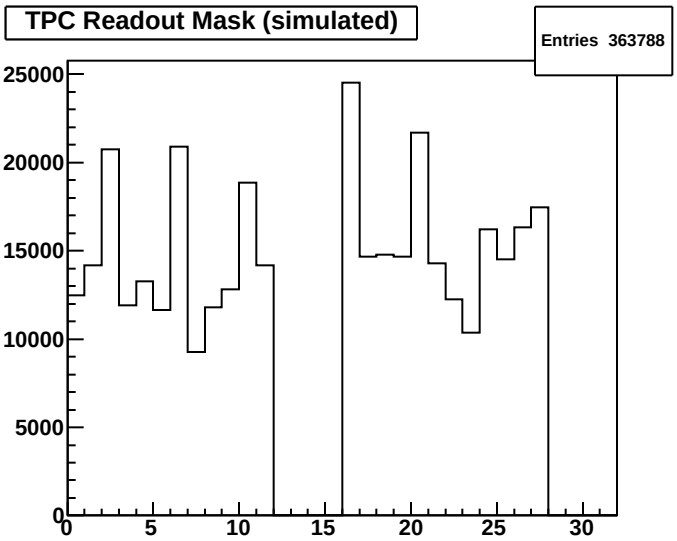

EMC L2 Input - HT01 bits/Partial JP ID
Entries 395
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

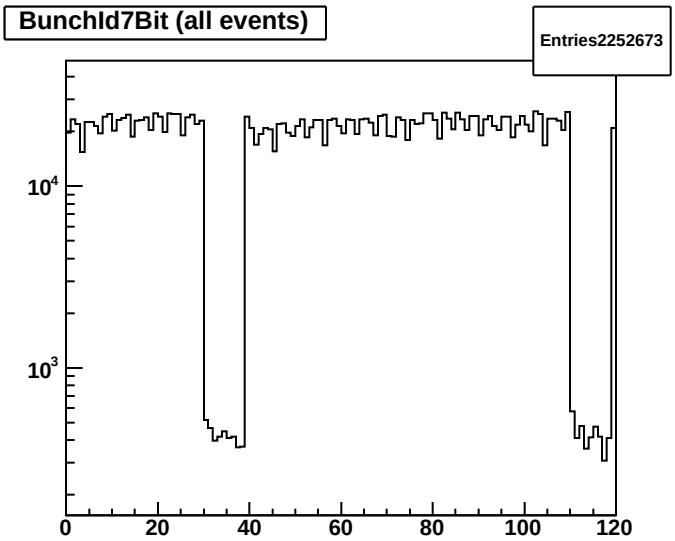

EMC L2 Input - HT23 bits
Entries 1.802138e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

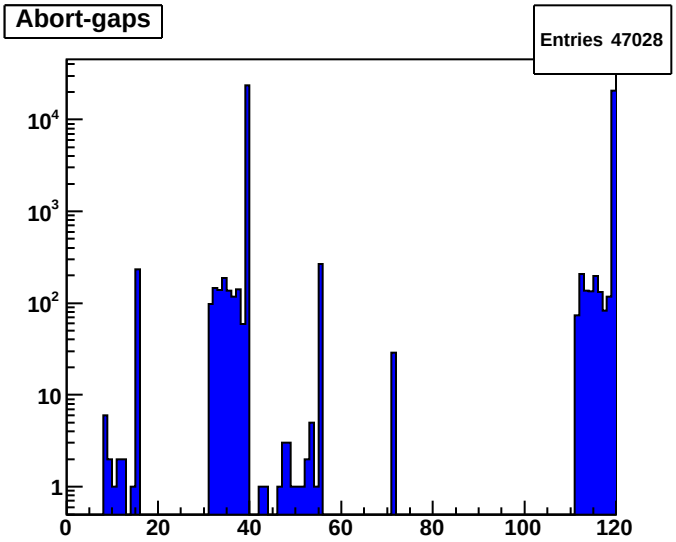

EMC L2 Input - HT23 bits
Entries 73
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

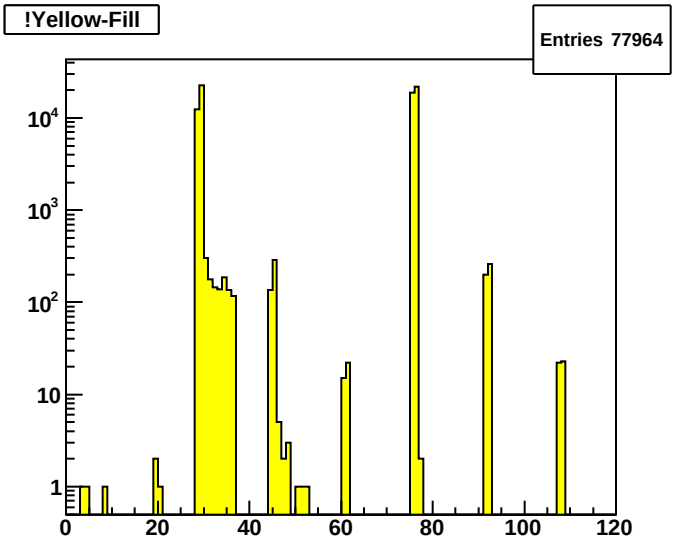

DAQ10k HT bits
Entries 204584

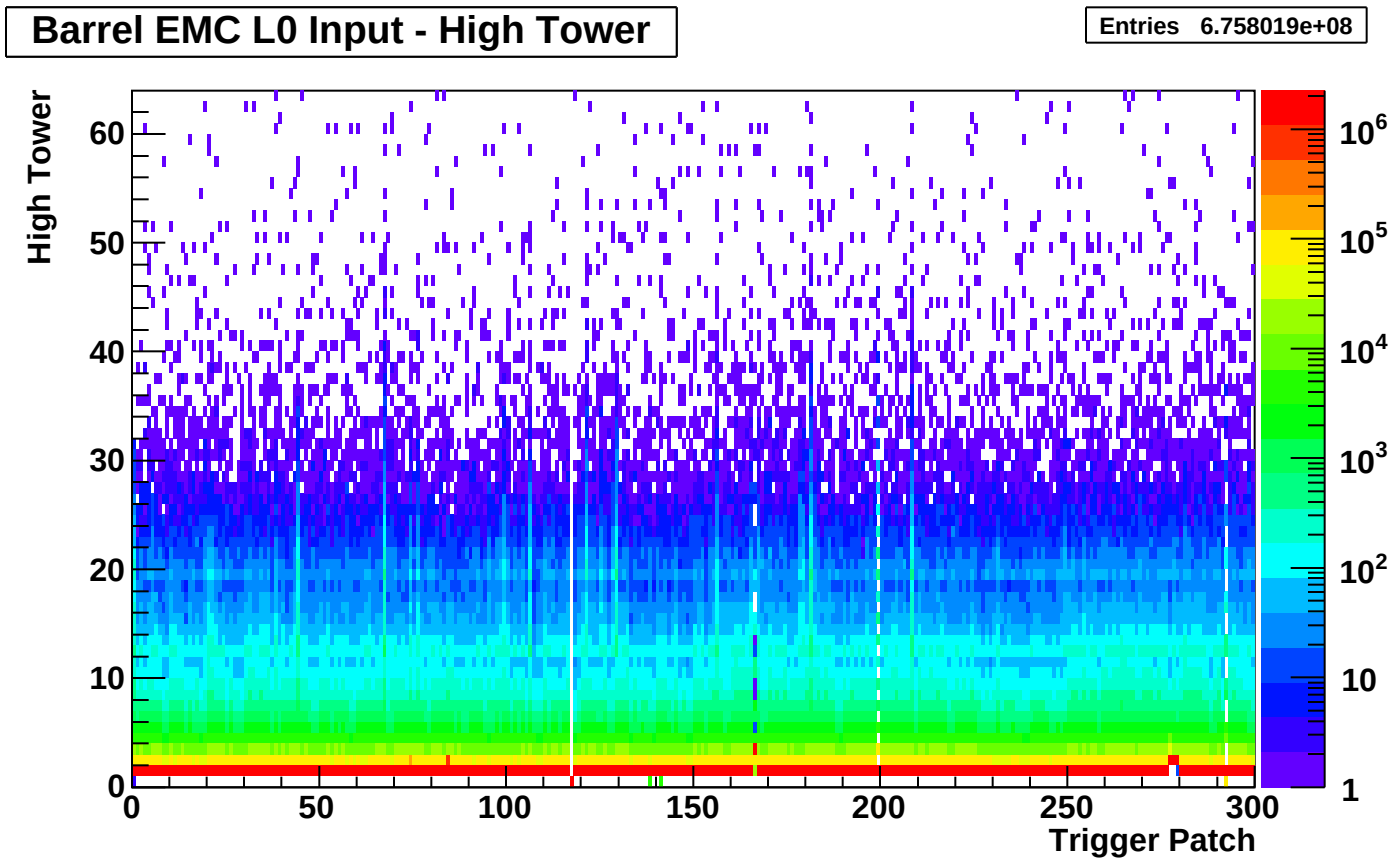

DAQ10k HT bits errors
Entries 47

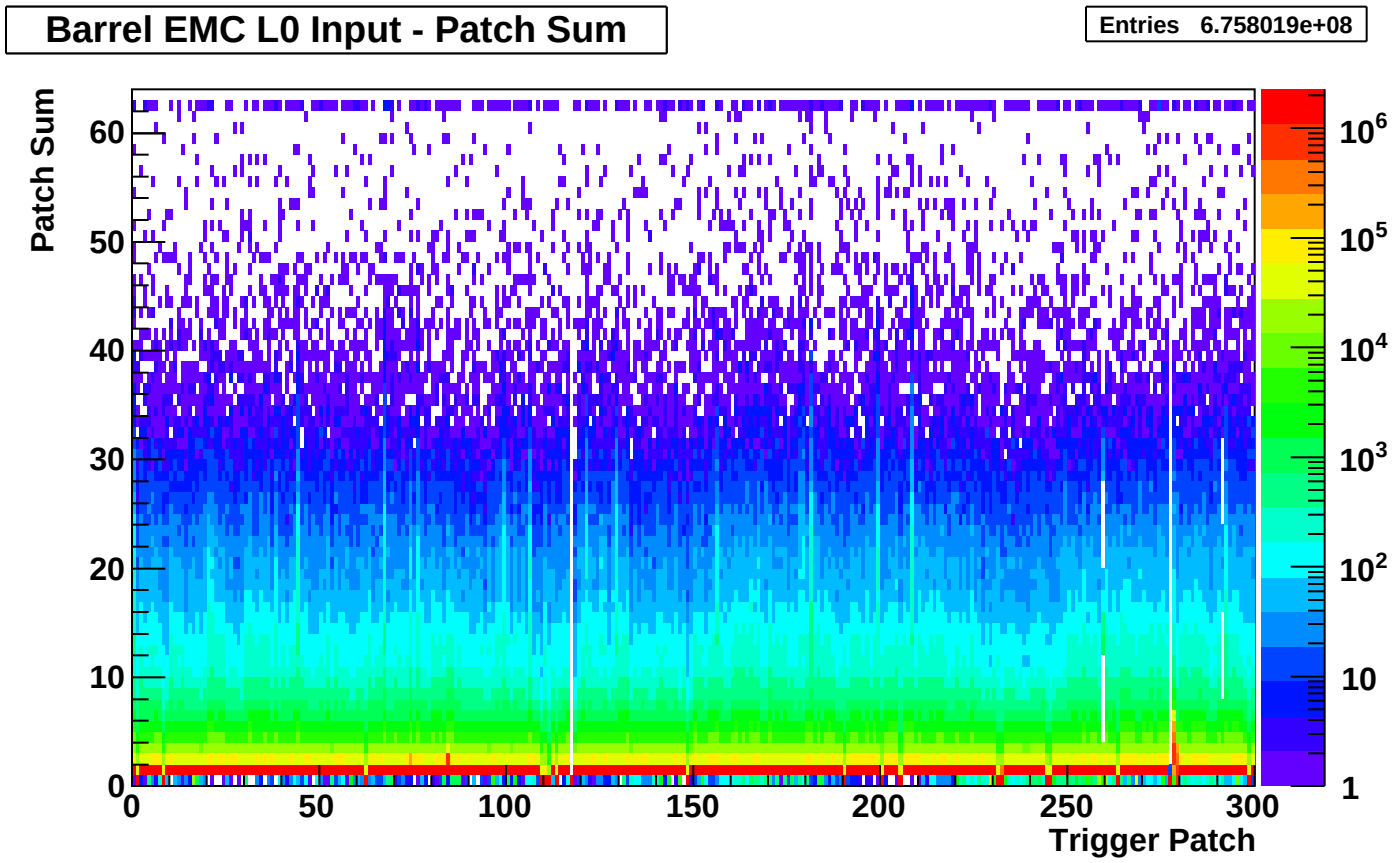

TPC Readout Mask (simulated)
Entries 363788

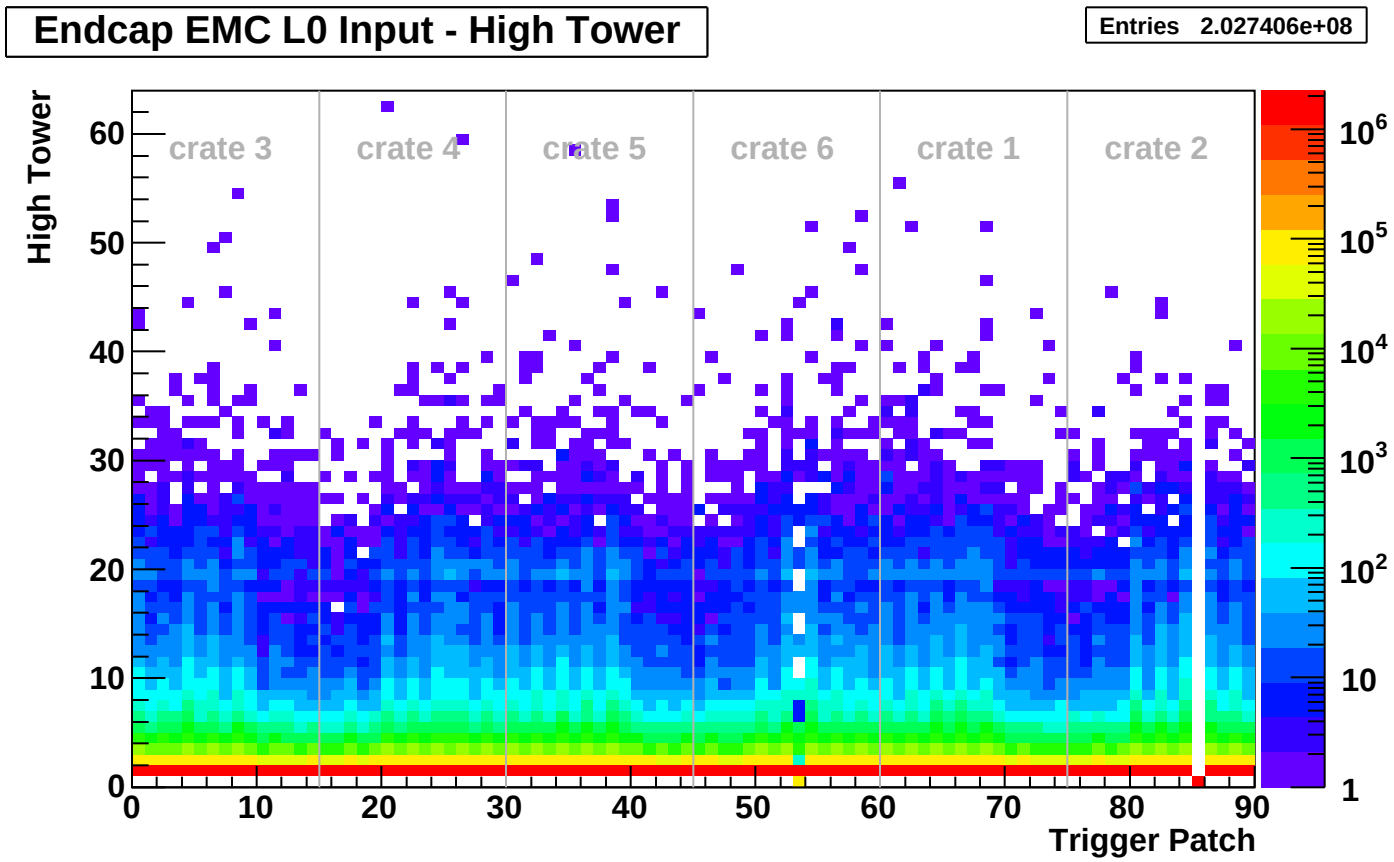

BunchId7Bit (all events)
Entries2252673

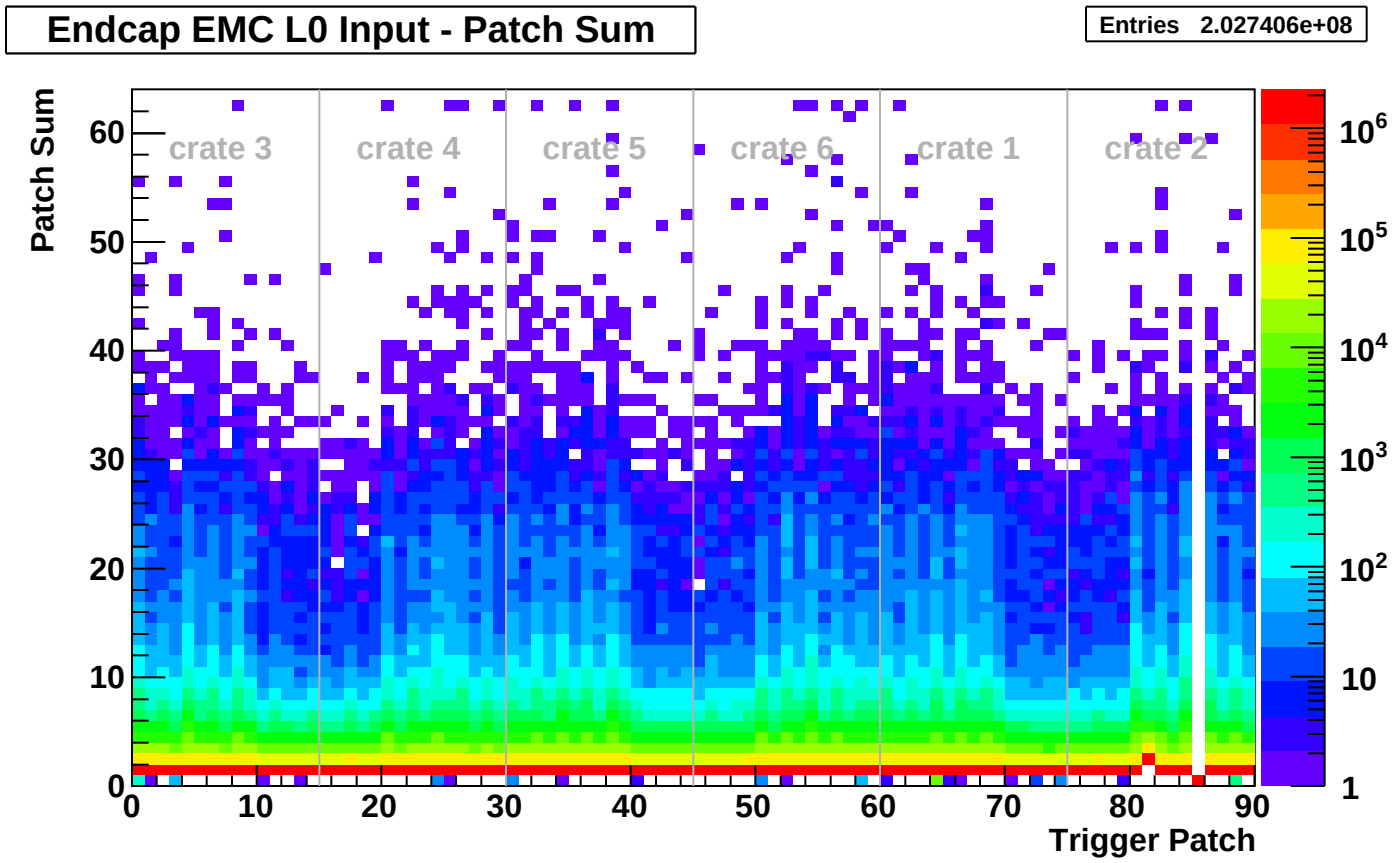

Abort-gaps
Entries 47028

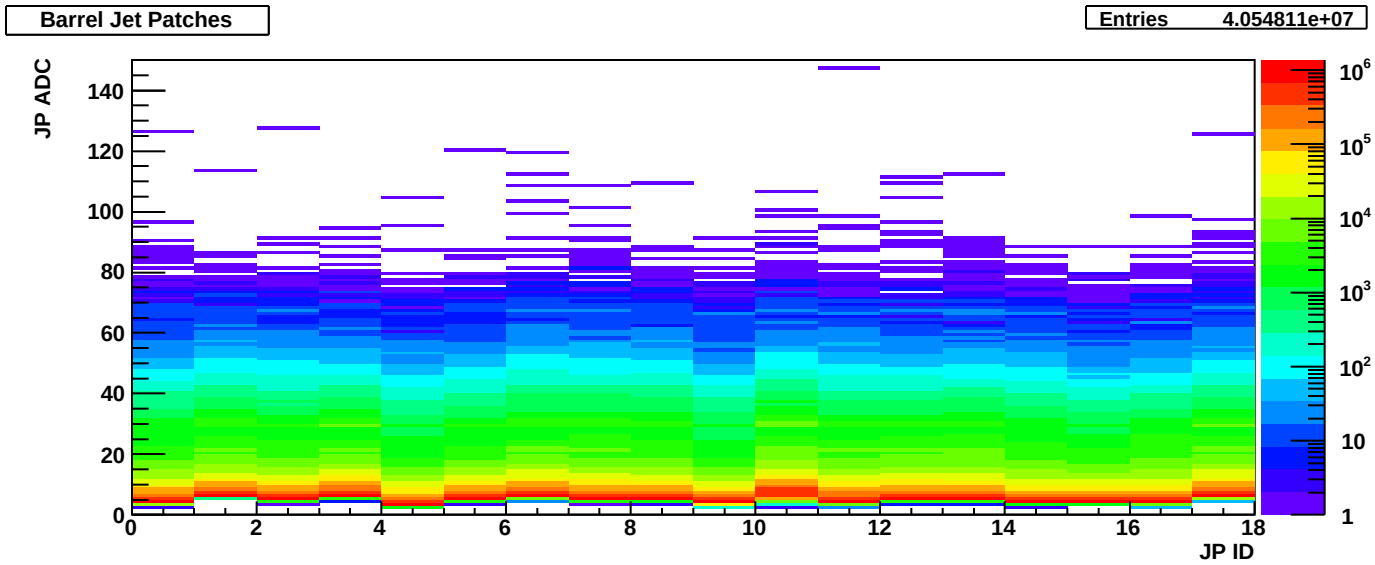

!Yellow-Fill
Entries 77964

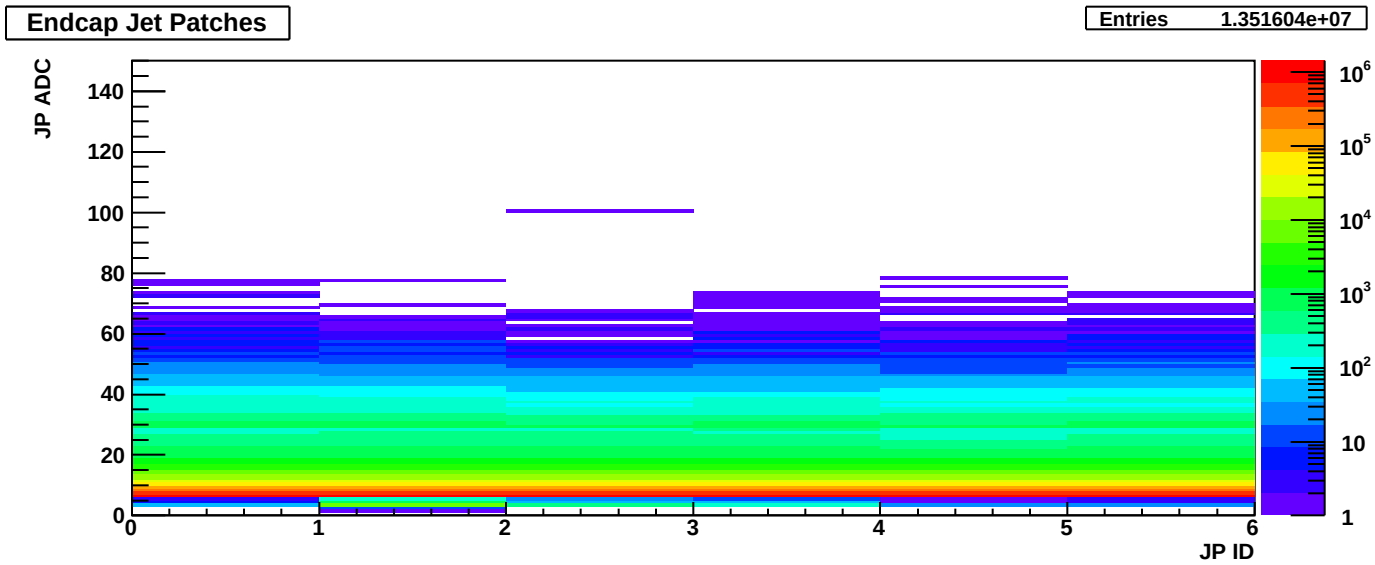

Barrel EMC L0 Input - High Tower
Entries 6.758019e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

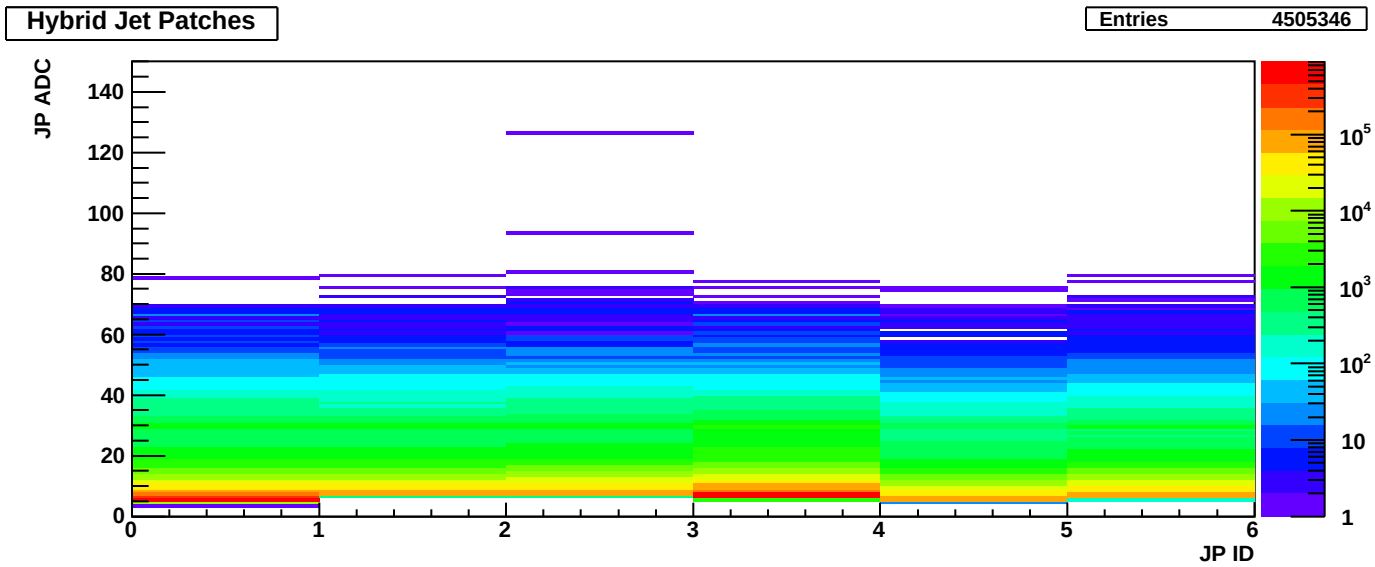

Barrel EMC L0 Input - Patch Sum
Entries 6.758019e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

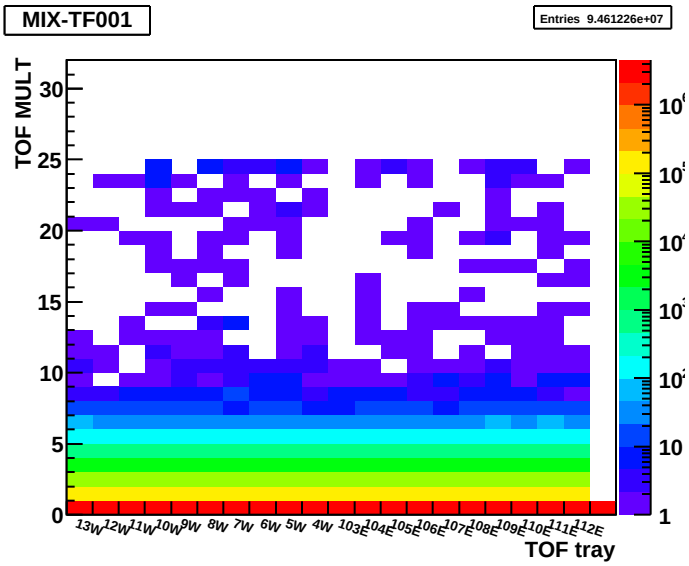

Endcap EMC L0 Input - High Tower
Entries 2.027406e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

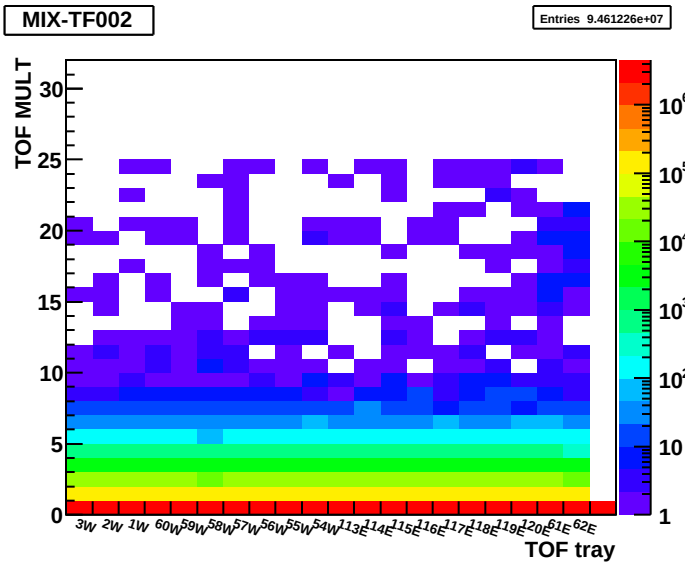

Endcap EMC L0 Input - Patch Sum
Entries 2.027406e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

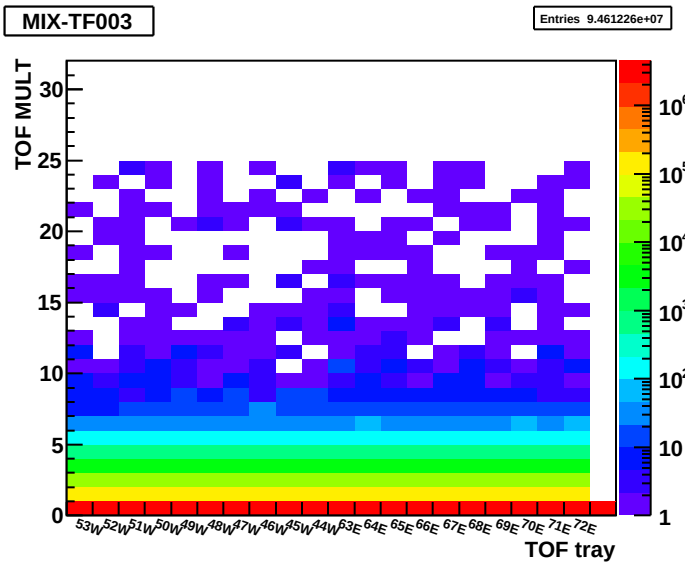

Barrel Jet Patches
Entries 4.054811e+07
JP ADC
JP ID

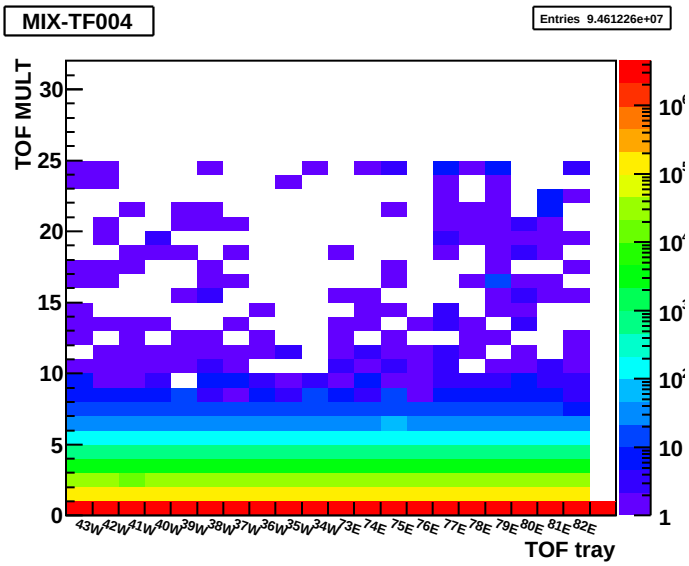

Endcap Jet Patches
Entries 1.351604e+07
JP ADC
JP ID

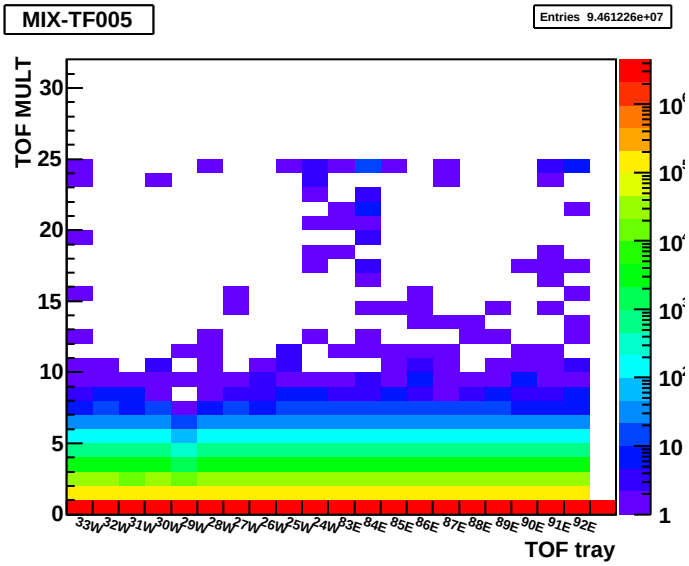

Hybrid Jet Patches
Entries 4505346
JP ADC
JP ID

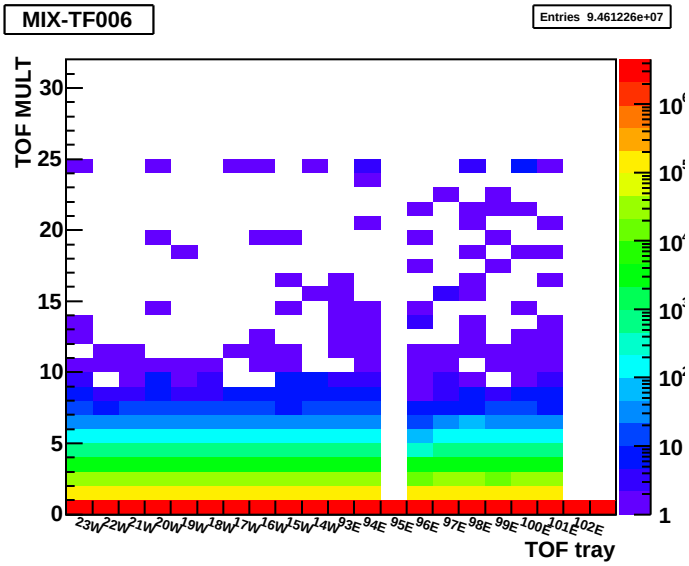

MIX-TF001
Entries 9.461226e+07
TOF MULT
TOF tray

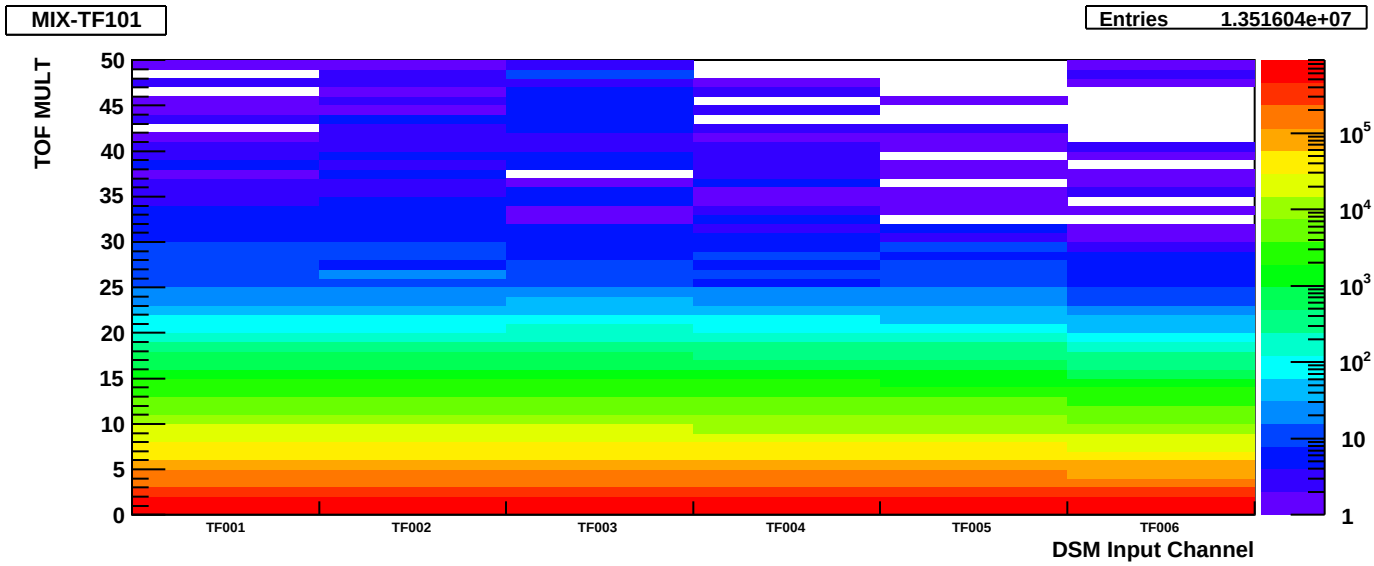

MIX-TF002
Entries 9.461226e+07
TOF MULT
TOF tray

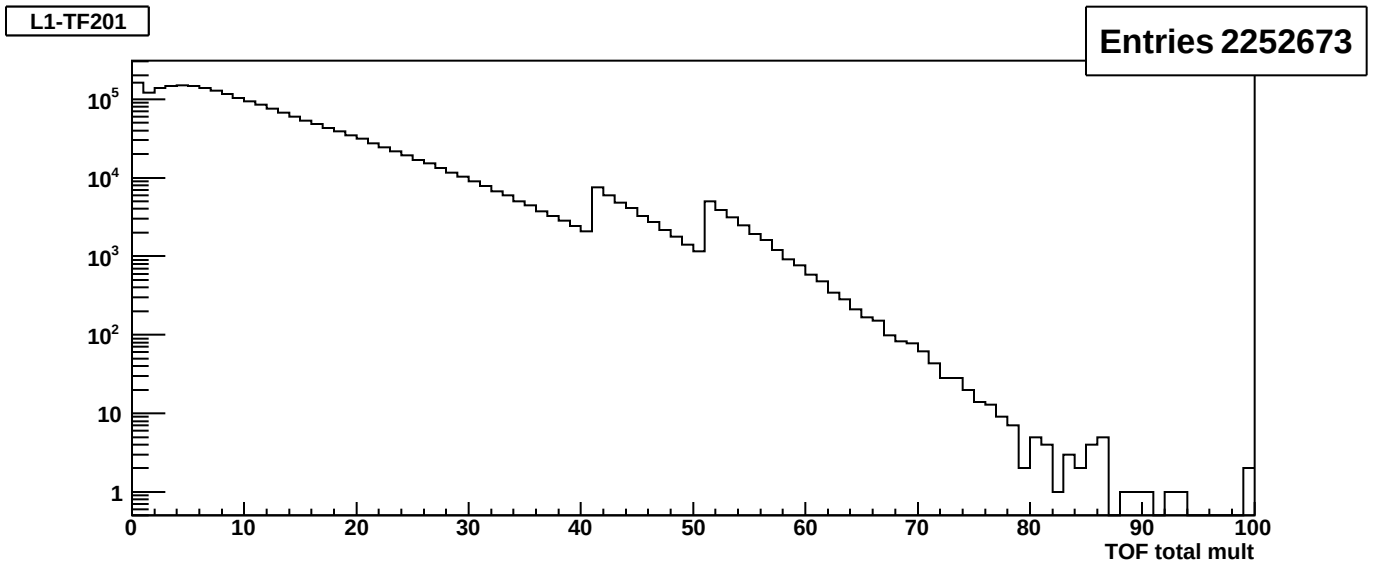

MIX-TF003
Entries 9.461226e+07
TOF MULT
TOF tray

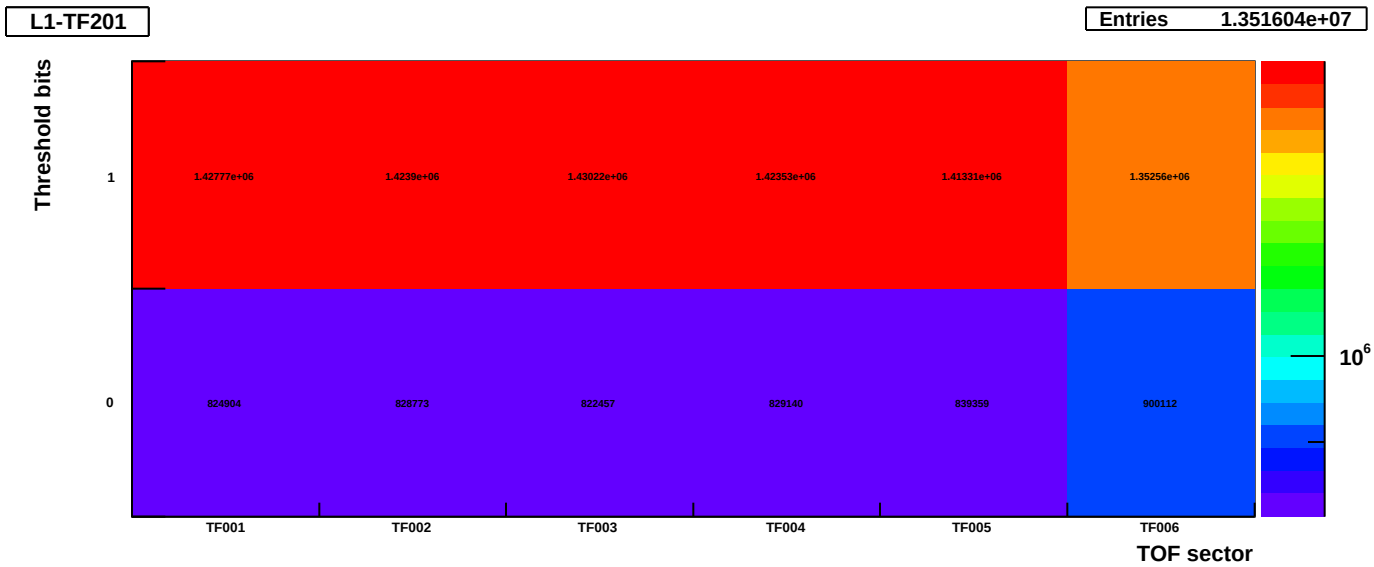

MIX-TF004
Entries 9.461226e+07
TOF MULT
TOF tray

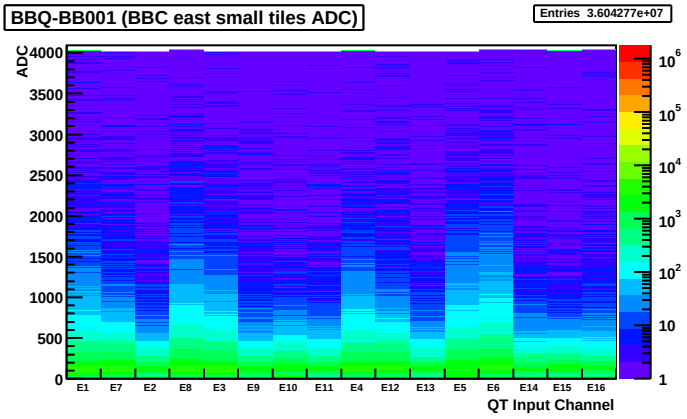

MIX-TF005
Entries 9.461226e+07
TOF MULT
TOF tray

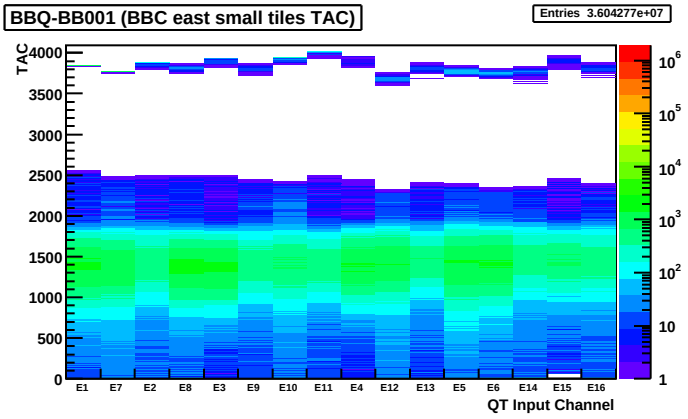

MIX-TF006
Entries 9.461226e+07
TOF MULT
TOF tray

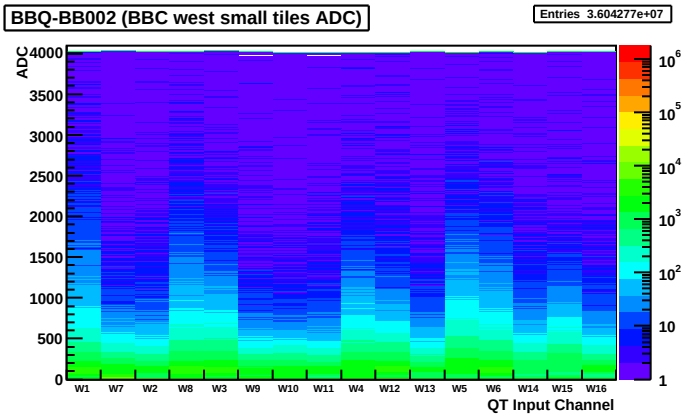

MIX-TF101
Entries 1.351604e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

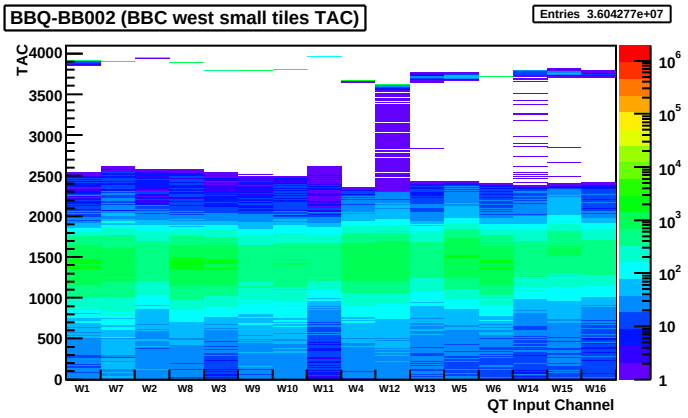

L1-TF201
Entries 2252673
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

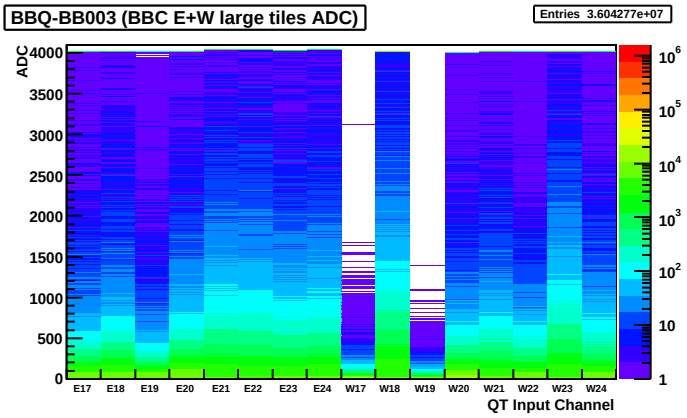

L1-TF201
Entries 1.351604e+07
Threshold bits
1
0
1.42777e+06
1.4239e+06
1.43022e+06
1.42353e+06
1.41331e+06
1.35256e+06
824904
828773
822457
829140
839359
900112
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

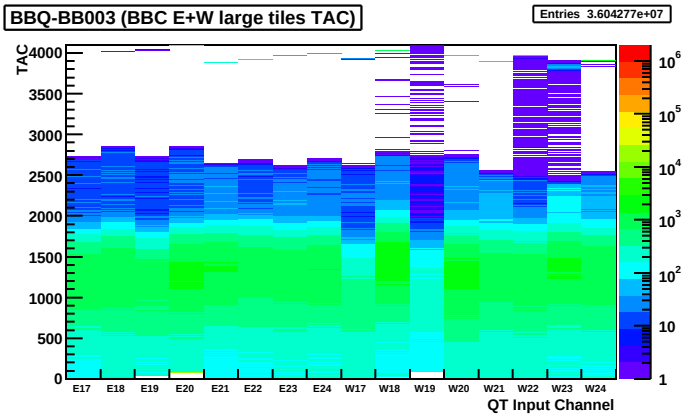

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.604277e+07
ADC
QT Input Channel

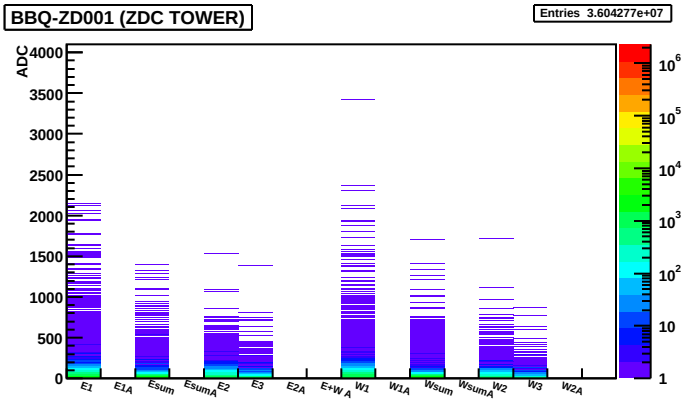

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.604277e+07
TAC
QT Input Channel

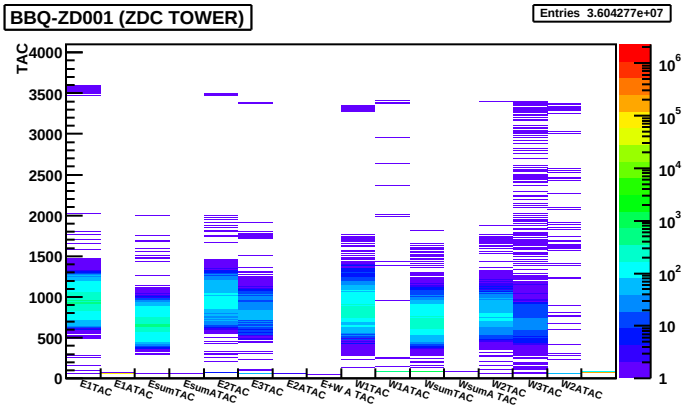

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.604277e+07
ADC
QT Input Channel

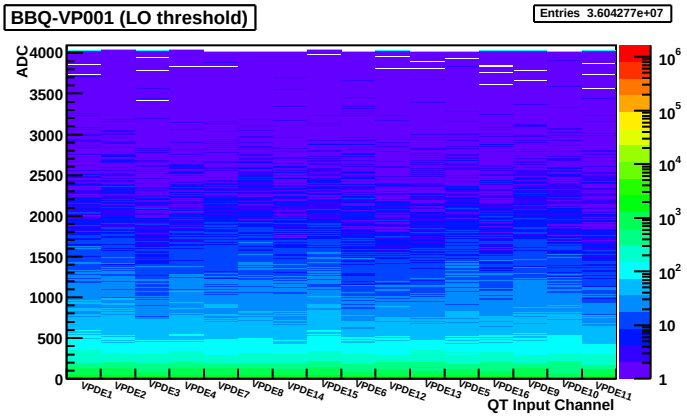

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.604277e+07
TAC
QT Input Channel

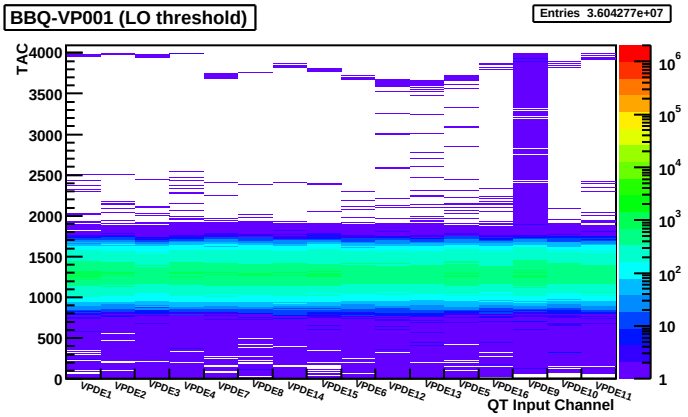

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.604277e+07
ADC
QT Input Channel

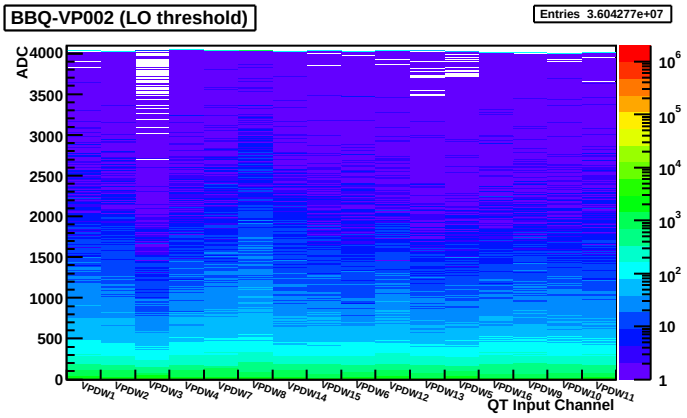

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.604277e+07
TAC
QT Input Channel

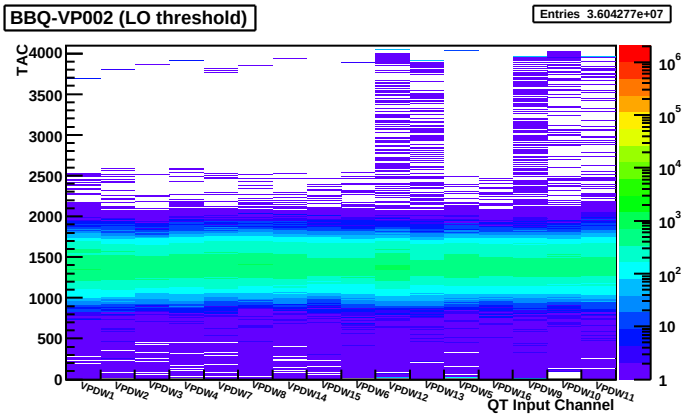

BBQ-ZD001 (ZDC TOWER)
Entries 3.604277e+07
ADC

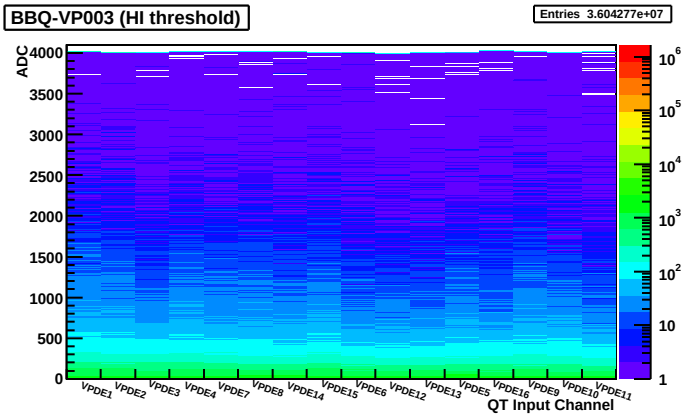

BBQ-ZD001 (ZDC TOWER)
Entries 3.604277e+07
TAC

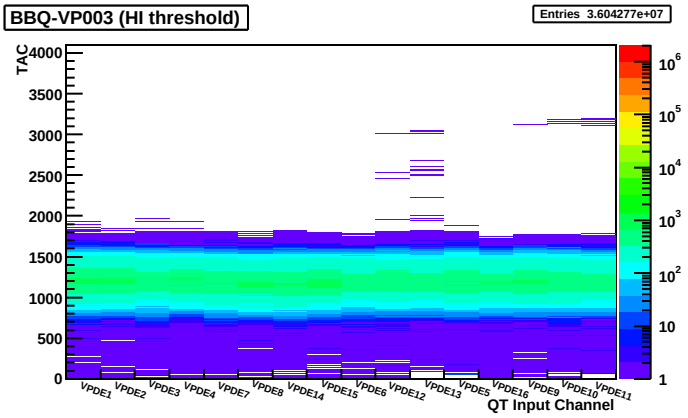

BBQ-VP001 (LO threshold)
Entries 3.604277e+07
ADC
QT Input Channel

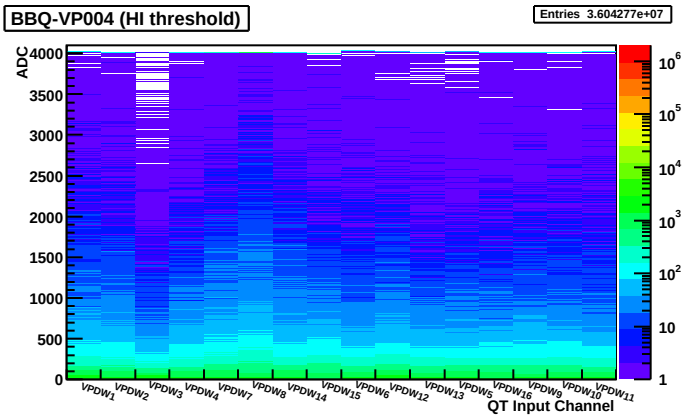

BBQ-VP001 (LO threshold)
Entries 3.604277e+07
TAC
QT Input Channel

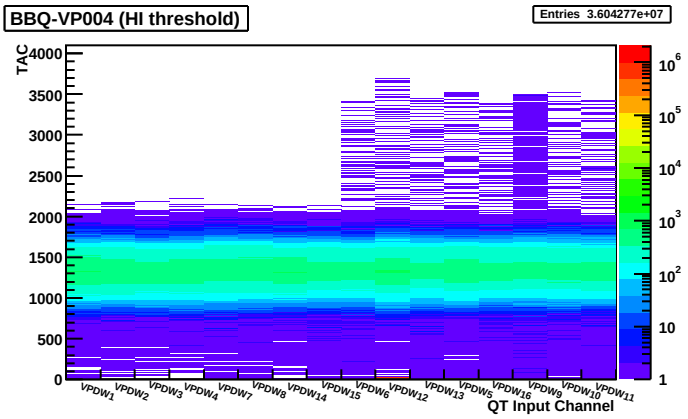

BBQ-VP002 (LO threshold)
Entries 3.604277e+07
ADC
QT Input Channel

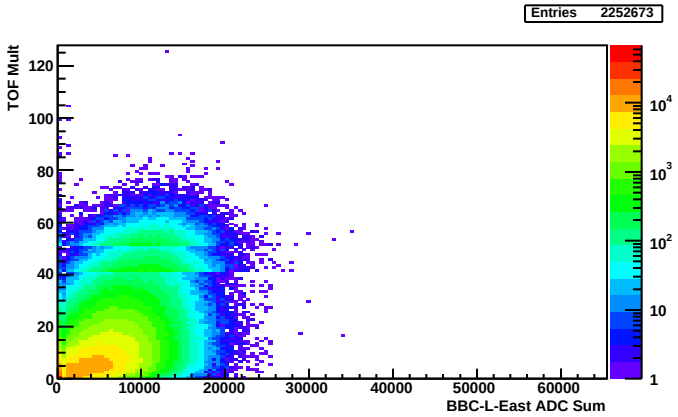

BBQ-VP002 (LO threshold)
Entries 3.604277e+07
TAC
QT Input Channel

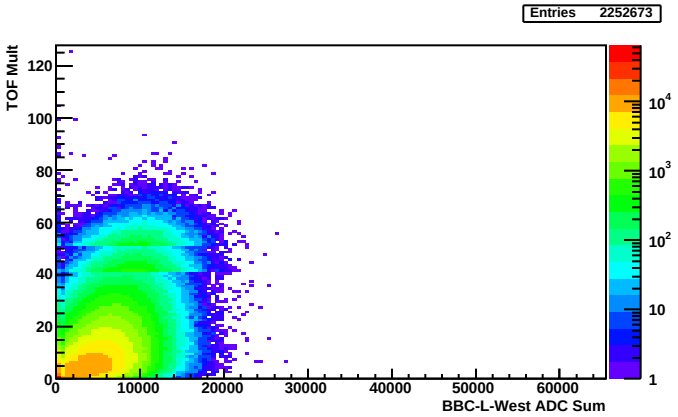

BBQ-VP003 (HI threshold)
Entries 3.604277e+07
ADC
QT Input Channel

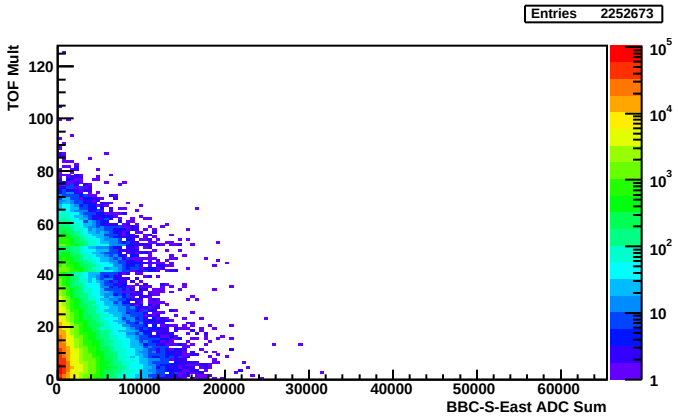

BBQ-VP003 (HI threshold)
Entries 3.604277e+07
TAC
QT Input Channel

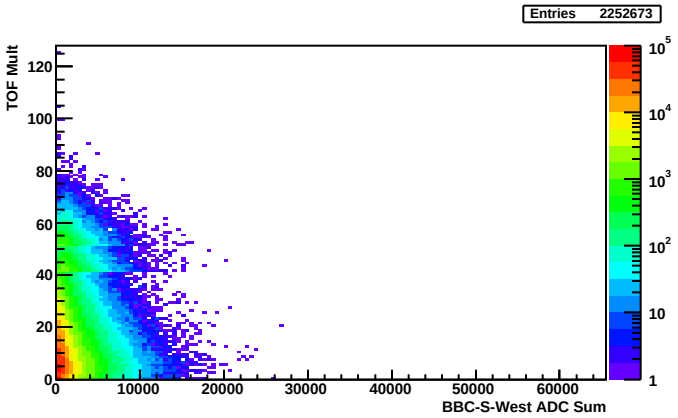

BBQ-VP004 (HI threshold)
Entries 3.604277e+07
ADC
QT Input Channel

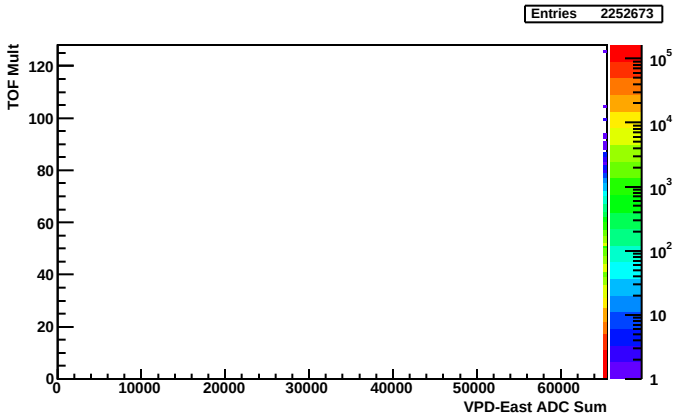

BBQ-VP004 (HI threshold)
Entries 3.604277e+07
TAC
QT Input Channel

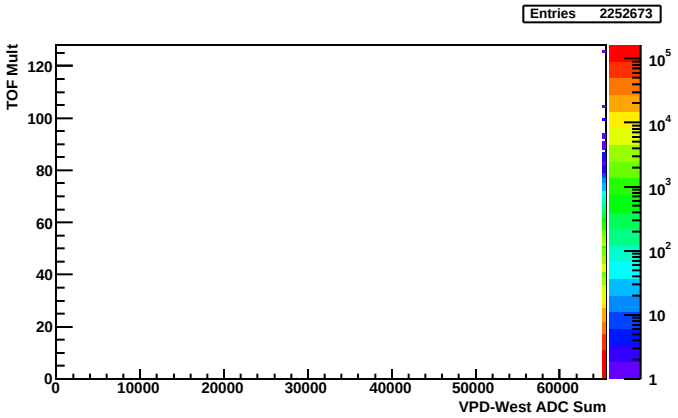

Entries 2252673
TOF Mult
BBC-L-East ADC Sum

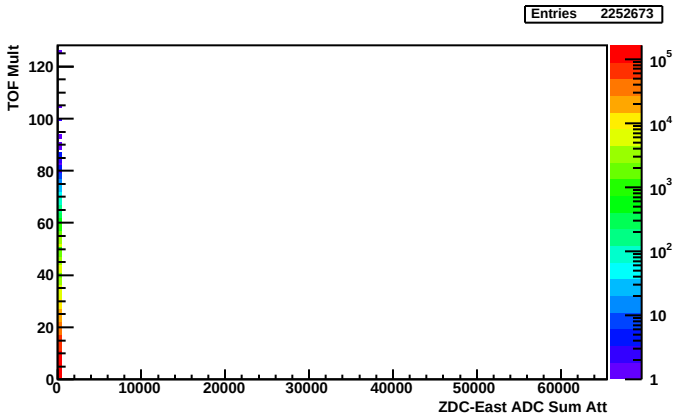

Entries 2252673
TOF Mult
BBC-L-West ADC Sum

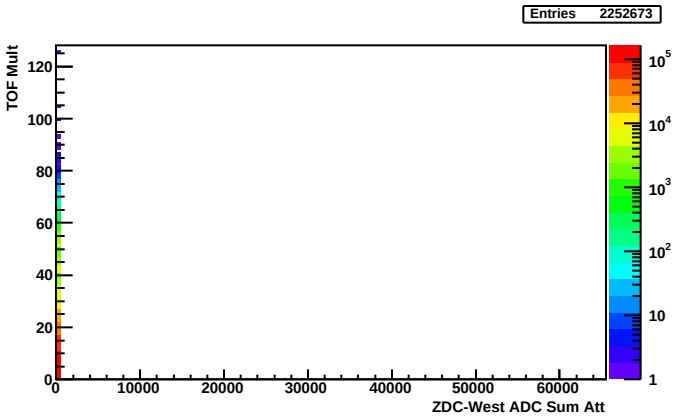

Entries 2252673
TOF Mult
BBC-S-East ADC Sum

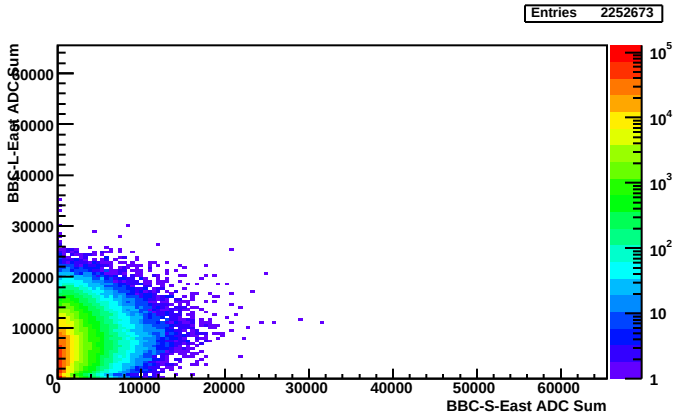

Entries 2252673
TOF Mult
BBC-S-West ADC Sum

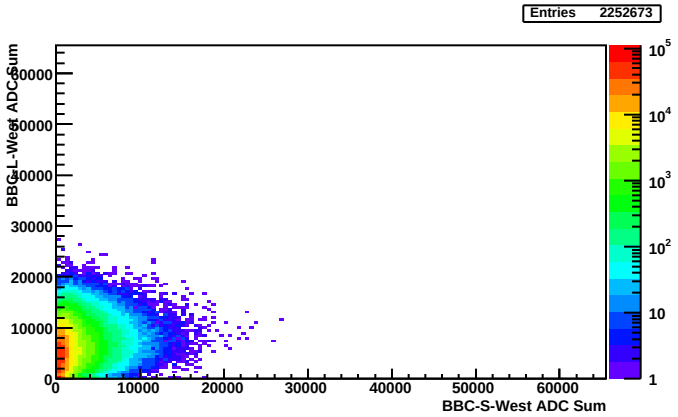

Entries 2252673
TOF Mult
VPD-East ADC Sum

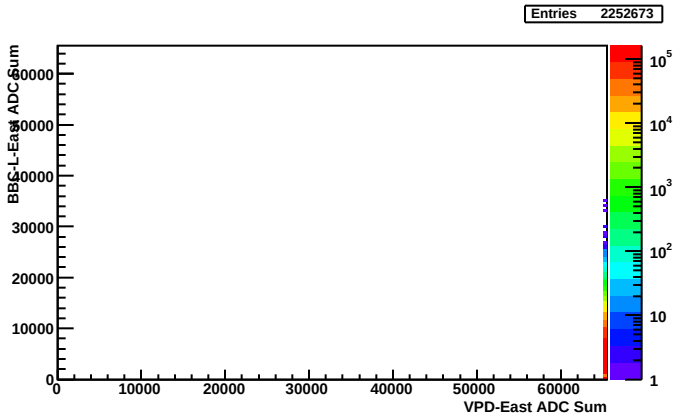

Entries 2252673
TOF Mult
VPD-West ADC Sum

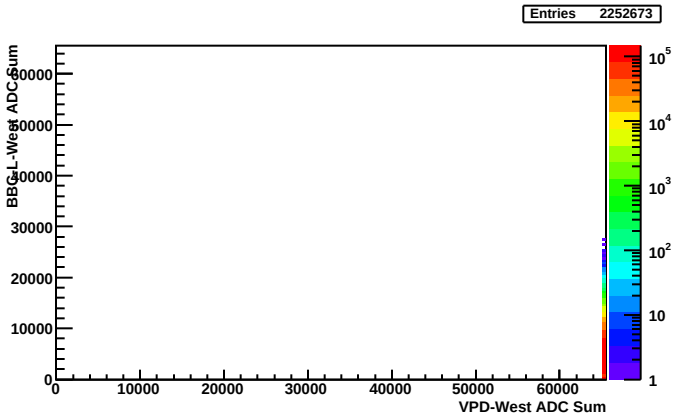

Entries 2252673
TOF Mult
ZDC-East ADC Sum Att

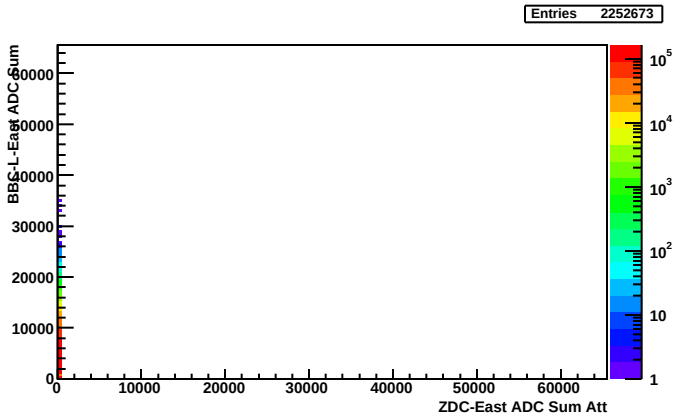

Entries 2252673
TOF Mult
ZDC-West ADC Sum Att

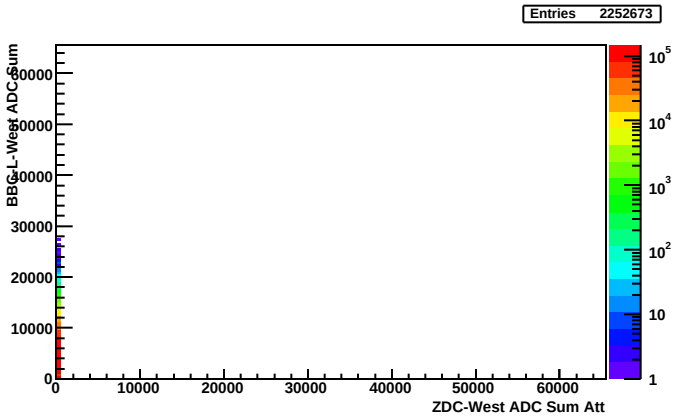

Entries 2252673
BBC-L-East ADC Sum
BBC-S-East ADC Sum

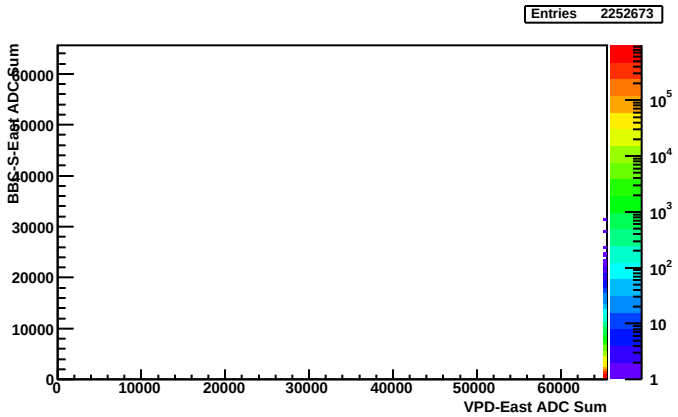

Entries 2252673
BBC-L-West ADC Sum
BBC-S-West ADC Sum

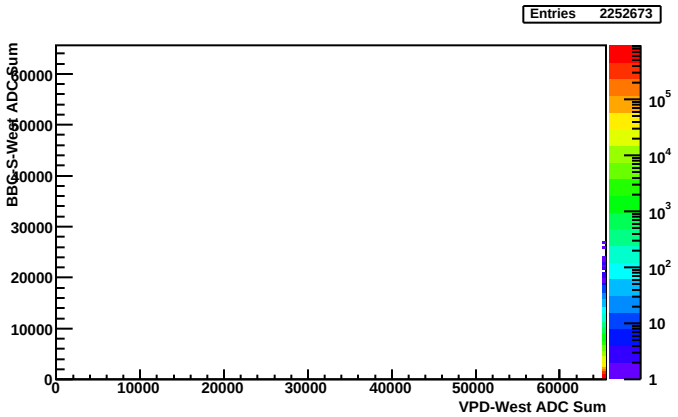

Entries 2252673
BBC-L-East ADC Sum
VPD-East ADC Sum

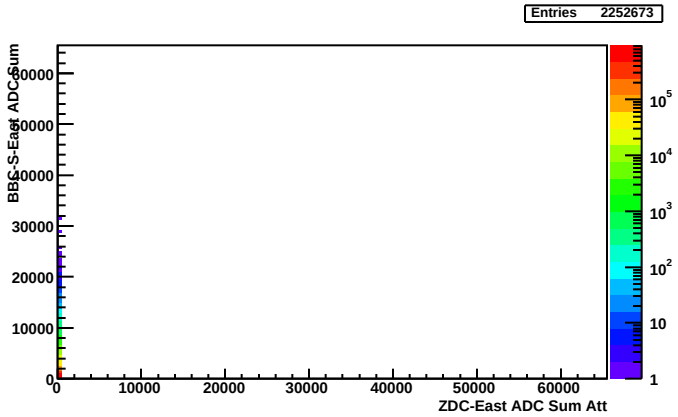

Entries 2252673
BBC-L-West ADC Sum
VPD-West ADC Sum

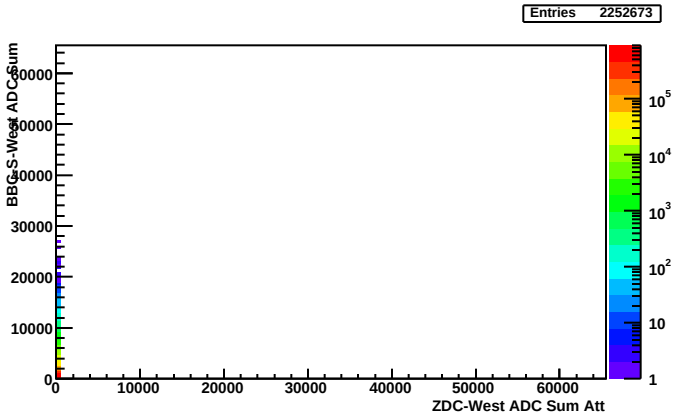

Entries 2252673
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

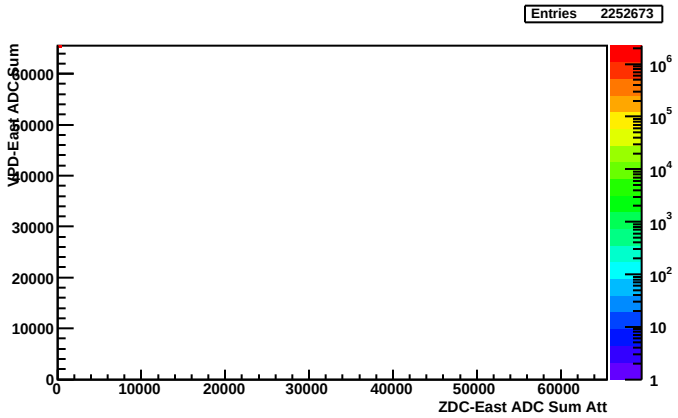

Entries 2252673
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

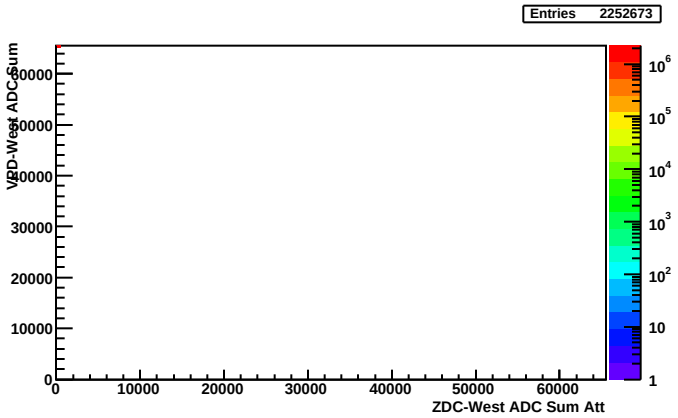

Entries 2252673
BBC-S-East ADC Sum
VPD-East ADC Sum

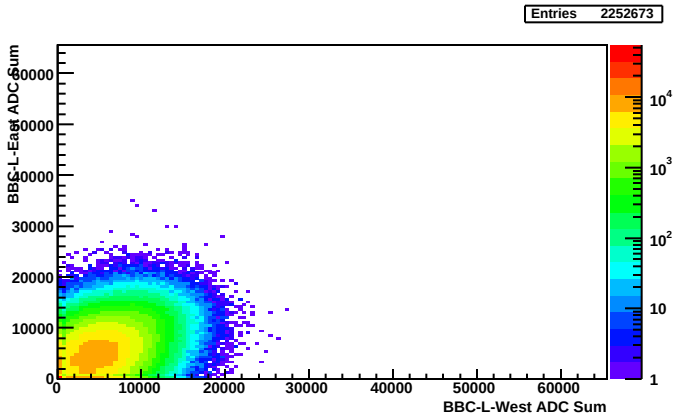

Entries 2252673
BBC-S-West ADC Sum
VPD-West ADC Sum

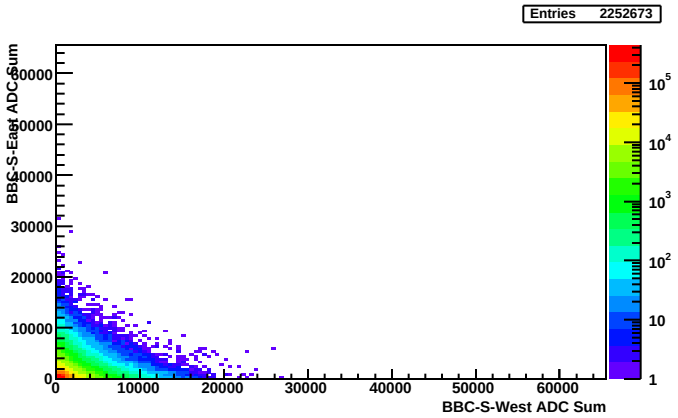

Entries 2252673
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

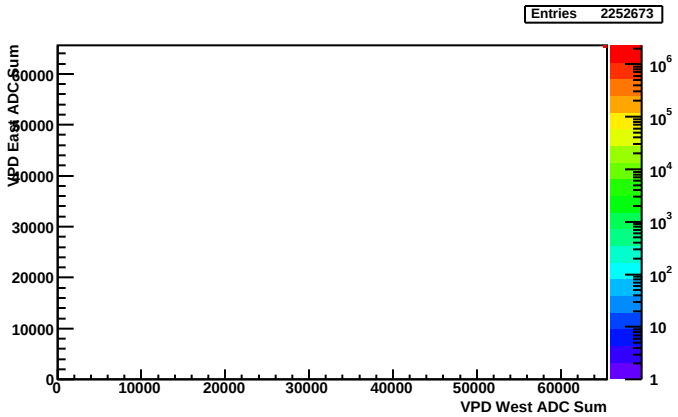

Entries 2252673
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

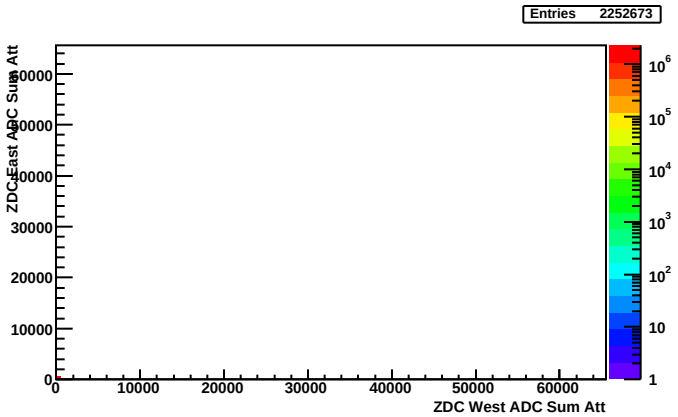

Entries 2252673
VPD-East ADC Sum
ZDC-East ADC Sum Att

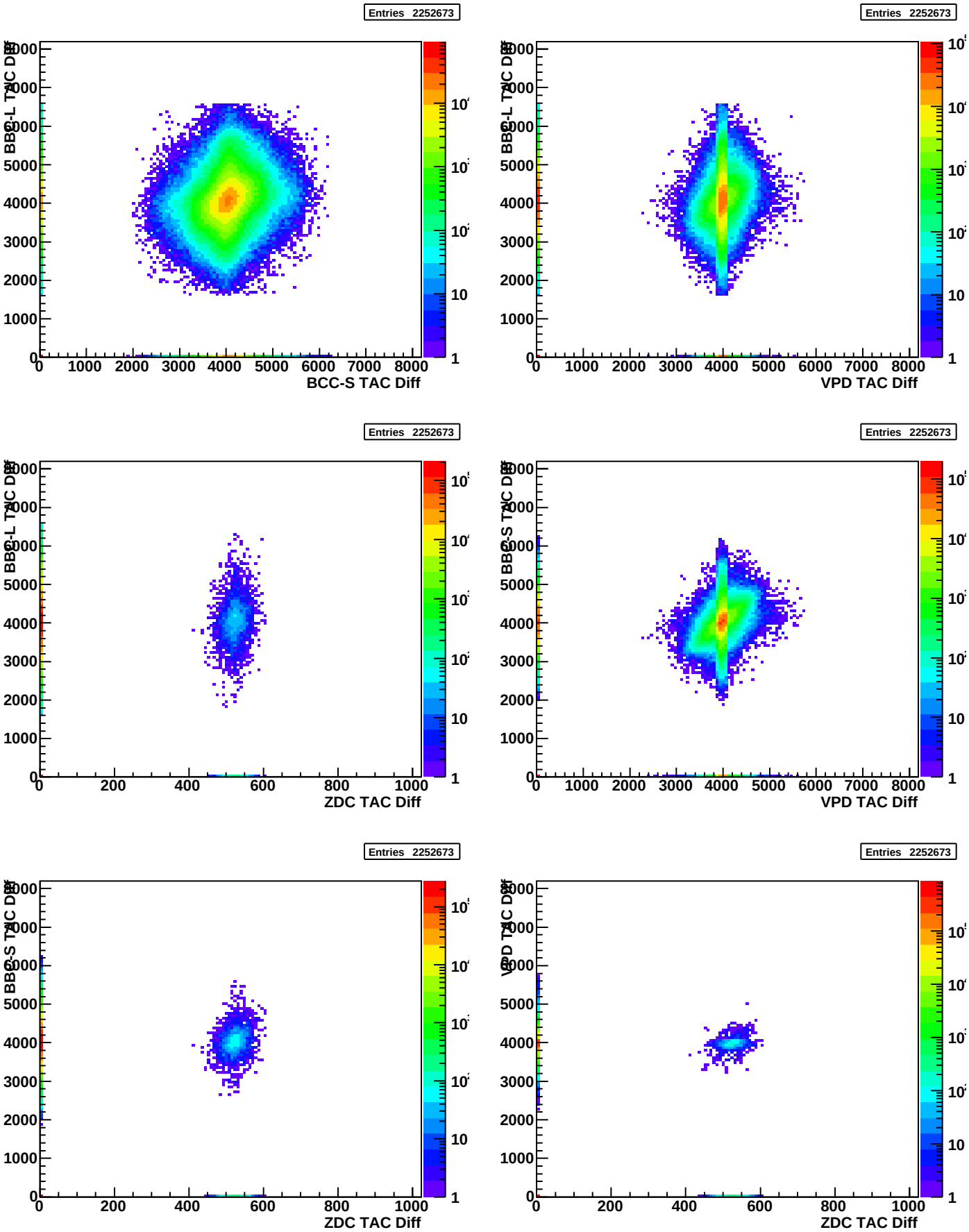

Entries 2252673
VPD-West ADC Sum
ZDC-West ADC Sum Att

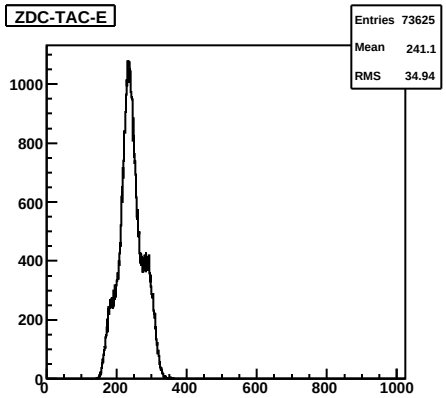

Entries 2252673
BBC-L-East ADC Sum
BBC-L-West ADC Sum

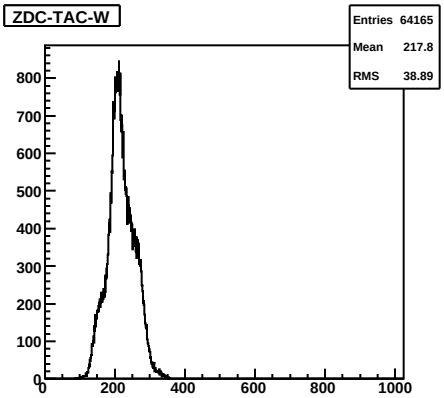

Entries 2252673
BBC-S-East ADC Sum
BBC-S-West ADC Sum

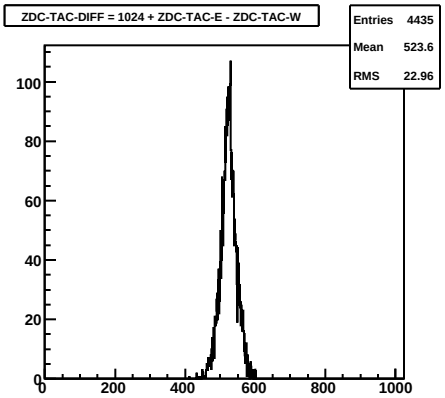

Entries 2252673
VPD East ADC Sum
VPD West ADC Sum

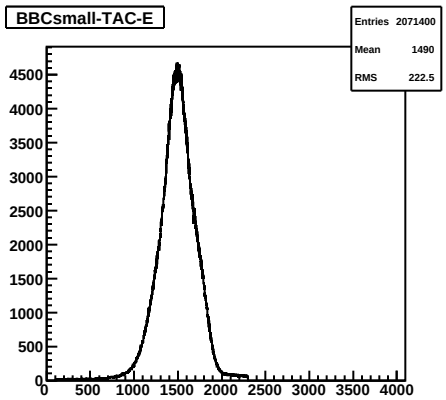

Entries 2252673
ZDC East ADC Sum Att
ZDC West ADC Sum Att

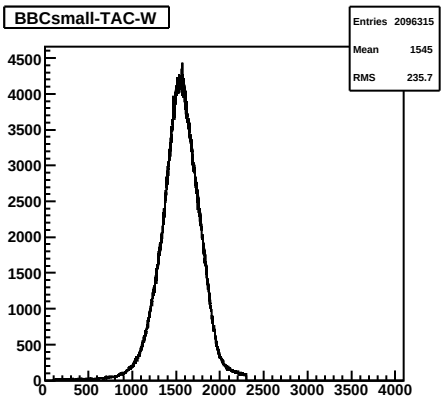

Entries 2252673
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2252673
BBC-L TAC Diff
VPD TAC Diff
Entries 2252673
BBC-L TAC Diff
ZDC TAC Diff
Entries 2252673
BBC-S TAC Diff
VPD TAC Diff
Entries 2252673
BBC-S TAC Diff
ZDC TAC Diff
Entries 2252673
VPD TAC Diff
ZDC TAC Diff

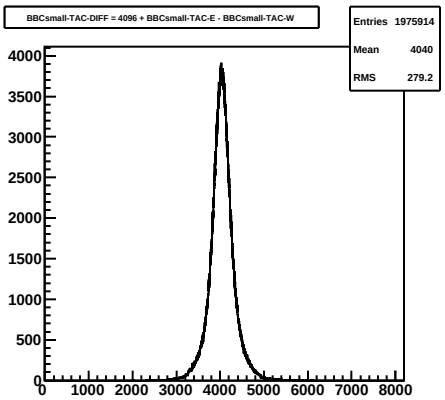

ZDC-TAC-E
Entries 73625
Mean 241.1
RMS 34.94

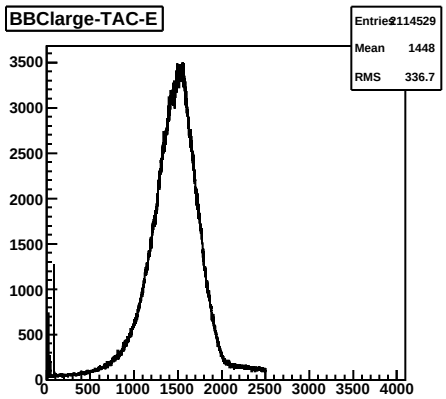

ZDC-TAC-W
Entries 64165
Mean 217.8
RMS 38.89

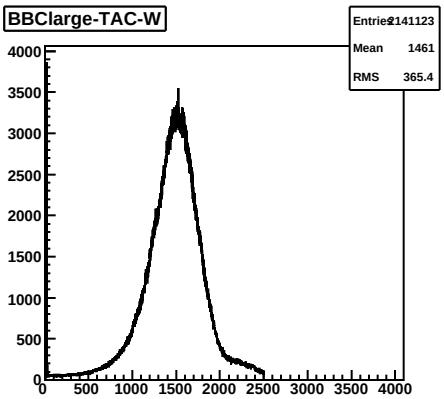

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4435
Mean 523.6
RMS 22.96

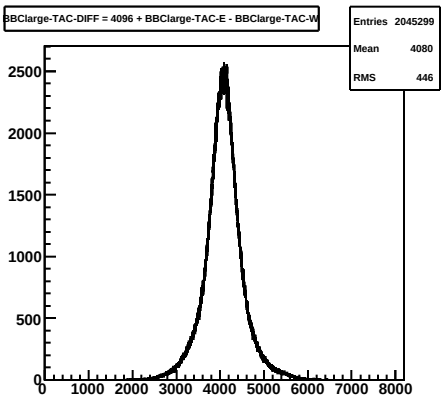

BBCsmall-TAC-E
Entries 2071400
Mean 1490
RMS 222.5

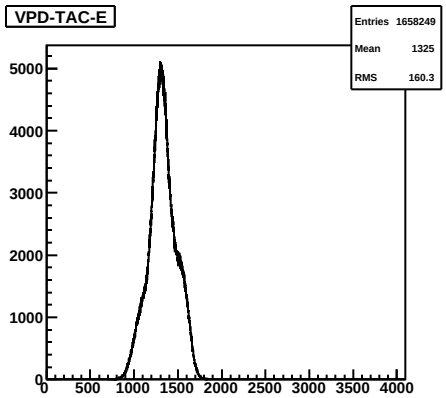

BBCsmall-TAC-W
Entries 2096315
Mean 1545
RMS 235.7

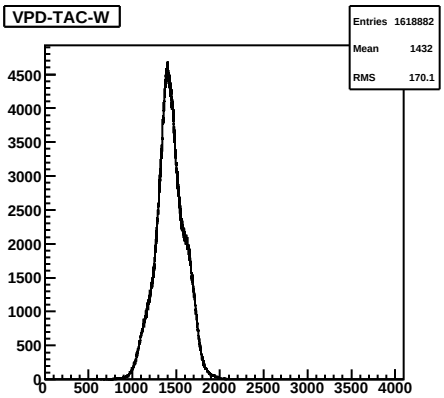

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1975914
Mean 4040
RMS 279.2

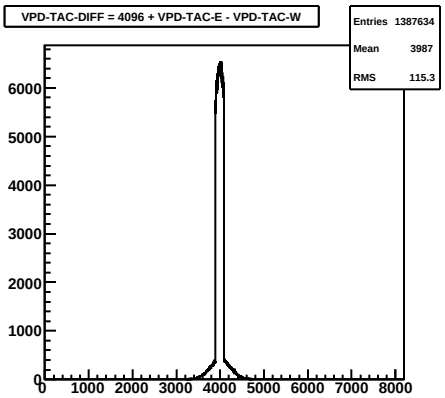

BBClarge-TAC-E
Entries 2114529
Mean 1448
RMS 336.7

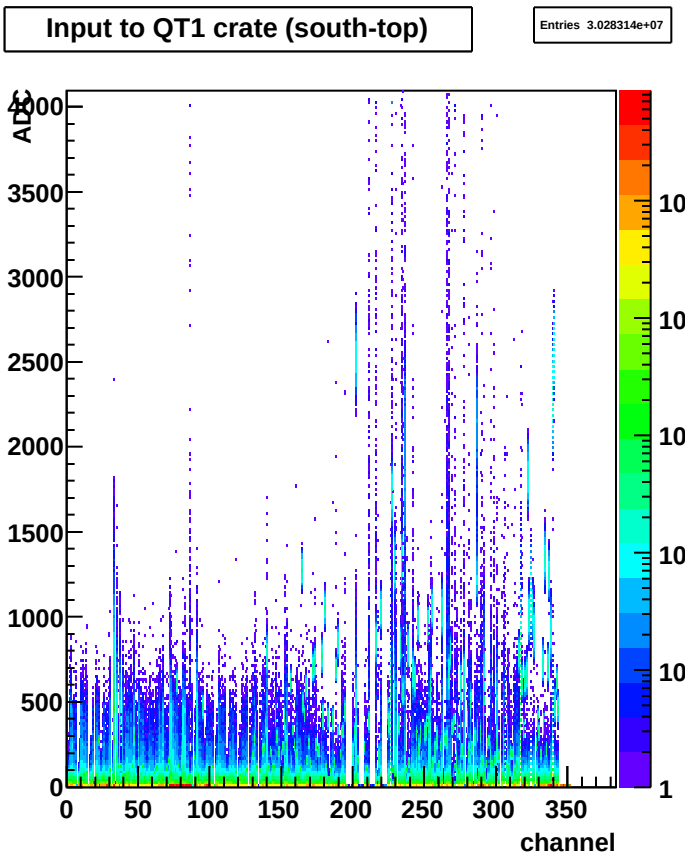

BBClarge-TAC-W
Entries 2141123
Mean 1461
RMS 365.4

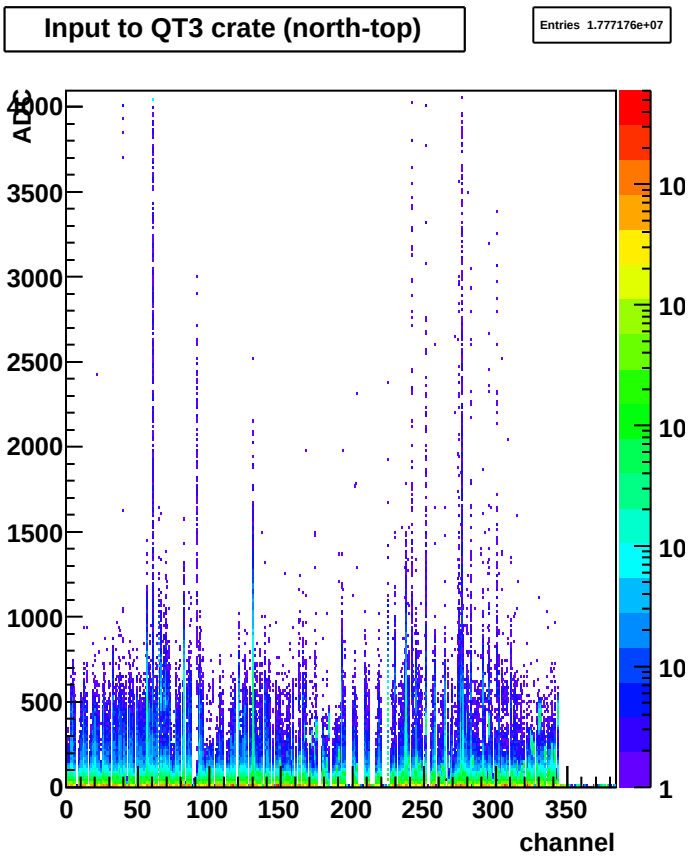

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2045299
Mean 4080
RMS 446

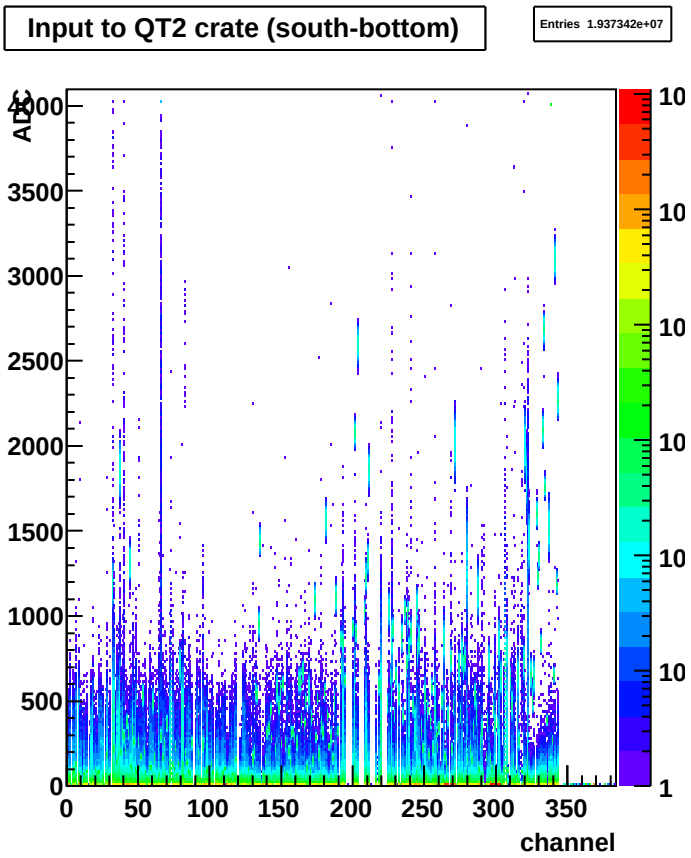

VPD-TAC-E
Entries 1658249
Mean 1325
RMS 160.3

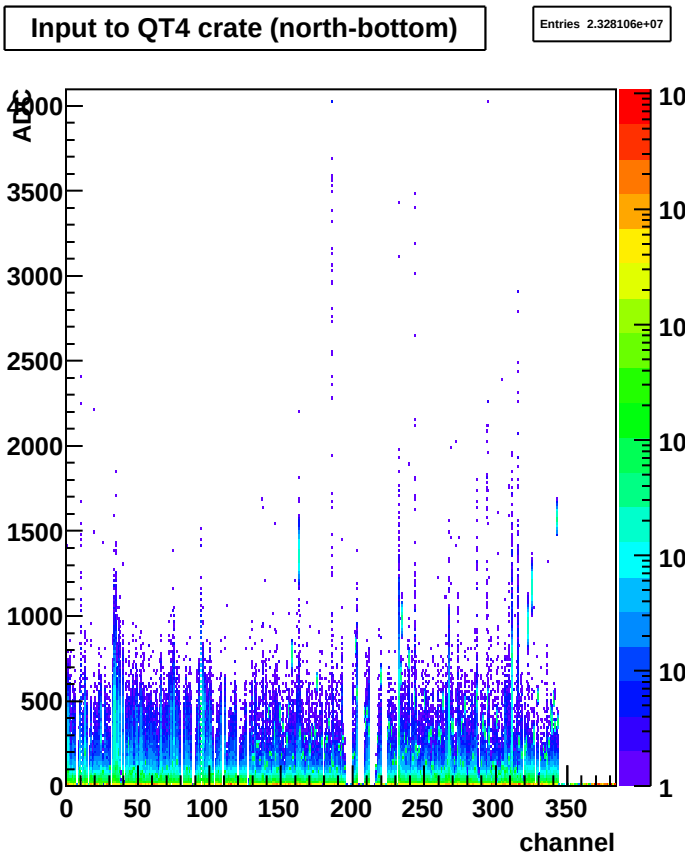

VPD-TAC-W
Entries 1618882
Mean 1432
RMS 170.1

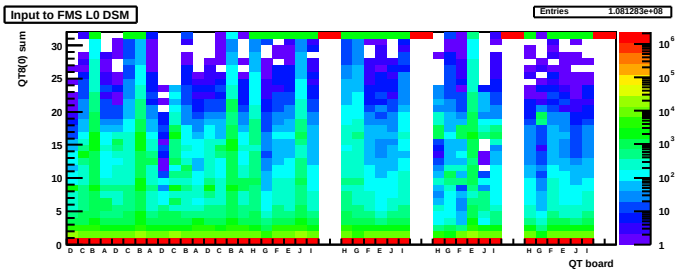

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1387634
Mean 3987
RMS 115.3

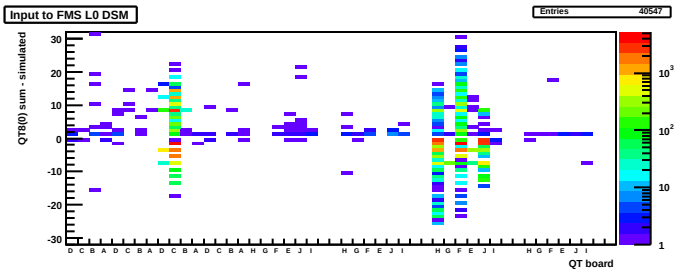

Input to QT1 crate (south-top)
Entries 3.028314e+07
ADC
channel

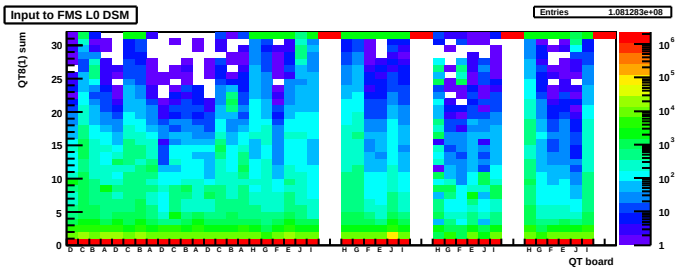

Input to QT3 crate (north-top)
Entries 1.777176e+07
ADC
channel

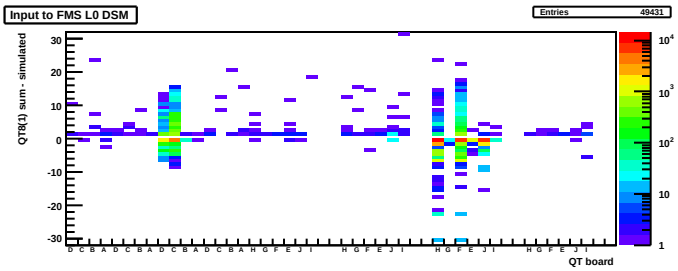

Input to QT2 crate (south-bottom)
Entries 1.937342e+07
ADC
channel

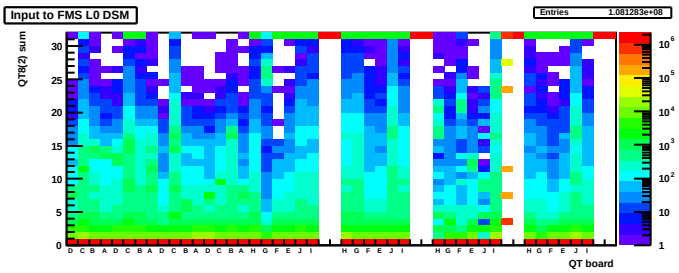

Input to QT4 crate (north-bottom)
Entries 2.328106e+07
ADC
channel

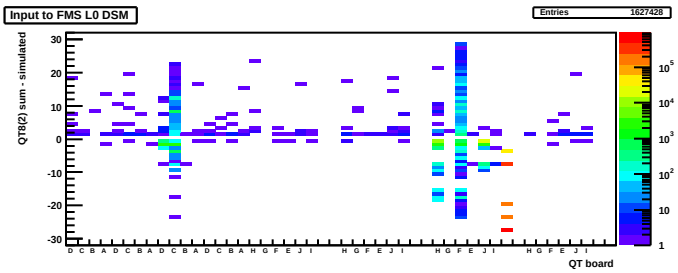

Input to FMS L0 DSM
Entries 1.081283e+08
QT8(0) sum
QT board

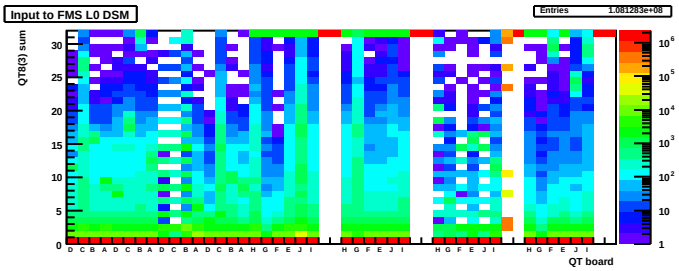

Input to FMS L0 DSM
Entries 40547
QT8(0) sum - simulated
QT board

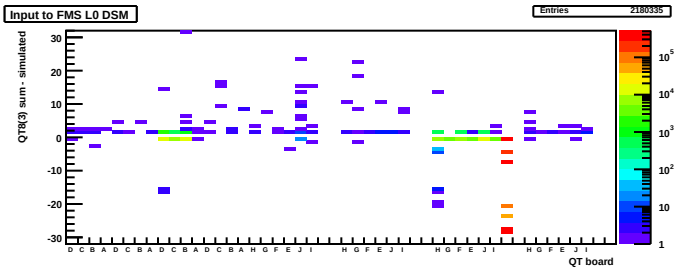

Input to FMS L0 DSM
Entries 1.081283e+08
QT8(1) sum
QT board

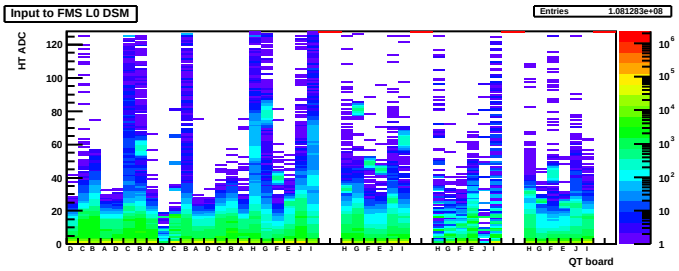

Input to FMS L0 DSM
Entries 49431
QT8(1) sum - simulated
QT board

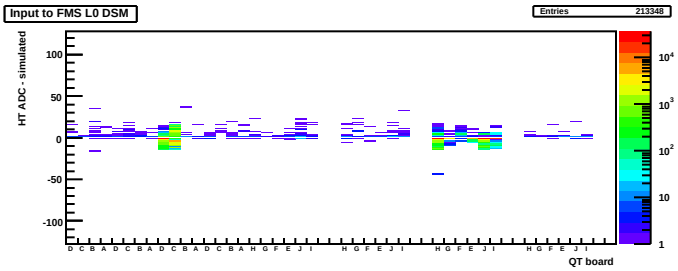

Input to FMS L0 DSM
Entries 1.081283e+08
QT8(2) sum
QT board

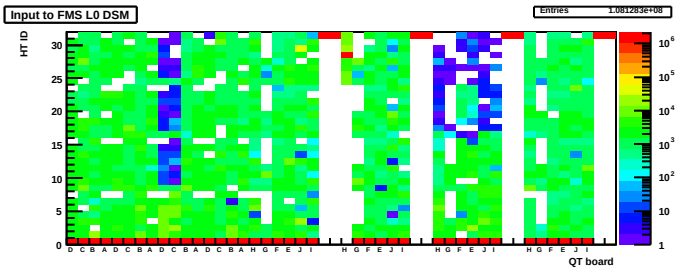

Input to FMS L0 DSM
Entries 1627428
QT8(2) sum - simulated
QT board

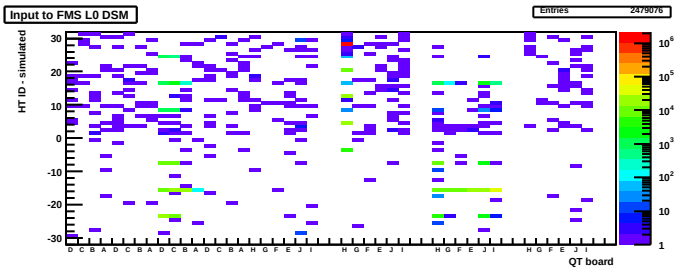

Input to FMS L0 DSM
Entries 1.081283e+08
QT8(3) sum
QT board

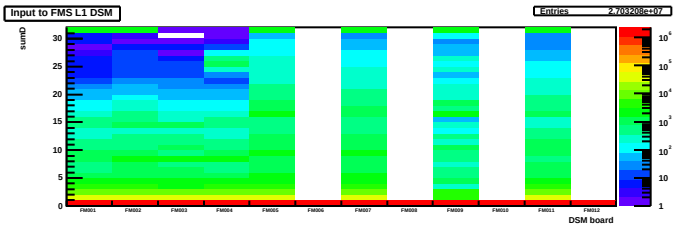

Input to FMS L0 DSM
Entries 2180335
QT8(3) sum - simulated
QT board

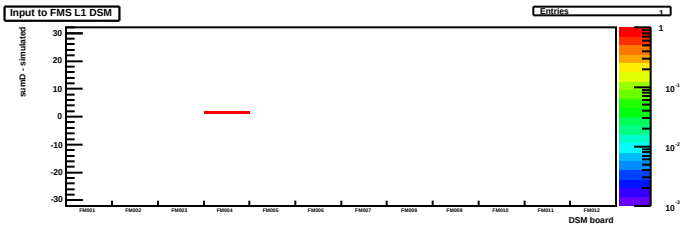

Input to FMS L0 DSM
Entries 1.081283e+08
HT ADC
QT board

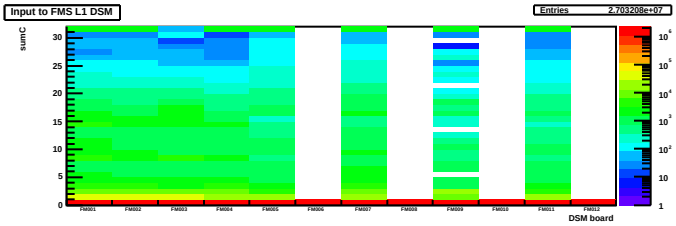

Input to FMS L0 DSM
Entries 213348
HT ADC - simulated
QT board

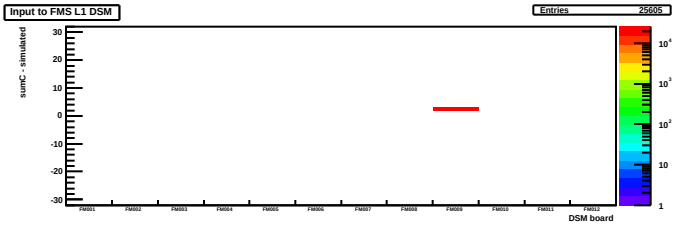

Input to FMS L0 DSM
Entries 1.081283e+08
HT ID
QT board

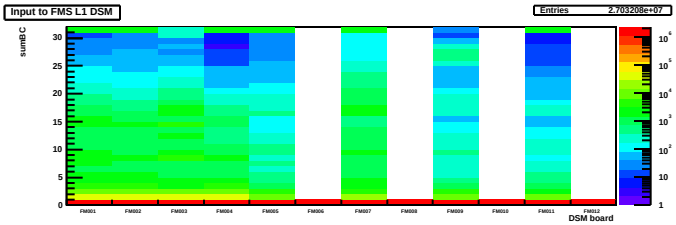

Input to FMS L0 DSM
Entries 2479076
HT ID - simulated
QT board

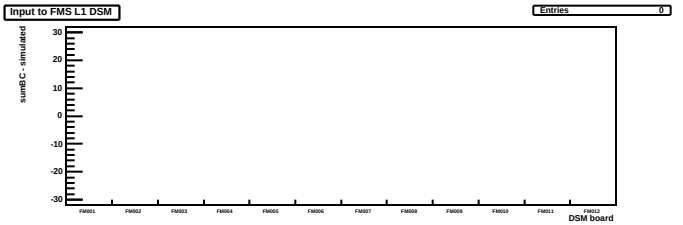

Input to FMS L1 DSM
Entries 2.703208e+07
sumD
DSM board

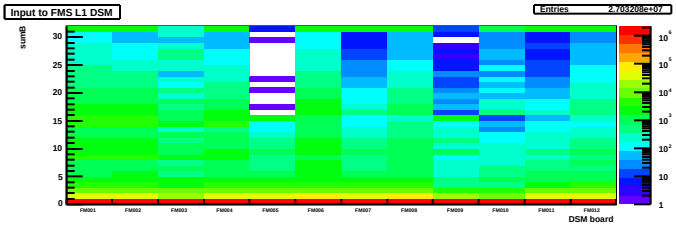

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

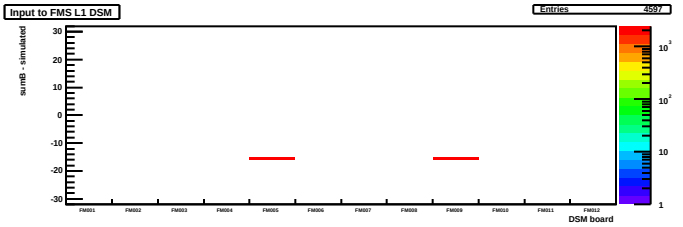

Input to FMS L1 DSM
Entries 2.703208e+07
sumC
DSM board

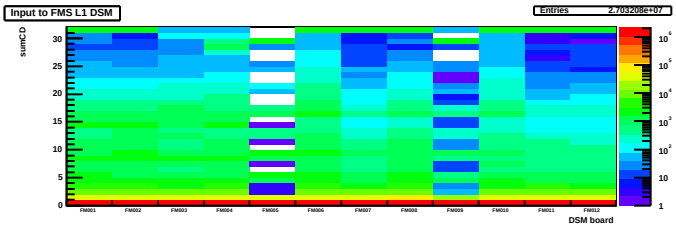

Input to FMS L1 DSM
Entries 25605
sumC - simulated
DSM board

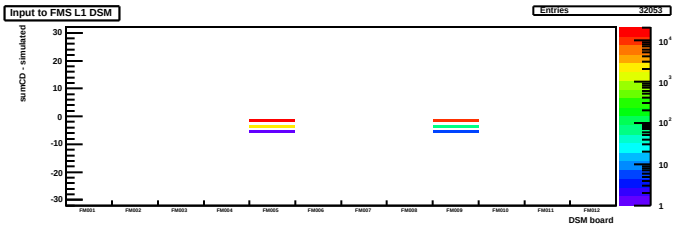

Input to FMS L1 DSM
Entries 2.703208e+07
sumBC
DSM board

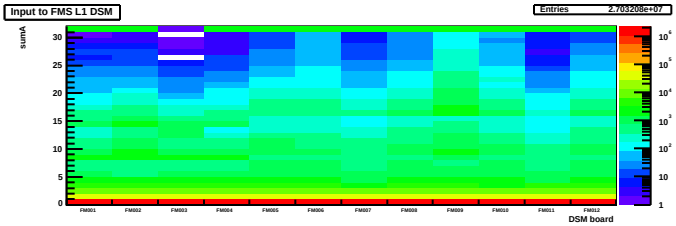

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

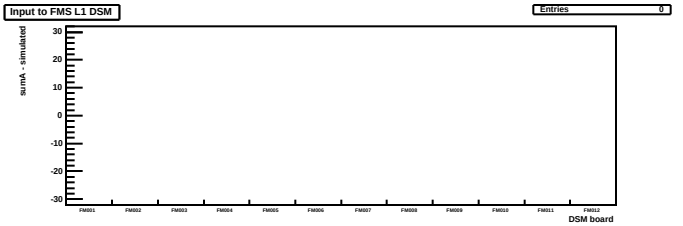

Input to FMS L1 DSM
Entries 2.703208e+07
sumB
DSM board

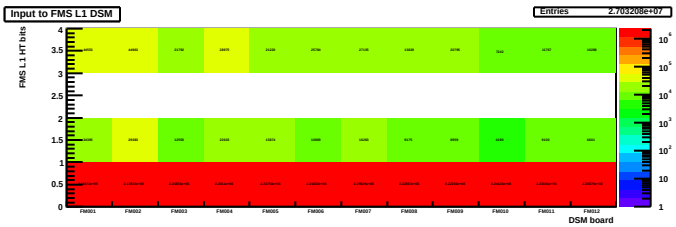

Input to FMS L1 DSM
Entries 4597
sumB - simulated
DSM board

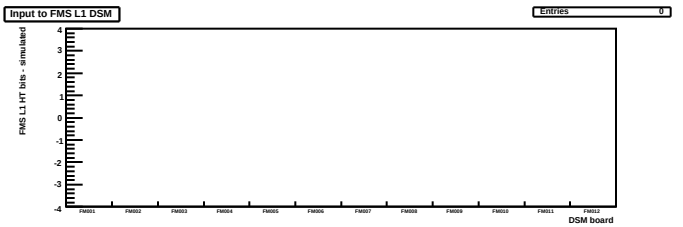

Input to FMS L1 DSM
Entries 2.703208e+07
sumCD
DSM board

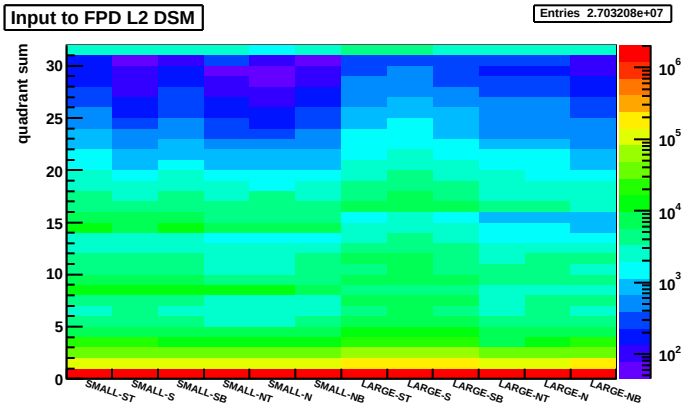

Input to FMS L1 DSM
Entries 32053
sumCD - simulated
DSM board

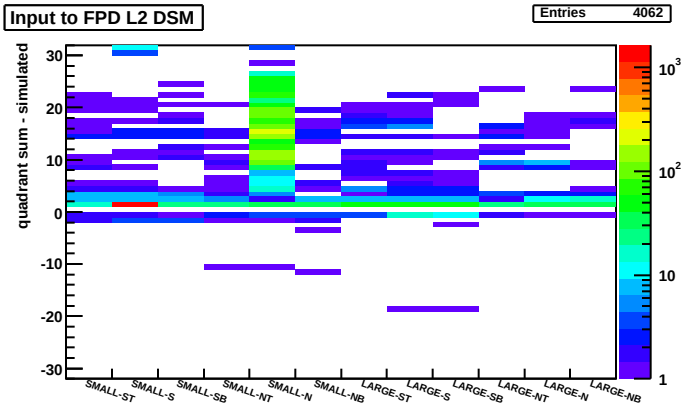

Input to FMS L1 DSM
Entries 2.703208e+07
sumA
DSM board

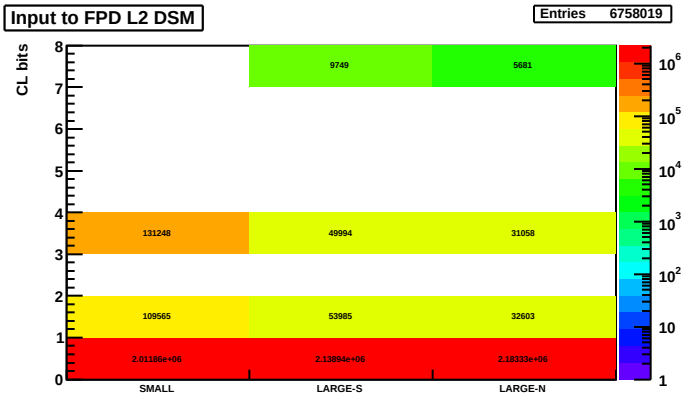

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

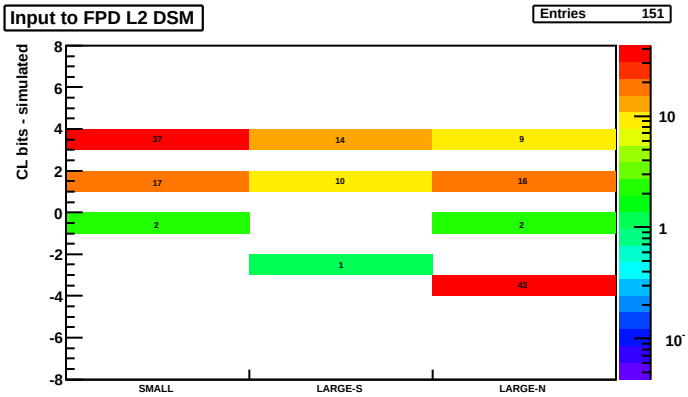

Input to FMS L1 DSM
Entries 2.703208e+07
FMS L1 HT bits
DSM board

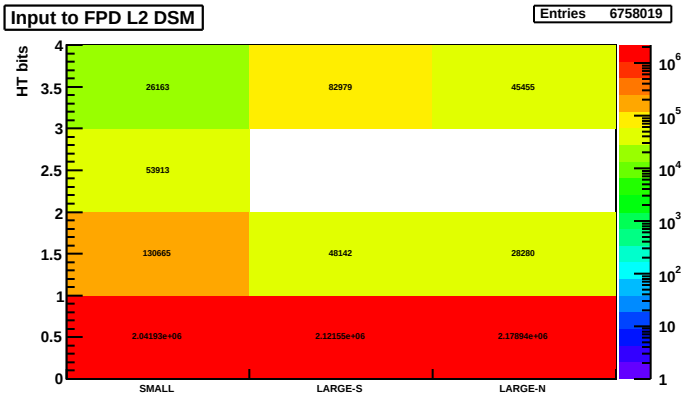

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

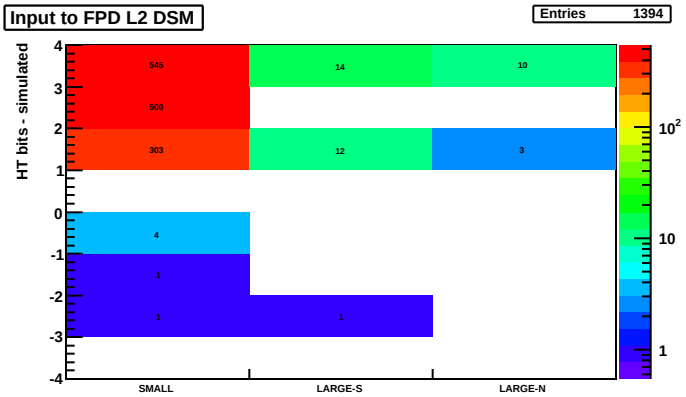

Input to FPD L2 DSM
Entries 2.703208e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

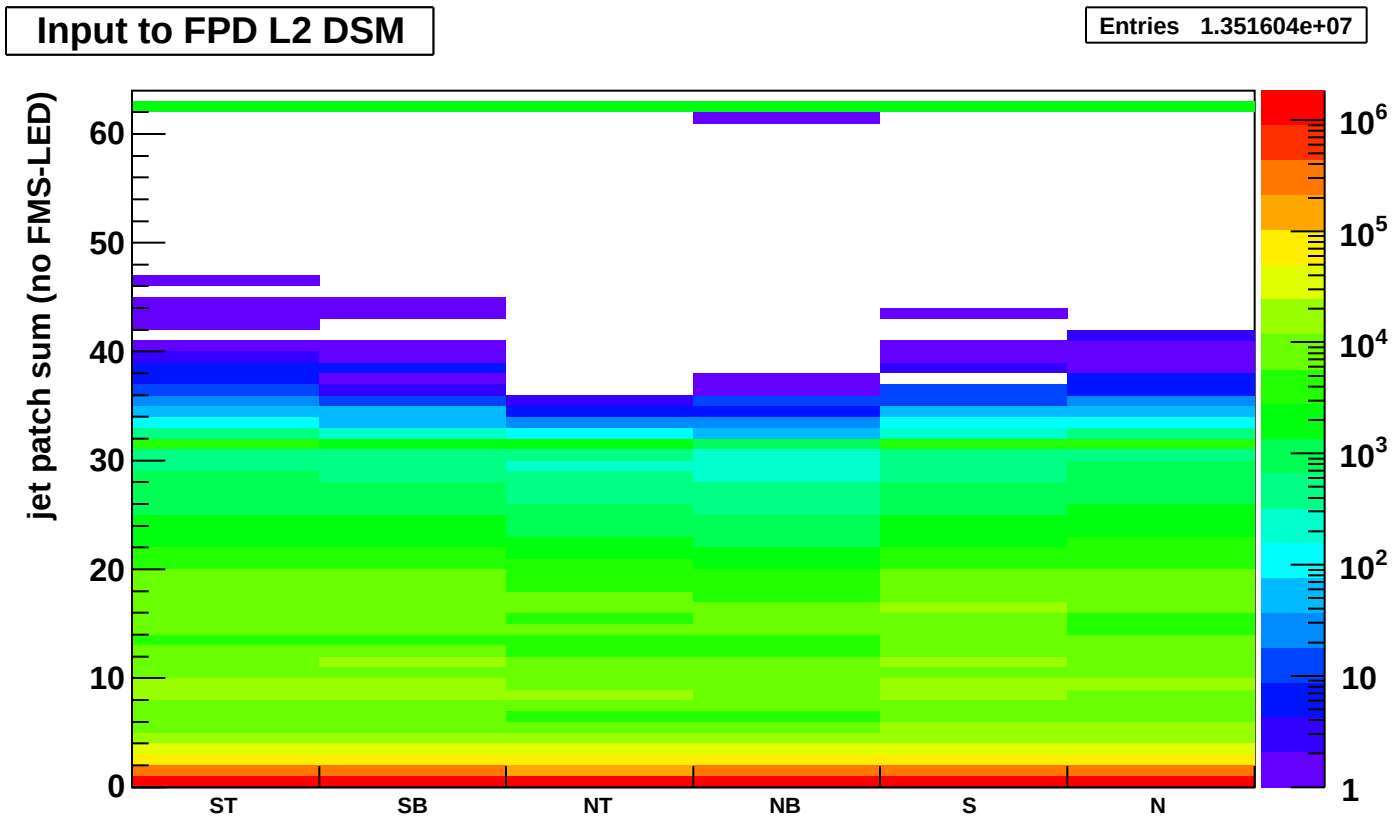

Input to FPD L2 DSM
Entries 4062
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

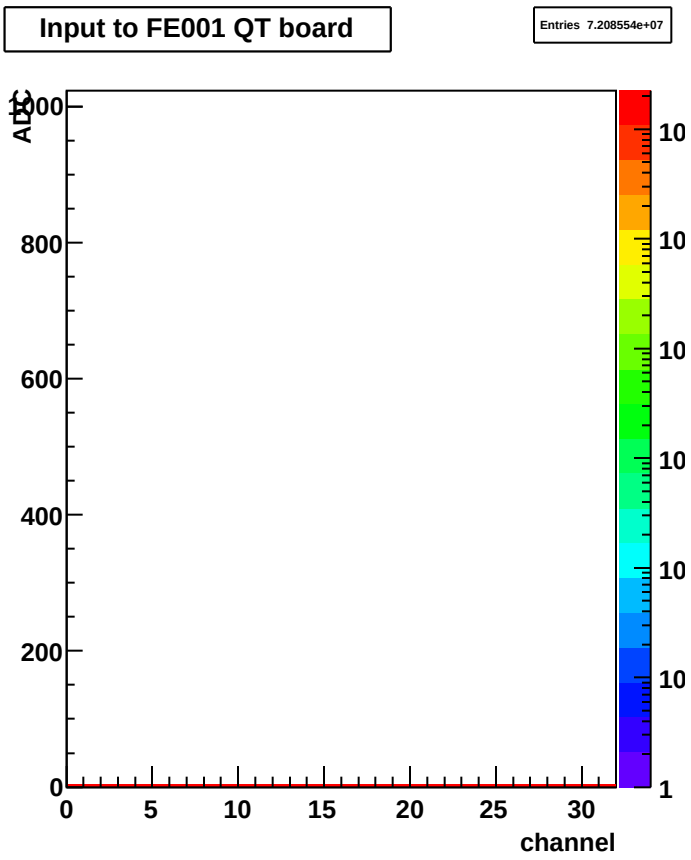

Input to FPD L2 DSM
Entries 6758019
CL bits
9749
5681
131248
49994
31058
109565
53985
32603
2.01186e+06
2.13894e+06
2.18333e+06
SMALL
LARGE-S
LARGE-N

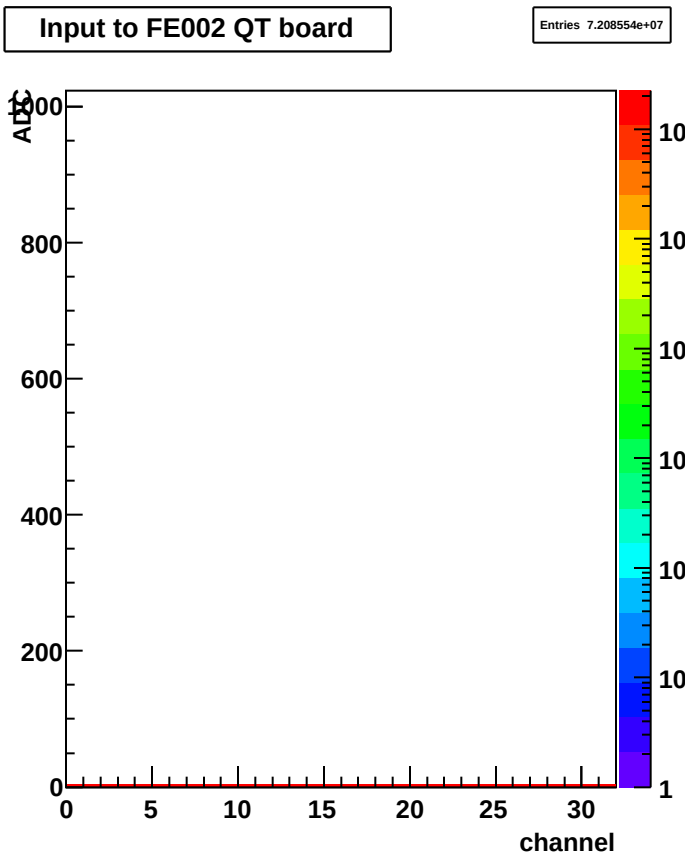

Input to FPD L2 DSM
Entries 151
CL bits - simulated
37
14
9
17
10
16
2
2
1
43
SMALL
LARGE-S
LARGE-N

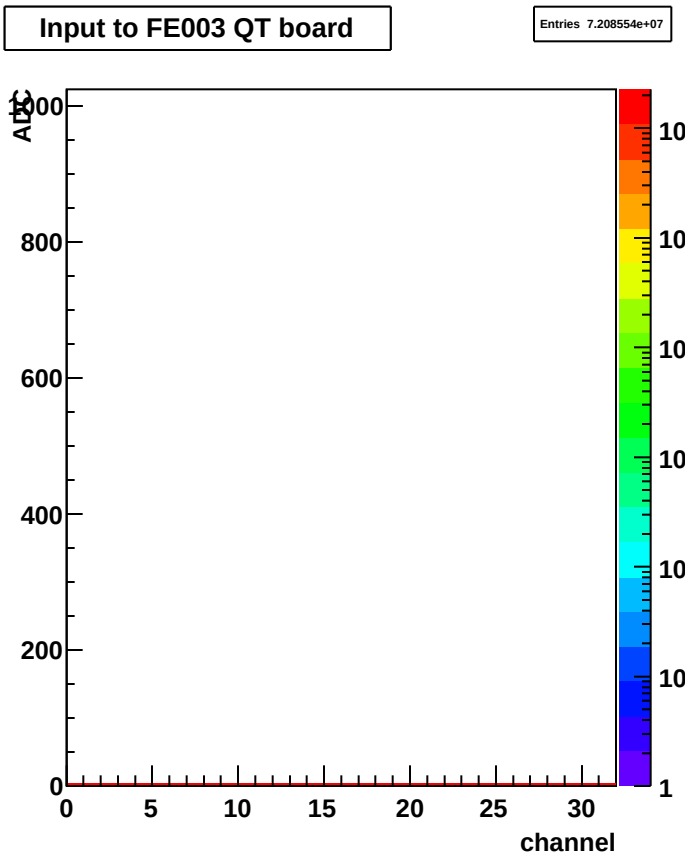

Input to FPD L2 DSM
Entries 6758019
HT bits
26163
82979
45455
53913
130665
48142
28280
2.04193e+06
2.12155e+06
2.17894e+06
SMALL
LARGE-S
LARGE-N

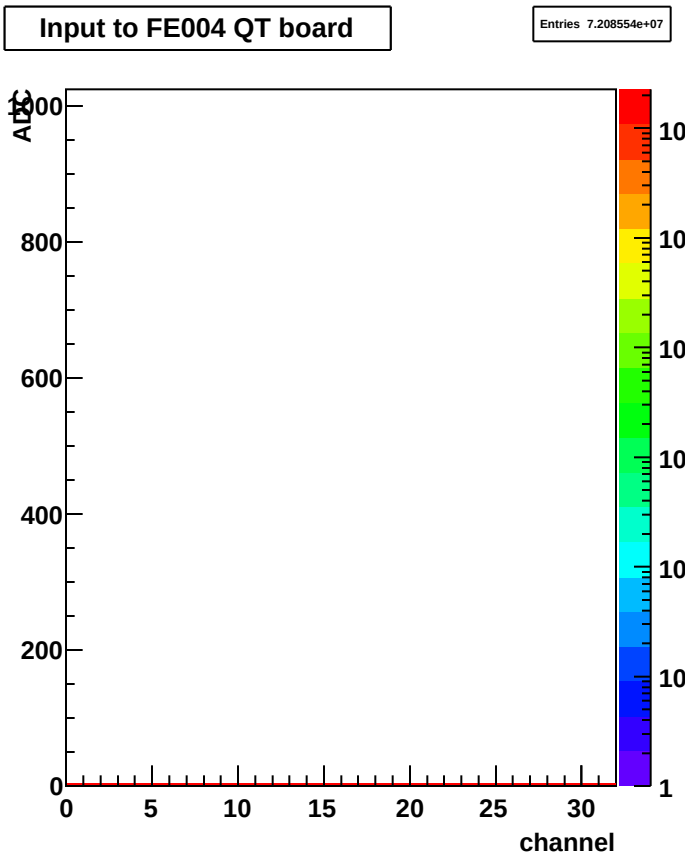

Input to FPD L2 DSM
Entries 1394
HT bits - simulated
545
14
10
500
303
12
3
4
1
1
1
SMALL
LARGE-S
LARGE-N

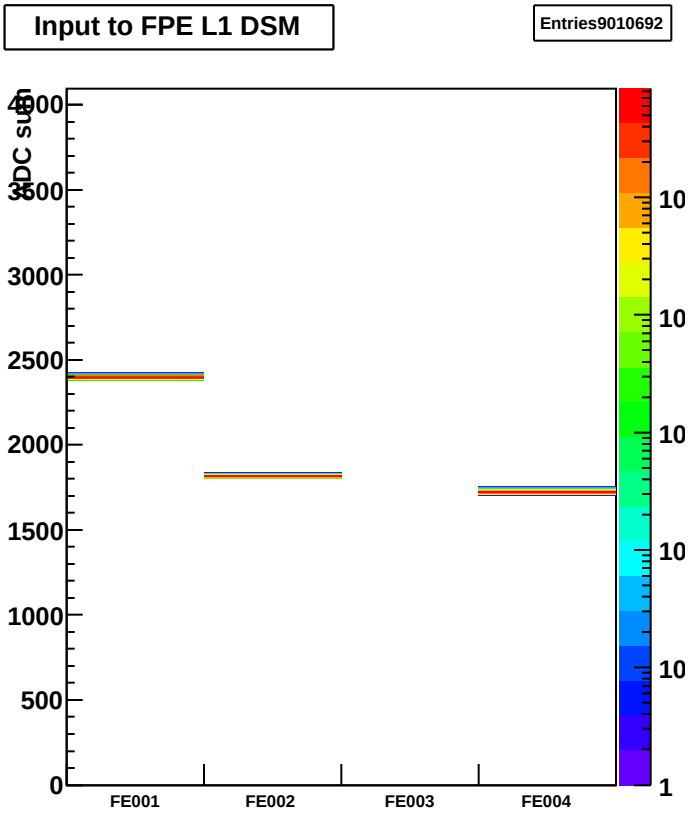

Input to FPD L2 DSM
Entries 1.351604e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.208554e+07
ADC
channel

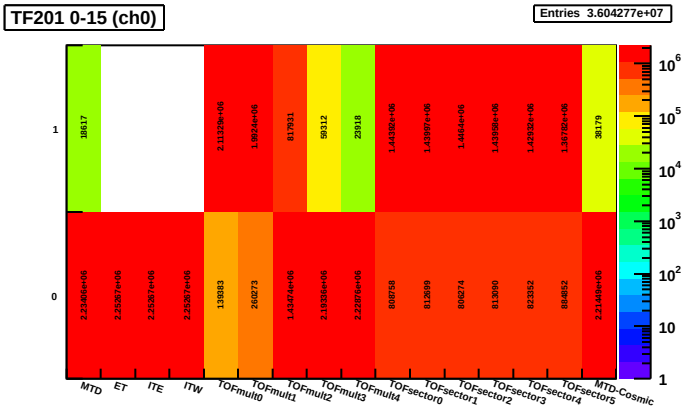

Input to FE002 QT board
Entries 7.208554e+07
ADC
channel


Input to FE003 QT board
Entries 7.208554e+07
ADC
channel

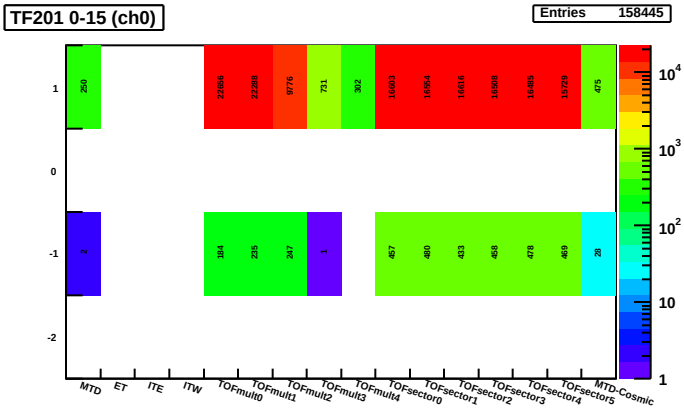

Input to FE004 QT board
Entries 7.208554e+07
ADC
channel


Input to FPE L1 DSM
Entries9010692
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

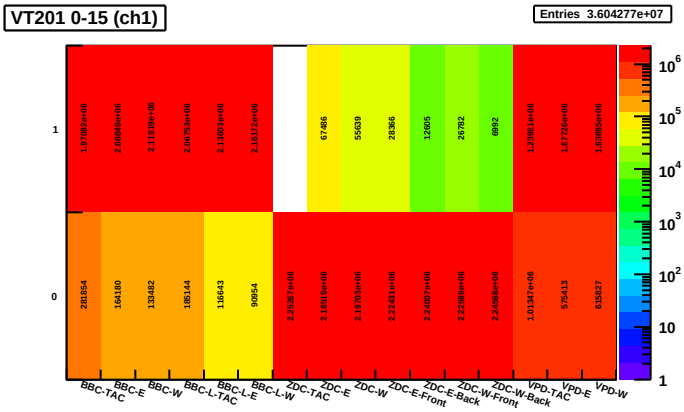
TF201 0-15 (ch0)



TF201 0-15 (ch0)



VT201 0-15 (ch1)



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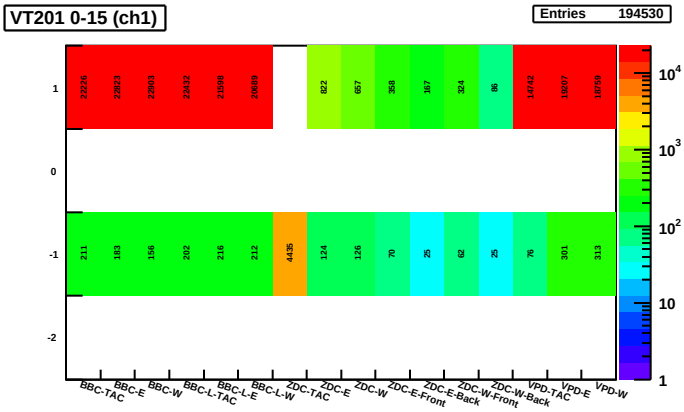

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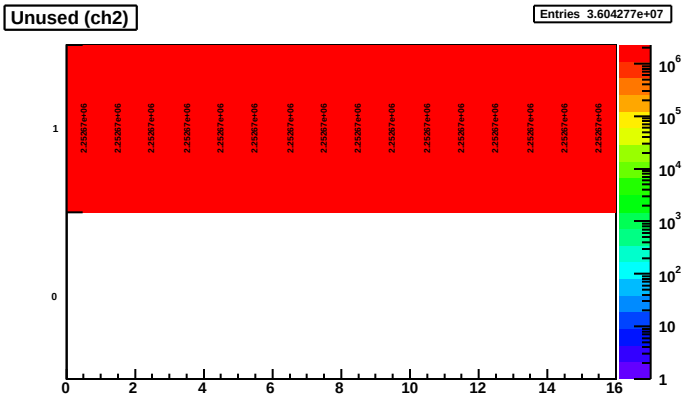

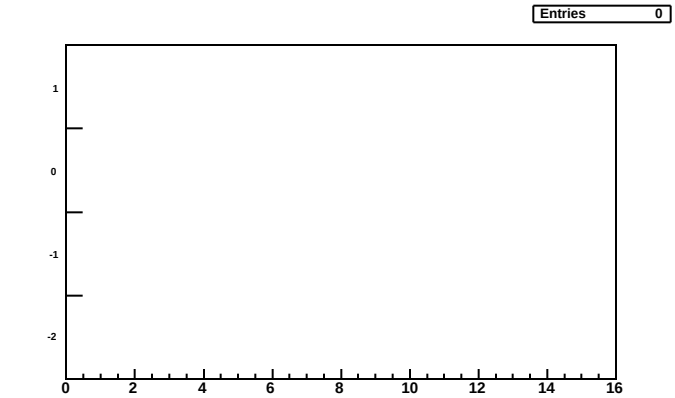

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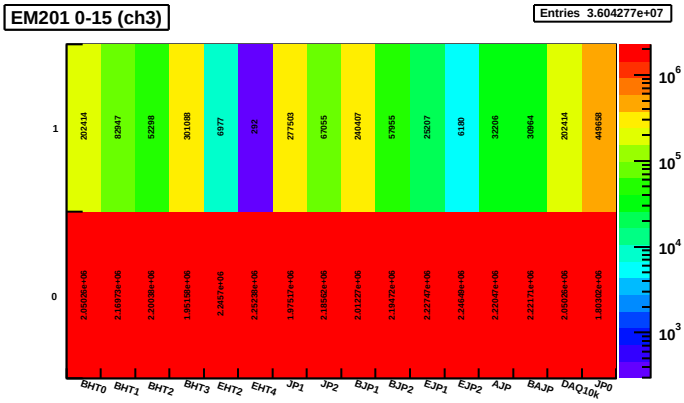

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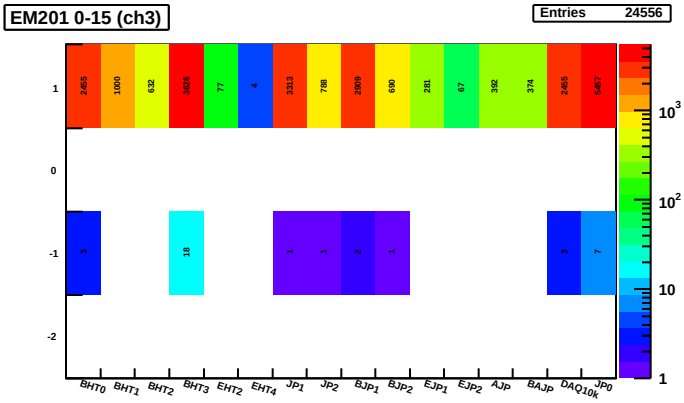
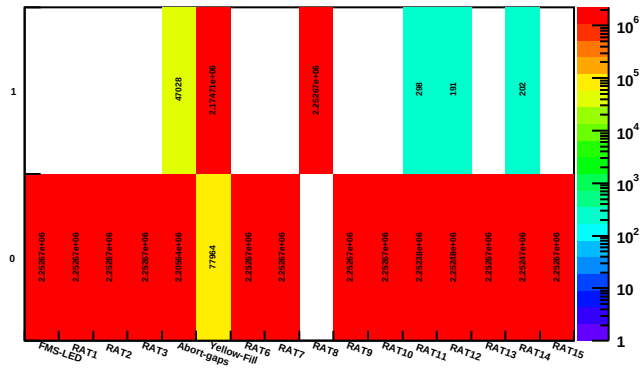
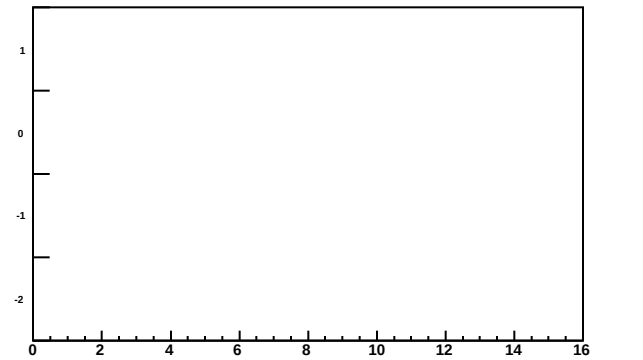


RAT board (ch4)





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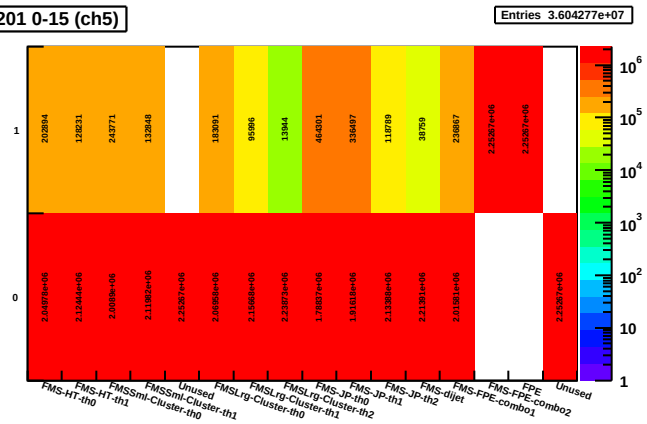
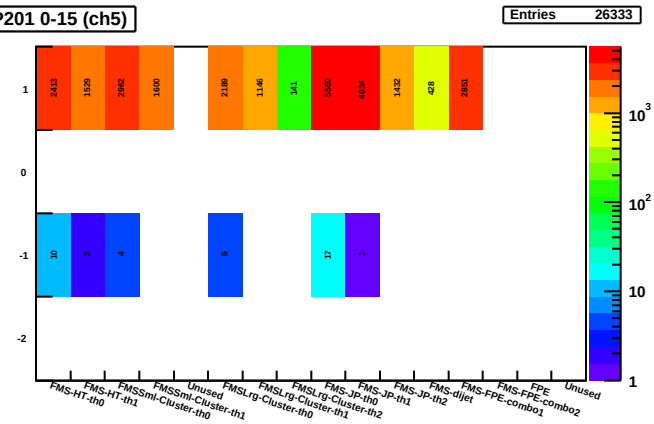


FP201 0-15 (ch5)



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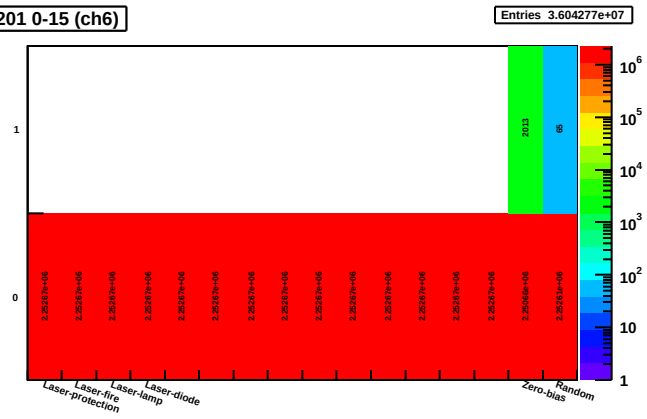

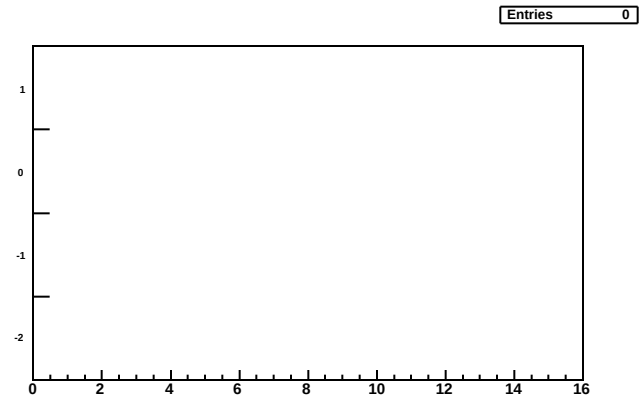
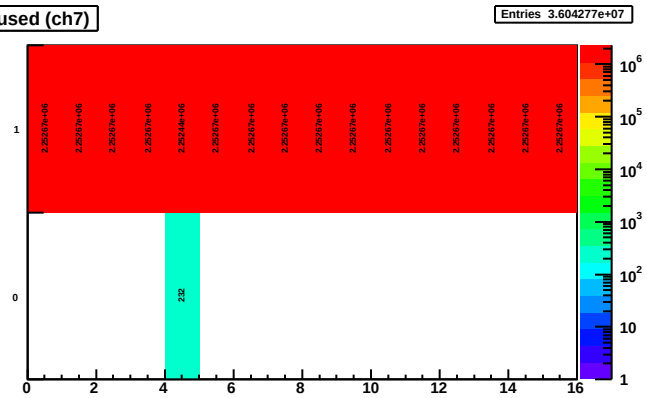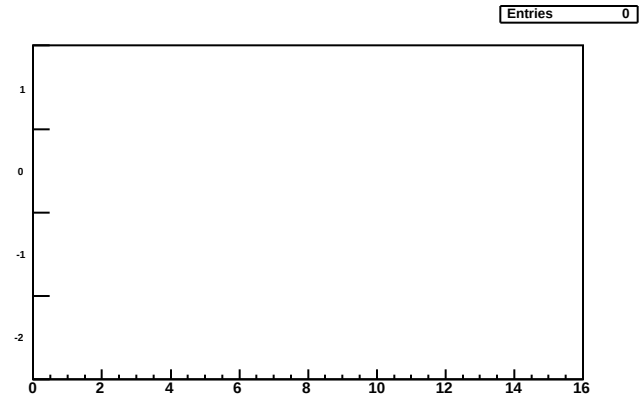
Unused (ch7)